

ASSEMBLY NOTES:

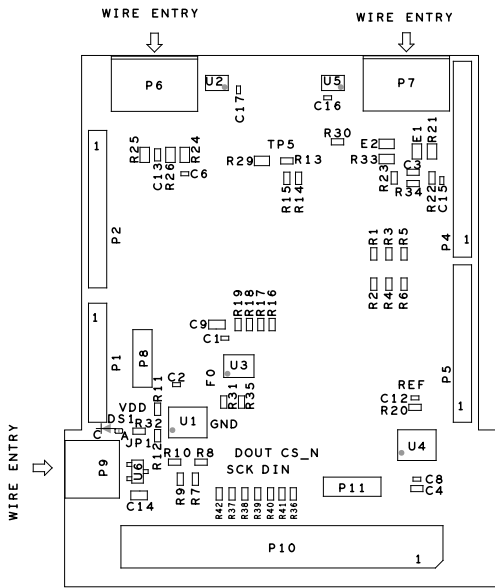
1. BOARD ACCEPTABILITY PER ANALOG DEVICES, INC. SPECIFICATION TST00119 (LATEST REVISION).
2. REPAIRS PER IPC-7711/21(LATEST REVISION) ARE ALLOWED.
3. REPAIRS ARE NOT ALLOWED IN SOLDERMASK FREE AREAS ON EITHER SIDE OF THE BOARD.
4. RoHS COMPLIANCE: ASSEMBLY VENDOR SHOULD ASSURE COMPLIANCE WITH LEAD-FREE AND RoHS PCB ASSEMBLY STANDARDS (EU RoHS DIRECTIVE 2002/95/EC).
5. BOARDS TO BE SHIPPED SINGULATED AFTER ASSEMBLY PROCESSES SMOOTHEN EDGES AND FREE FROM BURS AFTER DEPANELIZATION PROCESS
6. DON NOT TRIM EXCESS PINS ON THE FOLLOWING LOCATIONS:

- P1

- P2


- P4

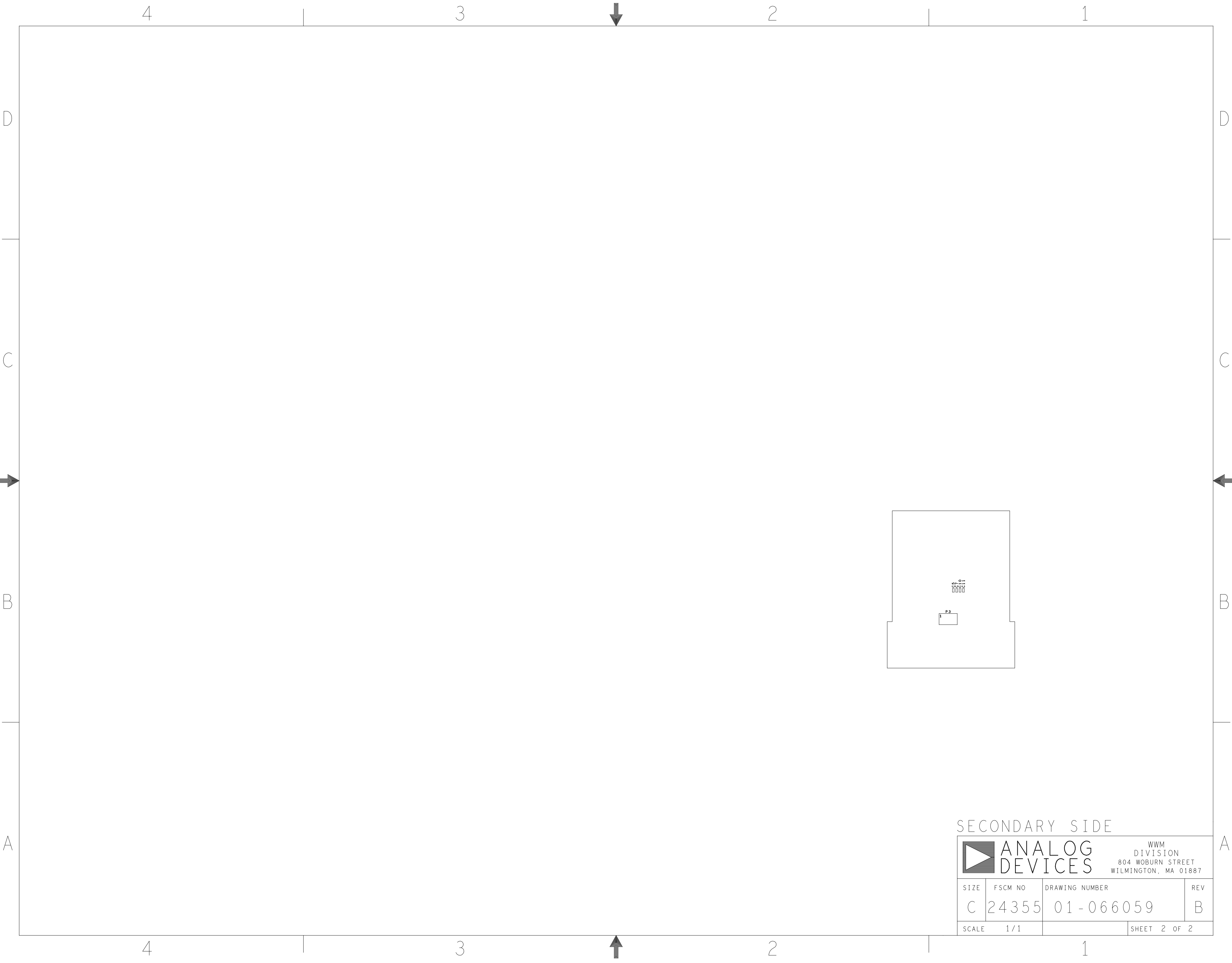
- P5



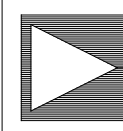
REVISIONS			
REV	DESCRIPTION	DATE	APPROVED
A	INITIAL RELEASE	30JAN21	K. NUEVA
B	AS PER ECR-103780	21JUL21	K. NUEVA
B1	AS PER ECR-105514	08NOV21	K. NUEVA

PRIMARY SIDE

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN MM			APPROVAL		DATE		<div><div></div><div>ANALOG DEVICES</div></div> <div>WWM DIVISION 804 WOBURN STREET WILMINGTON, MA 01887</div>		
TOLERANCES			TEMPLATE ENGINEER		N/A				
DECIMALS FRACTIONS ANGLES			HARDWARE SERVICES		30JAN21				
.XX -.254 --1/32 -- 2			M VALE						
.XXX -.127			HARDWARE SYSTEMS		N/A		TITLE ASSEMBLY EVAL - AD590 - ARDZ CUSTOMER EVAL BOARD Z		
.XXX -.127			N/A						
MATERIAL			TEST ENGINEER		N/A				
			N/A		N/A				
			COMPONENT ENGINEER		30JAN21				
			M. YAN						
			TEST PROCESS		N/A				
			N/A						
			HARDWARE RELEASE		30JAN21				
			K. JABATAN						
FINISH			DESIGNER		30JAN21		SIZE FSCM NO DRAWING NUMBER REV		
			M. VALE						
			PTD ENGINEER		30JAN21				
			K. NUEVA						
			CHECKER		N/A				
			N/A						
DO NOT SCALE DWG							SCALE 1 / 1 SHEET 1 OF 2		



SECONDARY SIDE

		ANALOG DEVICES		WWM DIVISION 804 WOBURN STREET WILMINGTON, MA 01887	
		SIZE	FSCM NO	DRAWING NUMBER	REV
C		24355	01-066059		B
SCALE		1 / 1		SHEET 2 OF 2	