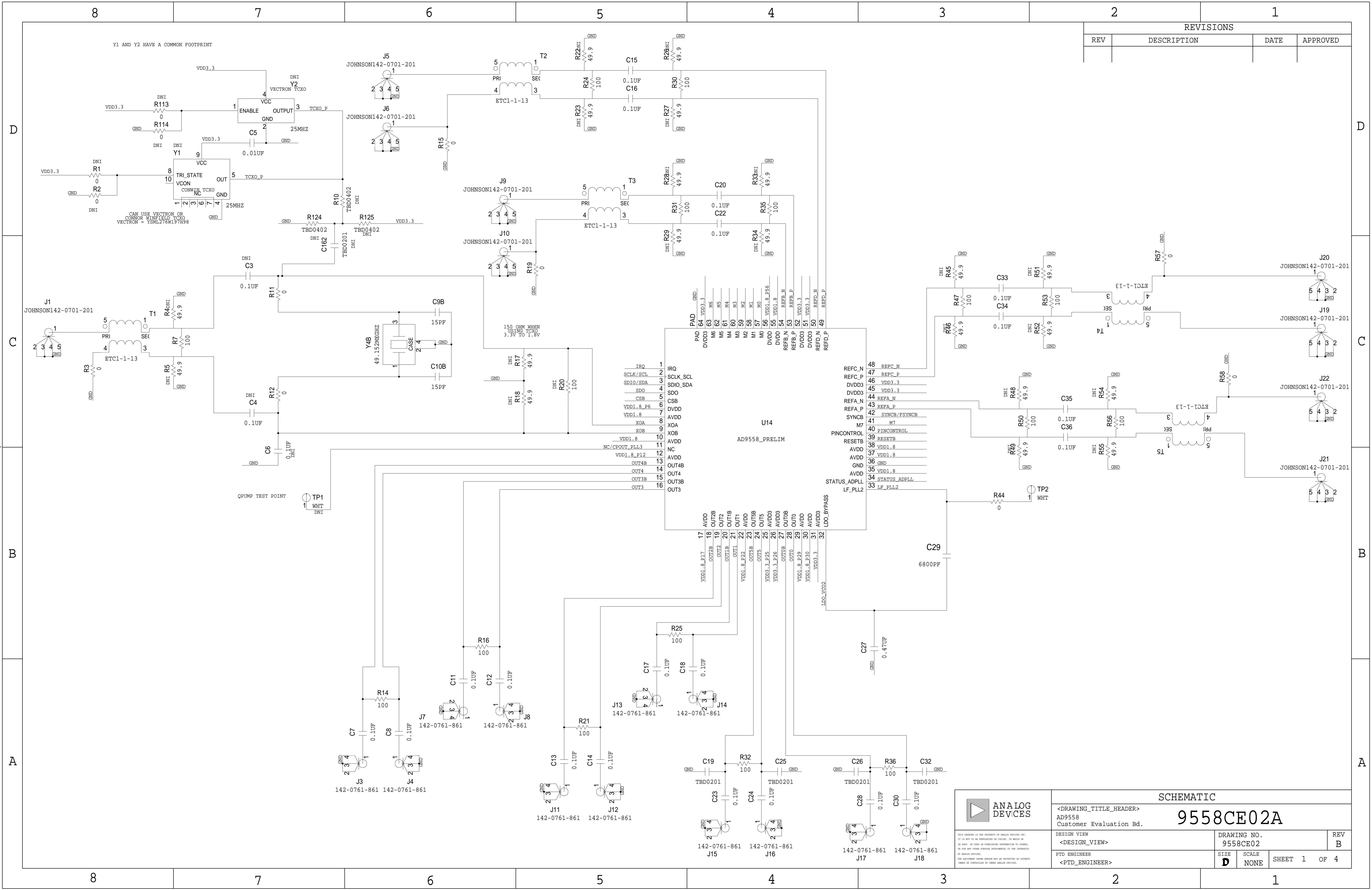


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REV	DESCRIPTION	DATE	APPROVED



	SCHEMATIC		
	<DRAWING_TITLE_HEADER> AD9558 Customer Evaluation Bd.		
	DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. 9558CE02A	REV B
	PTD ENGINEER <PTD_ENGINEER>	SIZE D	SCALE NONE

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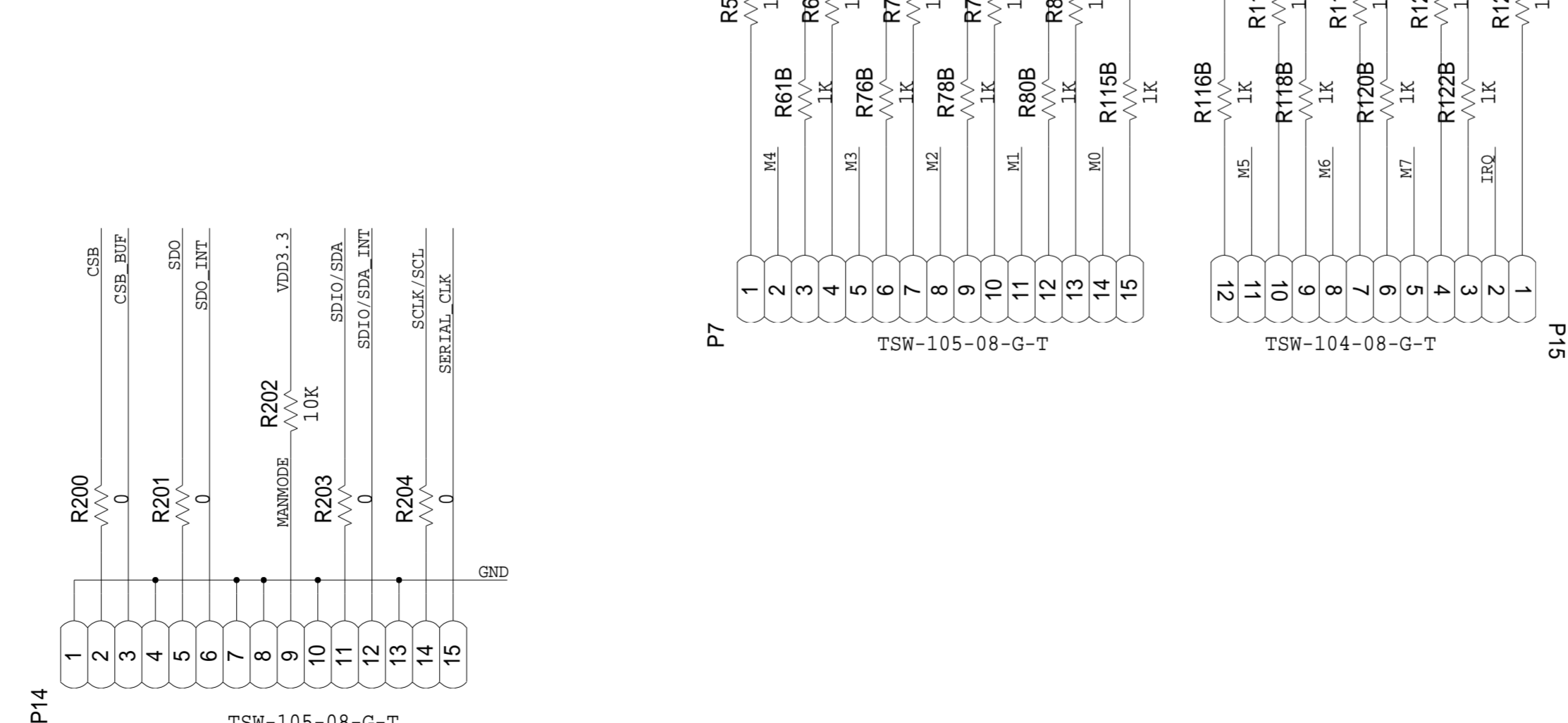
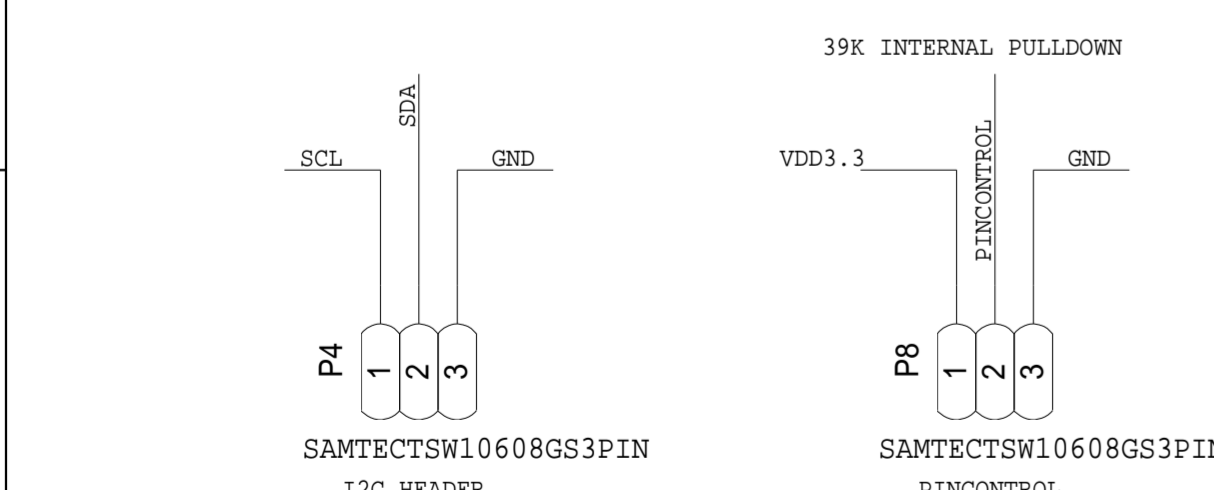
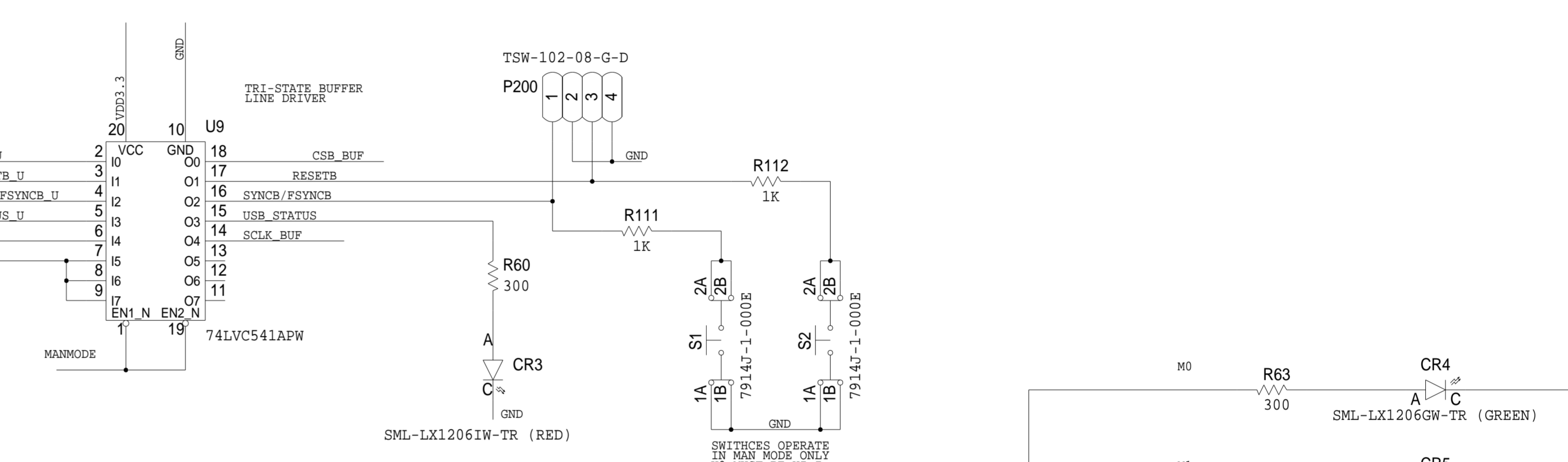
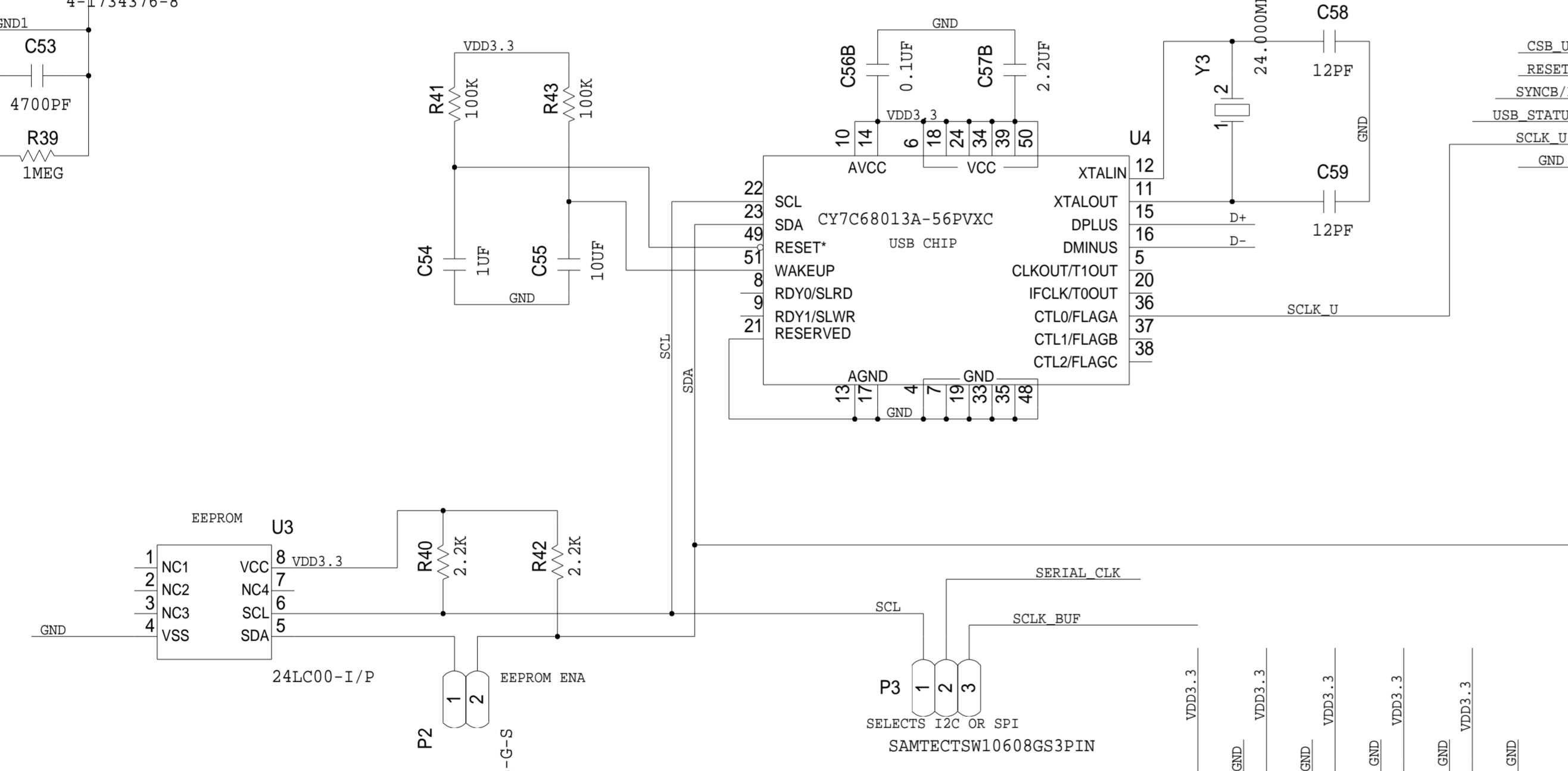
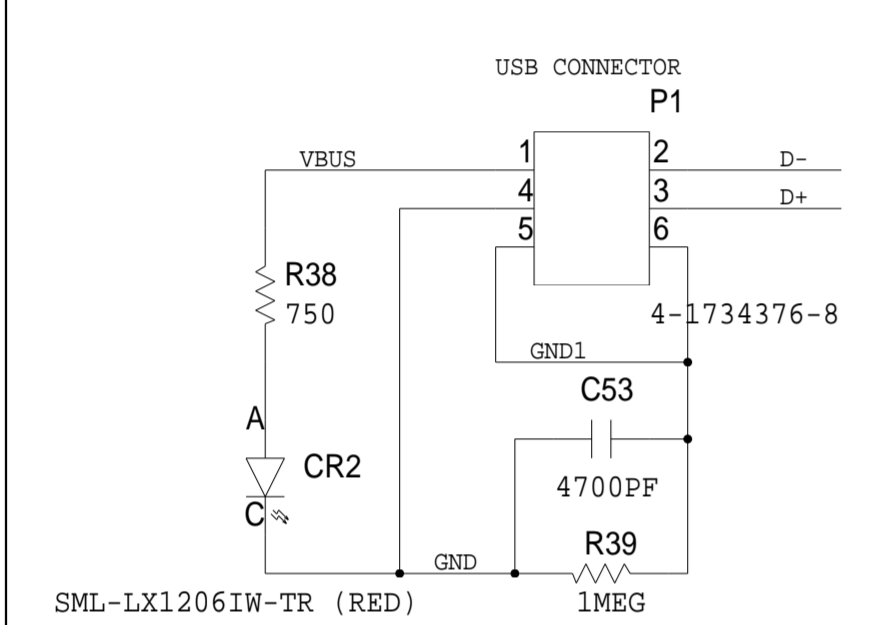
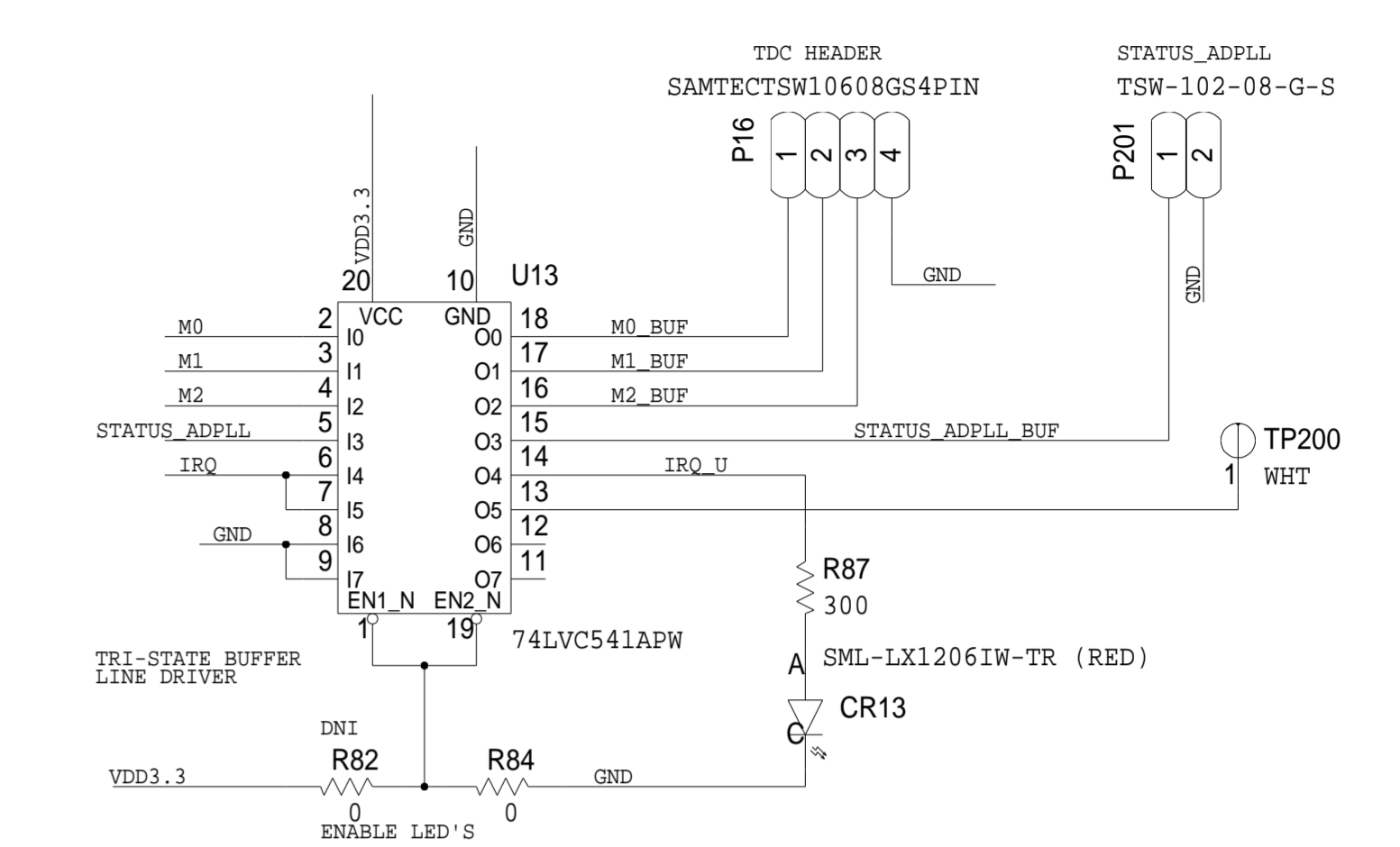
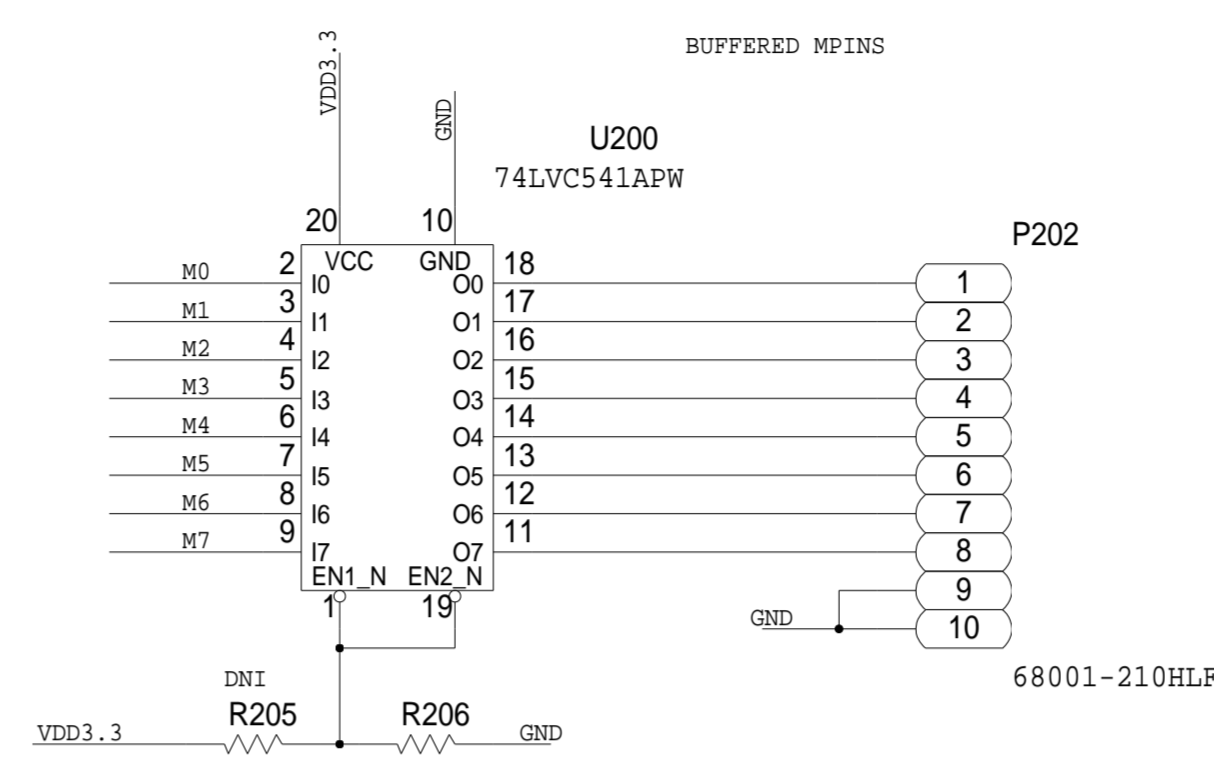
USB SECTION

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

CY7C68013A-56PVXC U4

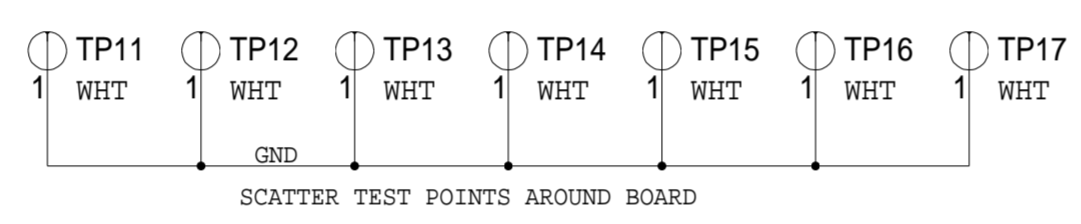
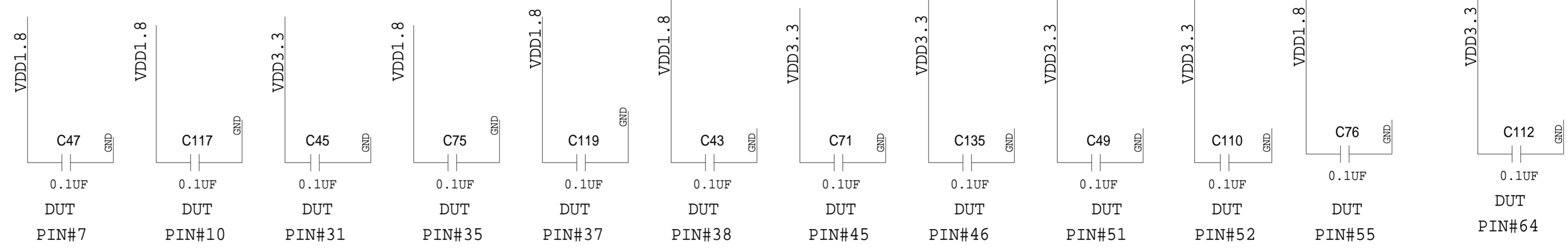
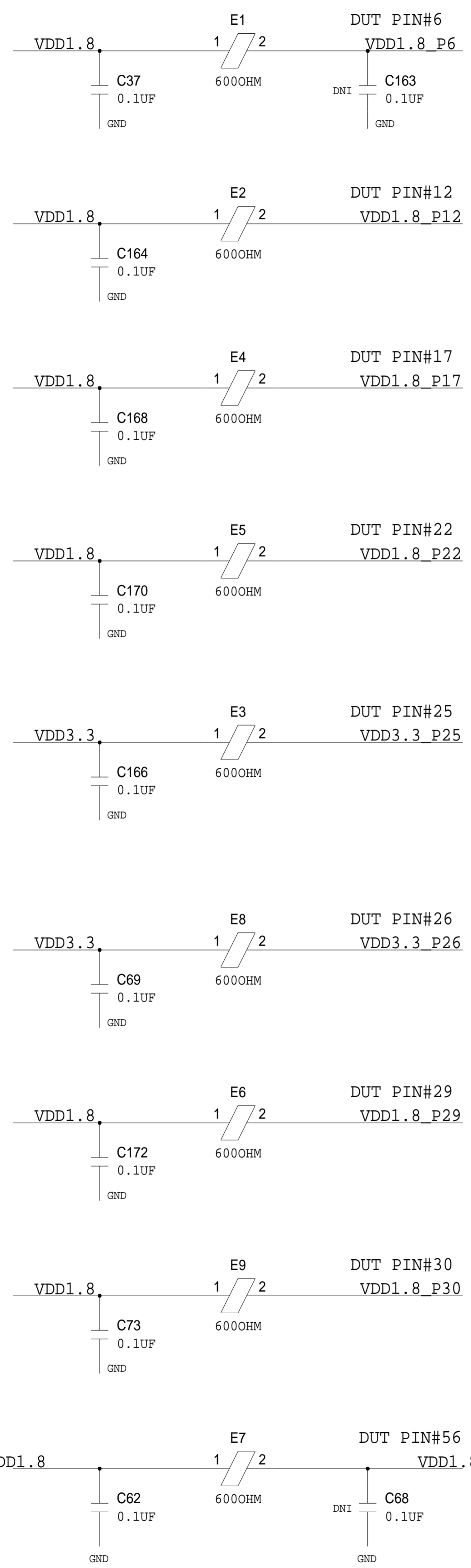
PA0/INT0	40	USB_STATUS_U
PA1/INT1	41	CSB_U
PA2/SLOE	42	RESETB_U
PA3/WU2	43	IRQ_U
PA4/FIFOADR0	44	SYNCRB/FSYNCRB_U
PA5/FIFOADR1	45	
PA6/PKTEND	46	
PA7/FLAGD/SLCS1	47	
PB0/FD0	25	SDIO_U
PB1/FD1	26	SDIO_INT
PB2/FD2	27	
PB3/TXD1/FD3	28	
PB4/FD4	29	
PB5/FD5	30	
PB6/FD6	31	
PB7/FD7	32	
PD0/FD8	52	M0
PD1/FD9	53	M1
PD2/FD10	54	M2
PD3/FD11	55	M3
PD4/FD12	56	M4
PD5/FD13	57	M5
PD6/FD14	58	M6
PD7/FD15	59	M7

USB CHIP

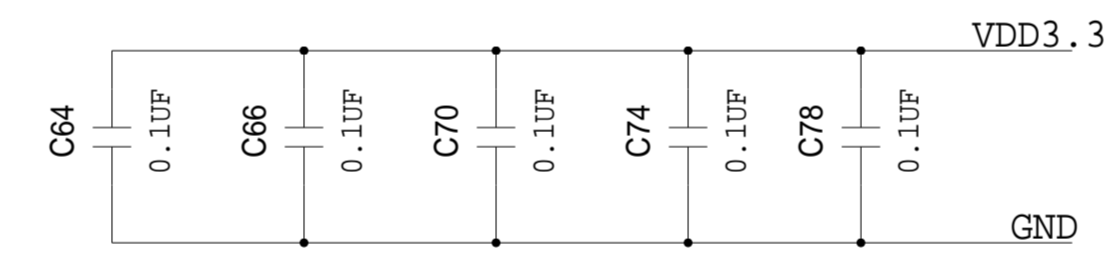


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<p><DRAWING_TITLE_HEADER> AD9558 Customer Evaluation Bd.</p>			
DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. 9558CE02	REV B	
PTD ENGINEER <PTD_ENGINEER>	SIZE D	SCALE NONE	SHEET 2 OF 4

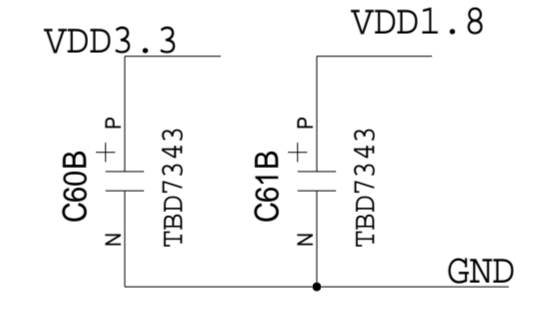
REVISIONS			
REV	DESCRIPTION	DATE	APPROVED



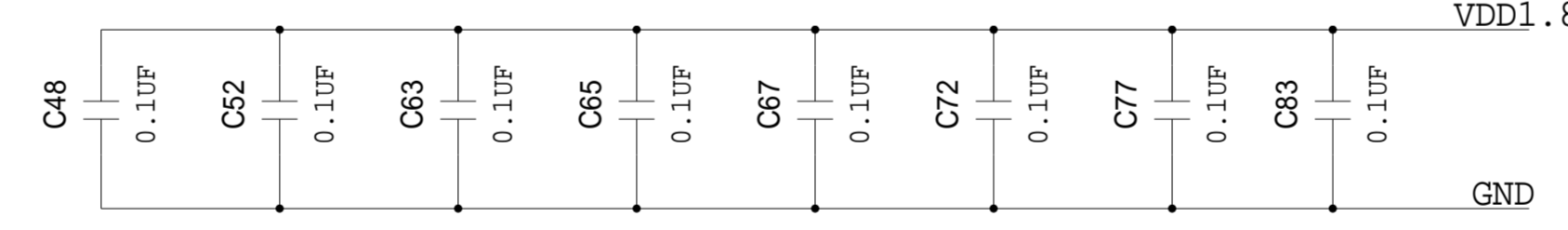
BYPASS CAPS & FERRITE BEADS



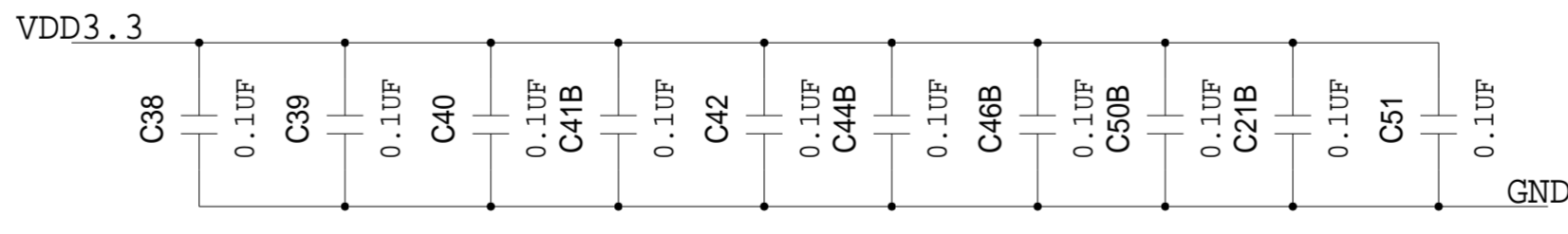
CAPS ON TOP OF EVB



CAPS ON BOT OF EVB



CAPS FOR USB SECTION

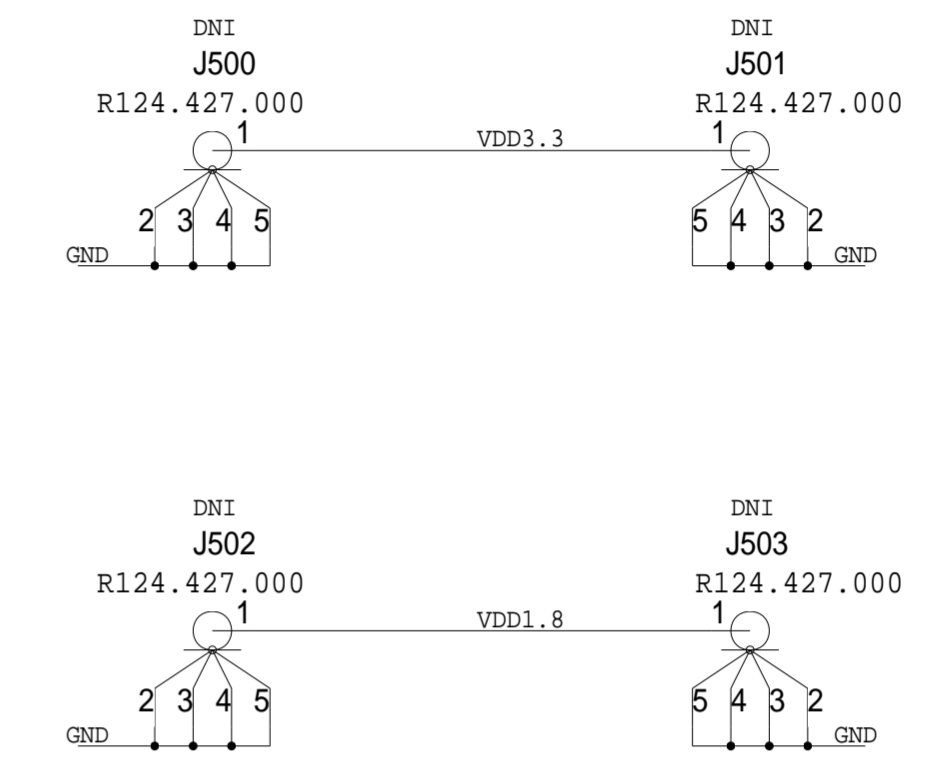
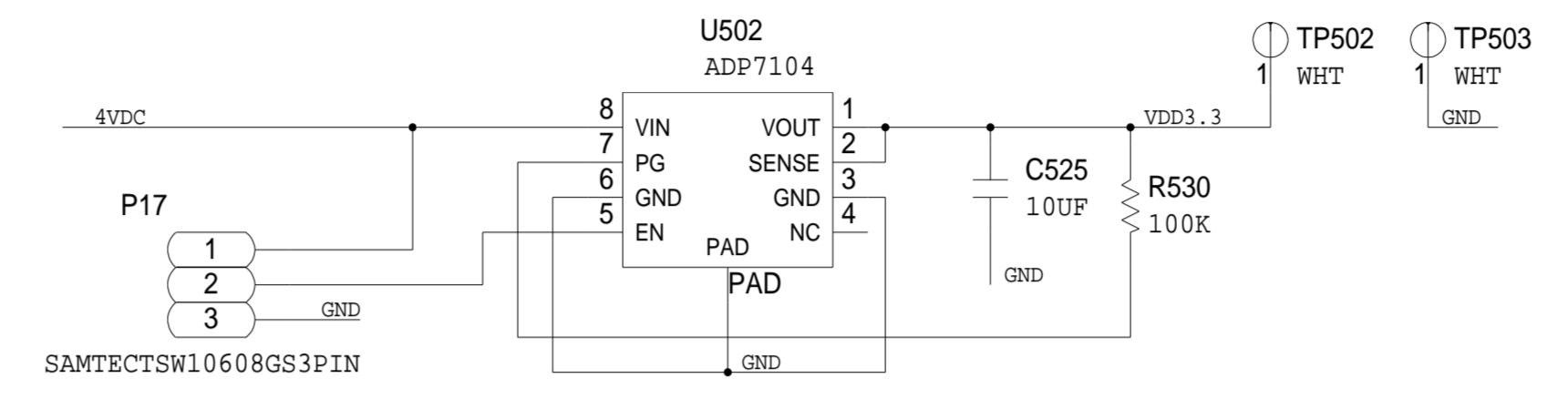
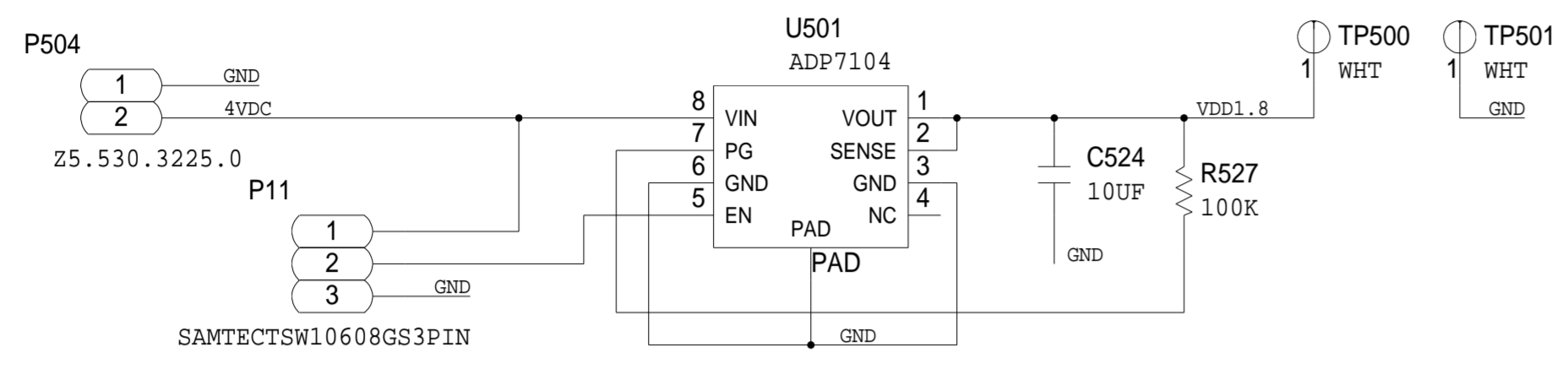


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	DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. 9558CE02	REV B
	PTD ENGINEER <PTD_ENGINEER>	SIZE D	SCALE NONE
		SHEET 3 OF 4	

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

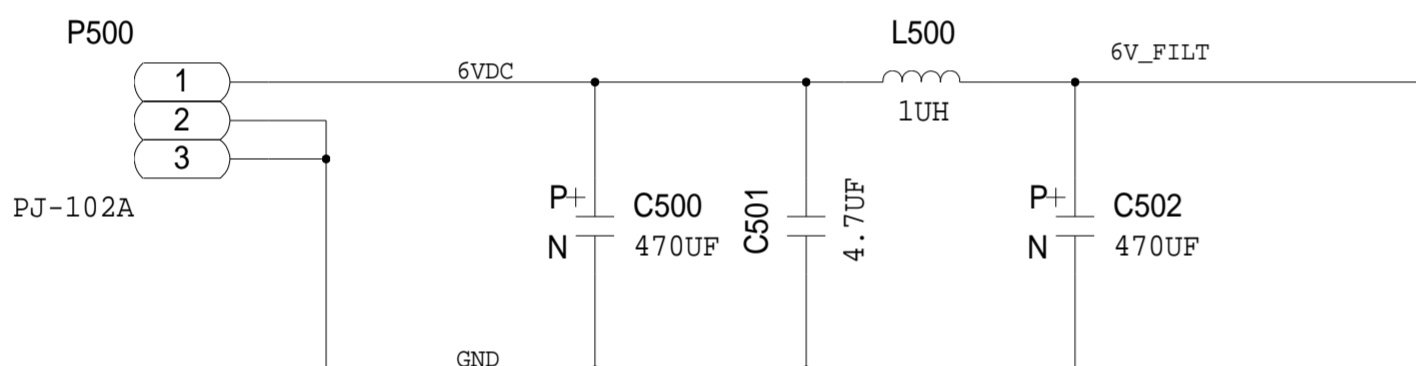
1.8V REGULATOR

3.3V REGULATOR



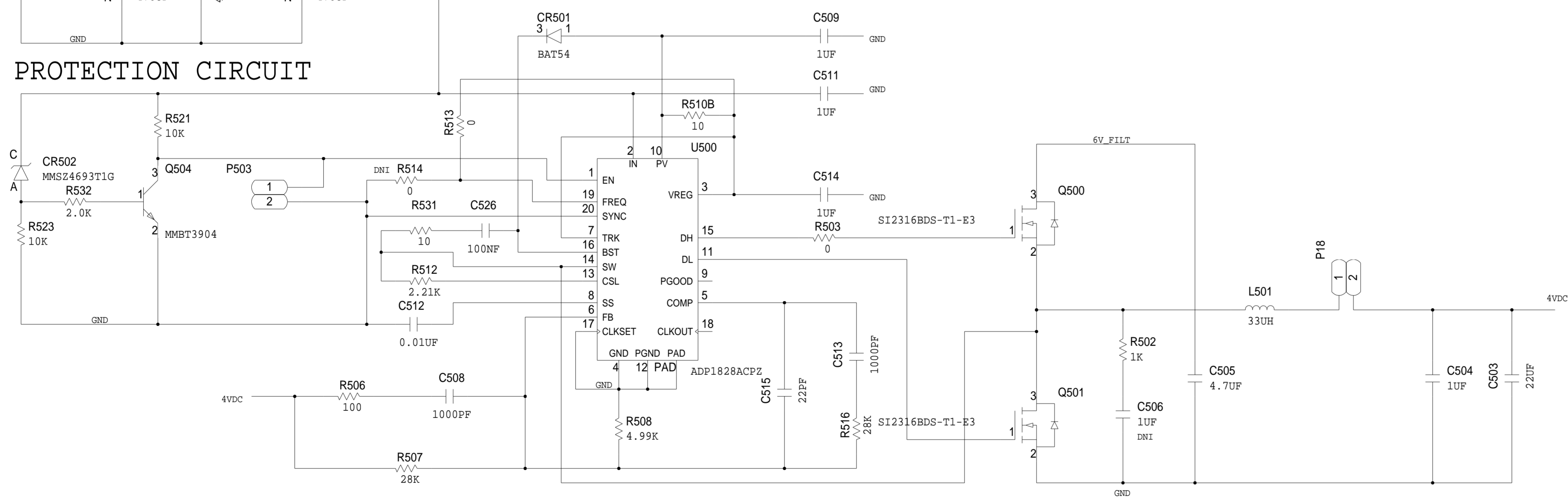
DC TO DC STEP DOWN

6V WALL WART (AC ADAPTER)



DC TO DC STEP DOWN

8.5V OVER PROTECTION CIRCUIT



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	<DRAWING_TITLE_HEADER> AD9558 Customer Evaluation Bd.		9558CE02A
	DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. 9558CE02	REV B
	PTD ENGINEER <PTD_ENGINEER>	SIZE D	SCALE NONE
		SHEET 4 OF 4	