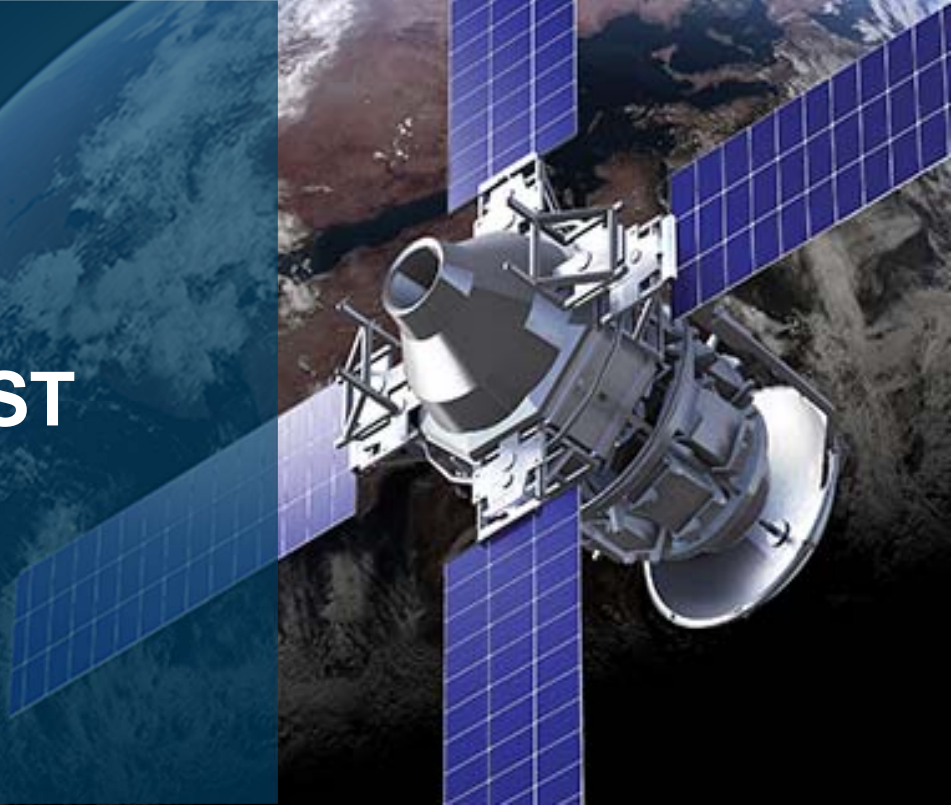


LOW DOSE RADIATION TEST REPORT ADA4084-4S

July 2018
Generic



Radiation Test Report

Product:	ADA4084-4S
Gamma:	0, 30k, 50k, 75k, 100k
Gamma Source:	Co60/TM1019 Condition D
Dose Rate:	<7.9 mRad/s
Facilities:	VPT RAD
Tested:	11/2017 - 5/2018

The RADTEST® DATA SERVICE is a compilation of radiation test results on Analog Devices' Space grade products. It is designed to assist customers in selecting the right product for applications where radiation is a consideration. Many products manufactured by Analog Devices, Inc. have been shown to be radiation tolerant to most tactical radiation environments. Analog Devices, Inc. does not make any claim to maintain or guarantee these levels of radiation tolerance without lot qualification test.

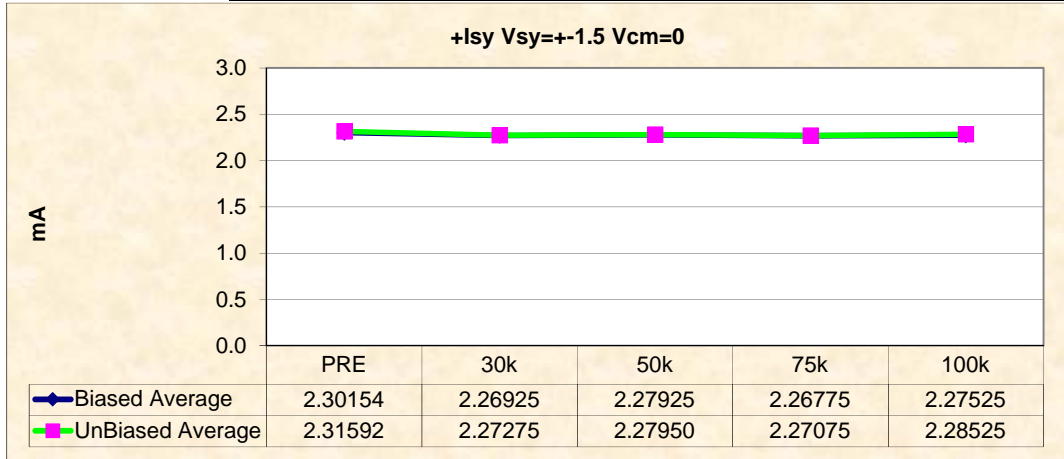
It is the responsibility of the Procuring Activity to screen products from Analog Devices, Inc. for compliance to Nuclear Hardness Critical Items (HCI) specifications.

Warning:

Analog Devices, Inc. does not recommend use of this data to qualify other product grades or process levels. Analog Devices, Inc. is not responsible and has no liability for any consequences, and all applicable Warranties are null and void if any Analog Devices product is modified in any way or used outside of normal environmental and operating conditions, including the parameters specified in the corresponding data sheet. Analog Devices, Inc. does not guarantee that wafer manufacturing is the same for all process levels.

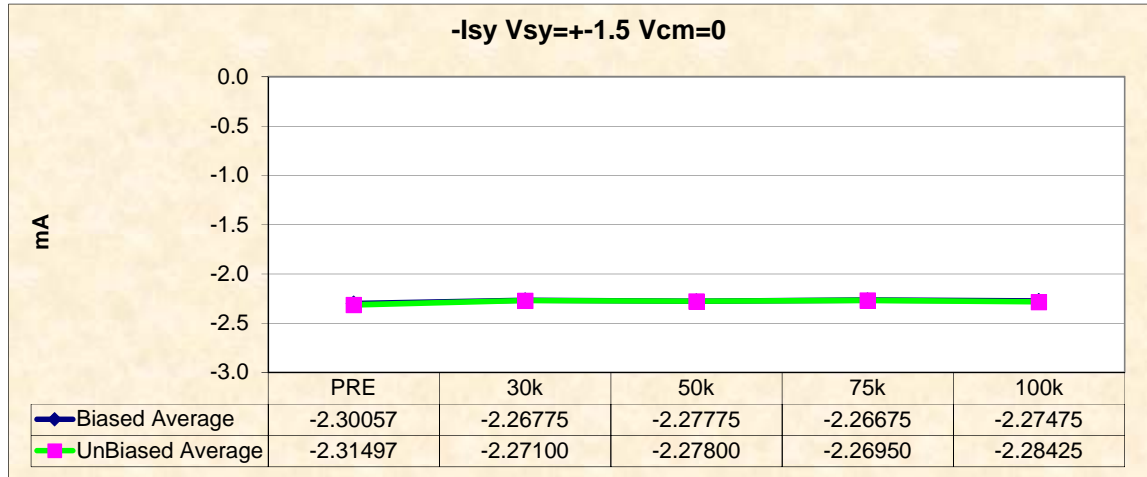
+Isy Vsy=+-1.5 Vcm=0						
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	2.28100	0.00000	0.00000	2.19000	2.20800
	54	2.26400	0.00000	0.00000	2.25600	2.27300
Biased	14	2.31013	2.29200	2.29100	2.28200	2.28600
	15	2.32715	2.28800	2.30500	2.29000	2.30000
	42	2.28253	2.24500	2.26200	2.24800	2.25700
	43	2.28635	2.25200	2.25900	2.25100	2.25800
	Min	2.28253	2.24500	2.25900	2.24800	2.25700
	Max	2.32715	2.29200	2.30500	2.29000	2.30000
	Average	2.30154	2.26925	2.27925	2.26775	2.27525
UnBiased	23	2.33084	2.27900	2.29100	2.28000	2.29500
	24	2.33153	2.27500	2.28900	2.27800	2.29400
	47	2.29874	2.25800	2.26600	2.25800	2.27100
	48	2.30256	2.27900	2.27200	2.26700	2.28100
	Min	2.29874	2.25800	2.26600	2.25800	2.27100
	Max	2.33153	2.27900	2.29100	2.28000	2.29500
	Average	2.31592	2.27275	2.27950	2.27075	2.28525

Limit: <2.7mA



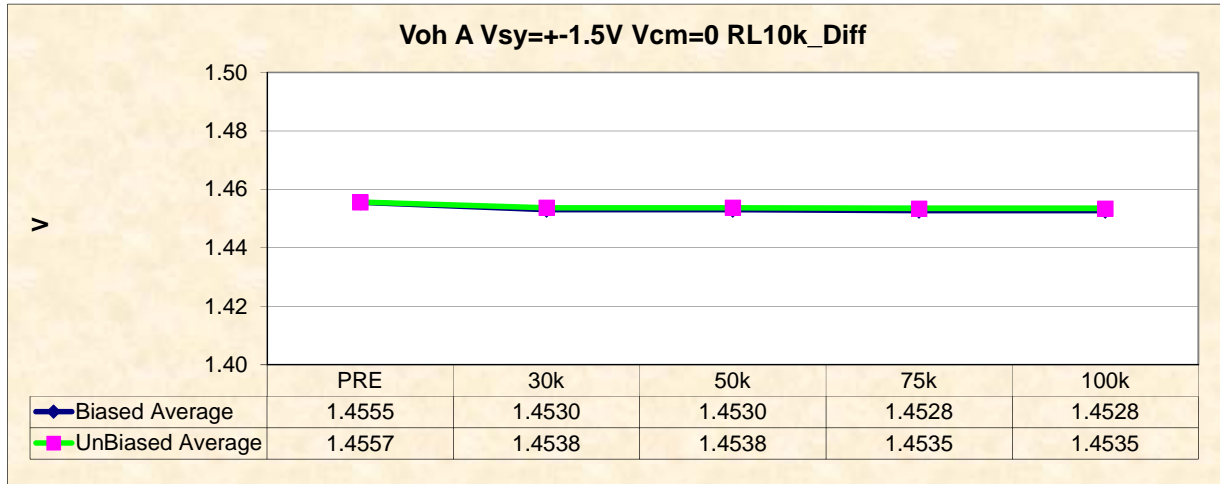
		-Isy Vsy=+-1.5 Vcm=0				
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	-2.28000	0.00000	0.00000	-2.18900	-2.20700
	54	-2.26300	0.00000	0.00000	-2.25400	-2.27200
Biased	14	-2.30911	-2.29100	-2.28900	-2.28100	-2.28600
	15	-2.32631	-2.28700	-2.30400	-2.28900	-2.29900
	42	-2.28146	-2.24300	-2.26000	-2.24700	-2.25600
	43	-2.28540	-2.25000	-2.25800	-2.25000	-2.25800
	Min	-2.32631	-2.29100	-2.30400	-2.28900	-2.29900
	Max	-2.28146	-2.24300	-2.25800	-2.24700	-2.25600
	Average	-2.30057	-2.26775	-2.27775	-2.26675	-2.27475
UnBiased	23	-2.32968	-2.27800	-2.28900	-2.27900	-2.29400
	24	-2.33056	-2.27300	-2.28800	-2.27600	-2.29300
	47	-2.29785	-2.25600	-2.26400	-2.25700	-2.27000
	48	-2.30179	-2.27700	-2.27100	-2.26600	-2.28000
	Min	-2.33056	-2.27800	-2.28900	-2.27900	-2.29400
	Max	-2.29785	-2.25600	-2.26400	-2.25700	-2.27000
	Average	-2.31497	-2.27100	-2.27800	-2.26950	-2.28425

Limit: >-2.7mA



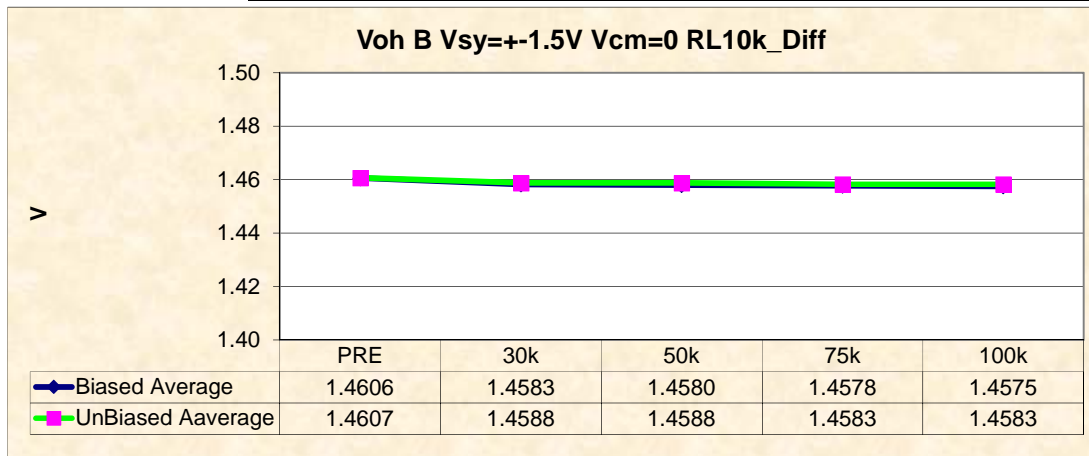
		Voh A Vsy=+-1.5V Vcm=0 RL10k_Diff				
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	1.4590	0.0000	0.0000	1.4540	1.4540
	54	1.4570	0.0000	0.0000	1.4530	1.4530
Biased	14	1.4552	1.4530	1.4530	1.4520	1.4520
	15	1.4558	1.4530	1.4530	1.4530	1.4530
	42	1.4558	1.4530	1.4530	1.4530	1.4530
	43	1.4554	1.4530	1.4530	1.4530	1.4530
	Min	1.4552	1.4530	1.4530	1.4520	1.4520
	Max	1.4558	1.4530	1.4530	1.4530	1.4530
	Average	1.4555	1.4530	1.4530	1.4528	1.4528
UnBiased	23	1.4557	1.4530	1.4530	1.4530	1.4540
	24	1.4560	1.4550	1.4550	1.4540	1.4530
	47	1.4553	1.4530	1.4530	1.4530	1.4530
	48	1.4557	1.4540	1.4540	1.4540	1.4540
	Min	1.4553	1.4530	1.4530	1.4530	1.4530
	Max	1.4560	1.4550	1.4550	1.4540	1.4540
	Average	1.4557	1.4538	1.4538	1.4535	1.4535

Limit: >1.4V



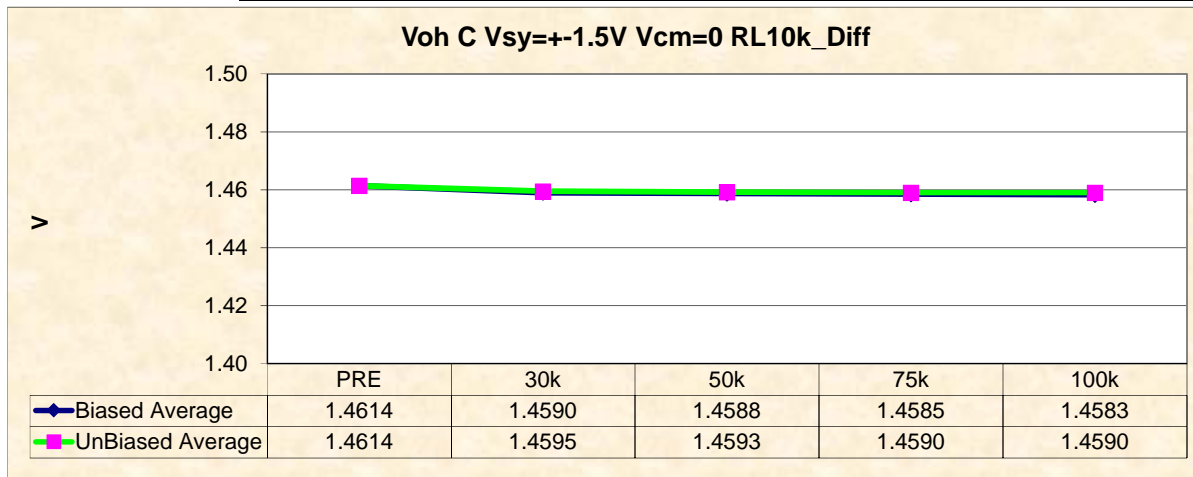
Voh B Vsy=+-1.5V Vcm=0 RL10k_Diff						
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	1.4600	0.0000	0.0000	1.4590	1.4590
	54	1.4590	0.0000	0.0000	1.4580	1.4580
Biased	14	1.4602	1.4580	1.4570	1.4570	1.4570
	15	1.4607	1.4580	1.4580	1.4580	1.4580
	42	1.4604	1.4580	1.4580	1.4580	1.4570
	43	1.4611	1.4590	1.4590	1.4580	1.4580
	Min	1.4602	1.4580	1.4570	1.4570	1.4570
	Max	1.4611	1.4590	1.4590	1.4580	1.4580
	Average	1.4606	1.4583	1.4580	1.4578	1.4575
UnBiased	23	1.4607	1.4580	1.4580	1.4570	1.4590
	24	1.4610	1.4600	1.4600	1.4590	1.4570
	47	1.4603	1.4580	1.4580	1.4580	1.4580
	48	1.4608	1.4590	1.4590	1.4590	1.4590
	Min	1.4603	1.4580	1.4580	1.4570	1.4570
	Max	1.4610	1.4600	1.4600	1.4590	1.4590
	Average	1.4607	1.4588	1.4588	1.4583	1.4583

Limit: >1.4V



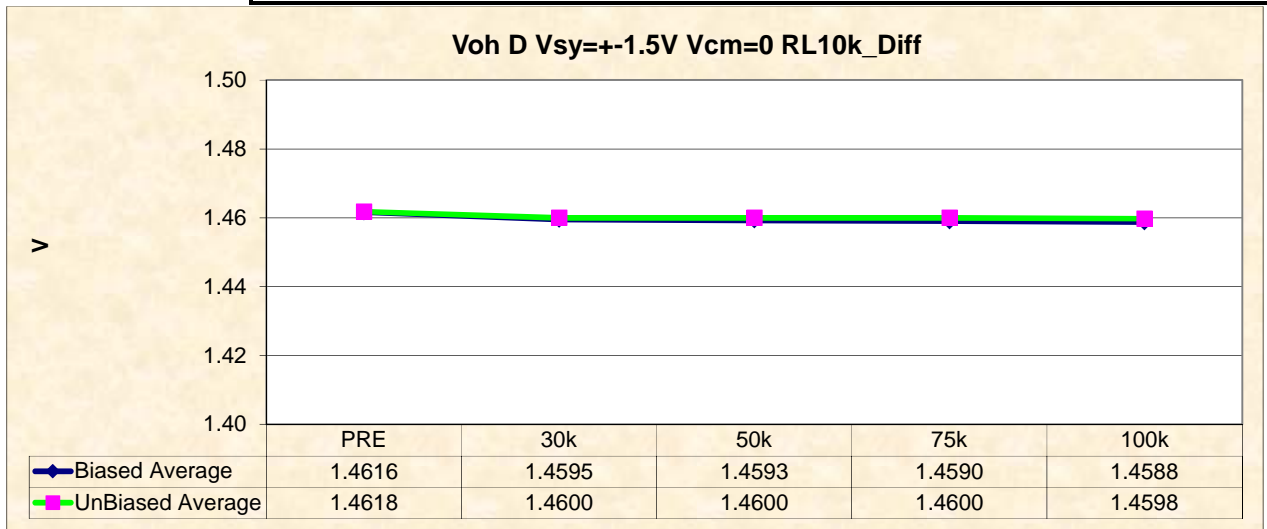
Voh C Vsy=+-1.5V Vcm=0 RL10k_Diff						
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	1.4610	0.0000	0.0000	1.4600	1.4600
	54	1.4590	0.0000	0.0000	1.4590	1.4590
Biased	14	1.4613	1.4590	1.4590	1.4580	1.4580
	15	1.4617	1.4590	1.4590	1.4590	1.4590
	42	1.4611	1.4590	1.4580	1.4580	1.4580
	43	1.4614	1.4590	1.4590	1.4590	1.4580
	Min	1.4611	1.4590	1.4580	1.4580	1.4580
	Max	1.4617	1.4590	1.4590	1.4590	1.4590
	Average	1.4614	1.4590	1.4588	1.4585	1.4583
UnBiased	23	1.4611	1.4580	1.4580	1.4580	1.4610
	24	1.4622	1.4610	1.4610	1.4610	1.4580
	47	1.4608	1.4590	1.4590	1.4580	1.4580
	48	1.4616	1.4600	1.4590	1.4590	1.4590
	Min	1.4608	1.4580	1.4580	1.4580	1.4580
	Max	1.4622	1.4610	1.4610	1.4610	1.4610
	Average	1.4614	1.4595	1.4593	1.4590	1.4590

Limit: >1.4V



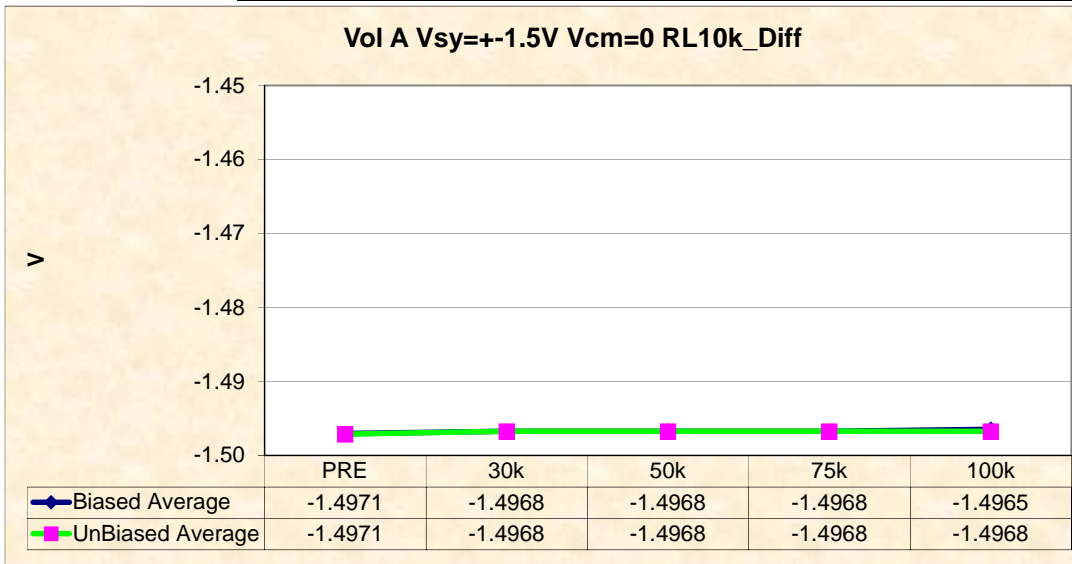
Voh D Vsy=+-1.5V Vcm=0 RL10k_Diff						
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	1.4610	0.0000	0.0000	1.4600	1.4600
	54	1.4590	0.0000	0.0000	1.4590	1.4590
Biased	8	1.4611	1.4590	1.4590	1.4580	1.4580
	9	1.4617	1.4600	1.4590	1.4590	1.4590
	55	1.4616	1.4590	1.4590	1.4590	1.4590
	56	1.4620	1.4600	1.4600	1.4600	1.4590
	Min	1.4611	1.4590	1.4590	1.4580	1.4580
	Max	1.4620	1.4600	1.4600	1.4600	1.4590
	Average	1.4616	1.4595	1.4593	1.4590	1.4588
UnBiased	10	1.4622	1.4600	1.4600	1.4600	1.4610
	11	1.4622	1.4610	1.4610	1.4610	1.4590
	57	1.4613	1.4590	1.4590	1.4590	1.4590
	58	1.4615	1.4600	1.4600	1.4600	1.4600
	Min	1.4613	1.4590	1.4590	1.4590	1.4590
	Max	1.4622	1.4610	1.4610	1.4610	1.4610
	Average	1.4618	1.4600	1.4600	1.4600	1.4598

Limit: >1.4V



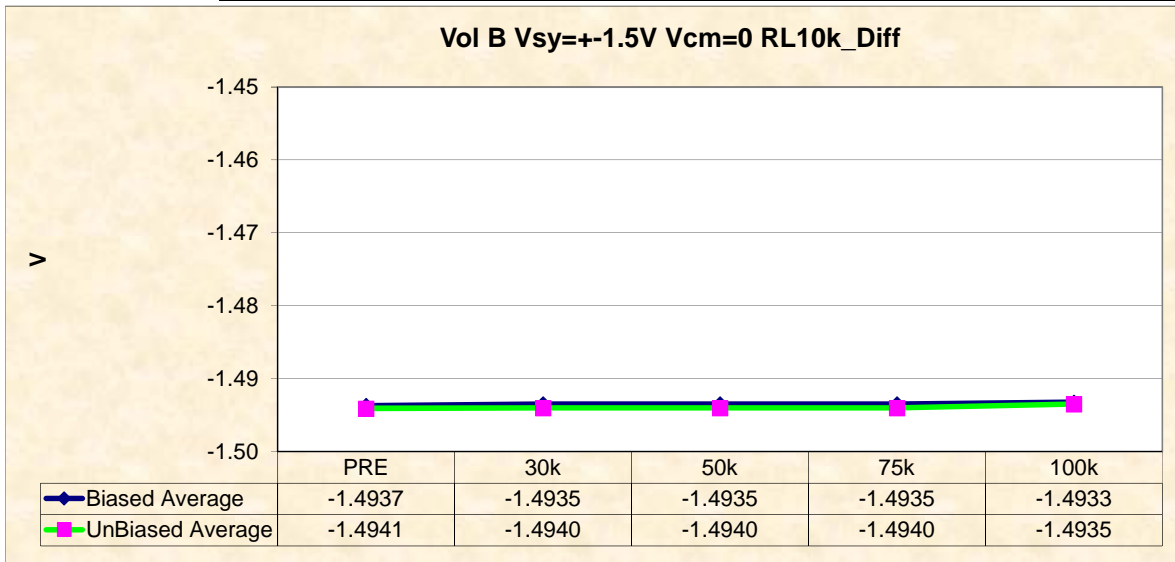
Vol A Vsy=+1.5V Vcm=0 RL10k_Diff						
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	-1.4950	0.0000	0.0000	-1.4970	-1.4970
	54	-1.4940	0.0000	0.0000	-1.4970	-1.4970
Biased	14	-1.4964	-1.4960	-1.4960	-1.4960	-1.4960
	15	-1.4975	-1.4970	-1.4970	-1.4970	-1.4970
	42	-1.4968	-1.4970	-1.4970	-1.4970	-1.4960
	43	-1.4976	-1.4970	-1.4970	-1.4970	-1.4970
	Min	-1.4976	-1.4970	-1.4970	-1.4970	-1.4970
	Max	-1.4964	-1.4960	-1.4960	-1.4960	-1.4960
	Average	-1.4971	-1.4968	-1.4968	-1.4968	-1.4965
UnBiased	23	-1.4974	-1.4970	-1.4970	-1.4970	-1.4960
	24	-1.4963	-1.4960	-1.4960	-1.4960	-1.4970
	47	-1.4972	-1.4970	-1.4970	-1.4970	-1.4970
	48	-1.4976	-1.4970	-1.4970	-1.4970	-1.4970
	Min	-1.4976	-1.4970	-1.4970	-1.4970	-1.4970
	Max	-1.4963	-1.4960	-1.4960	-1.4960	-1.4960
	Average	-1.4971	-1.4968	-1.4968	-1.4968	-1.4968

Limit: <-1.45V



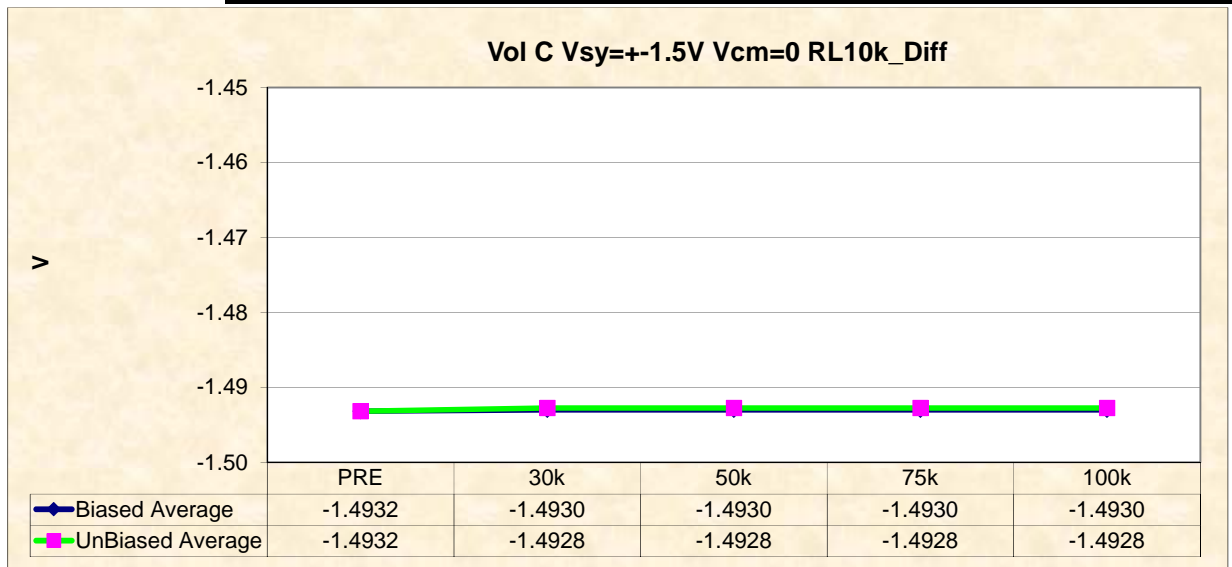
Vol B Vsy=+-1.5V Vcm=0 RL10k_Diff						
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	-1.4930	0.0000	0.0000	-1.4940	-1.4940
	54	-1.4930	0.0000	0.0000	-1.4940	-1.4940
Biased	14	-1.4932	-1.4930	-1.4930	-1.4930	-1.4930
	15	-1.4942	-1.4940	-1.4940	-1.4940	-1.4930
	42	-1.4933	-1.4930	-1.4930	-1.4930	-1.4930
	43	-1.4943	-1.4940	-1.4940	-1.4940	-1.4940
	Min	-1.4943	-1.4940	-1.4940	-1.4940	-1.4940
	Max	-1.4932	-1.4930	-1.4930	-1.4930	-1.4930
	Average	-1.4937	-1.4935	-1.4935	-1.4935	-1.4935
UnBiased	23	-1.4939	-1.4940	-1.4940	-1.4940	-1.4940
	24	-1.4942	-1.4940	-1.4940	-1.4940	-1.4930
	47	-1.4938	-1.4940	-1.4940	-1.4940	-1.4930
	48	-1.4943	-1.4940	-1.4940	-1.4940	-1.4940
	Min	-1.4943	-1.4940	-1.4940	-1.4940	-1.4940
	Max	-1.4938	-1.4940	-1.4940	-1.4940	-1.4930
	Average	-1.4941	-1.4940	-1.4940	-1.4940	-1.4935

Limit: <-1.45V



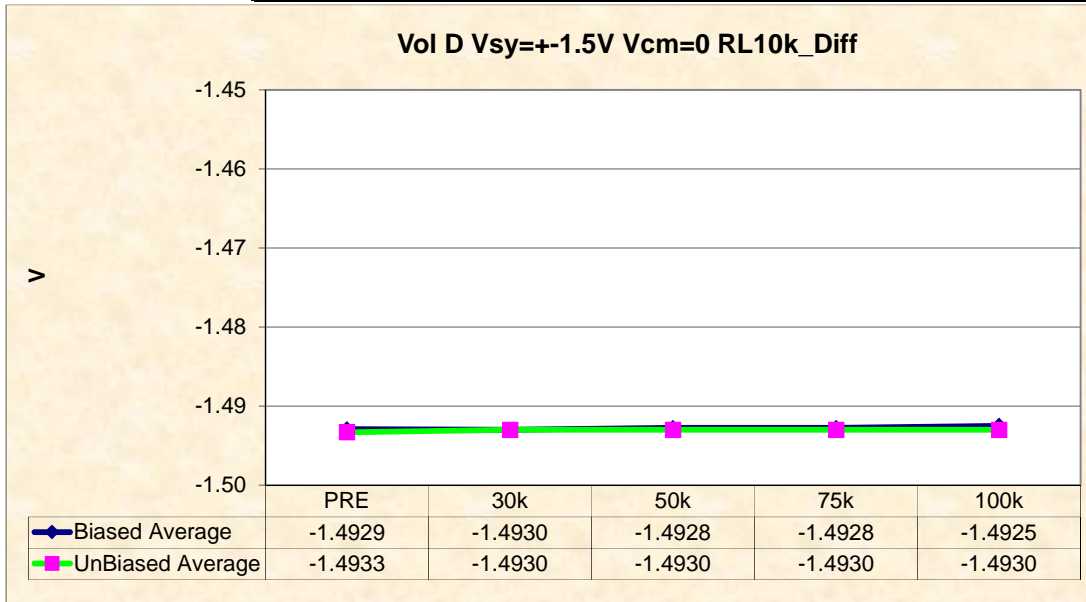
Vol C Vsy=+1.5V Vcm=0 RL10k_Diff						
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	-1.4930	0.0000	0.0000	-1.4940	-1.4930
	54	-1.4930	0.0000	0.0000	-1.4930	-1.4930
Biased	8	-1.4926	-1.4930	-1.4930	-1.4930	-1.4930
	9	-1.4936	-1.4930	-1.4930	-1.4930	-1.4930
	55	-1.4927	-1.4930	-1.4930	-1.4930	-1.4930
	56	-1.4938	-1.4930	-1.4930	-1.4930	-1.4930
	Min	-1.4938	-1.4930	-1.4930	-1.4930	-1.4930
	Max	-1.4926	-1.4930	-1.4930	-1.4930	-1.4930
	Average	-1.4932	-1.4930	-1.4930	-1.4930	-1.4930
UnBiased	10	-1.4935	-1.4930	-1.4930	-1.4930	-1.4920
	11	-1.4922	-1.4920	-1.4920	-1.4920	-1.4930
	57	-1.4933	-1.4930	-1.4930	-1.4930	-1.4930
	58	-1.4937	-1.4930	-1.4930	-1.4930	-1.4930
	Min	-1.4937	-1.4930	-1.4930	-1.4930	-1.4930
	Max	-1.4922	-1.4920	-1.4920	-1.4920	-1.4920
	Average	-1.4932	-1.4928	-1.4928	-1.4928	-1.4928

Limit: <-1.45V

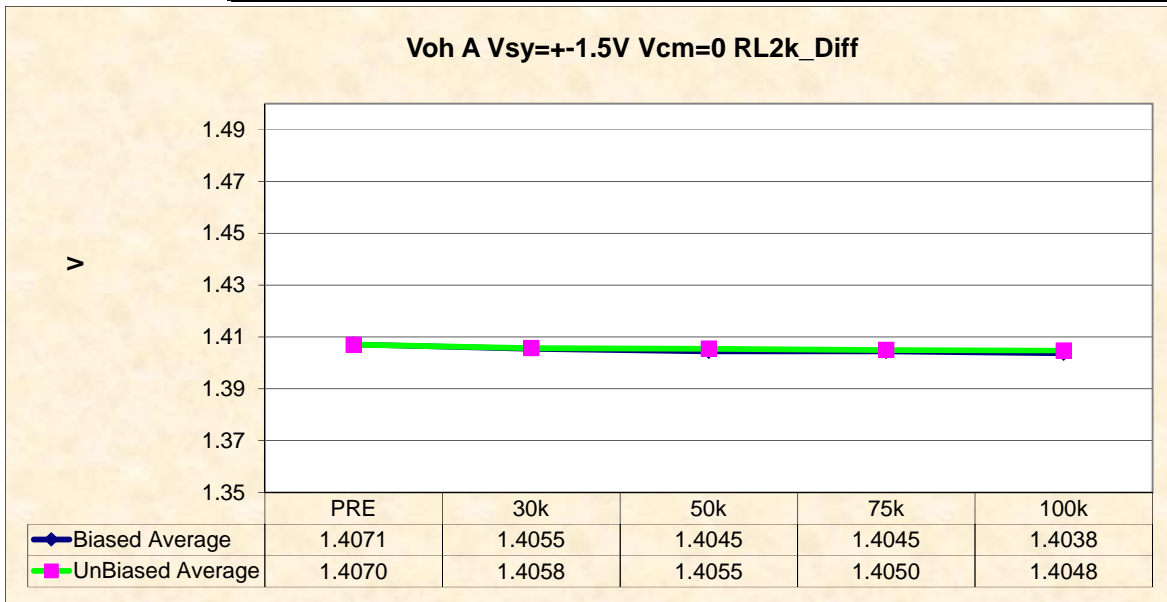


Vol D Vsy=+-1.5V Vcm=0 RL10k_Diff						
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	-1.4930	0.0000	0.0000	-1.4930	-1.4930
	54	-1.4930	0.0000	0.0000	-1.4930	-1.4930
Biased	14	-1.4922	-1.4930	-1.4920	-1.4920	-1.4920
	15	-1.4933	-1.4930	-1.4930	-1.4930	-1.4930
	42	-1.4926	-1.4930	-1.4930	-1.4930	-1.4920
	43	-1.4935	-1.4930	-1.4930	-1.4930	-1.4930
	Min	-1.4935	-1.4930	-1.4930	-1.4930	-1.4930
	Max	-1.4922	-1.4930	-1.4920	-1.4920	-1.4920
	Average	-1.4929	-1.4930	-1.4928	-1.4928	-1.4925
UnBiased	23	-1.4932	-1.4930	-1.4930	-1.4930	-1.4930
	24	-1.4934	-1.4930	-1.4930	-1.4930	-1.4930
	47	-1.4931	-1.4930	-1.4930	-1.4930	-1.4930
	48	-1.4935	-1.4930	-1.4930	-1.4930	-1.4930
	Min	-1.4935	-1.4930	-1.4930	-1.4930	-1.4930
	Max	-1.4931	-1.4930	-1.4930	-1.4930	-1.4930
	Average	-1.4933	-1.4930	-1.4930	-1.4930	-1.4930

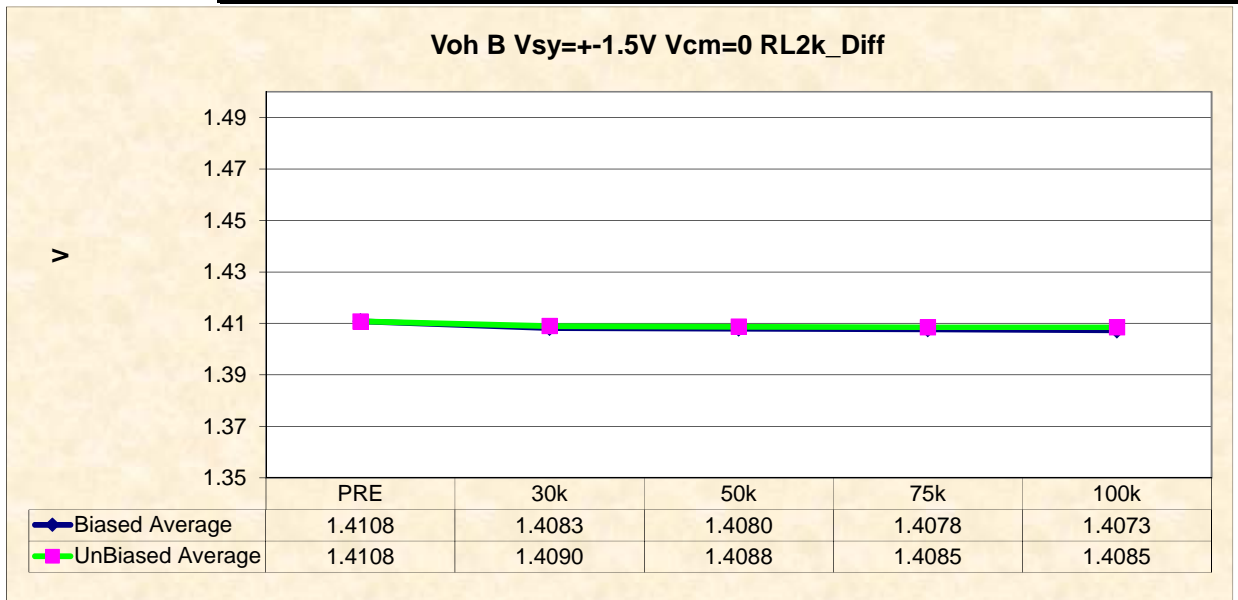
Limit: <-1.45V



Voh A Vsy=+-1.5V Vcm=0 RL2k_Diff						
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	1.4090	0.0000	0.0000	1.4060	1.4060
	54	1.4070	0.0000	0.0000	1.4040	1.4040
Biased	14	1.4067	1.4050	1.4040	1.4040	1.4030
	15	1.4073	1.4060	1.4050	1.4050	1.4040
	42	1.4076	1.4060	1.4050	1.4050	1.4040
	43	1.4069	1.4050	1.4040	1.4040	1.4040
	Min	1.4067	1.4050	1.4040	1.4040	1.4030
	Max	1.4076	1.4060	1.4050	1.4050	1.4040
	Average	1.4071	1.4055	1.4045	1.4045	1.4038
UnBiased	23	1.4069	1.4050	1.4040	1.4040	1.4060
	24	1.4074	1.4070	1.4070	1.4060	1.4040
	47	1.4065	1.4050	1.4050	1.4050	1.4040
	48	1.4072	1.4060	1.4060	1.4050	1.4050
	Min	1.4065	1.4050	1.4040	1.4040	1.4040
	Max	1.4074	1.4070	1.4070	1.4060	1.4060
	Average	1.4070	1.4058	1.4055	1.4050	1.4048
Limit: >1.35V						



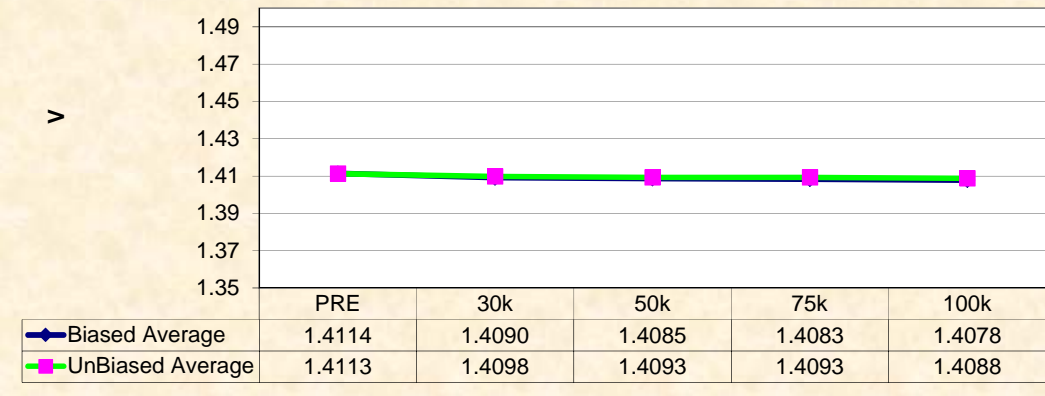
Voh B Vsy=+-1.5V Vcm=0 RL2k_Diff						
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	1.4100	0.0000	0.0000	1.4090	1.4090
	54	1.4090	0.0000	0.0000	1.4080	1.4080
Biased	8	1.4104	1.4080	1.4070	1.4070	1.4070
	9	1.4108	1.4080	1.4080	1.4080	1.4070
	55	1.4107	1.4080	1.4080	1.4080	1.4070
	56	1.4114	1.4090	1.4090	1.4080	1.4080
	Min	1.4104	1.4080	1.4070	1.4070	1.4070
	Max	1.4114	1.4090	1.4090	1.4080	1.4080
	Average	1.4108	1.4083	1.4080	1.4078	1.4073
UnBiased	10	1.4107	1.4080	1.4080	1.4070	1.4100
	11	1.4111	1.4110	1.4100	1.4100	1.4070
	57	1.4104	1.4080	1.4080	1.4080	1.4080
	58	1.4110	1.4090	1.4090	1.4090	1.4090
	Min	1.4104	1.4080	1.4080	1.4070	1.4070
	Max	1.4111	1.4110	1.4100	1.4100	1.4100
	Average	1.4108	1.4090	1.4088	1.4085	1.4085
Limit: >1.35V						



Voh C Vsy=+-1.5V Vcm=0 RL2k_Diff						
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	1.4110	0.0000	0.0000	1.4100	1.4090
	54	1.4090	0.0000	0.0000	1.4080	1.4080
Biased	14	1.4113	1.4090	1.4080	1.4080	1.4080
	15	1.4117	1.4090	1.4090	1.4090	1.4080
	42	1.4112	1.4090	1.4080	1.4080	1.4070
	43	1.4114	1.4090	1.4090	1.4080	1.4080
	Min	1.4112	1.4090	1.4080	1.4080	1.4070
	Max	1.4117	1.4090	1.4090	1.4090	1.4080
	Average	1.4114	1.4090	1.4085	1.4083	1.4078
UnBiased	23	1.4107	1.4080	1.4080	1.4080	1.4110
	24	1.4122	1.4120	1.4110	1.4110	1.4070
	47	1.4105	1.4090	1.4080	1.4080	1.4080
	48	1.4116	1.4100	1.4100	1.4100	1.4090
	Min	1.4105	1.4080	1.4080	1.4080	1.4070
	Max	1.4122	1.4120	1.4110	1.4110	1.4110
	Average	1.4113	1.4098	1.4093	1.4093	1.4088

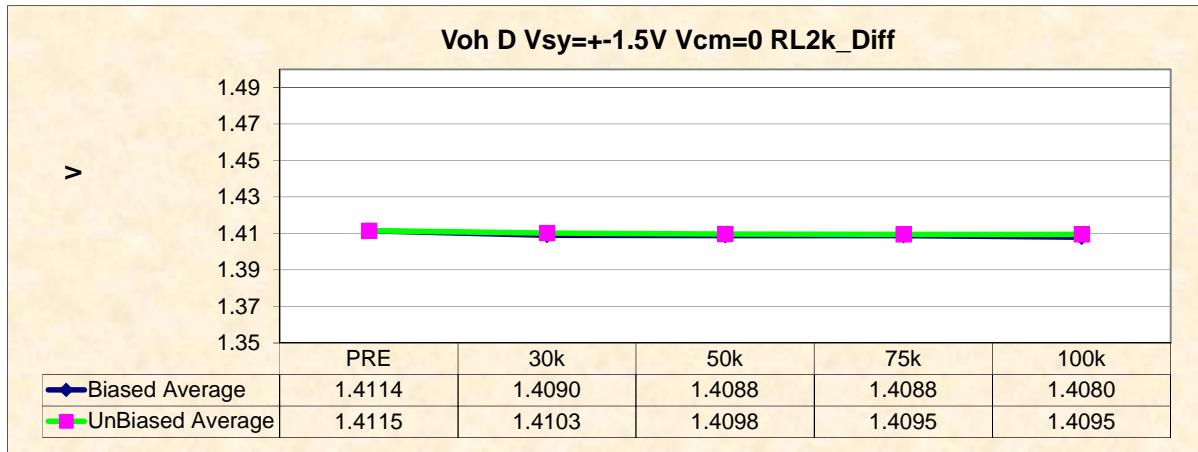
Limit: >1.35V

Voh C Vsy=+-1.5V Vcm=0 RL2k_Diff



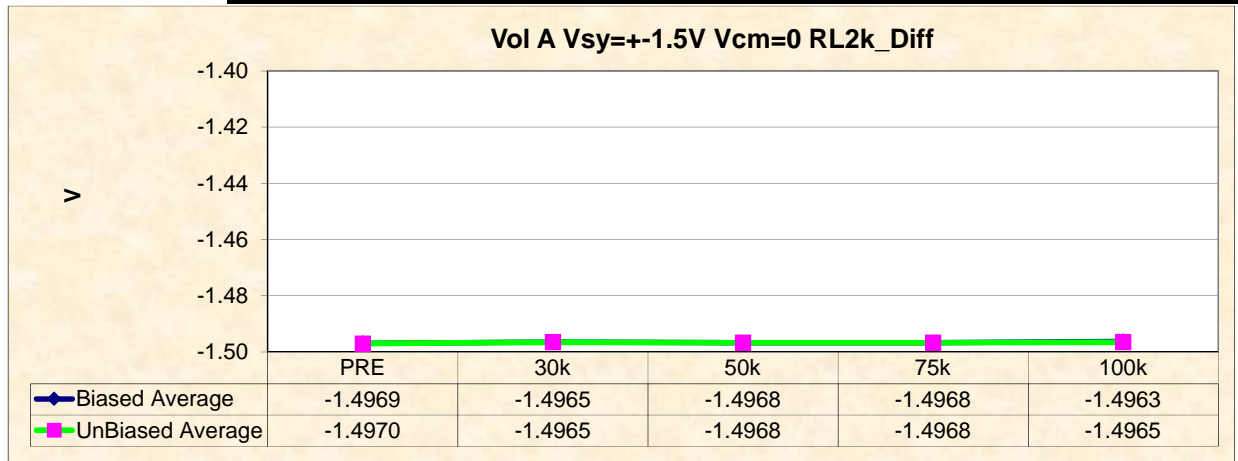
Voh D Vsy=+-1.5V Vcm=0 RL2k_Diff						
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	1.4110	0.0000	0.0000	1.4090	1.4090
	54	1.4080	0.0000	0.0000	1.4080	1.4080
Biased	14	1.4107	1.4080	1.4080	1.4080	1.4070
	15	1.4115	1.4090	1.4090	1.4090	1.4080
	42	1.4116	1.4090	1.4090	1.4090	1.4080
	43	1.4119	1.4100	1.4090	1.4090	1.4090
	Min	1.4107	1.4080	1.4080	1.4080	1.4070
	Max	1.4119	1.4100	1.4090	1.4090	1.4090
	Average	1.4114	1.4090	1.4088	1.4088	1.4080
UnBiased	23	1.4120	1.4100	1.4090	1.4090	1.4110
	24	1.4119	1.4120	1.4110	1.4110	1.4090
	47	1.4110	1.4090	1.4090	1.4090	1.4090
	48	1.4112	1.4100	1.4100	1.4090	1.4090
	Min	1.4110	1.4090	1.4090	1.4090	1.4090
	Max	1.4120	1.4120	1.4110	1.4110	1.4110
	Average	1.4115	1.4103	1.4098	1.4095	1.4095

Limit: >1.35V



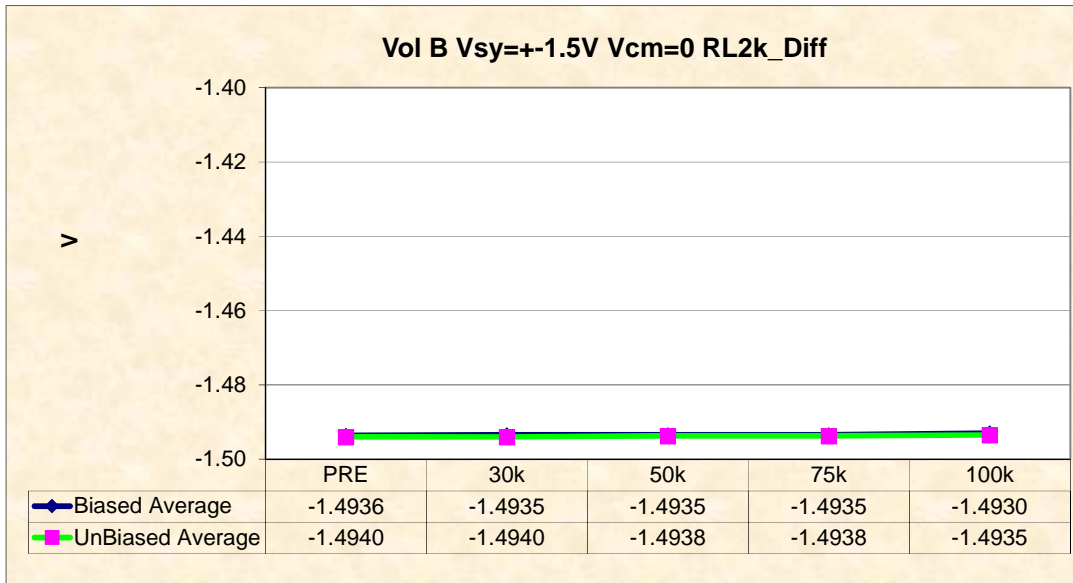
Vol A Vsy=+-1.5V Vcm=0 RL2k_Diff						
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	-1.4950	0.0000	0.0000	-1.4970	-1.4970
	54	-1.4940	0.0000	0.0000	-1.4970	-1.4970
Biased	8	-1.4963	-1.4960	-1.4960	-1.4960	-1.4960
	9	-1.4973	-1.4970	-1.4970	-1.4970	-1.4960
	55	-1.4967	-1.4960	-1.4970	-1.4970	-1.4960
	56	-1.4975	-1.4970	-1.4970	-1.4970	-1.4970
	Min	-1.4975	-1.4970	-1.4970	-1.4970	-1.4970
	Max	-1.4963	-1.4960	-1.4960	-1.4960	-1.4960
	Average	-1.4969	-1.4965	-1.4968	-1.4968	-1.4963
UnBiased	10	-1.4972	-1.4960	-1.4970	-1.4970	-1.4960
	11	-1.4962	-1.4960	-1.4960	-1.4960	-1.4960
	57	-1.4971	-1.4970	-1.4970	-1.4970	-1.4970
	58	-1.4975	-1.4970	-1.4970	-1.4970	-1.4970
	Min	-1.4975	-1.4970	-1.4970	-1.4970	-1.4970
	Max	-1.4962	-1.4960	-1.4960	-1.4960	-1.4960
	Average	-1.4970	-1.4965	-1.4968	-1.4968	-1.4965

Limit: <-1.4V



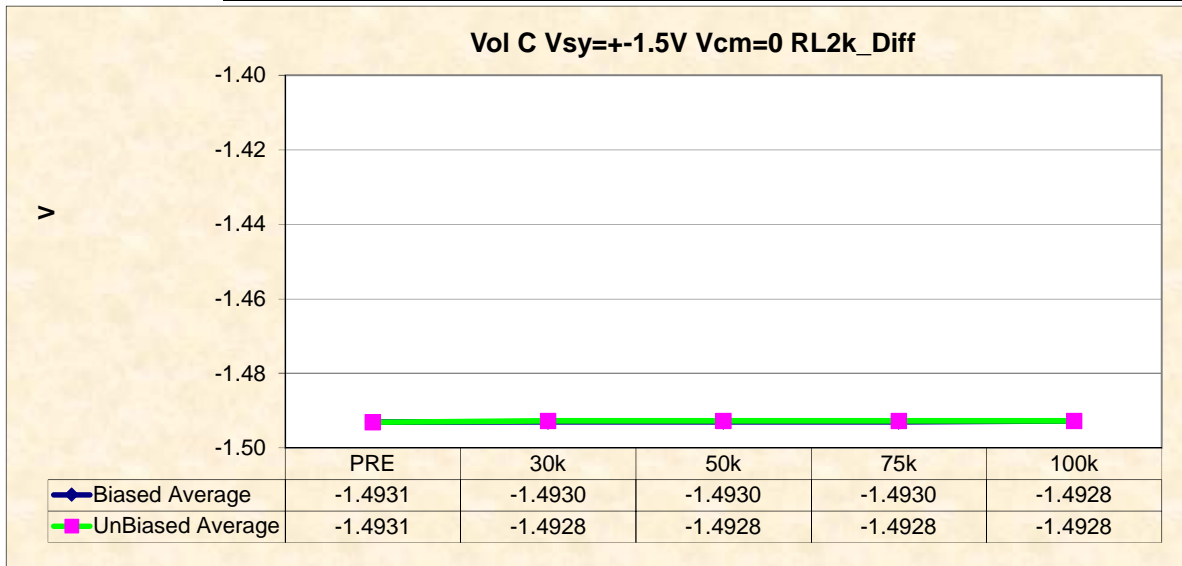
Vol B Vsy=+-1.5V Vcm=0 RL2k_Diff						
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	-1.4930	0.0000	0.0000	-1.4940	-1.4940
	54	-1.4930	0.0000	0.0000	-1.4940	-1.4940
Biased	14	-1.4930	-1.4930	-1.4930	-1.4930	-1.4930
	15	-1.4941	-1.4940	-1.4940	-1.4940	-1.4930
	42	-1.4931	-1.4930	-1.4930	-1.4930	-1.4930
	43	-1.4943	-1.4940	-1.4940	-1.4940	-1.4930
	Min	-1.4943	-1.4940	-1.4940	-1.4940	-1.4930
	Max	-1.4930	-1.4930	-1.4930	-1.4930	-1.4930
	Average	-1.4936	-1.4935	-1.4935	-1.4935	-1.4930
UnBiased	23	-1.4939	-1.4940	-1.4930	-1.4930	-1.4940
	24	-1.4942	-1.4940	-1.4940	-1.4940	-1.4930
	47	-1.4937	-1.4940	-1.4940	-1.4940	-1.4930
	48	-1.4942	-1.4940	-1.4940	-1.4940	-1.4940
	Min	-1.4942	-1.4940	-1.4940	-1.4940	-1.4940
	Max	-1.4937	-1.4940	-1.4930	-1.4930	-1.4930
	Average	-1.4940	-1.4940	-1.4938	-1.4938	-1.4935

Limit: <-1.4V



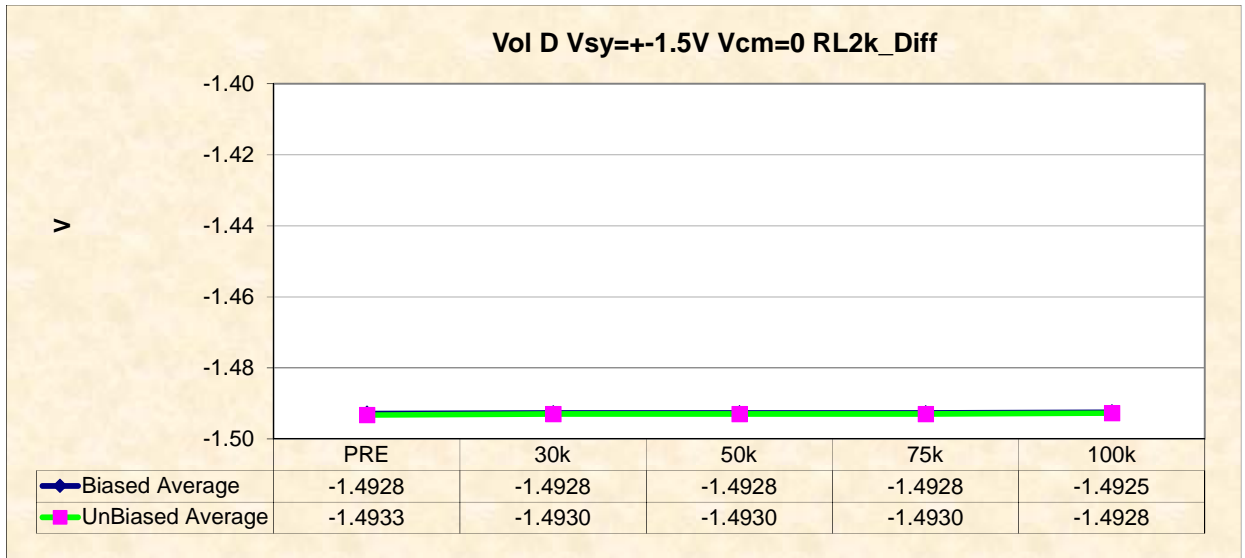
Vol C Vsy=+-1.5V Vcm=0 RL2k_Diff						
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	-1.4930	0.0000	0.0000	-1.4930	-1.4930
	54	-1.4930	0.0000	0.0000	-1.4930	-1.4930
Biased	14	-1.4925	-1.4930	-1.4930	-1.4930	-1.4920
	15	-1.4935	-1.4930	-1.4930	-1.4930	-1.4930
	42	-1.4927	-1.4930	-1.4930	-1.4930	-1.4930
	43	-1.4937	-1.4930	-1.4930	-1.4930	-1.4930
	Min	-1.4937	-1.4930	-1.4930	-1.4930	-1.4930
	Max	-1.4925	-1.4930	-1.4930	-1.4930	-1.4920
	Average	-1.4931	-1.4930	-1.4930	-1.4930	-1.4928
UnBiased	23	-1.4934	-1.4930	-1.4930	-1.4930	-1.4920
	24	-1.4922	-1.4920	-1.4920	-1.4920	-1.4930
	47	-1.4932	-1.4930	-1.4930	-1.4930	-1.4930
	48	-1.4937	-1.4930	-1.4930	-1.4930	-1.4930
	Min	-1.4937	-1.4930	-1.4930	-1.4930	-1.4930
	Max	-1.4922	-1.4920	-1.4920	-1.4920	-1.4920
	Average	-1.4931	-1.4928	-1.4928	-1.4928	-1.4928

Limit: <-1.4V



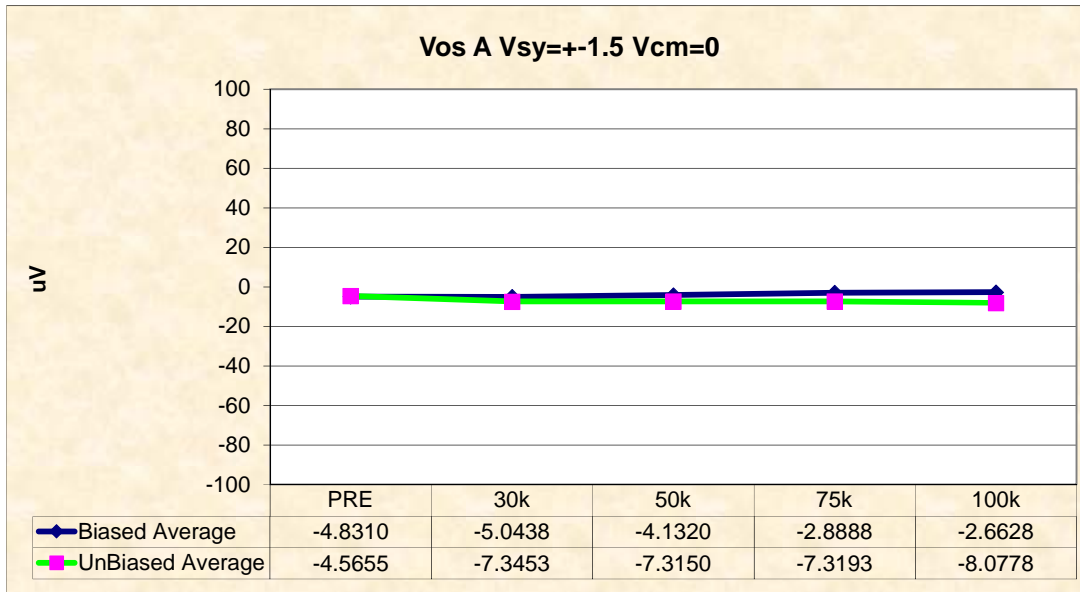
Vol D Vsy=+-1.5V Vcm=0 RL2k_Diff						
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	-1.4930	0.0000	0.0000	-1.4930	-1.4930
	54	-1.4930	0.0000	0.0000	-1.4930	-1.4930
Biased	8	-1.4922	-1.4920	-1.4920	-1.4920	-1.4920
	9	-1.4933	-1.4930	-1.4930	-1.4930	-1.4930
	55	-1.4925	-1.4930	-1.4930	-1.4930	-1.4920
	56	-1.4934	-1.4930	-1.4930	-1.4930	-1.4930
	Min	-1.4934	-1.4930	-1.4930	-1.4930	-1.4930
	Max	-1.4922	-1.4920	-1.4920	-1.4920	-1.4920
	Average	-1.4928	-1.4928	-1.4928	-1.4928	-1.4925
UnBiased	10	-1.4932	-1.4930	-1.4930	-1.4930	-1.4930
	11	-1.4934	-1.4930	-1.4930	-1.4930	-1.4920
	57	-1.4931	-1.4930	-1.4930	-1.4930	-1.4930
	58	-1.4934	-1.4930	-1.4930	-1.4930	-1.4930
	Min	-1.4934	-1.4930	-1.4930	-1.4930	-1.4930
	Max	-1.4931	-1.4930	-1.4930	-1.4930	-1.4920
	Average	-1.4933	-1.4930	-1.4930	-1.4930	-1.4928

Limit: <-1.4V



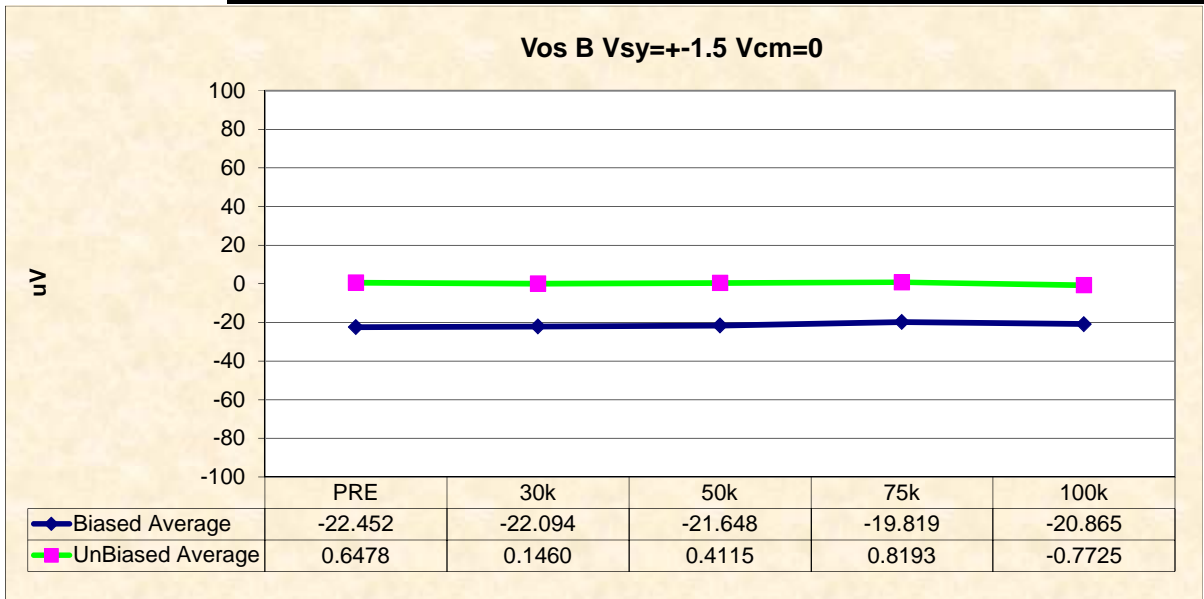
Vos A Vsy=+-1.5 Vcm=0						
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	-8.0190	0.0000	0.0000	-20.4460	-20.7310
	54	-14.4630	0.0000	0.0000	-15.6330	-15.7000
Biased	14	6.9310	9.0300	10.6070	11.3850	11.9470
	15	6.0210	7.4060	7.8430	9.1060	9.6830
	42	-29.8880	-32.1480	-31.1340	-29.7740	-30.1340
	43	-2.3880	-4.4630	-3.8440	-2.2720	-2.1470
	Min	-29.8880	-32.1480	-31.1340	-29.7740	-30.1340
	Max	6.9310	9.0300	10.6070	11.3850	11.9470
	Average	-4.8310	-5.0438	-4.1320	-2.8888	-2.6628
UnBiased	23	16.0850	14.3840	14.9500	14.8920	-21.0410
	24	-17.6270	-21.0380	-20.5680	-20.1240	14.4210
	47	-17.5750	-18.1810	-18.8190	-19.2960	-19.9260
	48	0.8550	-4.5460	-4.8230	-4.7490	-5.7650
	Min	-17.6270	-21.0380	-20.5680	-20.1240	-21.0410
	Max	16.0850	14.3840	14.9500	14.8920	14.4210
Average	-4.5655	-7.3453	-7.3150	-7.3193	-8.0778	

Limit: +/-100uV



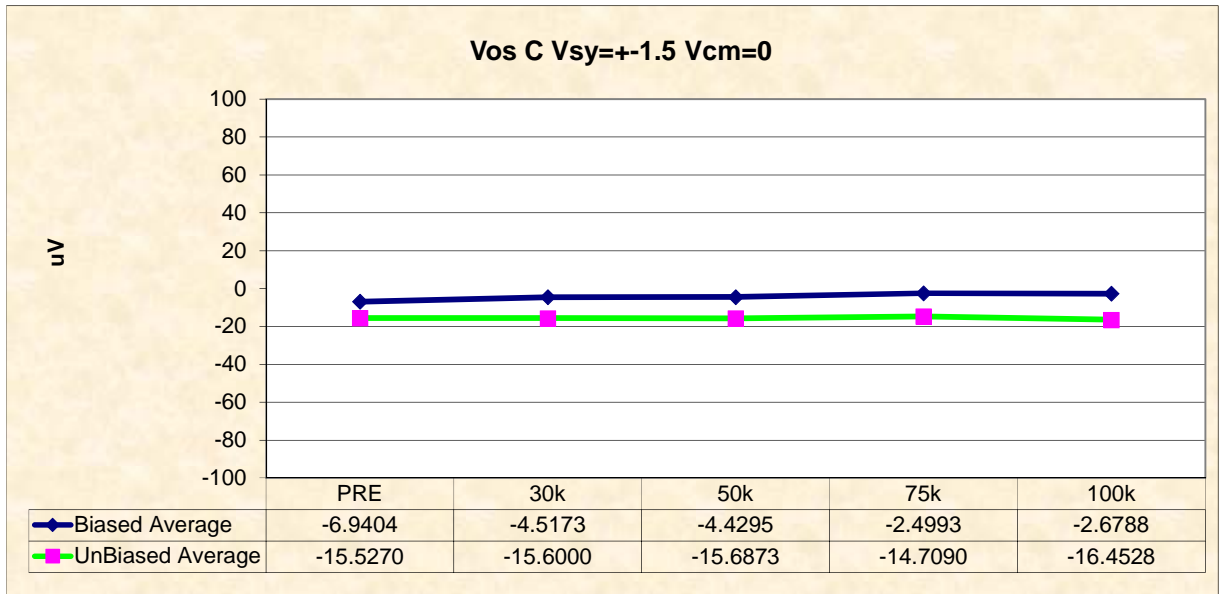
Vos B Vsy=+-1.5 Vcm=0						
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	14.497	0.000	0.000	-7.446	-8.343
	54	-12.077	0.000	0.000	-12.848	-14.085
Biased	14	-34.415	-33.403	-32.334	-30.869	-31.755
	15	-14.631	-13.420	-13.727	-10.922	-12.145
	42	-27.294	-26.321	-25.788	-24.014	-24.946
	43	-13.468	-15.233	-14.741	-13.470	-14.614
	Min	-34.415	-33.403	-32.334	-30.869	-31.755
	Max	-13.468	-13.420	-13.727	-10.922	-12.145
	Average	-22.452	-22.094	-21.648	-19.819	-20.865
UnBiased	23	-6.817	-3.744	-3.687	-1.755	-9.591
	24	-7.951	-6.786	-7.000	-7.271	-3.884
	47	-3.139	-4.361	-4.362	-3.830	-4.694
	48	20.4980	15.4750	16.6950	16.1330	15.0790
	Min	-7.9510	-6.7860	-7.0000	-7.2710	-9.5910
	Max	20.4980	15.4750	16.6950	16.1330	15.0790
	Average	0.6478	0.1460	0.4115	0.8193	-0.7725

Limit: +/-100uV



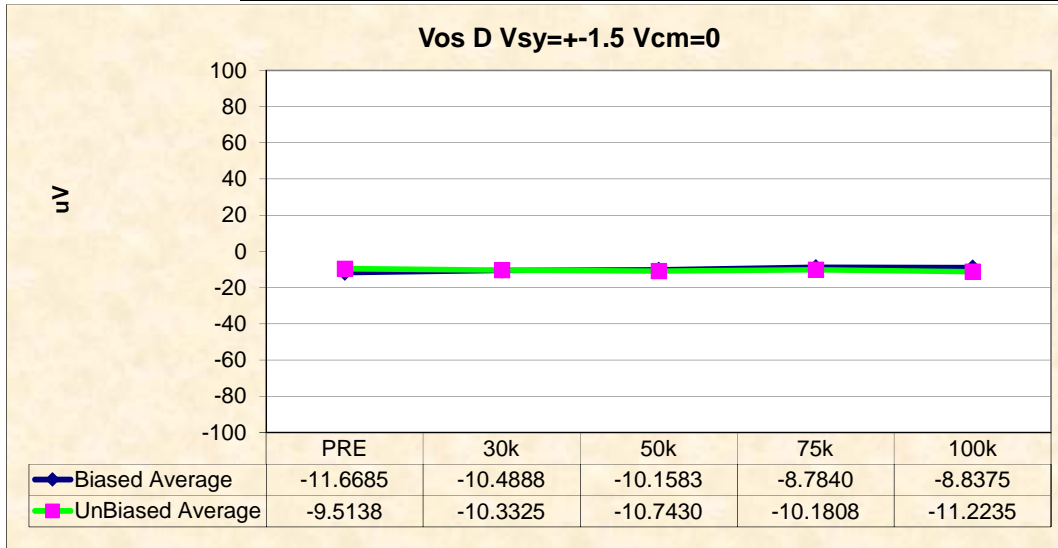
		Vos C Vsy=+-1.5 Vcm=0				
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	-42.9360	0.0000	0.0000	-24.7400	-25.4210
	54	-19.1370	0.0000	0.0000	-20.0070	-21.2600
Biased	8	-7.9427	-4.8970	-4.3280	-2.1570	-2.4110
	9	2.0610	5.0200	4.9750	6.7380	6.2930
	55	-37.1970	-32.7680	-33.3790	-31.1930	-31.2730
	56	15.3170	14.5760	15.0140	16.6150	16.6760
	Min	-37.1970	-32.7680	-33.3790	-31.1930	-31.2730
	Max	15.3170	14.5760	15.0140	16.6150	16.6760
	Average	-6.9404	-4.5173	-4.4295	-2.4993	-2.6788
UnBiased	10	-22.3910	-23.2420	-24.2500	-22.0680	-37.4090
	11	-35.9170	-36.9000	-36.7610	-35.2760	-24.1650
	57	3.5990	6.5570	6.7160	7.1000	5.8480
	58	-7.3990	-8.8150	-8.4540	-8.5920	-10.0850
	Min	-35.9170	-36.9000	-36.7610	-35.2760	-37.4090
	Max	3.5990	6.5570	6.7160	7.1000	5.8480
	Average	-15.5270	-15.6000	-15.6873	-14.7090	-16.4528

Limit: +/-100uV



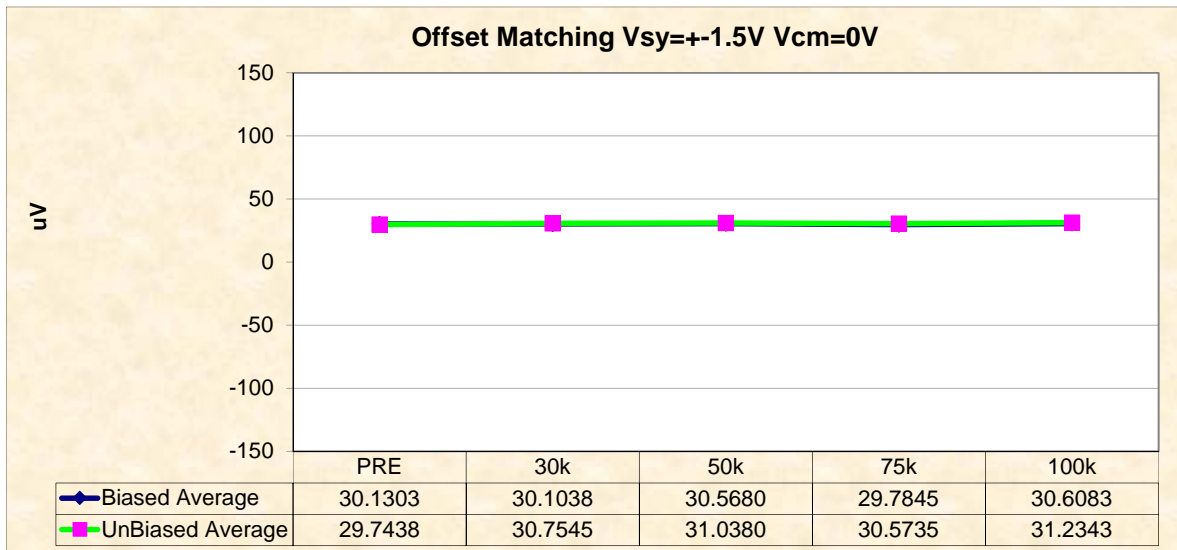
Vos D Vsy=+1.5 Vcm=0						
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	2.4880	0.0000	0.0000	-24.7350	-26.3490
	54	7.5010	0.0000	0.0000	6.6770	5.5420
Biased	14	4.9251	5.7380	6.6370	8.5430	7.5500
	15	-2.3630	-0.6770	-0.9300	-0.0070	0.1360
	42	-21.6140	-18.6020	-18.4860	-17.3120	-17.0410
	43	-27.6220	-28.4140	-27.8540	-26.3600	-25.9950
	Min	-27.6220	-28.4140	-27.8540	-26.3600	-25.9950
	Max	4.9251	5.7380	6.6370	8.5430	7.5500
	Average	-11.6685	-10.4888	-10.1583	-8.7840	-8.8375
UnBiased	23	-23.7560	-25.4880	-25.9020	-24.8300	-25.2640
	24	-26.2990	-24.5530	-24.5580	-24.0300	-27.0300
	47	-10.5950	-10.7670	-12.0600	-11.4440	-12.4090
	48	22.5950	19.4780	19.5480	19.5810	19.8090
	Min	-26.2990	-25.4880	-25.9020	-24.8300	-27.0300
	Max	22.5950	19.4780	19.5480	19.5810	19.8090
	Average	-9.5138	-10.3325	-10.7430	-10.1808	-11.2235

Limit: +/-100uV



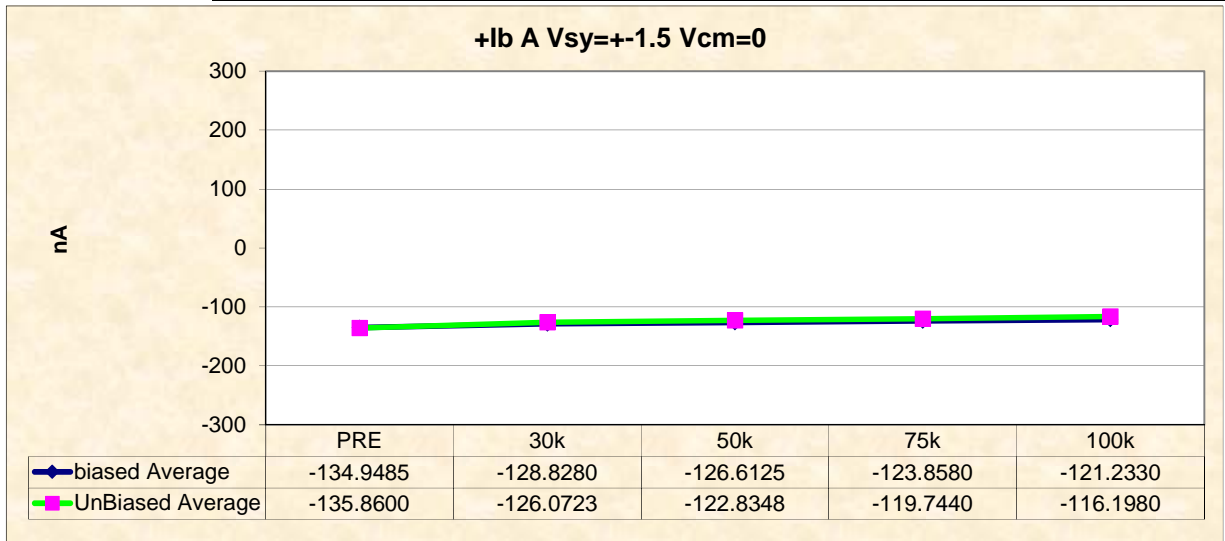
Offset Matching Vsy=+-1.5V Vcm=0V						
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	57.4330	0.0000	0.0000	17.2940	18.0070
	54	26.6380	0.0000	0.0000	26.6840	26.8020
Biased	14	41.3460	42.4330	42.9410	42.2540	43.7020
	15	20.6520	20.8260	21.5700	20.0280	21.8280
	42	15.5830	14.1660	14.8930	13.8810	14.2320
	43	42.9400	42.9900	42.8680	42.9750	42.6710
	Min	15.5830	14.1660	14.8930	13.8810	14.2320
	Max	42.9400	42.9900	42.9410	42.9750	43.7020
	Average	30.1303	30.1038	30.5680	29.7845	30.6083
UnBiased	23	39.8400	39.8720	40.8530	39.7220	27.8180
	24	27.9660	30.1140	29.7610	28.0040	41.4510
	47	21.1750	24.7380	25.5360	26.3950	25.7740
	48	29.9940	28.2940	28.0020	28.1730	29.8940
	Min	21.1750	24.7380	25.5360	26.3950	25.7740
	Max	39.8400	39.8720	40.8530	39.7220	41.4510
	Average	29.7438	30.7545	31.0380	30.5735	31.2343

Limit: <150uV



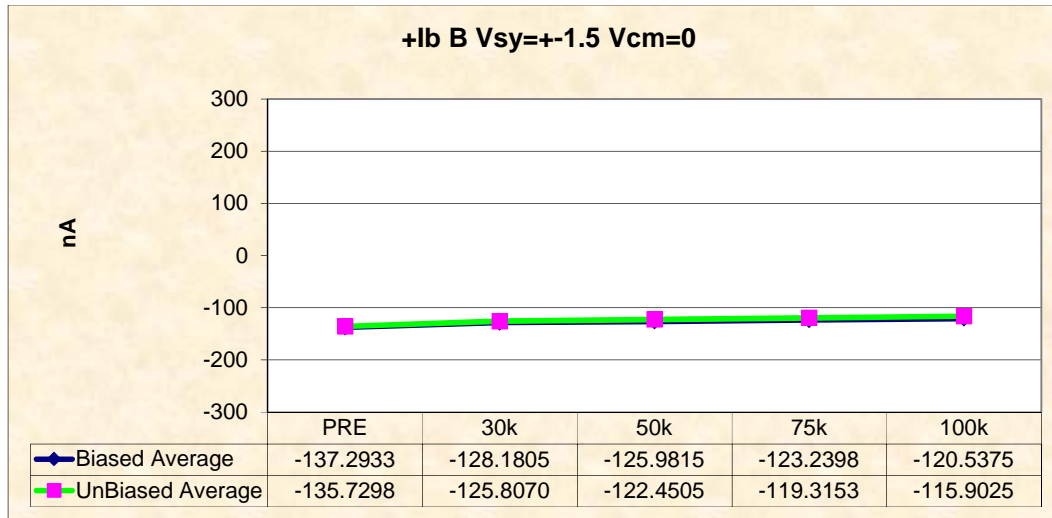
		+Ib A Vsy=+-1.5 Vcm=0				
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	-119.2420	0.0000	0.0000	-114.8950	-114.2830
	54	-139.3460	0.0000	0.0000	-140.1500	-139.4880
Biased	8	-137.5140	-130.7320	-128.8540	-126.0750	-123.3380
	9	-132.0630	-128.5160	-125.9010	-122.8210	-120.4990
	55	-142.4990	-131.0390	-128.5020	-125.9290	-123.0780
	56	-127.7180	-125.0250	-123.1930	-120.6070	-118.0170
	Min	-142.4990	-131.0390	-128.8540	-126.0750	-123.3380
	Max	-127.7180	-125.0250	-123.1930	-120.6070	-118.0170
	Average	-134.9485	-128.8280	-126.6125	-123.8580	-121.2330
UnBiased	10	-137.9860	-123.3700	-119.6470	-116.4430	-113.0270
	11	-126.4280	-123.3570	-120.3070	-117.0400	-113.1720
	57	-148.2420	-133.5680	-130.0650	-127.0240	-123.5500
	58	-130.7840	-123.9940	-121.3200	-118.4690	-115.0430
	Min	-148.2420	-133.5680	-130.0650	-127.0240	-123.5500
	Max	-126.4280	-123.3570	-119.6470	-116.4430	-113.0270
	Average	-135.8600	-126.0723	-122.8348	-119.7440	-116.1980

Limit: +/-300nA



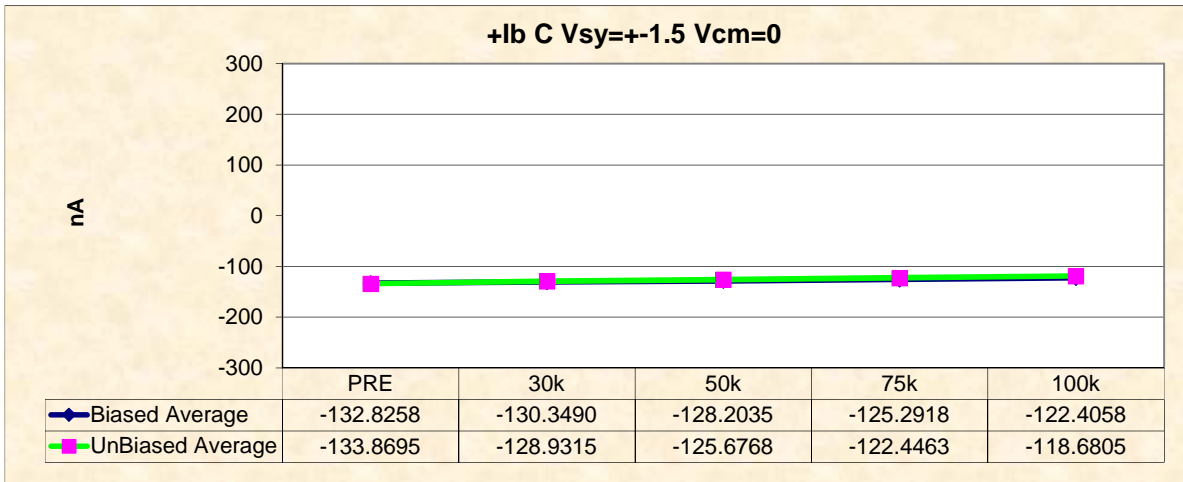
+Ib B Vsy=+-1.5 Vcm=0						
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	-119.1140	0.0000	0.0000	-113.0680	-112.4560
	54	-138.2940	0.0000	0.0000	-139.3040	-138.5710
Biased	14	-141.6730	-131.2800	-129.4780	-126.5640	-124.1160
	15	-132.0680	-127.1120	-124.5220	-121.9730	-119.1590
	42	-146.0610	-129.3080	-126.7370	-123.7990	-120.9300
	43	-129.3710	-125.0220	-123.1890	-120.6230	-117.9450
	Min	-146.0610	-131.2800	-129.4780	-126.5640	-124.1160
	Max	-129.3710	-125.0220	-123.1890	-120.6230	-117.9450
	Average	-137.2933	-128.1805	-125.9815	-123.2398	-120.5375
UnBiased	23	-140.4560	-122.6310	-118.8740	-115.6990	-113.3830
	24	-127.6530	-123.1650	-119.8930	-117.1680	-112.4390
	47	-144.7790	-133.5230	-129.9000	-126.4140	-123.2320
	48	-130.0310	-123.9090	-121.1350	-117.9800	-114.5560
	Min	-144.7790	-133.5230	-129.9000	-126.4140	-123.2320
	Max	-127.6530	-122.6310	-118.8740	-115.6990	-112.4390
Average	-135.7298	-125.8070	-122.4505	-119.3153	-115.9025	

Limit: +/-300nA



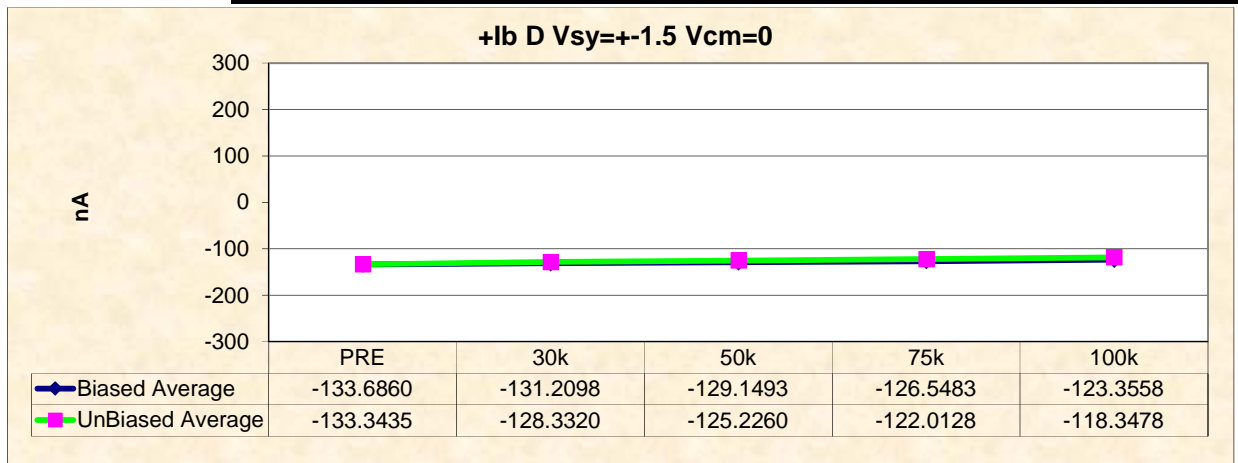
+Ib C Vsy=+-1.5 Vcm=0						
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	-119.8420	0.0000	0.0000	-116.4950	-115.8920
	54	-141.3740	0.0000	0.0000	-142.1120	-141.3860
Biased	14	-136.4400	-132.6590	-130.8700	-127.8240	-124.7220
	15	-133.1710	-130.2840	-127.9000	-124.8840	-121.9860
	42	-133.8750	-132.0350	-129.6900	-126.7950	-124.2600
	43	-127.8170	-126.4180	-124.3540	-121.6640	-118.6550
	Min	-136.4400	-132.6590	-130.8700	-127.8240	-124.7220
	Max	-127.8170	-126.4180	-124.3540	-121.6640	-118.6550
	Average	-132.8258	-130.3490	-128.2035	-125.2918	-122.4058
UnBiased	23	-135.2870	-125.8550	-122.3680	-119.2480	-115.3180
	24	-127.9420	-126.0360	-122.8490	-119.5190	-115.5910
	47	-141.3810	-137.4120	-133.8010	-130.4450	-127.2990
	48	-130.8680	-126.4230	-123.6890	-120.5730	-116.5140
	Min	-141.3810	-137.4120	-133.8010	-130.4450	-127.2990
	Max	-127.9420	-125.8550	-122.3680	-119.2480	-115.3180
	Average	-133.8695	-128.9315	-125.6768	-122.4463	-118.6805

Limit: +/-300nA



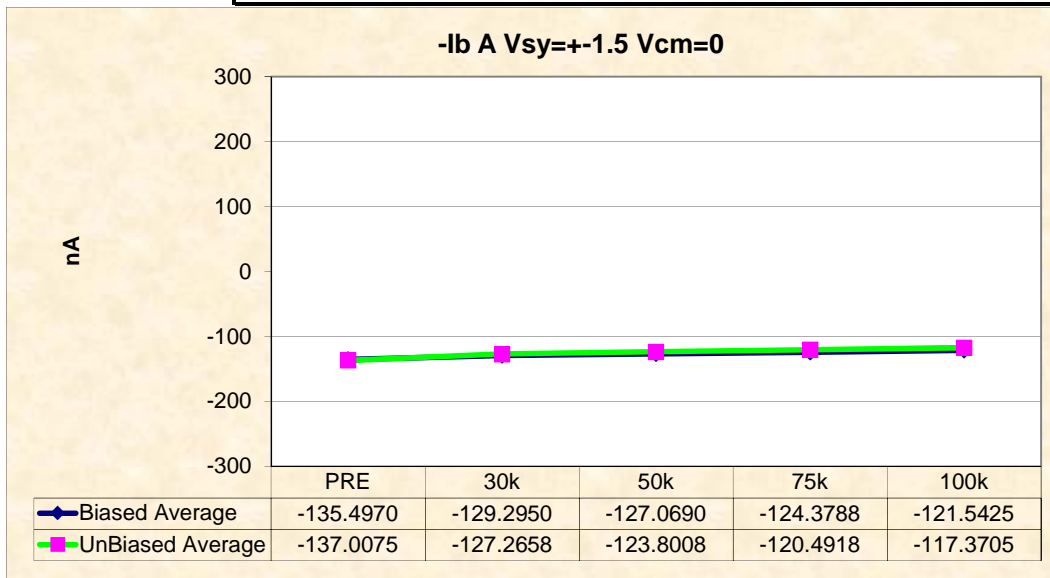
		+Ib D Vsy=+-1.5 Vcm=0				
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	-119.6210	0.0000	0.0000	-119.1120	-118.3900
	54	-141.8210	0.0000	0.0000	-142.6380	-142.0280
Biased	8	-137.3440	-133.4790	-131.7340	-128.7520	-125.6560
	9	-133.0690	-130.5160	-128.2670	-125.5050	-121.9860
	55	-134.2370	-132.3650	-129.9300	-127.4970	-124.5420
	56	-130.0940	-128.4790	-126.6660	-124.4390	-121.2390
	Min	-137.3440	-133.4790	-131.7340	-128.7520	-125.6560
	Max	-130.0940	-128.4790	-126.6660	-124.4390	-121.2390
	Average	-133.6860	-131.2098	-129.1493	-126.5483	-123.3558
UnBiased	10	-134.5890	-125.0080	-121.6070	-118.3950	-116.5180
	11	-128.6800	-126.9730	-123.8500	-120.8700	-114.8720
	57	-141.9100	-137.7140	-134.2130	-131.0140	-127.6130
	58	-128.1950	-123.6330	-121.2340	-117.7720	-114.3880
	Min	-141.9100	-137.7140	-134.2130	-131.0140	-127.6130
	Max	-128.1950	-123.6330	-121.2340	-117.7720	-114.3880
	Average	-133.3435	-128.3320	-125.2260	-122.0128	-118.3478

Limit: +/-300nA

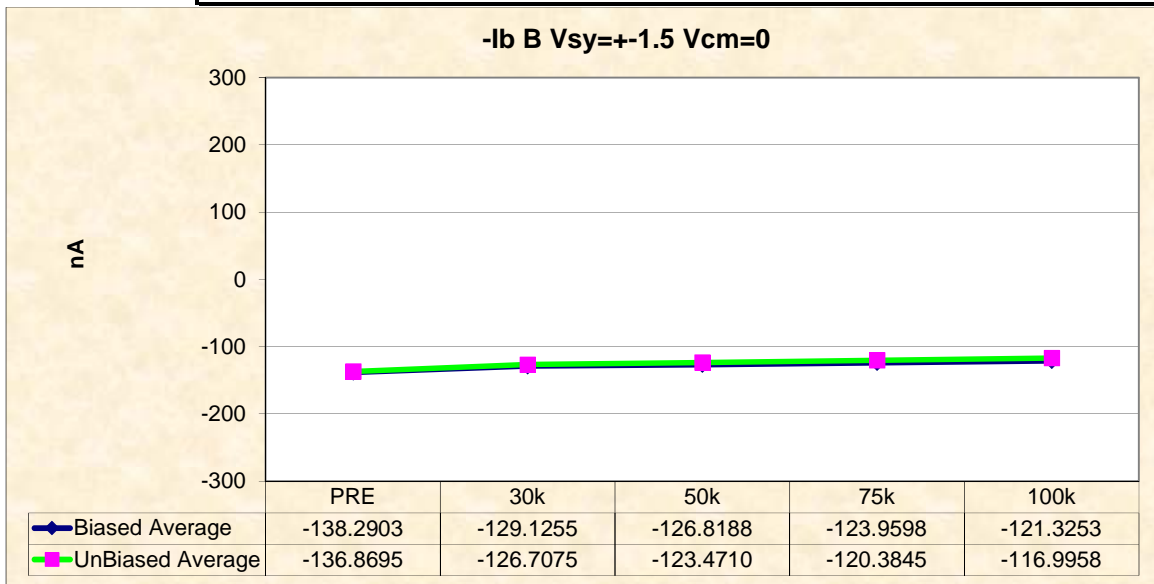


-Ib A Vsy=+-1.5 Vcm=0						
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	-118.6190	0.0000	0.0000	-116.5760	-115.8490
	54	-140.3830	0.0000	0.0000	-141.1820	-140.4880
Biased	14	-138.2000	-131.6650	-129.6810	-126.8690	-123.8450
	15	-133.0250	-129.1610	-126.5230	-123.8420	-121.1700
	42	-143.4200	-131.5490	-129.1350	-126.5900	-123.6610
	43	-127.3430	-124.8050	-122.9370	-120.2140	-117.4940
	Min	-143.4200	-131.6650	-129.6810	-126.8690	-123.8450
	Max	-127.3430	-124.8050	-122.9370	-120.2140	-117.4940
	Average	-135.4970	-129.2950	-127.0690	-124.3788	-121.5425
UnBiased	23	-139.1850	-124.9090	-120.9640	-117.8010	-113.5970
	24	-127.4370	-123.8660	-120.3280	-116.9250	-114.8950
	47	-149.0270	-134.3720	-130.9930	-127.7060	-124.6170
	48	-132.3810	-125.9160	-122.9180	-119.5350	-116.3730
	Min	-149.0270	-134.3720	-130.9930	-127.7060	-124.6170
	Max	-127.4370	-123.8660	-120.3280	-116.9250	-113.5970
	Average	-137.0075	-127.2658	-123.8008	-120.4918	-117.3705

Limit: +/-300nA

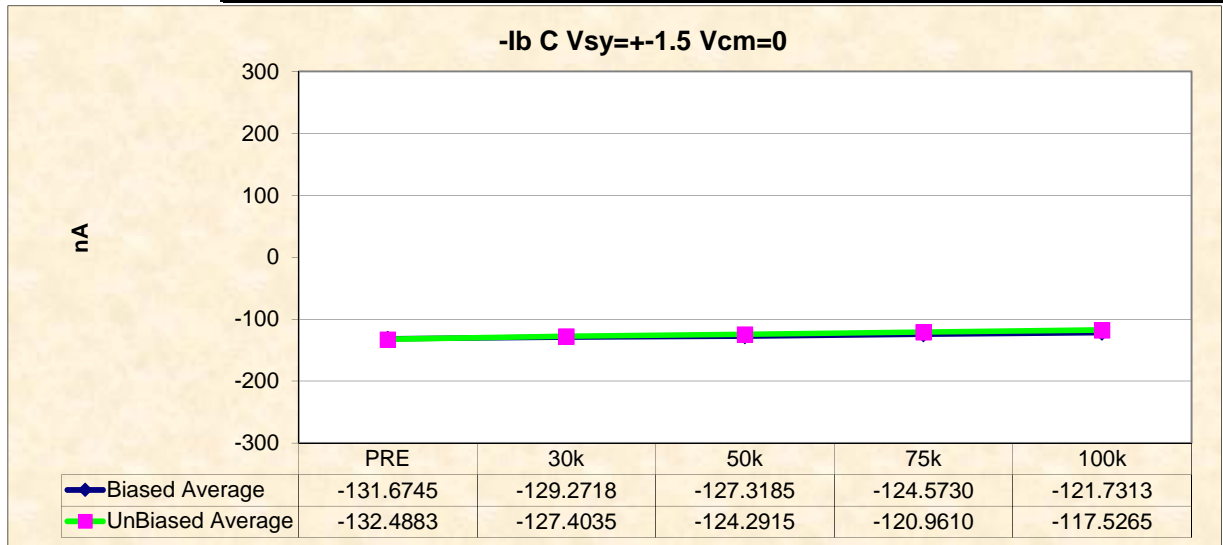


-Ib B Vsy=+-1.5 Vcm=0						
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	-120.1820	0.0000	0.0000	-114.6830	-114.0170
	54	-138.1440	0.0000	0.0000	-139.1500	-138.4390
Biased	14	-142.0960	-131.7020	-129.8530	-126.6590	-124.0380
	15	-134.0540	-129.2900	-126.5460	-123.6110	-121.0540
	42	-147.1160	-129.8000	-127.2080	-124.4240	-121.8910
	43	-129.8950	-125.7100	-123.6680	-121.1450	-118.3180
	Min	-147.1160	-131.7020	-129.8530	-126.6590	-124.0380
	Max	-129.8950	-125.7100	-123.6680	-121.1450	-118.3180
	Average	-138.2903	-129.1255	-126.8188	-123.9598	-121.3253
UnBiased	23	-140.8850	-123.0300	-119.2540	-116.1200	-115.7510
	24	-129.8310	-125.0230	-122.0990	-118.7040	-112.7910
	47	-146.1390	-134.5510	-131.0340	-127.9550	-124.3760
	48	-130.6230	-124.2260	-121.4970	-118.7590	-115.0650
	Min	-146.1390	-134.5510	-131.0340	-127.9550	-124.3760
	Max	-129.8310	-123.0300	-119.2540	-116.1200	-112.7910
	Average	-136.8695	-126.7075	-123.4710	-120.3845	-116.9958
Limit: +/-300nA						



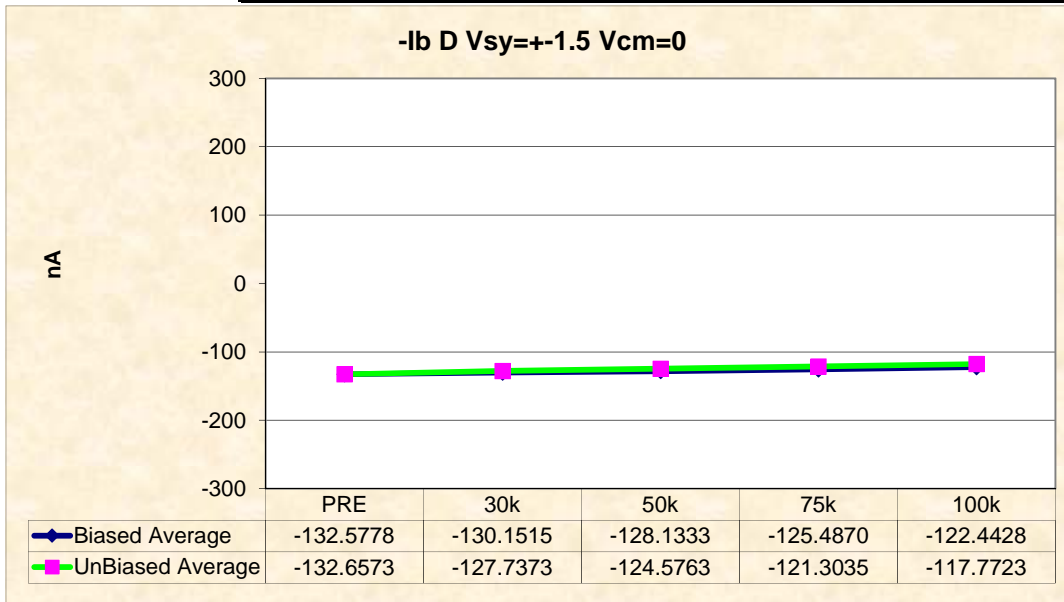
		-Ib C Vsy=+-1.5 Vcm=0				
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	-117.7760	0.0000	0.0000	-117.3340	-116.7350
	54	-140.1700	0.0000	0.0000	-140.8560	-140.2880
Biased	8	-133.4910	-129.5350	-128.0090	-125.2700	-122.4700
	9	-131.3620	-128.6750	-126.5140	-123.8140	-120.6180
	55	-132.4880	-130.9670	-128.7390	-126.1720	-123.4630
	56	-129.3570	-127.9100	-126.0120	-123.0360	-120.3740
	Min	-133.4910	-130.9670	-128.7390	-126.1720	-123.4630
	Max	-129.3570	-127.9100	-126.0120	-123.0360	-120.3740
	Average	-131.6745	-129.2718	-127.3185	-124.5730	-121.7313
UnBiased	10	-133.0540	-123.6020	-119.6300	-116.4360	-114.9100
	11	-126.8900	-125.1410	-122.0220	-118.5770	-113.4890
	57	-140.6900	-136.5930	-133.5640	-130.0430	-126.6330
	58	-129.3190	-124.2780	-121.9500	-118.7880	-115.0740
	Min	-140.6900	-136.5930	-133.5640	-130.0430	-126.6330
	Max	-126.8900	-123.6020	-119.6300	-116.4360	-113.4890
	Average	-132.4883	-127.4035	-124.2915	-120.9610	-117.5265

Limit: +/-300nA



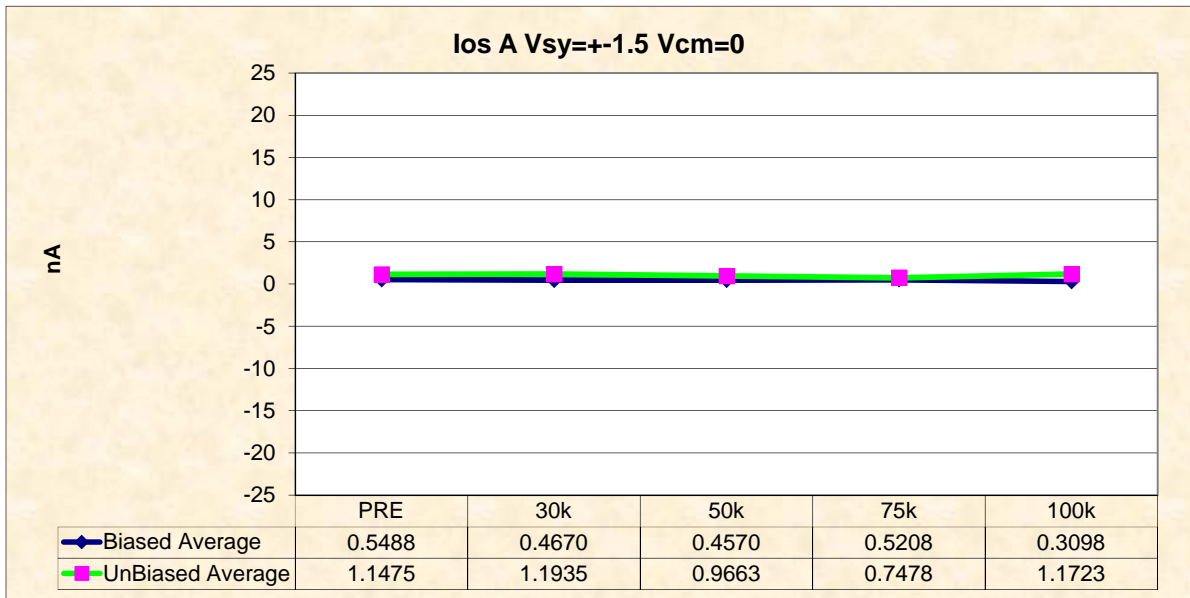
-Ib D Vsy=+-1.5 Vcm=0						
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	-119.0690	0.0000	0.0000	-116.7100	-116.0020
	54	-141.9260	0.0000	0.0000	-142.8360	-141.9610
Biased	14	-136.6570	-133.1110	-131.4640	-128.5710	-125.3740
	15	-132.5860	-129.6710	-127.1590	-124.6810	-121.5430
	42	-132.3260	-130.5470	-128.5150	-125.8830	-123.2020
	43	-128.7420	-127.2770	-125.3950	-122.8130	-119.6520
	Min	-136.6570	-133.1110	-131.4640	-128.5710	-125.3740
	Max	-128.7420	-127.2770	-125.3950	-122.8130	-119.6520
	Average	-132.5778	-130.1515	-128.1333	-125.4870	-122.4428
UnBiased	23	-133.8380	-124.6190	-121.0550	-117.5840	-114.5830
	24	-126.7870	-124.8140	-121.2510	-118.0590	-114.1080
	47	-139.9450	-135.9150	-133.0830	-129.7410	-126.4120
	48	-130.0590	-125.6010	-122.9160	-119.8300	-115.9860
	Min	-139.9450	-135.9150	-133.0830	-129.7410	-126.4120
	Max	-126.7870	-124.6190	-121.0550	-117.5840	-114.1080
	Average	-132.6573	-127.7373	-124.5763	-121.3035	-117.7723

Limit: +/-300nA



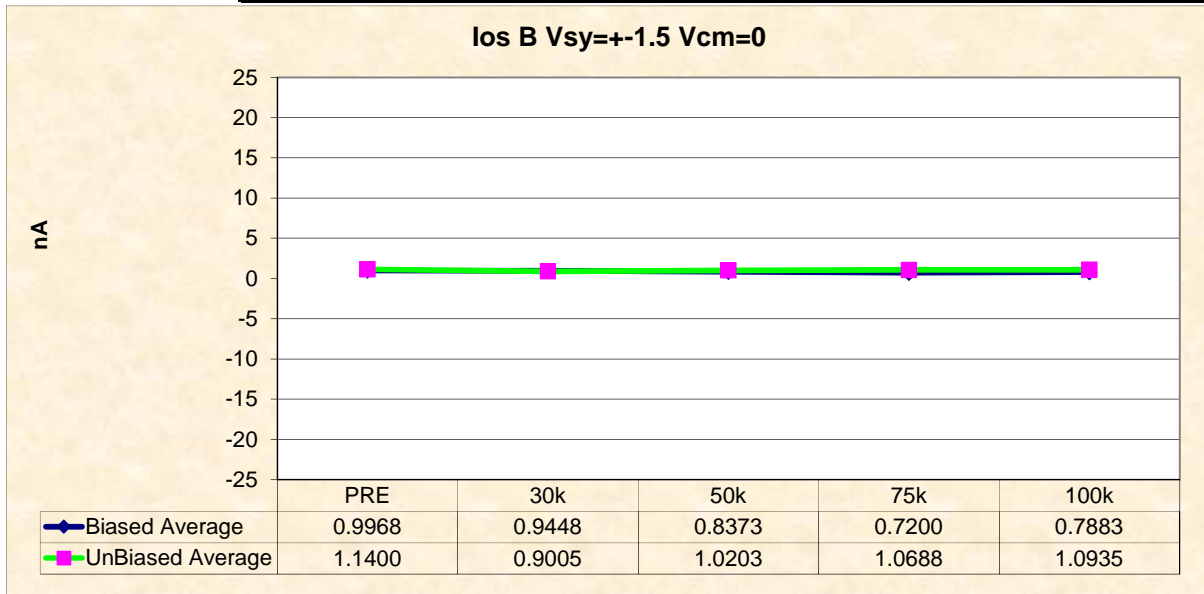
Ios A Vsy=+-1.5 Vcm=0						
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	-0.6230	0.0000	0.0000	1.6810	1.5660
	54	1.0360	0.0000	0.0000	1.0320	1.0000
Biased	14	0.6870	0.9330	0.8270	0.7940	0.5060
	15	0.9630	0.6450	0.6230	1.0210	0.6720
	42	0.9200	0.5100	0.6330	0.6610	0.5840
	43	-0.3750	-0.2200	-0.2550	-0.3930	-0.5230
	Min	-0.3750	-0.2200	-0.2550	-0.3930	-0.5230
	Max	0.9630	0.9330	0.8270	1.0210	0.6720
	Average	0.5488	0.4670	0.4570	0.5208	0.3098
UnBiased	23	1.1990	1.5390	1.3160	1.3570	0.5700
	24	1.0090	0.5090	0.0220	-0.1150	1.7230
	47	0.7850	0.8040	0.9290	0.6820	1.0670
	48	1.5970	1.9220	1.5980	1.0670	1.3290
	Min	0.7850	0.5090	0.0220	-0.1150	0.5700
	Max	1.5970	1.9220	1.5980	1.3570	1.7230
	Average	1.1475	1.1935	0.9663	0.7478	1.1723

Limit: +/-25nA



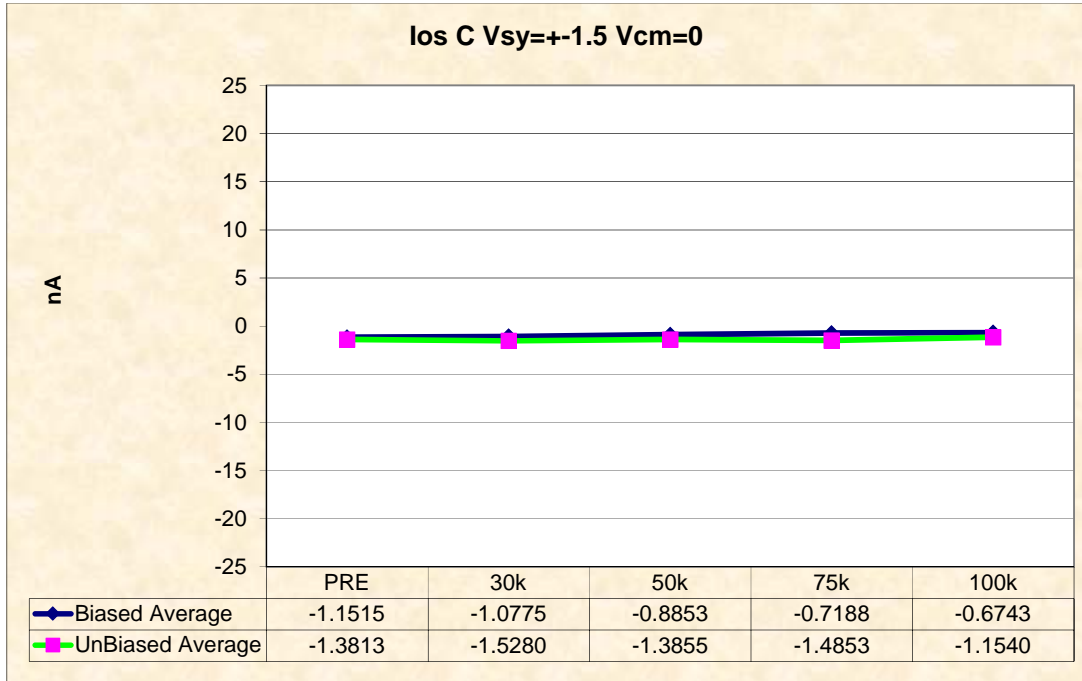
		Ios B Vsy=+-1.5 Vcm=0				
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	1.0680	0.0000	0.0000	1.6140	1.5610
	54	-0.1510	0.0000	0.0000	-0.1540	-0.1320
Biased	8	0.4230	0.4210	0.3750	0.0950	-0.0770
	9	1.9860	2.1780	2.0240	1.6390	1.8960
	55	1.0540	0.4920	0.4710	0.6250	0.9610
	56	0.5240	0.6880	0.4790	0.5210	0.3730
	Min	0.4230	0.4210	0.3750	0.0950	-0.0770
	Max	1.9860	2.1780	2.0240	1.6390	1.8960
	Average	0.9968	0.9448	0.8373	0.7200	0.7883
UnBiased	10	0.4290	0.3990	0.3790	0.4210	2.3680
	11	2.1780	1.8580	2.2060	1.5350	0.3530
	57	1.3610	1.0280	1.1340	1.5410	1.1440
	58	0.5920	0.3170	0.3620	0.7780	0.5090
	Min	0.4290	0.3170	0.3620	0.4210	0.3530
	Max	2.1780	1.8580	2.2060	1.5410	2.3680
	Average	1.1400	0.9005	1.0203	1.0688	1.0935

Limit: +/-25nA



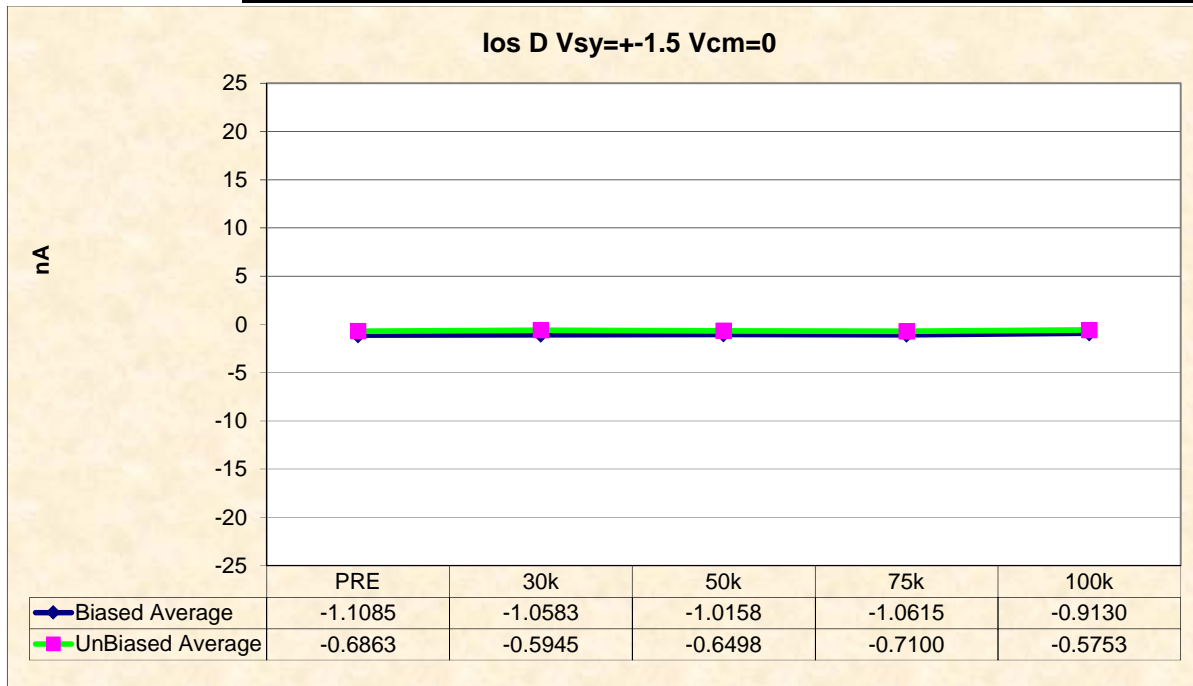
Ios C Vsy=+-1.5 Vcm=0						
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	-2.0660	0.0000	0.0000	0.8390	0.8440
	54	-1.2030	0.0000	0.0000	-1.2560	-1.0980
Biased	14	-2.9490	-3.1240	-2.8610	-2.5540	-2.2520
	15	-1.8090	-1.6090	-1.3870	-1.0700	-1.3680
	42	-1.3870	-1.0680	-0.9520	-0.6230	-0.7960
	43	1.5390	1.4910	1.6590	1.3720	1.7190
	Min	-2.9490	-3.1240	-2.8610	-2.5540	-2.2520
	Max	1.5390	1.4910	1.6590	1.3720	1.7190
	Average	-1.1515	-1.0775	-0.8853	-0.7188	-0.6743
UnBiased	23	-2.2330	-2.2540	-2.7380	-2.8110	-0.4080
	24	-1.0520	-0.8950	-0.8270	-0.9430	-2.1020
	47	-0.6910	-0.8180	-0.2380	-0.4010	-0.6660
	48	-1.5490	-2.1450	-1.7390	-1.7860	-1.4400
	Min	-2.2330	-2.2540	-2.7380	-2.8110	-2.1020
	Max	-0.6910	-0.8180	-0.2380	-0.4010	-0.4080
	Average	-1.3813	-1.5280	-1.3855	-1.4853	-1.1540

Limit: +/-25nA



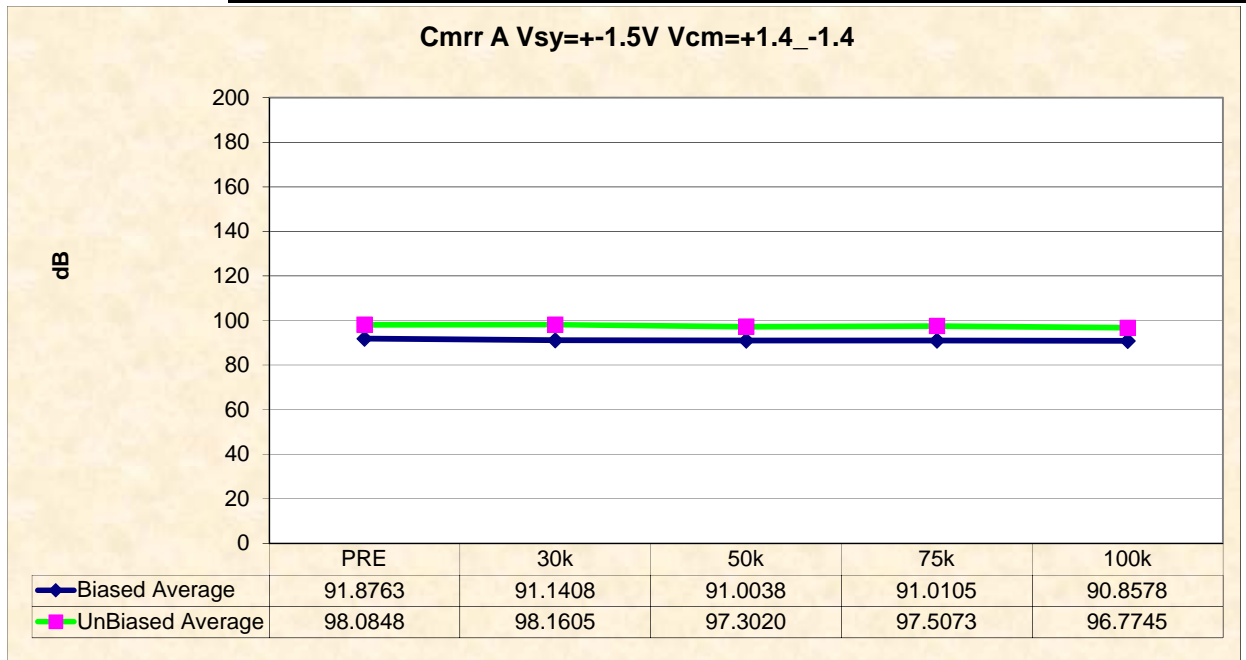
Ios D Vsy=+-1.5 Vcm=0						
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	-0.5520	0.0000	0.0000	-2.4010	-2.3880
	54	0.1050	0.0000	0.0000	0.1980	-0.0670
Biased	14	-0.6870	-0.3680	-0.2700	-0.1820	-0.2820
	15	-0.4830	-0.8450	-1.1070	-0.8240	-0.4440
	42	-1.9120	-1.8180	-1.4150	-1.6140	-1.3400
	43	-1.3520	-1.2020	-1.2710	-1.6260	-1.5860
	Min	-1.9120	-1.8180	-1.4150	-1.6260	-1.5860
	Max	-0.4830	-0.3680	-0.2700	-0.1820	-0.2820
	Average	-1.1085	-1.0583	-1.0158	-1.0615	-0.9130
UnBiased	23	-0.7510	-0.3890	-0.5520	-0.8120	-1.9350
	24	-1.8930	-2.1580	-2.5990	-2.8120	-0.7640
	47	-1.9650	-1.7990	-1.1300	-1.2730	-1.2010
	48	1.8640	1.9680	1.6820	2.0570	1.5990
	Min	-1.9650	-2.1580	-2.5990	-2.8120	-1.9350
	Max	1.8640	1.9680	1.6820	2.0570	1.5990
	Average	-0.6863	-0.5945	-0.6498	-0.7100	-0.5753

Limit: +/-25nA



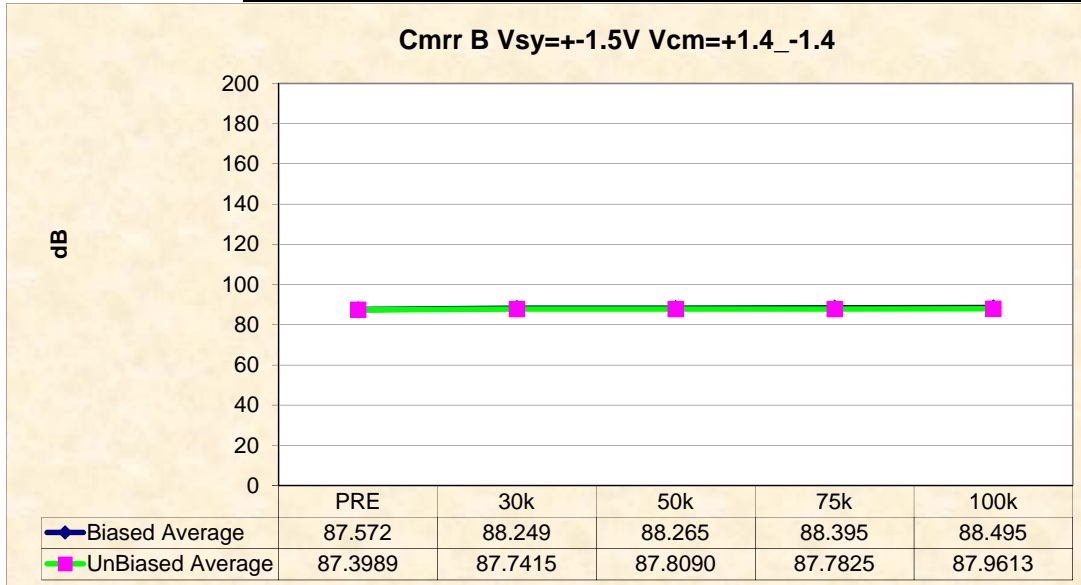
Cmrr A Vsy=+1.5V Vcm=+1.4_-1.4						
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	96.5550	0.0000	0.0000	100.9930	100.0840
	54	107.0190	0.0000	0.0000	107.7450	109.5420
Biased	8	102.5267	100.7670	100.4240	100.4020	99.9650
	9	88.3545	88.6460	88.7810	88.7830	88.9860
	55	87.2888	86.6230	86.4500	86.4720	86.3260
	56	89.3352	88.5270	88.3600	88.3850	88.1540
	Min	87.2888	86.6230	86.4500	86.4720	86.3260
	Max	102.5267	100.7670	100.4240	100.4020	99.9650
	Average	91.8763	91.1408	91.0038	91.0105	90.8578
UnBiased	10	88.8226	89.8470	89.9490	89.7950	109.1610
	11	119.1598	115.4780	111.6890	112.8820	90.0110
	57	98.9914	102.1440	102.3700	102.1460	102.9820
	58	85.3653	85.1730	85.2000	85.2060	84.9440
	Min	85.3653	85.1730	85.2000	85.2060	84.9440
	Max	119.1598	115.4780	111.6890	112.8820	109.1610
	Average	98.0848	98.1605	97.3020	97.5073	96.7745

Limit: >64dB

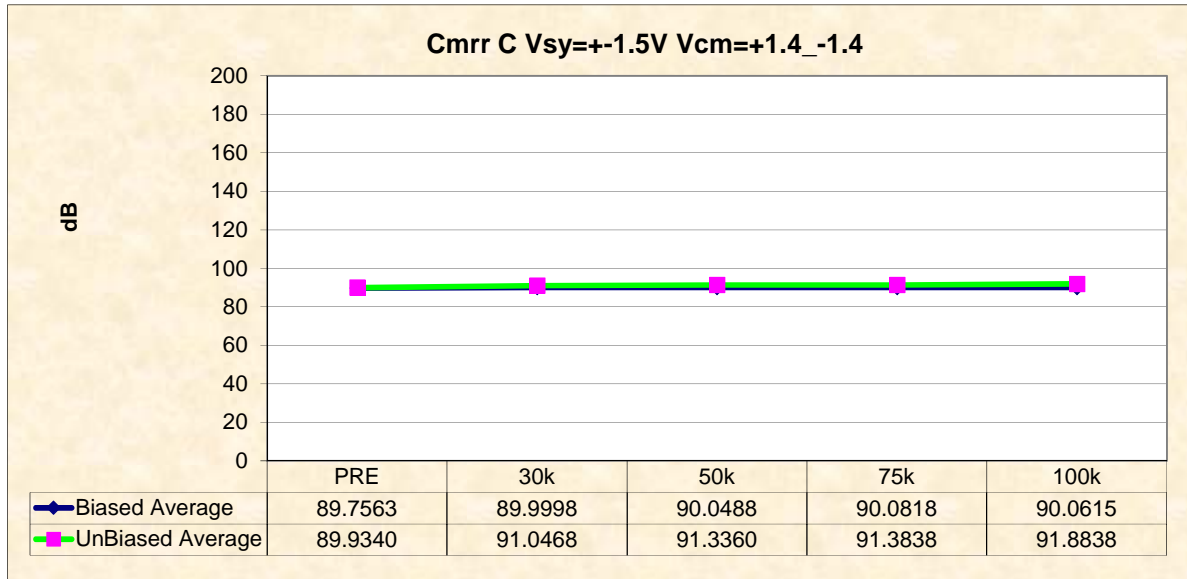


Cmrr B Vsy=+-1.5V Vcm=+1.4_-1.4						
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	83.308	0.000	0.000	89.014	89.188
	54	90.029	0.000	0.000	89.994	90.167
Biased	14	88.974	89.766	89.433	89.887	89.868
	15	82.246	82.662	82.728	82.734	82.818
	42	94.846	95.734	95.987	96.035	96.265
	43	84.223	84.832	84.911	84.924	85.029
	Min	82.246	82.662	82.728	82.734	82.818
	Max	94.846	95.734	95.987	96.035	96.265
	Average	87.572	88.249	88.265	88.395	88.495
UnBiased	23	89.942	90.365	90.511	90.432	90.882
	24	90.256	90.501	90.662	90.592	90.636
	47	83.745	83.984	83.975	84.001	84.067
	48	85.6523	86.1160	86.0880	86.1050	86.2600
	Min	83.7452	83.9840	83.9750	84.0010	84.0670
	Max	90.2563	90.5010	90.6620	90.5920	90.8820
	Average	87.3989	87.7415	87.8090	87.7825	87.9613

Limit: >64dB

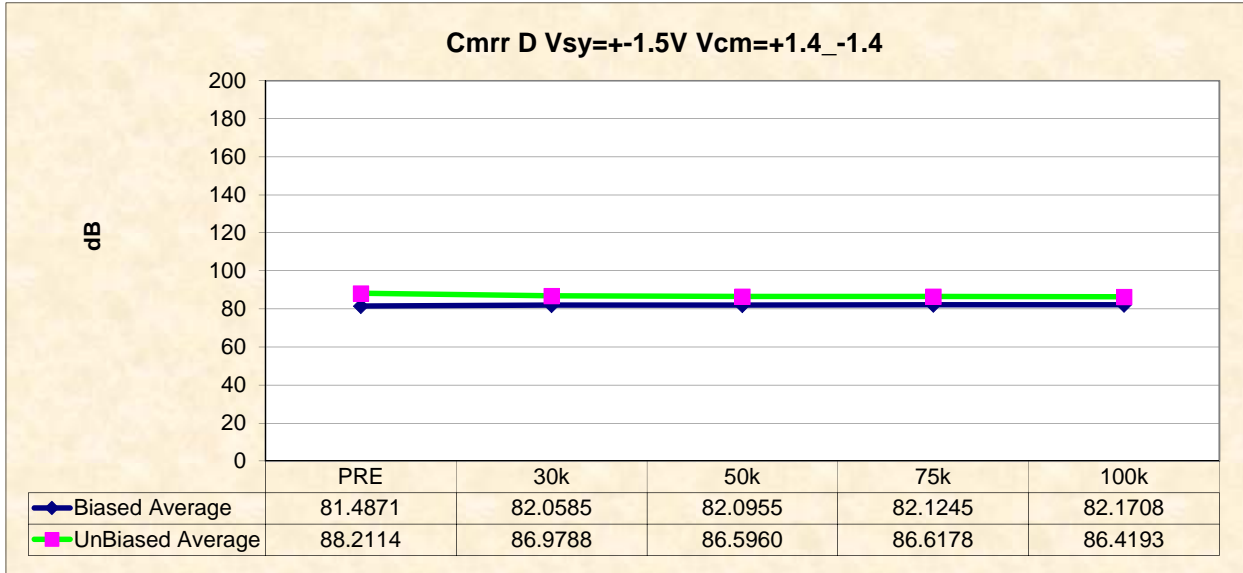


Cmrr C Vsy=+-1.5V Vcm=+1.4_-1.4						
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	85.7770	0.0000	0.0000	98.7320	98.1970
	54	83.1720	0.0000	0.0000	83.2730	83.3160
Biased	14	90.1644	91.1160	91.2520	91.3450	91.4420
	15	84.6767	85.3010	85.3850	85.4230	85.4410
	42	90.1152	90.4850	90.6310	90.7480	90.9130
	43	94.0689	93.0970	92.9270	92.8110	92.4500
	Min	84.6767	85.3010	85.3850	85.4230	85.4410
	Max	94.0689	93.0970	92.9270	92.8110	92.4500
	Average	89.7563	89.9998	90.0488	90.0818	90.0615
UnBiased	23	84.6646	85.4350	85.4820	85.5060	106.3110
	24	102.4924	103.8440	104.7680	104.8460	85.5510
	47	95.3838	97.5060	97.6960	97.7570	98.2310
	48	77.1949	77.4020	77.3980	77.4260	77.4420
	Min	77.1949	77.4020	77.3980	77.4260	77.4420
	Max	102.4924	103.8440	104.7680	104.8460	106.3110
	Average	89.9340	91.0468	91.3360	91.3838	91.8838
Limit: >64dB						



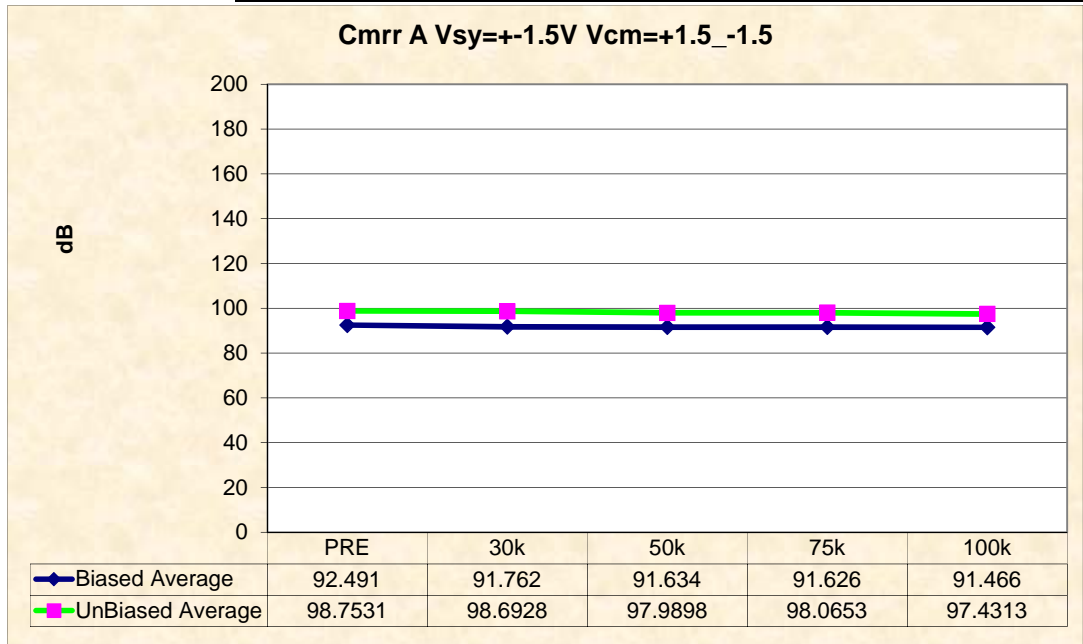
Cmrr D Vsy=+-1.5V Vcm=+1.4_-1.4						
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	82.9100	0.0000	0.0000	75.5070	75.4780
	54	85.4050	0.0000	0.0000	85.4800	85.5460
Biased	8	87.2512	88.0600	88.1950	88.1970	88.3950
	9	75.8245	76.1510	76.1250	76.1550	76.1350
	55	79.4793	79.7760	79.7800	79.8110	79.8200
	56	83.3934	84.2470	84.2820	84.3350	84.3330
	Min	75.8245	76.1510	76.1250	76.1550	76.1350
	Max	87.2512	88.0600	88.1950	88.1970	88.3950
	Average	81.4871	82.0585	82.0955	82.1245	82.1708
UnBiased	10	79.1518	79.4440	79.4590	79.4850	104.5980
	11	112.9346	106.9750	105.4670	105.4360	79.4810
	57	80.8091	81.3390	81.3150	81.3720	81.3910
	58	79.9500	80.1570	80.1430	80.1780	80.2070
	Min	79.1518	79.4440	79.4590	79.4850	79.4810
	Max	112.9346	106.9750	105.4670	105.4360	104.5980
	Average	88.2114	86.9788	86.5960	86.6178	86.4193

Limit: >64dB



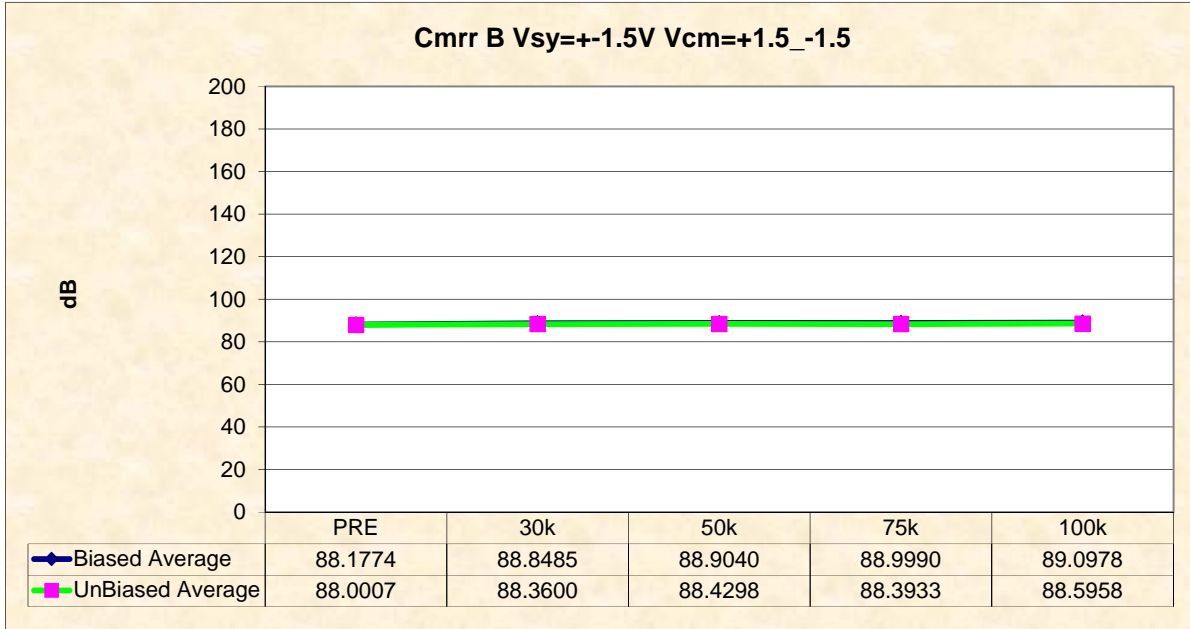
Cmrr A Vsy=+1.5V Vcm=+1.5_-1.5						
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	97.189	0.000	0.000	101.598	100.723
	54	107.450	0.000	0.000	108.253	110.040
Biased	14	103.190	101.437	101.165	101.066	100.604
	15	88.964	89.263	89.382	89.392	89.608
	42	87.879	87.217	87.034	87.063	86.914
	43	89.930	89.129	88.956	88.982	88.739
	Min	87.879	87.217	87.034	87.063	86.914
	Max	103.190	101.437	101.165	101.066	100.604
	Average	92.491	91.762	91.634	91.626	91.466
UnBiased	23	89.432	90.424	90.608	90.451	109.858
	24	120.063	115.925	112.587	113.384	90.675
	47	99.561	102.642	102.983	102.640	103.669
	48	85.9564	85.7800	85.7810	85.7860	85.5230
	Min	85.9564	85.7800	85.7810	85.7860	85.5230
	Max	120.0633	115.9250	112.5870	113.3840	109.8580
	Average	98.7531	98.6928	97.9898	98.0653	97.4313

Limit: >64dB



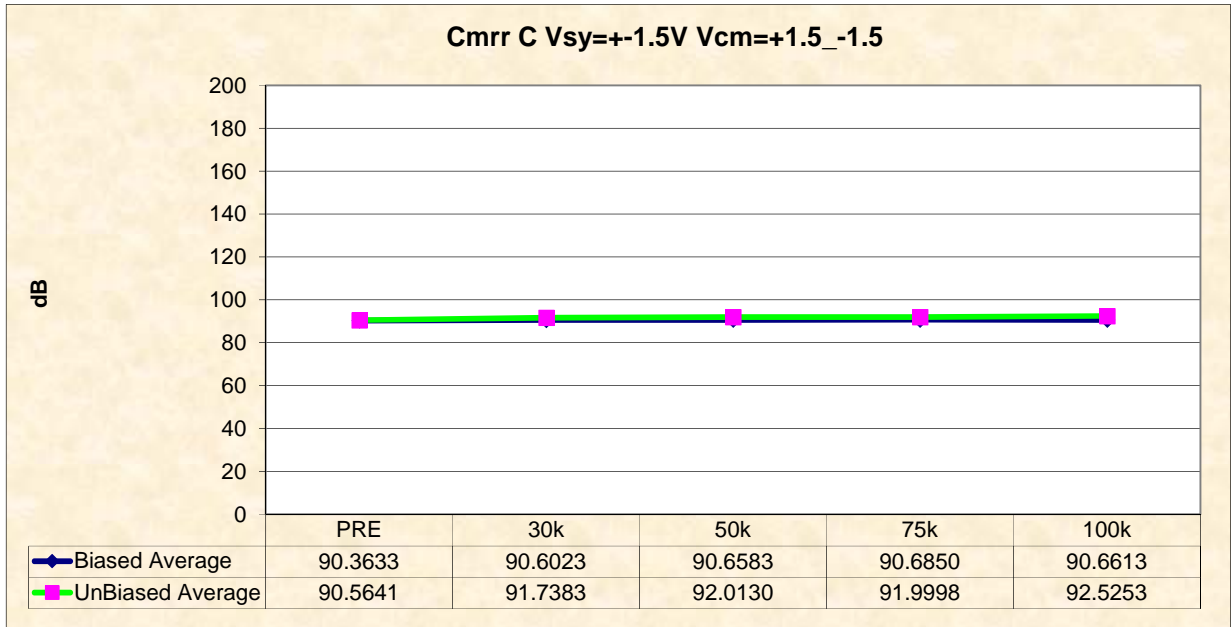
Cmrr B Vsy=+-1.5V Vcm=+1.5_-1.5						
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	83.9060	0.0000	0.0000	89.6470	89.8040
	54	90.6210	0.0000	0.0000	90.6000	90.7700
Biased	14	89.5926	90.3830	90.1860	90.5070	90.5080
	15	82.8537	83.2770	83.3420	83.3440	83.4240
	42	95.4385	96.3090	96.5790	96.6140	96.8260
	43	84.8246	85.4250	85.5090	85.5310	85.6330
	Min	82.8537	83.2770	83.3420	83.3440	83.4240
	Max	95.4385	96.3090	96.5790	96.6140	96.8260
	Average	88.1774	88.8485	88.9040	88.9990	89.0978
UnBiased	23	90.5154	90.9610	91.0930	91.0300	91.5180
	24	90.8717	91.1350	91.2740	91.1940	91.2620
	47	84.3474	84.6020	84.6040	84.6070	84.7070
	48	86.2681	86.7420	86.7480	86.7420	86.8960
	Min	84.3474	84.6020	84.6040	84.6070	84.7070
	Max	90.8717	91.1350	91.2740	91.1940	91.5180
	Average	88.0007	88.3600	88.4298	88.3933	88.5958

Limit: >64dB



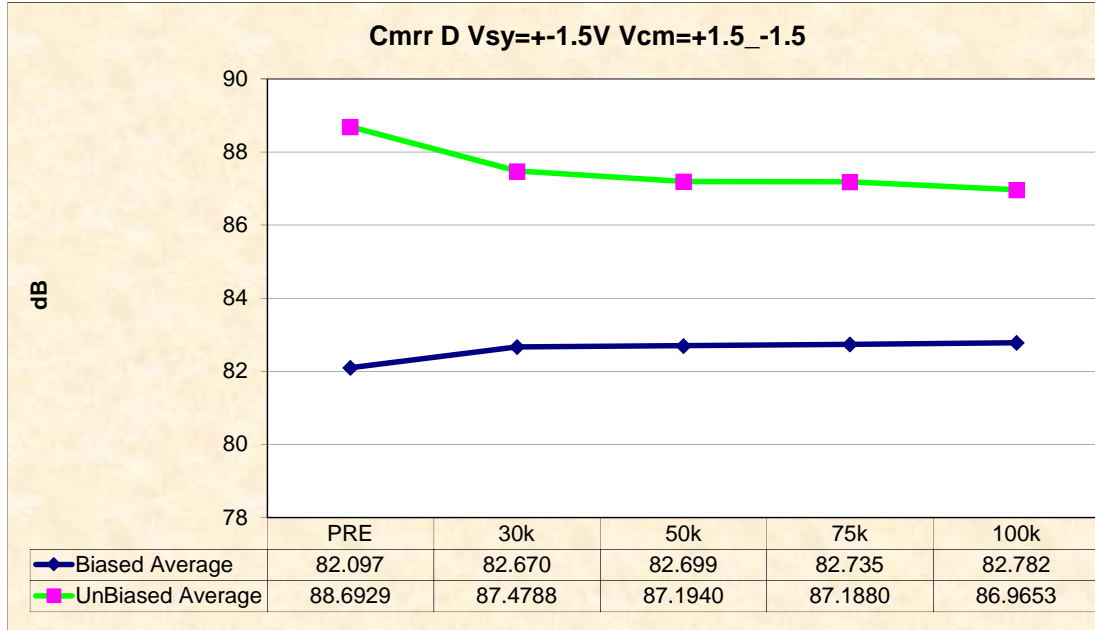
Cmrr C Vsy=+1.5V Vcm=+1.5_-1.5						
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	86.3770	0.0000	0.0000	99.3210	98.8150
	54	83.7960	0.0000	0.0000	83.8930	83.9370
Biased	8	90.7681	91.7020	91.8600	91.9470	92.0560
	9	85.2869	85.9100	86.0070	86.0360	86.0490
	55	90.7215	91.1090	91.2500	91.3710	91.5200
	56	94.6766	93.6880	93.5160	93.3860	93.0200
	Min	85.2869	85.9100	86.0070	86.0360	86.0490
	Max	94.6766	93.6880	93.5160	93.3860	93.0200
	Average	90.3633	90.6023	90.6583	90.6850	90.6613
UnBiased	10	85.2723	86.0740	86.1070	86.1080	106.9940
	11	103.1341	104.7320	105.5950	105.3880	86.1410
	57	96.0456	98.1380	98.3370	98.4620	98.9170
	58	77.8045	78.0090	78.0130	78.0410	78.0490
	Min	77.8045	78.0090	78.0130	78.0410	78.0490
	Max	103.1341	104.7320	105.5950	105.3880	106.9940
	Average	90.5641	91.7383	92.0130	91.9998	92.5253

Limit: >64dB



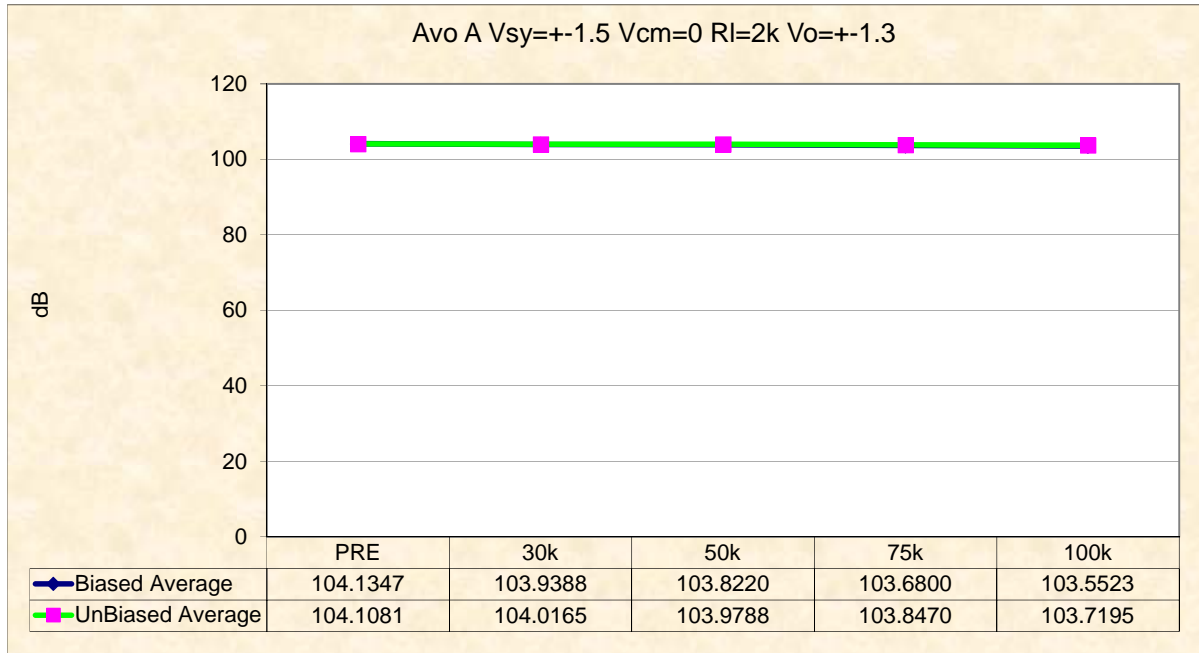
Cmrr D Vsy=+1.5V Vcm=+1.5_-1.5						
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	83.510	0.000	0.000	76.107	76.080
	54	86.019	0.000	0.000	86.088	86.172
Biased	14	87.870	88.690	88.797	88.818	89.025
	15	76.423	76.748	76.723	76.753	76.734
	42	80.092	80.386	80.392	80.423	80.427
	43	84.001	84.857	84.882	84.944	84.940
	Min	76.423	76.748	76.723	76.753	76.734
	Max	87.870	88.690	88.797	88.818	89.025
	Average	82.097	82.670	82.699	82.735	82.782
UnBiased	23	79.745	80.038	80.060	80.074	104.984
	24	113.048	107.162	106.006	105.906	80.077
	47	81.414	81.937	81.941	81.984	81.978
	48	80.5644	80.7780	80.7690	80.7880	80.8220
	Min	79.7454	80.0380	80.0600	80.0740	80.0770
	Max	113.0481	107.1620	106.0060	105.9060	104.9840
	Average	88.6929	87.4788	87.1940	87.1880	86.9653

Limit: >64dB

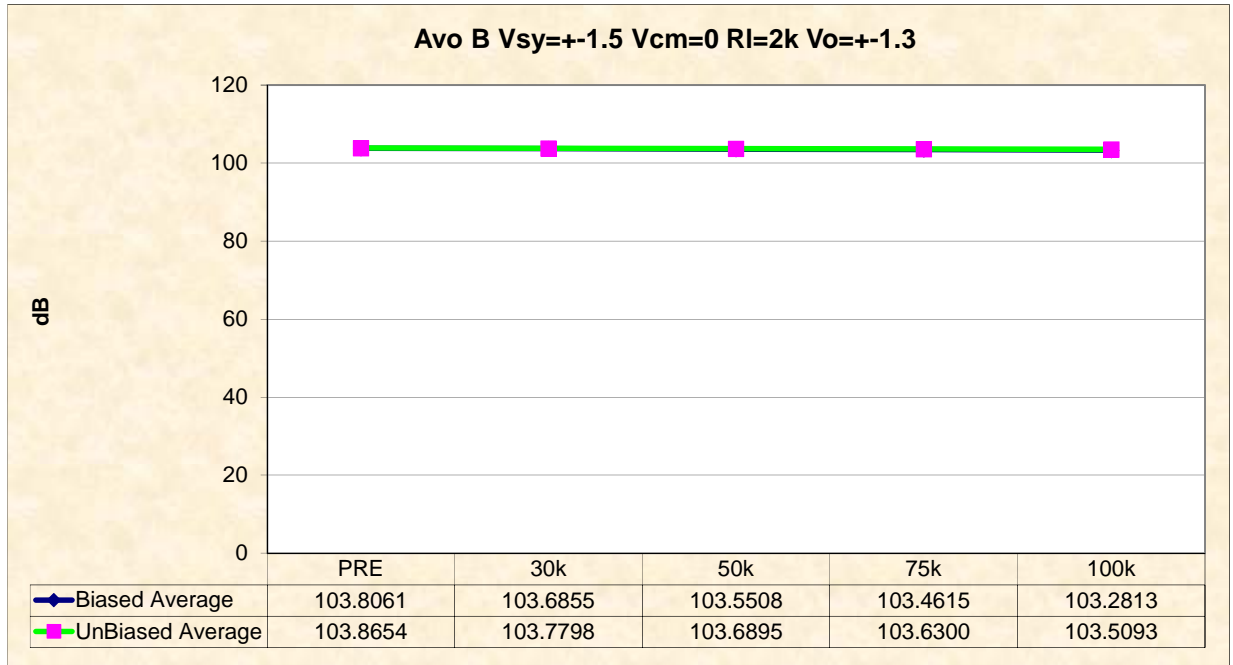


Avo A Vsy=+-1.5 Vcm=0 RI=2k Vo=+-1.3						
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	103.9950	0.0000	0.0000	104.0070	104.0200
	54	103.7280	0.0000	0.0000	103.7210	103.7710
Biased	14	104.2254	103.9960	103.8330	103.7350	103.6250
	15	104.1754	103.9480	103.8070	103.6870	103.5530
	42	103.9204	103.8700	103.7700	103.6000	103.4550
	43	104.2176	103.9410	103.8780	103.6980	103.5760
	Min	103.9204	103.8700	103.7700	103.6000	103.4550
	Max	104.2254	103.9960	103.8780	103.7350	103.6250
	Average	104.1347	103.9388	103.8220	103.6800	103.5523
UnBiased	23	104.0182	103.8680	103.8900	103.7340	103.5290
	24	103.9285	103.8770	103.8340	103.6090	103.6500
	47	103.9924	104.0190	103.9180	103.8710	103.6790
	48	104.4933	104.3020	104.2730	104.1740	104.0200
	Min	103.9285	103.8680	103.8340	103.6090	103.5290
	Max	104.4933	104.3020	104.2730	104.1740	104.0200
	Average	104.1081	104.0165	103.9788	103.8470	103.7195

Limit: >100dB

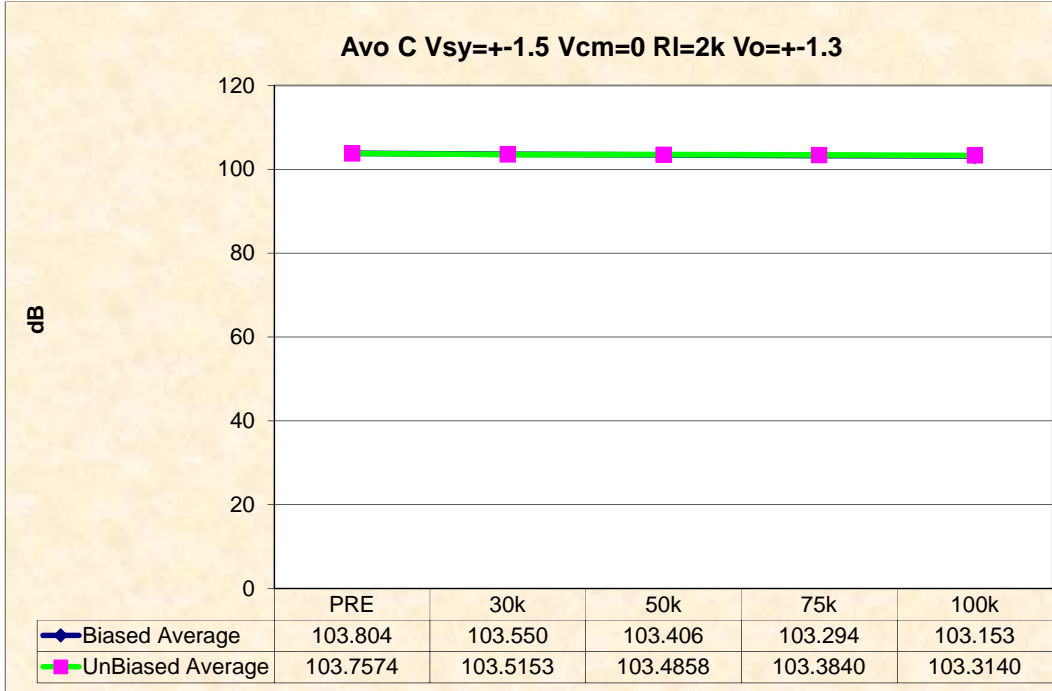


Avo B Vsy=+-1.5 Vcm=0 RI=2k Vo=+-1.3						
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	103.8570	0.0000	0.0000	103.7500	103.7410
	54	103.9890	0.0000	0.0000	104.0300	104.0250
Biased	8	103.9239	103.7720	103.5950	103.5550	103.3960
	9	103.8605	103.6410	103.4940	103.4090	103.2030
	55	103.5269	103.5930	103.4990	103.3730	103.2010
	56	103.9131	103.7360	103.6150	103.5090	103.3250
	Min	103.5269	103.5930	103.4940	103.3730	103.2010
	Max	103.9239	103.7720	103.6150	103.5550	103.3960
	Average	103.8061	103.6855	103.5508	103.4615	103.2813
UnBiased	10	103.6198	103.5230	103.4000	103.4430	103.7860
	11	104.1299	104.1260	103.9640	103.9000	103.2930
	57	103.7792	103.7520	103.6770	103.6140	103.4470
	58	103.9326	103.7180	103.7170	103.5630	103.5110
	Min	103.6198	103.5230	103.4000	103.4430	103.2930
	Max	104.1299	104.1260	103.9640	103.9000	103.7860
	Average	103.8654	103.7798	103.6895	103.6300	103.5093
Limit: >100dB						



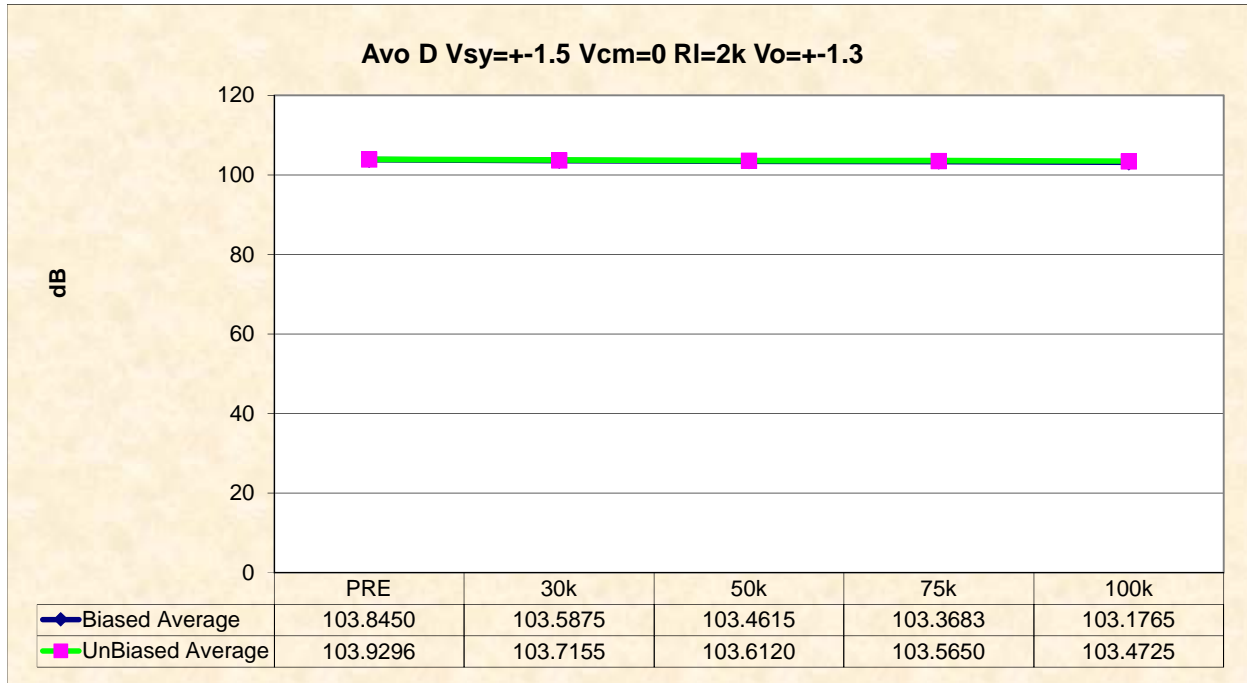
Avo C Vsy=+-1.5 Vcm=0 RI=2k Vo=+-1.3						
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	103.651	0.000	0.000	103.702	103.708
	54	103.717	0.000	0.000	103.754	103.725
Biased	14	103.673	103.432	103.279	103.203	103.071
	15	103.820	103.535	103.394	103.275	103.143
	42	103.512	103.231	103.084	102.950	102.820
	43	104.212	104.003	103.865	103.747	103.577
	Min	103.512	103.231	103.084	102.950	102.820
	Max	104.212	104.003	103.865	103.747	103.577
	Average	103.804	103.550	103.406	103.294	103.153
UnBiased	23	103.726	103.349	103.324	103.245	103.481
	24	103.842	103.697	103.644	103.532	103.226
	47	103.810	103.585	103.618	103.473	103.337
	48	103.6524	103.4300	103.3570	103.2860	103.2120
	Min	103.6524	103.3490	103.3240	103.2450	103.2120
	Max	103.8420	103.6970	103.6440	103.5320	103.4810
	Average	103.7574	103.5153	103.4858	103.3840	103.3140

Limit: >100dB



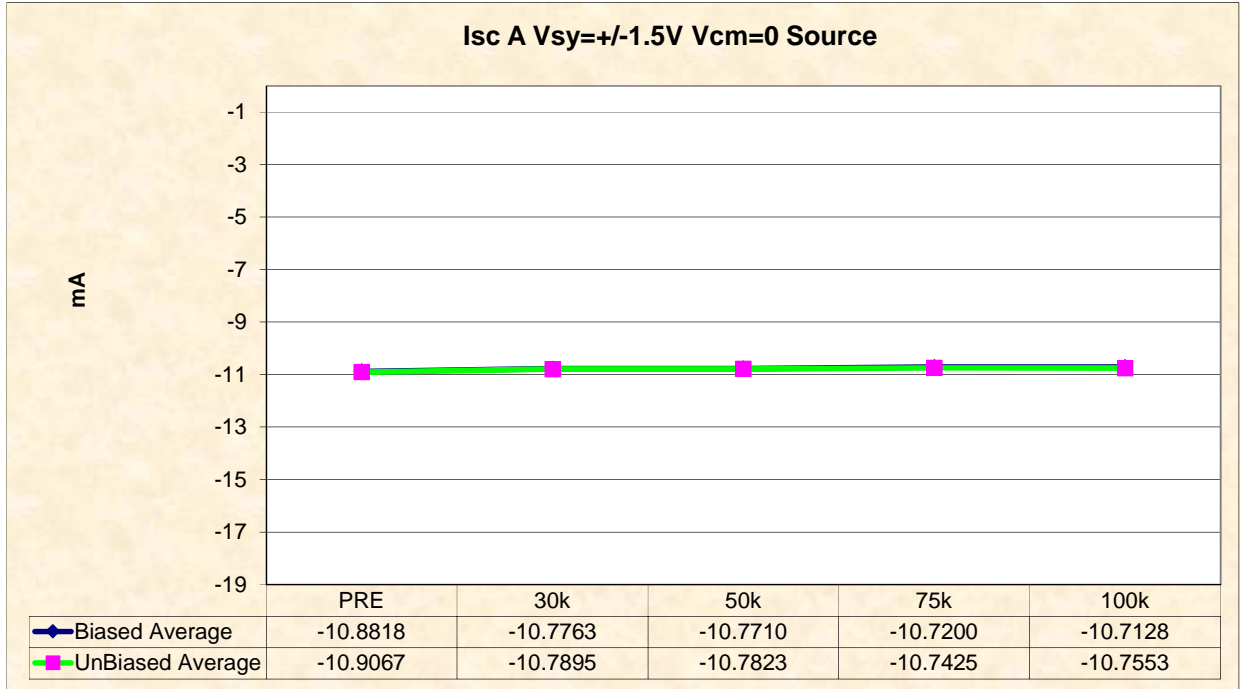
Avo D Vsy=+-1.5 Vcm=0 RI=2k Vo=+-1.3						
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	103.8360	0.0000	0.0000	103.4990	103.5140
	54	103.8860	0.0000	0.0000	103.9130	103.9340
Biased	14	104.0746	103.8160	103.7100	103.6620	103.3970
	15	103.6536	103.3880	103.2300	103.1520	102.9740
	42	103.6715	103.4510	103.2980	103.1910	103.0140
	43	103.9804	103.6950	103.6080	103.4680	103.3210
	Min	103.6536	103.3880	103.2300	103.1520	102.9740
	Max	104.0746	103.8160	103.7100	103.6620	103.3970
	Average	103.8450	103.5875	103.4615	103.3683	103.1765
UnBiased	23	103.8338	103.4580	103.4290	103.4310	103.8430
	24	104.1782	104.1380	103.9490	103.9000	103.3330
	47	103.8112	103.6170	103.4090	103.3530	103.2760
	48	103.8953	103.6490	103.6610	103.5760	103.4380
	Min	103.8112	103.4580	103.4090	103.3530	103.2760
	Max	104.1782	104.1380	103.9490	103.9000	103.8430
	Average	103.9296	103.7155	103.6120	103.5650	103.4725

Limit: >100dB



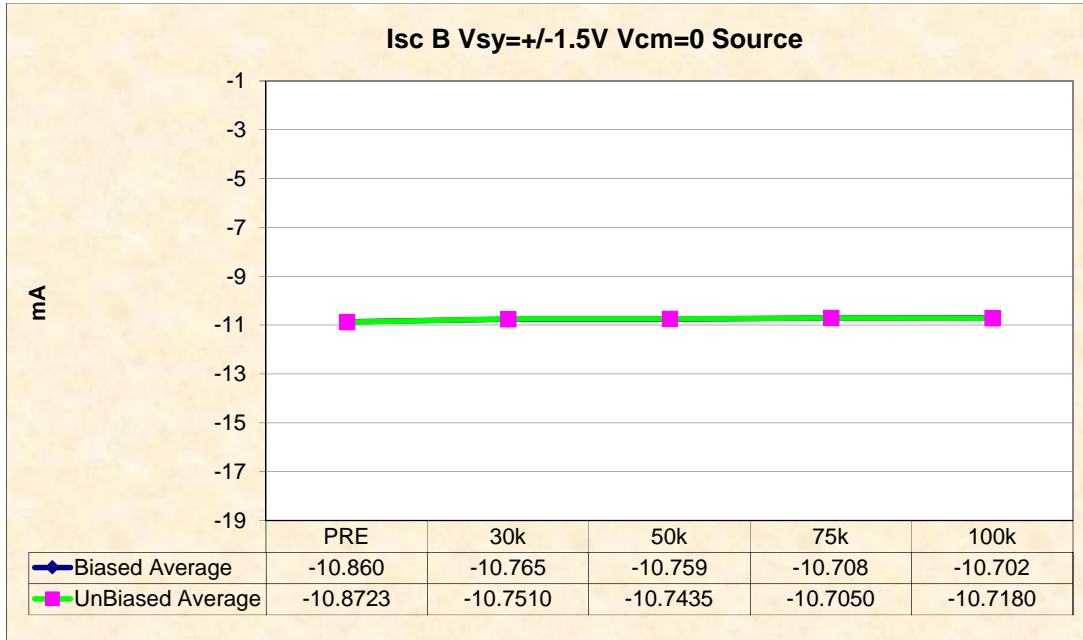
Isc A Vsy=+-1.5V Vcm=0 Source						
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	-10.9800	0.0000	0.0000	-10.8630	-10.9110
	54	-10.6290	0.0000	0.0000	-10.6100	-10.6570
Biased	8	-10.7549	-10.6800	-10.6520	-10.6070	-10.5960
	9	-10.9172	-10.8040	-10.8120	-10.7560	-10.7510
	55	-10.9670	-10.8400	-10.8520	-10.7910	-10.7890
	56	-10.8880	-10.7810	-10.7680	-10.7260	-10.7150
	Min	-10.9670	-10.8400	-10.8520	-10.7910	-10.7890
	Max	-10.7549	-10.6800	-10.6520	-10.6070	-10.5960
	Average	-10.8818	-10.7763	-10.7710	-10.7200	-10.7128
UnBiased	10	-10.8899	-10.7370	-10.7430	-10.7020	-10.8510
	11	-11.0049	-10.8750	-10.8800	-10.8320	-10.7170
	57	-10.7841	-10.6700	-10.6670	-10.6290	-10.6390
	58	-10.9477	-10.8760	-10.8390	-10.8070	-10.8140
	Min	-11.0049	-10.8760	-10.8800	-10.8320	-10.8510
	Max	-10.7841	-10.6700	-10.6670	-10.6290	-10.6390
	Average	-10.9067	-10.7895	-10.7823	-10.7425	-10.7553

Limit: >-19mA

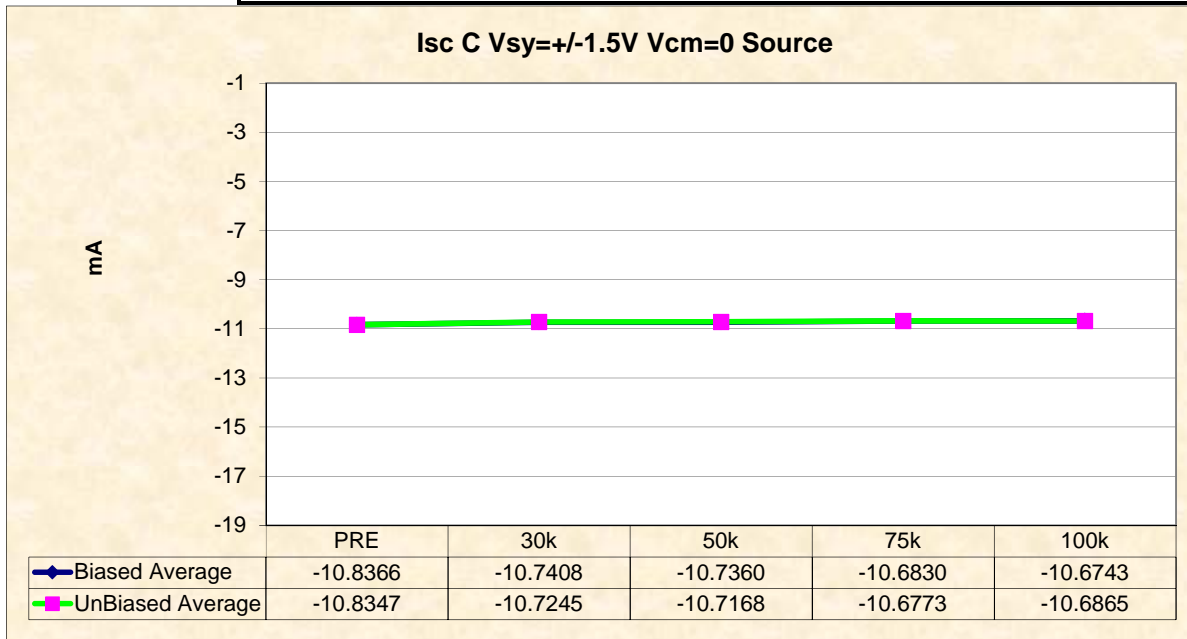


Isc B Vsy=+/-1.5V Vcm=0 Source						
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	-10.902	0.000	0.000	-10.838	-10.884
	54	-10.613	0.000	0.000	-10.602	-10.654
Biased	14	-10.735	-10.664	-10.636	-10.592	-10.582
	15	-10.860	-10.749	-10.752	-10.700	-10.697
	42	-10.869	-10.754	-10.767	-10.704	-10.705
	43	-10.977	-10.893	-10.879	-10.837	-10.825
	Min	-10.977	-10.893	-10.879	-10.837	-10.825
	Max	-10.735	-10.664	-10.636	-10.592	-10.582
	Average	-10.860	-10.765	-10.759	-10.708	-10.702
UnBiased	23	-10.844	-10.679	-10.684	-10.647	-10.821
	24	-10.971	-10.845	-10.850	-10.804	-10.659
	47	-10.778	-10.654	-10.651	-10.609	-10.624
	48	-10.8968	-10.8260	-10.7890	-10.7600	-10.7680
	Min	-10.9705	-10.8450	-10.8500	-10.8040	-10.8210
	Max	-10.7781	-10.6540	-10.6510	-10.6090	-10.6240
	Average	-10.8723	-10.7510	-10.7435	-10.7050	-10.7180

Limit: >-19mA

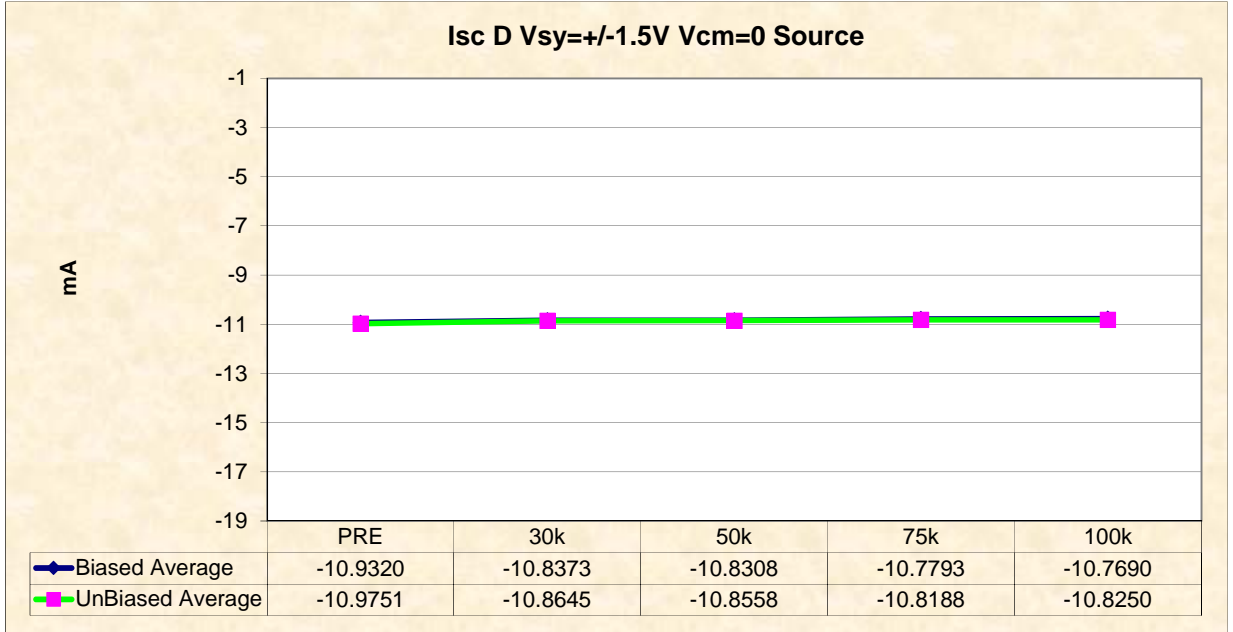


Isc C Vsy=+-1.5V Vcm=0 Source						
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	-10.9650	0.0000	0.0000	-10.8250	-10.8680
	54	-10.5810	0.0000	0.0000	-10.5660	-10.6100
Biased	14	-10.7649	-10.6880	-10.6620	-10.6180	-10.6020
	15	-10.9092	-10.8080	-10.8130	-10.7540	-10.7470
	42	-10.8080	-10.6910	-10.7030	-10.6400	-10.6380
	43	-10.8642	-10.7760	-10.7660	-10.7200	-10.7100
	Min	-10.9092	-10.8080	-10.8130	-10.7540	-10.7470
	Max	-10.7649	-10.6880	-10.6620	-10.6180	-10.6020
	Average	-10.8366	-10.7408	-10.7360	-10.6830	-10.6743
UnBiased	23	-10.7693	-10.6130	-10.6200	-10.5800	-10.8600
	24	-10.9948	-10.8910	-10.8940	-10.8480	-10.5850
	47	-10.6806	-10.5700	-10.5650	-10.5250	-10.5370
	48	-10.8942	-10.8240	-10.7880	-10.7560	-10.7640
	Min	-10.9948	-10.8910	-10.8940	-10.8480	-10.8600
	Max	-10.6806	-10.5700	-10.5650	-10.5250	-10.5370
	Average	-10.8347	-10.7245	-10.7168	-10.6773	-10.6865
Limit: >-19mA						



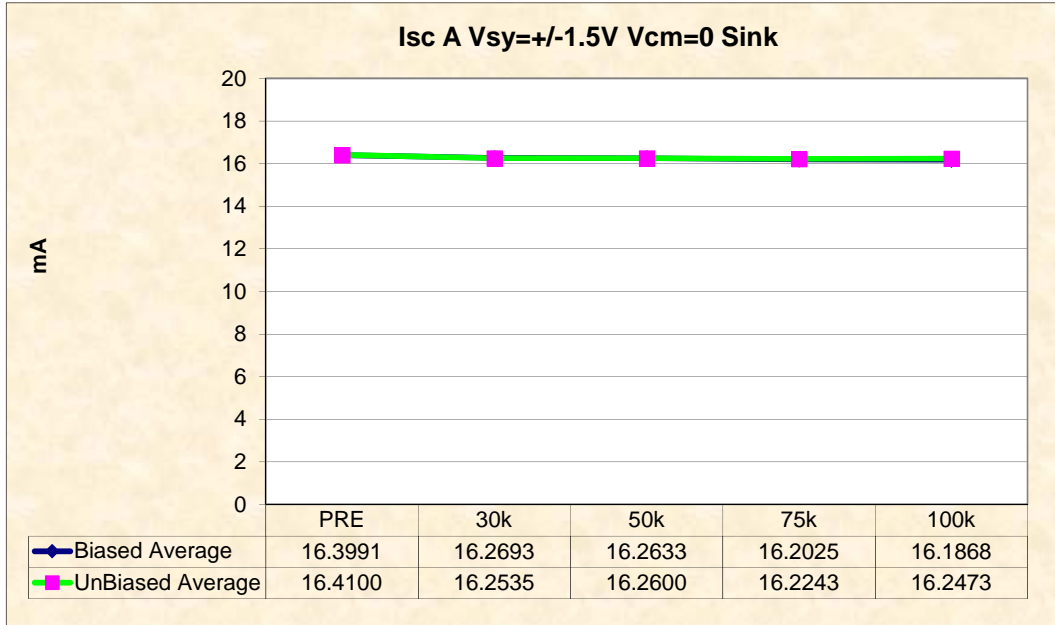
Isc D Vsy=+/-1.5V Vcm=0 Source						
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	-11.0610	0.0000	0.0000	-10.8210	-10.8610
	54	-10.5770	0.0000	0.0000	-10.5820	-10.6280
Biased	8	-10.7503	-10.6820	-10.6560	-10.6120	-10.5960
	9	-10.9646	-10.8480	-10.8550	-10.8000	-10.7900
	55	-10.9653	-10.8650	-10.8700	-10.8070	-10.8050
	56	-11.0478	-10.9540	-10.9420	-10.8980	-10.8850
	Min	-11.0478	-10.9540	-10.9420	-10.8980	-10.8850
	Max	-10.7503	-10.6820	-10.6560	-10.6120	-10.5960
	Average	-10.9320	-10.8373	-10.8308	-10.7793	-10.7690
UnBiased	10	-11.0384	-10.8850	-10.8850	-10.8500	-10.9180
	11	-11.0571	-10.9470	-10.9530	-10.9080	-10.8560
	57	-10.8534	-10.7440	-10.7370	-10.6980	-10.7050
	58	-10.9515	-10.8820	-10.8480	-10.8190	-10.8210
	Min	-11.0571	-10.9470	-10.9530	-10.9080	-10.9180
	Max	-10.8534	-10.7440	-10.7370	-10.6980	-10.7050
	Average	-10.9751	-10.8645	-10.8558	-10.8188	-10.8250

Limit: >-19mA



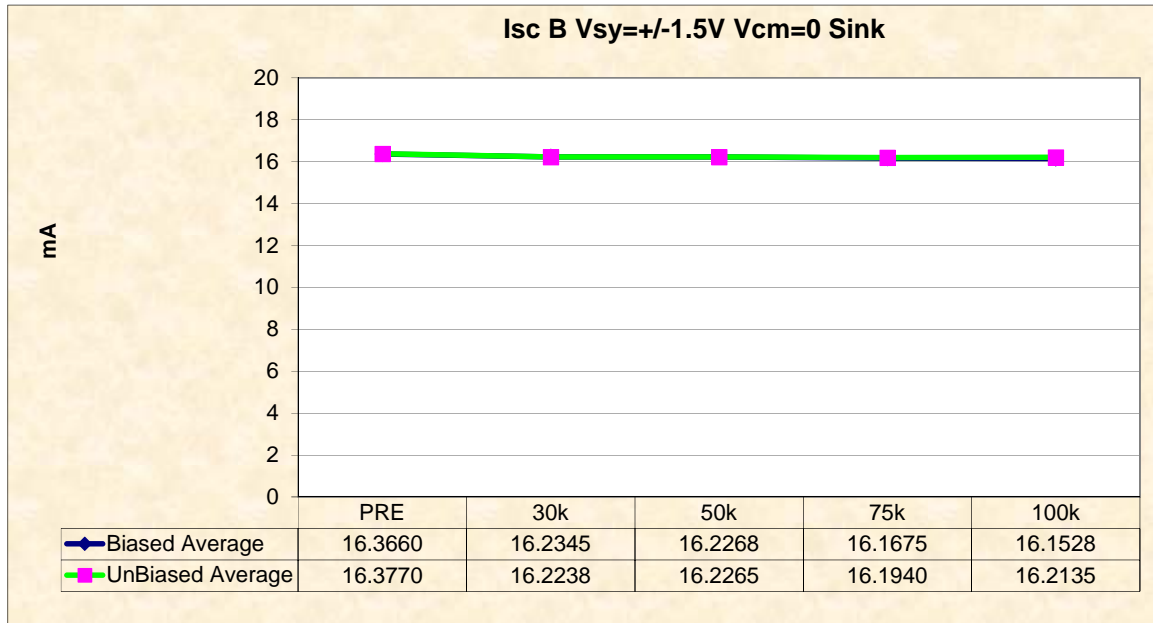
Isc A Vsy=+-1.5V Vcm=0 Sink						
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	16.3680	0.0000	0.0000	15.9490	15.9940
	54	16.2320	0.0000	0.0000	16.1850	16.2300
Biased	14	16.4007	16.3410	16.3100	16.2600	16.2370
	15	16.5562	16.3840	16.3970	16.3350	16.3170
	42	16.2701	16.1360	16.1480	16.0670	16.0560
	43	16.3696	16.2160	16.1980	16.1480	16.1370
	Min	16.2701	16.1360	16.1480	16.0670	16.0560
	Max	16.5562	16.3840	16.3970	16.3350	16.3170
	Average	16.3991	16.2693	16.2633	16.2025	16.1868
UnBiased	23	16.5127	16.3280	16.3470	16.3100	16.2240
	24	16.3820	16.1910	16.2290	16.1850	16.3360
	47	16.3323	16.1790	16.1850	16.1480	16.1680
	48	16.4131	16.3160	16.2790	16.2540	16.2610
	Min	16.3323	16.1790	16.1850	16.1480	16.1680
	Max	16.5127	16.3280	16.3470	16.3100	16.3360
	Average	16.4100	16.2535	16.2600	16.2243	16.2473

Limit: <20mA



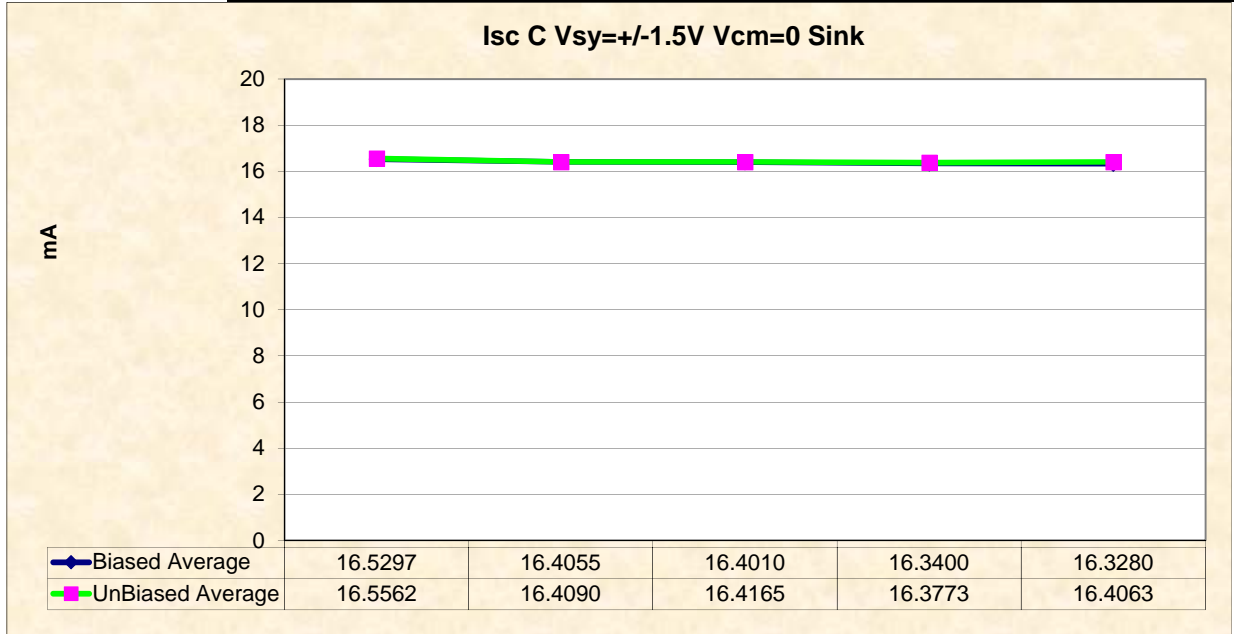
Isc B Vsy=+-1.5V Vcm=0 Sink						
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	16.3750	0.0000	0.0000	15.8470	15.9110
	54	16.1810	0.0000	0.0000	16.1280	16.1790
Biased	14	16.3754	16.3030	16.2780	16.2350	16.2040
	15	16.4941	16.3410	16.3470	16.2780	16.2670
	42	16.2192	16.0780	16.0850	16.0160	16.0040
	43	16.3754	16.2160	16.1970	16.1410	16.1360
	Min	16.2192	16.0780	16.0850	16.0160	16.0040
	Max	16.4941	16.3410	16.3470	16.2780	16.2670
	Average	16.3660	16.2345	16.2268	16.1675	16.1528
UnBiased	23	16.4566	16.2660	16.2840	16.2410	16.1790
	24	16.3442	16.1600	16.1780	16.1530	16.2730
	47	16.2754	16.1410	16.1530	16.1100	16.1230
	48	16.4317	16.3280	16.2910	16.2720	16.2790
	Min	16.2754	16.1410	16.1530	16.1100	16.1230
	Max	16.4566	16.3280	16.2910	16.2720	16.2790
	Average	16.3770	16.2238	16.2265	16.1940	16.2135

Limit: <20mA



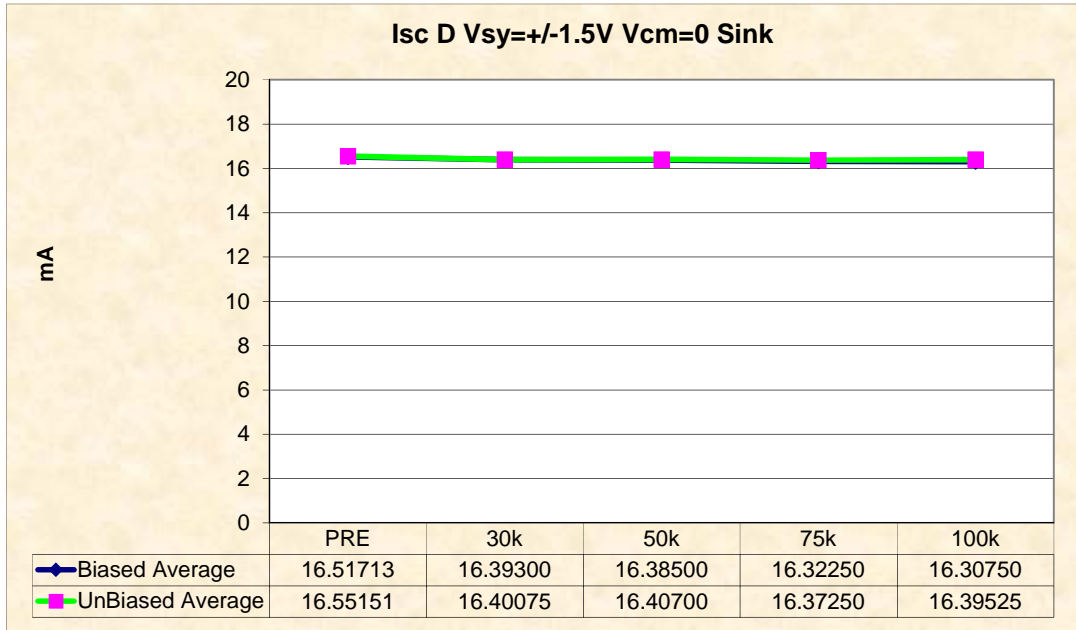
		Isc C Vsy=+/-1.5V Vcm=0 Sink				
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	16.5150	0.0000	0.0000	16.1870	16.2360
	54	16.3080	0.0000	0.0000	16.2620	16.3230
Biased	8	16.5672	16.5050	16.4680	16.4180	16.3920
	9	16.6983	16.5370	16.5490	16.4740	16.4730
	55	16.3735	16.2620	16.2810	16.2060	16.2110
	56	16.4797	16.3180	16.3060	16.2620	16.2360
	Min	16.3735	16.2620	16.2810	16.2060	16.2110
	Max	16.6983	16.5370	16.5490	16.4740	16.4730
	Average	16.5297	16.4055	16.4010	16.3400	16.3280
UnBiased	10	16.6484	16.4620	16.4930	16.4490	16.3860
	11	16.5422	16.3560	16.3870	16.3490	16.4800
	57	16.4485	16.3310	16.3370	16.2930	16.3170
	58	16.5859	16.4870	16.4490	16.4180	16.4420
	Min	16.4485	16.3310	16.3370	16.2930	16.3170
	Max	16.6484	16.4870	16.4930	16.4490	16.4800
	Average	16.5562	16.4090	16.4165	16.3773	16.4063

Limit: <20mA



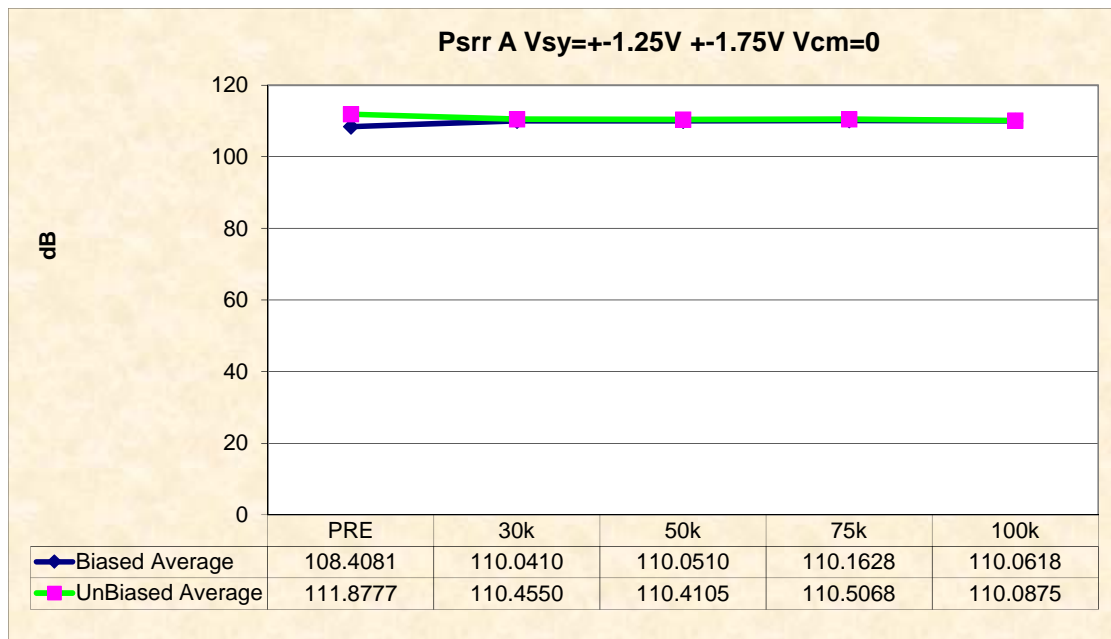
Isc D Vsy=+-1.5V Vcm=0 Sink						
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	16.54500	0.00000	0.00000	16.18000	16.24200
	54	16.32000	0.00000	0.00000	16.26800	16.31700
Biased	14	16.49838	16.43700	16.39900	16.34300	16.31700
	15	16.70463	16.53700	16.54900	16.48000	16.47300
	42	16.37963	16.26800	16.28000	16.19900	16.19800
	43	16.48588	16.33000	16.31200	16.26800	16.24200
	Min	16.37963	16.26800	16.28000	16.19900	16.19800
	Max	16.70463	16.53700	16.54900	16.48000	16.47300
	Average	16.51713	16.39300	16.38500	16.32250	16.30750
UnBiased	23	16.66713	16.48700	16.51200	16.46800	16.35500
	24	16.52338	16.33700	16.35500	16.31800	16.49800
	47	16.43588	16.30500	16.31200	16.28000	16.29800
	48	16.57963	16.47400	16.44900	16.42400	16.43000
	Min	16.43588	16.30500	16.31200	16.28000	16.29800
	Max	16.66713	16.48700	16.51200	16.46800	16.49800
	Average	16.55151	16.40075	16.40700	16.37250	16.39525

Limit: <20mA

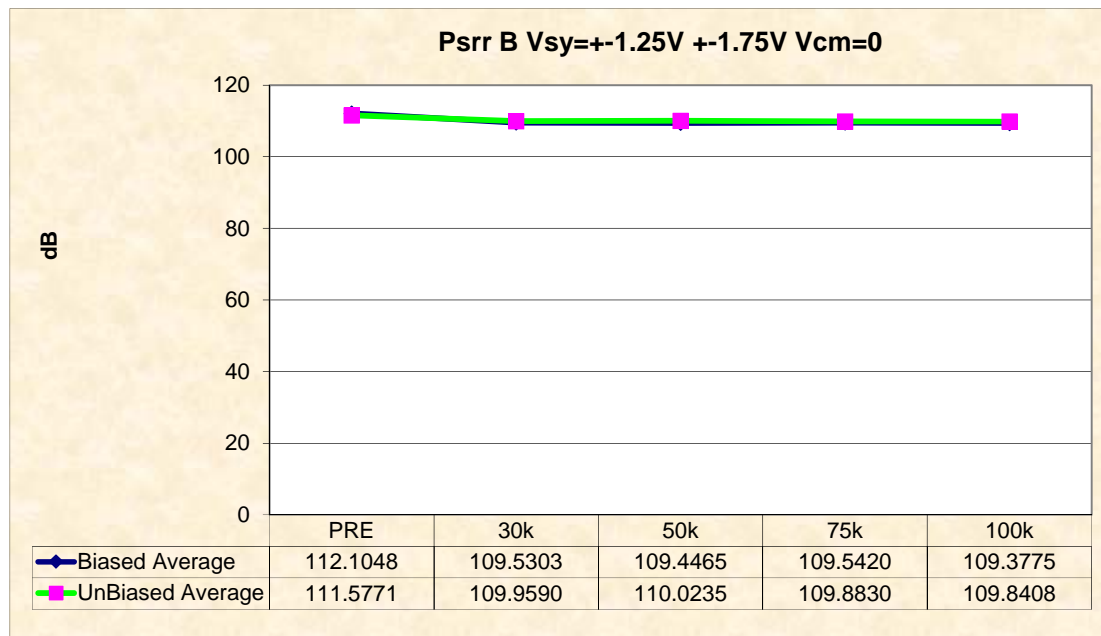


Psrr A Vsy=+-1.25V +-1.75V Vcm=0						
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	108.9820	0.0000	0.0000	108.9520	108.9430
	54	113.2430	0.0000	0.0000	113.4550	113.1110
Biased	14	111.6867	110.9000	110.9990	111.1460	110.8680
	15	114.2175	109.6870	109.6280	109.7860	109.7180
	42	101.8841	110.2350	110.1550	110.4040	110.2480
	43	105.8442	109.3420	109.4220	109.3150	109.4130
	Min	101.8841	109.3420	109.4220	109.3150	109.4130
	Max	114.2175	110.9000	110.9990	111.1460	110.8680
	Average	108.4081	110.0410	110.0510	110.1628	110.0618
UnBiased	23	120.8358	108.1380	108.2050	108.6310	109.9950
	24	109.8708	110.5480	110.6290	110.4150	108.1560
	47	111.9975	110.6140	110.6250	110.6890	110.0920
	48	104.8065	112.5200	112.1830	112.2920	112.1070
	Min	104.8065	108.1380	108.2050	108.6310	108.1560
	Max	120.8358	112.5200	112.1830	112.2920	112.1070
	Average	111.8777	110.4550	110.4105	110.5068	110.0875

Limit: >85dB

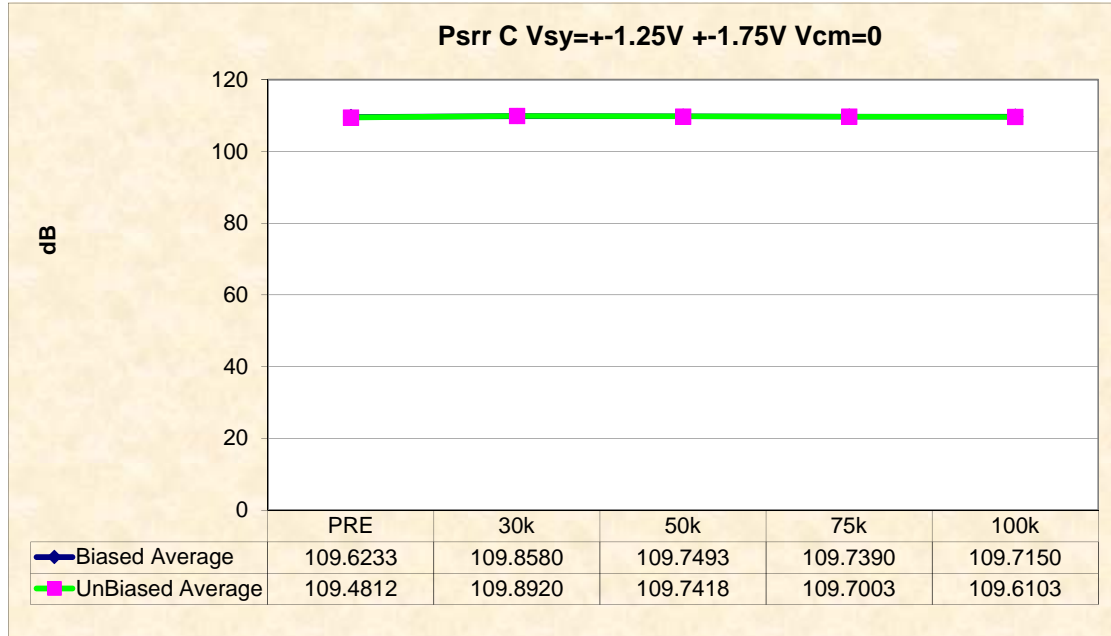


Psrr B Vsy=+-1.25V +-1.75V Vcm=0						
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	108.5720	0.0000	0.0000	109.6600	109.5830
	54	109.2940	0.0000	0.0000	109.3930	109.2210
Biased	8	114.4673	109.0060	109.1640	109.1180	108.9570
	9	109.1421	109.9950	109.6950	110.0370	109.7930
	55	112.4588	109.5070	109.3540	109.3550	109.3320
	56	112.3511	109.6130	109.5730	109.6580	109.4280
	Min	109.1421	109.0060	109.1640	109.1180	108.9570
	Max	114.4673	109.9950	109.6950	110.0370	109.7930
	Average	112.1048	109.5303	109.4465	109.5420	109.3775
UnBiased	10	103.5154	110.9390	110.9590	110.6050	109.2560
	11	114.4573	109.6570	109.4020	109.2240	110.9620
	57	105.2092	110.1030	110.2570	110.4890	109.8350
	58	123.1264	109.1370	109.4760	109.2140	109.3100
	Min	103.5154	109.1370	109.4020	109.2140	109.2560
	Max	123.1264	110.9390	110.9590	110.6050	110.9620
	Average	111.5771	109.9590	110.0235	109.8830	109.8408
Limit: >85dB						



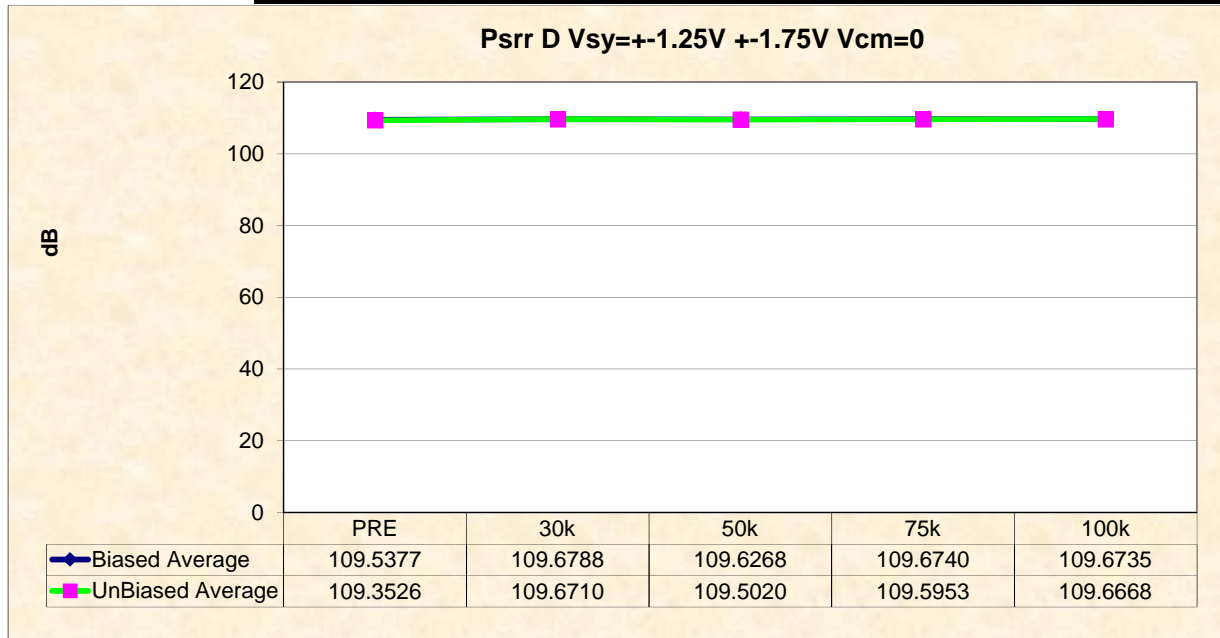
Psrr C Vsy=+-1.25V +-1.75V Vcm=0						
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	109.7810	0.0000	0.0000	109.2080	108.9190
	54	108.9230	0.0000	0.0000	108.9950	108.7670
Biased	14	109.4799	109.6490	109.4780	109.3430	109.5030
	15	108.2501	108.3640	108.2880	108.2120	108.2940
	42	109.2836	109.5610	109.3360	109.3520	109.3670
	43	111.4796	111.8580	111.8950	112.0490	111.6960
	Min	108.2501	108.3640	108.2880	108.2120	108.2940
	Max	111.4796	111.8580	111.8950	112.0490	111.6960
	Average	109.6233	109.8580	109.7493	109.7390	109.7150
UnBiased	23	109.5083	109.7340	109.4510	109.5050	109.7110
	24	109.5330	110.2680	110.2790	109.9960	109.7030
	47	109.7169	110.2140	109.8940	110.2680	109.7200
	48	109.1667	109.3520	109.3430	109.0320	109.3070
	Min	109.1667	109.3520	109.3430	109.0320	109.3070
	Max	109.7169	110.2680	110.2790	110.2680	109.7200
	Average	109.4812	109.8920	109.7418	109.7003	109.6103

Limit: >85dB



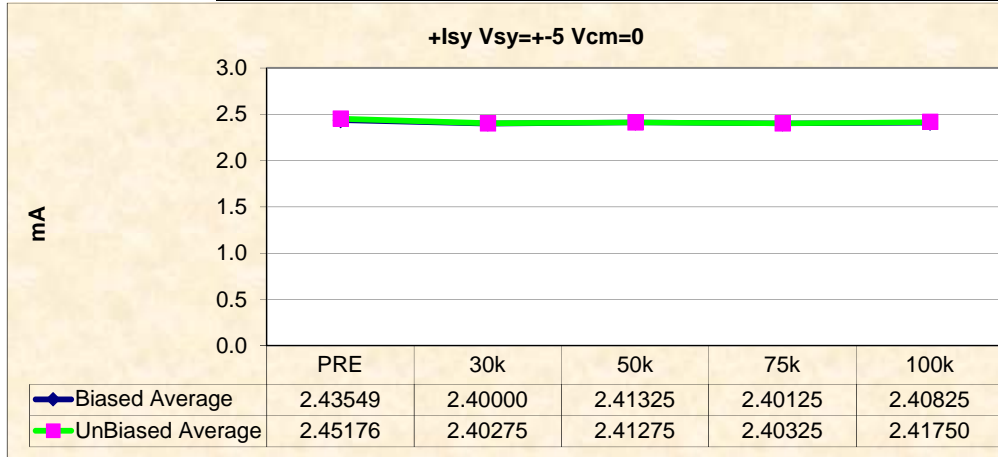
Psrr D Vsy=+-1.25V +-1.75V Vcm=0						
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	109.3440	0.0000	0.0000	110.7800	110.8020
	54	110.1400	0.0000	0.0000	110.3730	110.0660
Biased	8	109.4112	109.4240	109.4790	109.5010	109.5270
	9	109.0771	109.2630	109.1570	109.3050	109.1520
	55	109.1133	109.1940	109.1920	109.1720	109.1430
	56	110.5491	110.8340	110.6790	110.7180	110.8720
	Min	109.0771	109.1940	109.1570	109.1720	109.1430
	Max	110.5491	110.8340	110.6790	110.7180	110.8720
	Average	109.5377	109.6788	109.6268	109.6740	109.6735
UnBiased	10	109.1030	109.6800	109.0130	109.4240	109.6340
	11	109.2131	109.3810	109.5550	109.6240	109.2840
	57	109.8426	110.2400	109.9170	109.8370	109.9010
	58	109.2517	109.3830	109.5230	109.4960	109.8480
	Min	109.1030	109.3810	109.0130	109.4240	109.2840
	Max	109.8426	110.2400	109.9170	109.8370	109.9010
	Average	109.3526	109.6710	109.5020	109.5953	109.6668

Limit: >85dB



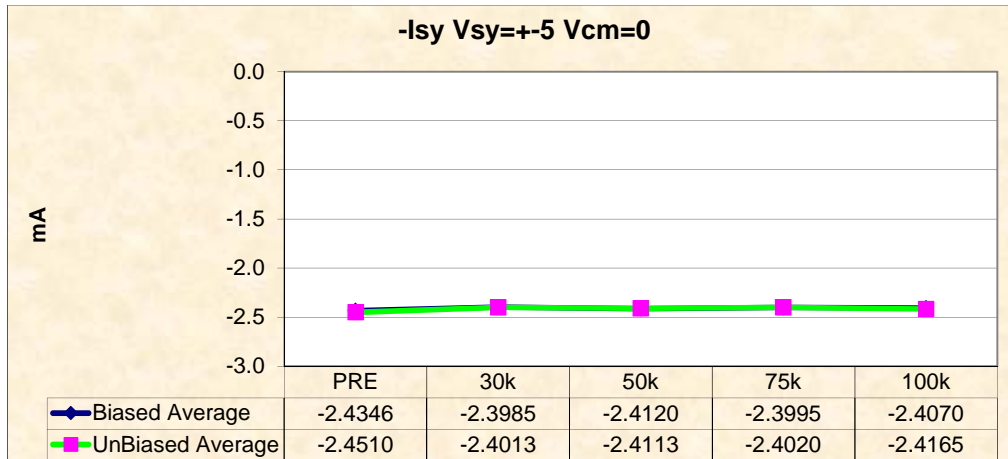
		+Isy Vsy=+-5 Vcm=0				
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	2.41700	0.00000	0.00000	2.32200	2.34100
	54	2.39400	0.00000	0.00000	2.38400	2.40300
Biased	14	2.44411	2.42600	2.42600	2.41600	2.42000
	15	2.46345	2.42100	2.44200	2.42600	2.43600
	42	2.41514	2.37200	2.39300	2.37900	2.38700
	43	2.41927	2.38100	2.39200	2.38400	2.39000
	Min	2.41514	2.37200	2.39200	2.37900	2.38700
	Max	2.46345	2.42600	2.44200	2.42600	2.43600
	Average	2.43549	2.40000	2.41325	2.40125	2.40825
UnBiased	23	2.46701	2.41000	2.42500	2.41300	2.43000
	24	2.46901	2.40500	2.42400	2.41200	2.42600
	47	2.43410	2.38600	2.39700	2.38900	2.40100
	48	2.43691	2.41000	2.40500	2.39900	2.41300
	Min	2.43410	2.38600	2.39700	2.38900	2.40100
	Max	2.46901	2.41000	2.42500	2.41300	2.43000
	Average	2.45176	2.40275	2.41275	2.40325	2.41750

Limit: <2.8mA



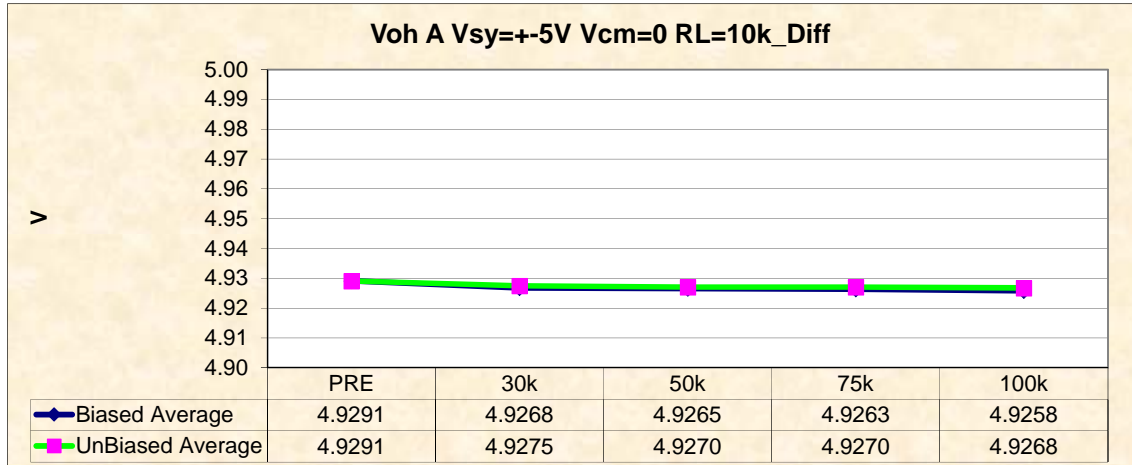
		-Isy Vsy=+-5 Vcm=0				
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	-2.4160	0.0000	0.0000	-2.3200	-2.3400
	54	-2.3930	0.0000	0.0000	-2.3830	-2.4010
Biased	8	-2.4432	-2.4240	-2.4250	-2.4140	-2.4190
	9	-2.4626	-2.4190	-2.4410	-2.4250	-2.4340
	55	-2.4144	-2.3710	-2.3920	-2.3770	-2.3860
	56	-2.4184	-2.3800	-2.3900	-2.3820	-2.3890
	Min	-2.4626	-2.4240	-2.4410	-2.4250	-2.4340
	Max	-2.4144	-2.3710	-2.3900	-2.3770	-2.3860
	Average	-2.4346	-2.3985	-2.4120	-2.3995	-2.4070
UnBiased	10	-2.4662	-2.4080	-2.4230	-2.4120	-2.4290
	11	-2.4686	-2.4040	-2.4220	-2.4100	-2.4250
	57	-2.4333	-2.3850	-2.3960	-2.3880	-2.4000
	58	-2.4362	-2.4080	-2.4040	-2.3980	-2.4120
	Min	-2.4686	-2.4080	-2.4230	-2.4120	-2.4290
	Max	-2.4333	-2.3850	-2.3960	-2.3880	-2.4000
	Average	-2.4510	-2.4013	-2.4113	-2.4020	-2.4165

Limit: >-2.8mA



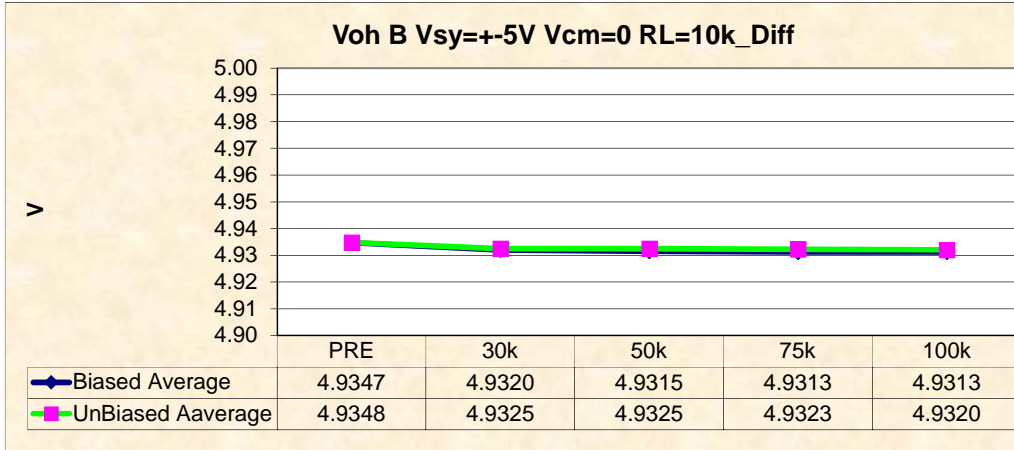
Voh A Vsy=+-5V Vcm=0 RL=10k_Diff						
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	4.9320	0.0000	0.0000	4.9280	4.9280
	54	4.9300	0.0000	0.0000	4.9260	4.9260
Biased	8	4.9287	4.9260	4.9260	4.9260	4.9250
	9	4.9294	4.9270	4.9270	4.9270	4.9260
	55	4.9295	4.9270	4.9270	4.9260	4.9260
	56	4.9288	4.9270	4.9260	4.9260	4.9260
	Min	4.9287	4.9260	4.9260	4.9260	4.9250
	Max	4.9295	4.9270	4.9270	4.9270	4.9260
	Average	4.9291	4.9268	4.9265	4.9263	4.9258
UnBiased	10	4.9290	4.9270	4.9260	4.9260	4.9280
	11	4.9296	4.9290	4.9280	4.9280	4.9260
	57	4.9285	4.9270	4.9270	4.9270	4.9260
	58	4.9293	4.9270	4.9270	4.9270	4.9270
	Min	4.9285	4.9270	4.9260	4.9260	4.9260
	Max	4.9296	4.9290	4.9280	4.9280	4.9280
	Average	4.9291	4.9275	4.9270	4.9270	4.9268

Limit: >4.9V



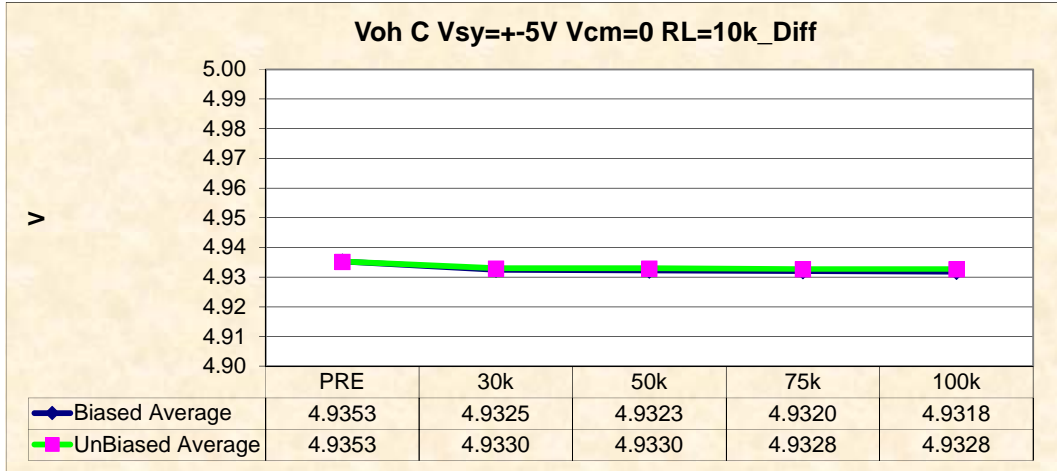
Voh B Vsy=+-5V Vcm=0 RL=10k_Diff						
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	4.9340	0.0000	0.0000	4.9330	4.9330
	54	4.9330	0.0000	0.0000	4.9320	4.9310
Biased	14	4.9341	4.9310	4.9310	4.9310	4.9310
	15	4.9348	4.9320	4.9320	4.9310	4.9310
	42	4.9344	4.9320	4.9310	4.9310	4.9310
	43	4.9353	4.9330	4.9320	4.9320	4.9320
	Min	4.9341	4.9310	4.9310	4.9310	4.9310
	Max	4.9353	4.9330	4.9320	4.9320	4.9320
	Average	4.9347	4.9320	4.9315	4.9313	4.9313
UnBiased	23	4.9347	4.9310	4.9310	4.9310	4.9330
	24	4.9352	4.9340	4.9340	4.9340	4.9310
	47	4.9343	4.9320	4.9320	4.9320	4.9320
	48	4.9349	4.9330	4.9330	4.9320	4.9320
	Min	4.9343	4.9310	4.9310	4.9310	4.9310
	Max	4.9352	4.9340	4.9340	4.9340	4.9330
	Average	4.9348	4.9325	4.9325	4.9323	4.9320

Limit: >4.9V



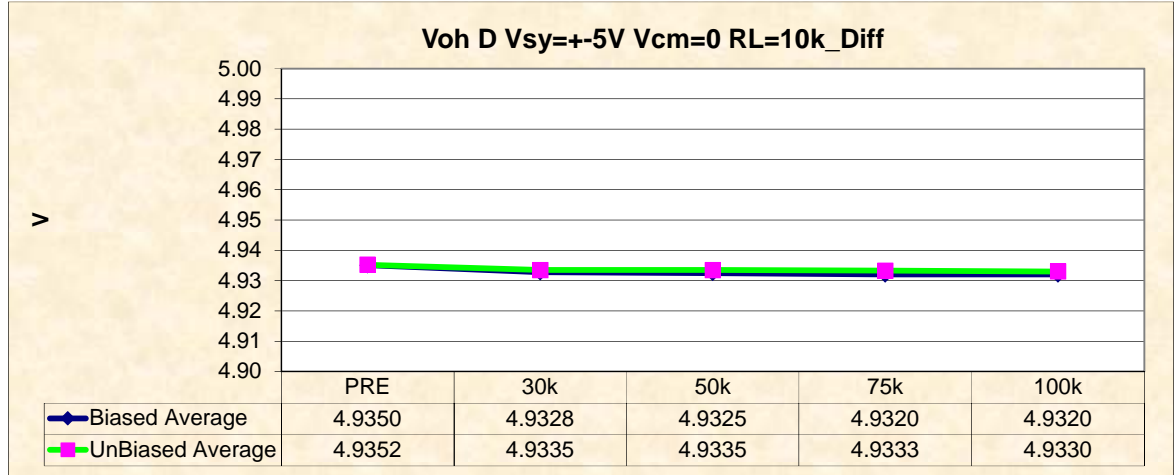
Voh C Vsy=+-5V Vcm=0 RL=10k_Diff						
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	4.9350	0.0000	0.0000	4.9330	4.9330
	54	4.9330	0.0000	0.0000	4.9320	4.9320
Biased	8	4.9352	4.9320	4.9320	4.9320	4.9320
	9	4.9358	4.9330	4.9330	4.9330	4.9320
	55	4.9349	4.9320	4.9320	4.9310	4.9310
	56	4.9353	4.9330	4.9320	4.9320	4.9320
	Min	4.9349	4.9320	4.9320	4.9310	4.9310
	Max	4.9358	4.9330	4.9330	4.9330	4.9320
	Average	4.9353	4.9325	4.9323	4.9320	4.9318
UnBiased	10	4.9348	4.9320	4.9320	4.9310	4.9350
	11	4.9364	4.9350	4.9350	4.9350	4.9310
	57	4.9343	4.9320	4.9320	4.9320	4.9320
	58	4.9355	4.9330	4.9330	4.9330	4.9330
	Min	4.9343	4.9320	4.9320	4.9310	4.9310
	Max	4.9364	4.9350	4.9350	4.9350	4.9350
	Average	4.9353	4.9330	4.9330	4.9328	4.9328

Limit: >4.9V



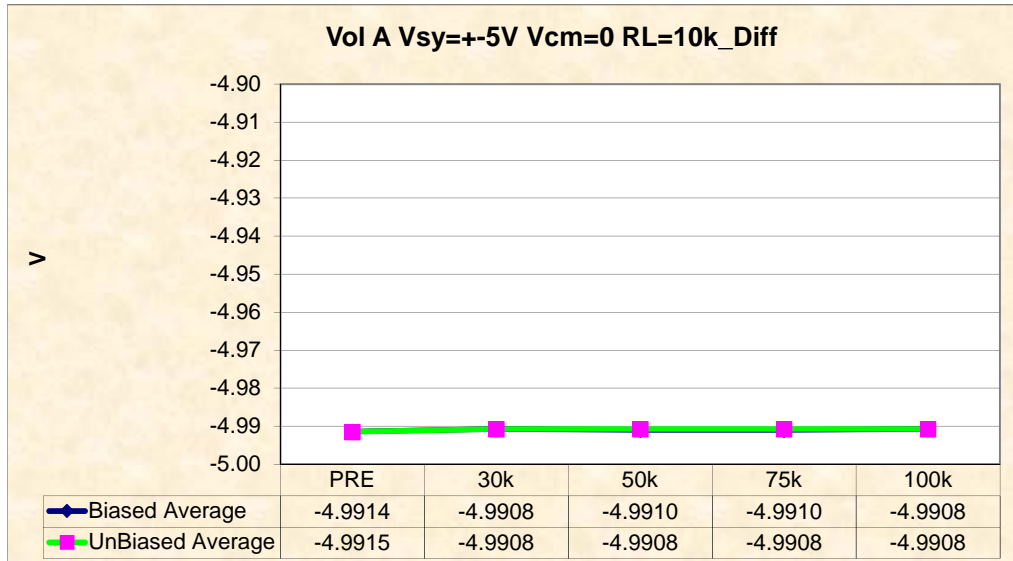
		Voh D Vsy=+5V Vcm=0 RL=10k_Diff				
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	4.9350	0.0000	0.0000	4.9330	4.9330
	54	4.9320	0.0000	0.0000	4.9320	4.9310
Biased	8	4.9342	4.9320	4.9320	4.9310	4.9310
	9	4.9352	4.9330	4.9330	4.9320	4.9320
	55	4.9350	4.9330	4.9320	4.9320	4.9320
	56	4.9356	4.9330	4.9330	4.9330	4.9330
	Min	4.9342	4.9320	4.9320	4.9310	4.9310
	Max	4.9356	4.9330	4.9330	4.9330	4.9330
	Average	4.9350	4.9328	4.9325	4.9320	4.9320
UnBiased	10	4.9357	4.9330	4.9330	4.9330	4.9340
	11	4.9358	4.9350	4.9350	4.9350	4.9330
	57	4.9345	4.9330	4.9330	4.9320	4.9320
	58	4.9348	4.9330	4.9330	4.9330	4.9330
	Min	4.9345	4.9330	4.9330	4.9320	4.9320
	Max	4.9358	4.9350	4.9350	4.9350	4.9340
	Average	4.9352	4.9335	4.9335	4.9333	4.9330

Limit: >4.9V



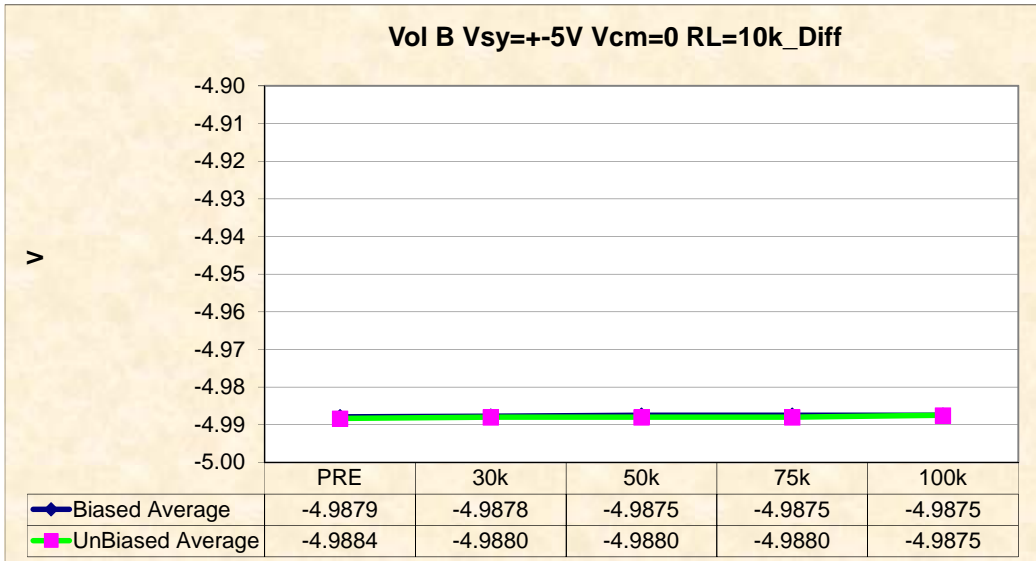
Vol A Vsy=+-5V Vcm=0 RL=10k_Diff						
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	-4.9890	0.0000	0.0000	-4.9920	-4.9920
	54	-4.9880	0.0000	0.0000	-4.9910	-4.9910
Biased	14	-4.9906	-4.9900	-4.9910	-4.9910	-4.9900
	15	-4.9919	-4.9910	-4.9910	-4.9910	-4.9910
	42	-4.9911	-4.9910	-4.9910	-4.9910	-4.9910
	43	-4.9921	-4.9910	-4.9910	-4.9910	-4.9910
	Min	-4.9921	-4.9910	-4.9910	-4.9910	-4.9910
	Max	-4.9906	-4.9900	-4.9910	-4.9910	-4.9900
	Average	-4.9914	-4.9908	-4.9910	-4.9910	-4.9908
UnBiased	23	-4.9918	-4.9910	-4.9910	-4.9910	-4.9900
	24	-4.9904	-4.9900	-4.9900	-4.9900	-4.9910
	47	-4.9916	-4.9910	-4.9910	-4.9910	-4.9910
	48	-4.9922	-4.9910	-4.9910	-4.9910	-4.9910
	Min	-4.9922	-4.9910	-4.9910	-4.9910	-4.9910
	Max	-4.9904	-4.9900	-4.9900	-4.9900	-4.9900
	Average	-4.9915	-4.9908	-4.9908	-4.9908	-4.9908

Limit: <-4.9V



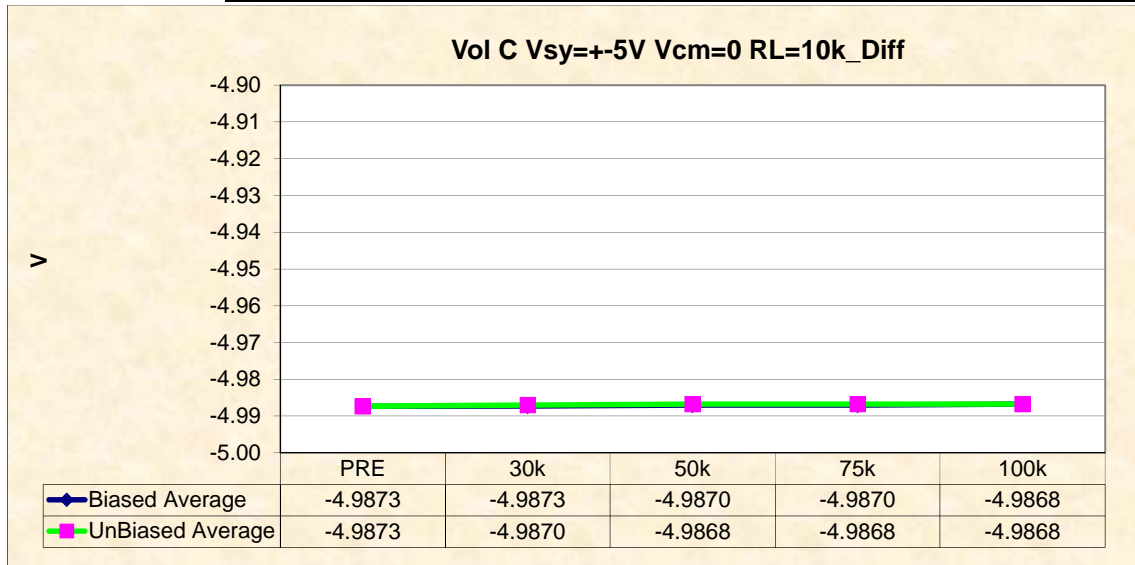
Vol B Vsy=+-5V Vcm=0 RL=10k_Diff						
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	-4.9870	0.0000	0.0000	-4.9880	-4.9880
	54	-4.9870	0.0000	0.0000	-4.9880	-4.9880
Biased	8	-4.9872	-4.9870	-4.9870	-4.9870	-4.9870
	9	-4.9885	-4.9880	-4.9880	-4.9880	-4.9880
	55	-4.9873	-4.9880	-4.9870	-4.9870	-4.9870
	56	-4.9887	-4.9880	-4.9880	-4.9880	-4.9880
	Min	-4.9887	-4.9880	-4.9880	-4.9880	-4.9880
	Max	-4.9872	-4.9870	-4.9870	-4.9870	-4.9870
	Average	-4.9879	-4.9878	-4.9875	-4.9875	-4.9875
UnBiased	10	-4.9882	-4.9880	-4.9880	-4.9880	-4.9880
	11	-4.9886	-4.9880	-4.9880	-4.9880	-4.9870
	57	-4.9879	-4.9880	-4.9880	-4.9880	-4.9870
	58	-4.9887	-4.9880	-4.9880	-4.9880	-4.9880
	Min	-4.9887	-4.9880	-4.9880	-4.9880	-4.9880
	Max	-4.9879	-4.9880	-4.9880	-4.9880	-4.9870
	Average	-4.9884	-4.9880	-4.9880	-4.9880	-4.9875

Limit: <-4.9V



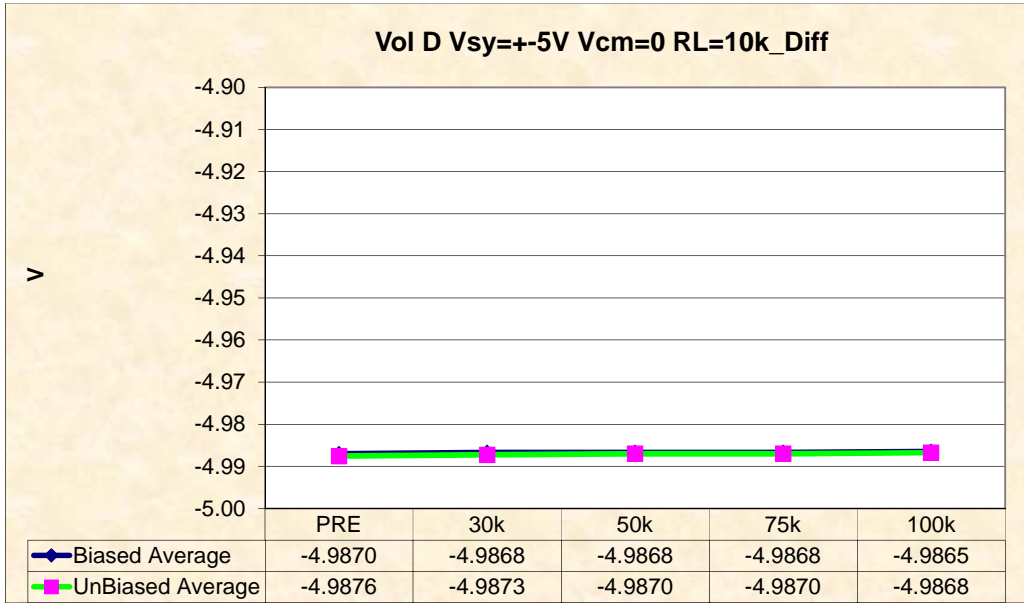
		Vol C Vsy=+-5V Vcm=0 RL=10k_Diff				
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	-4.9870	0.0000	0.0000	-4.9880	-4.9880
	54	-4.9870	0.0000	0.0000	-4.9870	-4.9870
Biased	8	-4.9867	-4.9870	-4.9870	-4.9870	-4.9860
	9	-4.9879	-4.9870	-4.9870	-4.9870	-4.9870
	55	-4.9867	-4.9870	-4.9870	-4.9870	-4.9870
	56	-4.9881	-4.9880	-4.9870	-4.9870	-4.9870
	Min	-4.9881	-4.9880	-4.9870	-4.9870	-4.9870
	Max	-4.9867	-4.9870	-4.9870	-4.9870	-4.9860
	Average	-4.9873	-4.9873	-4.9870	-4.9870	-4.9868
UnBiased	10	-4.9877	-4.9870	-4.9870	-4.9870	-4.9860
	11	-4.9861	-4.9860	-4.9860	-4.9860	-4.9870
	57	-4.9874	-4.9870	-4.9870	-4.9870	-4.9870
	58	-4.9881	-4.9880	-4.9870	-4.9870	-4.9870
	Min	-4.9881	-4.9880	-4.9870	-4.9870	-4.9870
	Max	-4.9861	-4.9860	-4.9860	-4.9860	-4.9860
	Average	-4.9873	-4.9870	-4.9868	-4.9868	-4.9868

Limit: <-4.9V



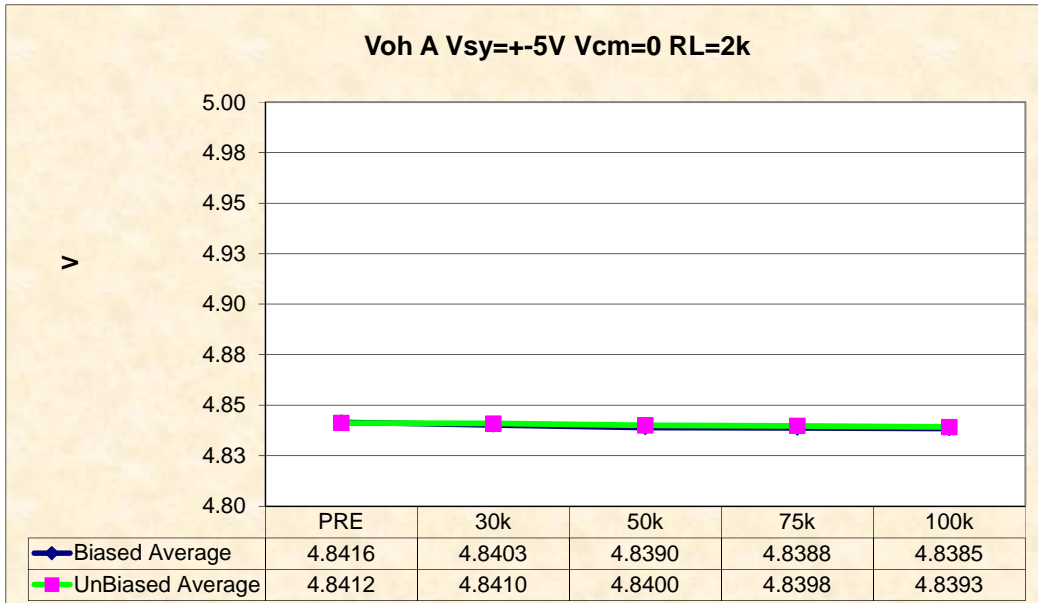
Vol D Vsy=+-5V Vcm=0 RL=10k_Diff						
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	-4.9870	0.0000	0.0000	-4.9870	-4.9870
	54	-4.9870	0.0000	0.0000	-4.9870	-4.9870
Biased	14	-4.9862	-4.9860	-4.9860	-4.9860	-4.9860
	15	-4.9876	-4.9870	-4.9870	-4.9870	-4.9870
	42	-4.9865	-4.9870	-4.9870	-4.9870	-4.9860
	43	-4.9878	-4.9870	-4.9870	-4.9870	-4.9870
	Min	-4.9878	-4.9870	-4.9870	-4.9870	-4.9870
	Max	-4.9862	-4.9860	-4.9860	-4.9860	-4.9860
	Average	-4.9870	-4.9868	-4.9868	-4.9868	-4.9865
UnBiased	23	-4.9874	-4.9870	-4.9870	-4.9870	-4.9870
	24	-4.9877	-4.9880	-4.9870	-4.9870	-4.9860
	47	-4.9873	-4.9870	-4.9870	-4.9870	-4.9870
	48	-4.9878	-4.9870	-4.9870	-4.9870	-4.9870
	Min	-4.9878	-4.9880	-4.9870	-4.9870	-4.9870
	Max	-4.9873	-4.9870	-4.9870	-4.9870	-4.9860
	Average	-4.9876	-4.9873	-4.9870	-4.9870	-4.9868

Limit: <-4.9V



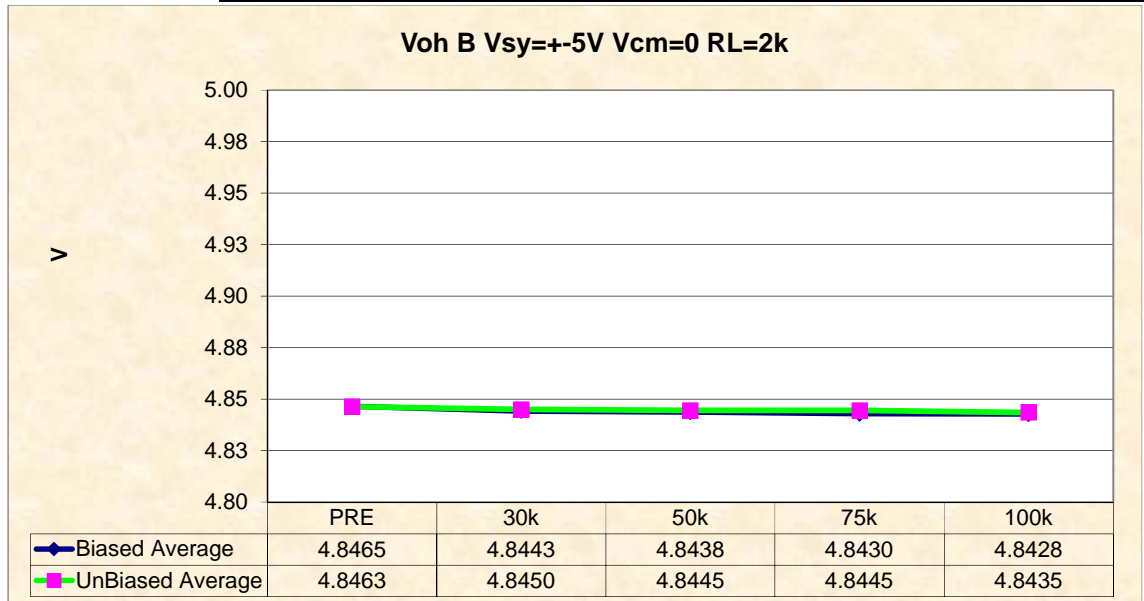
		Voh A Vsy=+-5V Vcm=0 RL=2k				
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	4.8450	0.0000	0.0000	4.8410	4.8410
	54	4.8410	0.0000	0.0000	4.8390	4.8390
Biased	8	4.8409	4.8390	4.8380	4.8380	4.8380
	9	4.8419	4.8410	4.8390	4.8390	4.8390
	55	4.8426	4.8410	4.8400	4.8400	4.8390
	56	4.8409	4.8400	4.8390	4.8380	4.8380
	Min	4.8409	4.8390	4.8380	4.8380	4.8380
	Max	4.8426	4.8410	4.8400	4.8400	4.8390
	Average	4.8416	4.8403	4.8390	4.8388	4.8385
UnBiased	10	4.8408	4.8400	4.8390	4.8390	4.8410
	11	4.8419	4.8430	4.8420	4.8410	4.8380
	57	4.8404	4.8400	4.8390	4.8390	4.8380
	58	4.8417	4.8410	4.8400	4.8400	4.8400
	Min	4.8404	4.8400	4.8390	4.8390	4.8380
	Max	4.8419	4.8430	4.8420	4.8410	4.8410
	Average	4.8412	4.8410	4.8400	4.8398	4.8393

Limit: >4.8V



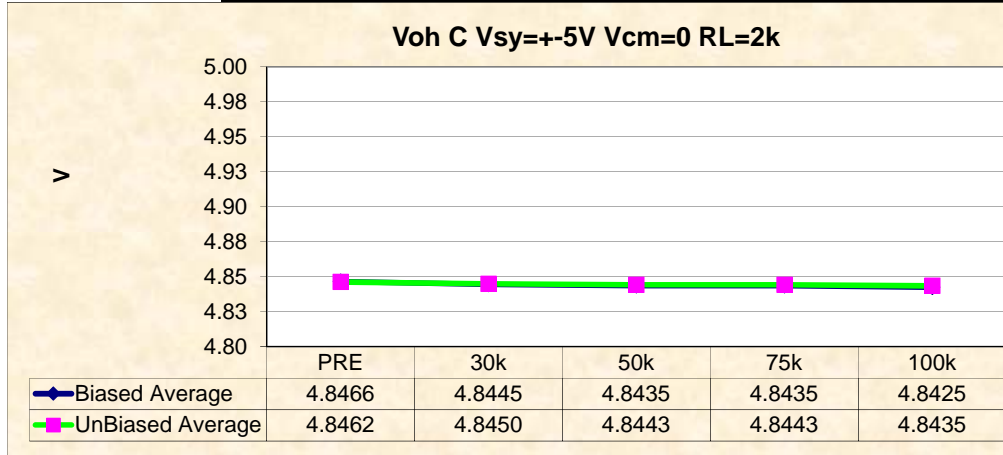
		Voh B Vsy=+-5V Vcm=0 RL=2k				
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	4.8470	0.0000	0.0000	4.8460	4.8450
	54	4.8440	0.0000	0.0000	4.8440	4.8430
Biased	8	4.8456	4.8430	4.8430	4.8420	4.8420
	9	4.8465	4.8440	4.8430	4.8430	4.8420
	55	4.8465	4.8450	4.8440	4.8430	4.8430
	56	4.8474	4.8450	4.8450	4.8440	4.8440
	Min	4.8456	4.8430	4.8430	4.8420	4.8420
	Max	4.8474	4.8450	4.8450	4.8440	4.8440
	Average	4.8465	4.8443	4.8438	4.8430	4.8428
UnBiased	10	4.8461	4.8440	4.8430	4.8430	4.8450
	11	4.8468	4.8470	4.8460	4.8460	4.8420
	57	4.8457	4.8440	4.8440	4.8440	4.8430
	58	4.8467	4.8450	4.8450	4.8450	4.8440
	Min	4.8457	4.8440	4.8430	4.8430	4.8420
	Max	4.8468	4.8470	4.8460	4.8460	4.8450
	Average	4.8463	4.8450	4.8445	4.8445	4.8435

Limit: >4.8V



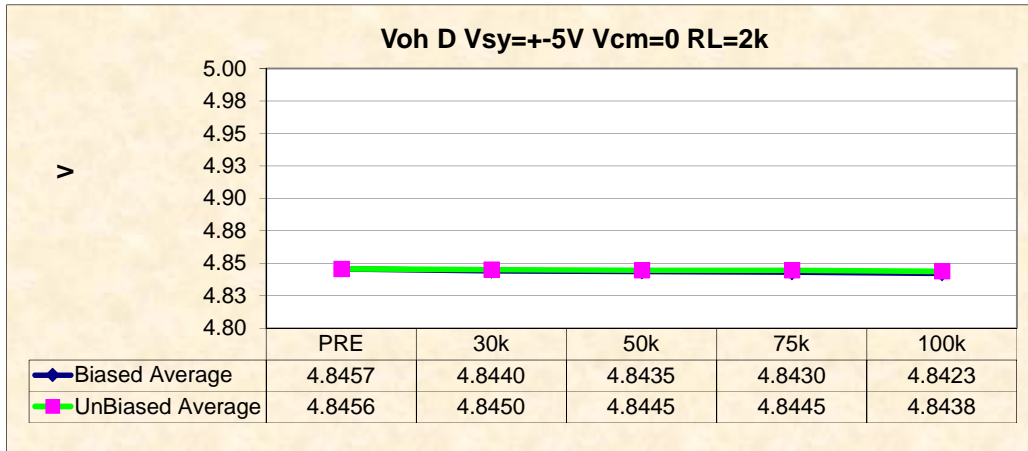
		Voh C Vsy=+5V Vcm=0 RL=2k				
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	4.8470	0.0000	0.0000	4.8460	4.8450
	54	4.8440	0.0000	0.0000	4.8440	4.8430
Biased	14	4.8462	4.8440	4.8430	4.8430	4.8420
	15	4.8471	4.8450	4.8440	4.8440	4.8430
	42	4.8464	4.8440	4.8430	4.8430	4.8420
	43	4.8466	4.8450	4.8440	4.8440	4.8430
	Min	4.8462	4.8440	4.8430	4.8430	4.8420
	Max	4.8471	4.8450	4.8440	4.8440	4.8430
	Average	4.8466	4.8445	4.8435	4.8435	4.8425
UnBiased	23	4.8453	4.8430	4.8420	4.8420	4.8460
	24	4.8476	4.8480	4.8470	4.8470	4.8420
	47	4.8451	4.8440	4.8430	4.8430	4.8420
	48	4.8469	4.8450	4.8450	4.8450	4.8440
	Min	4.8451	4.8430	4.8420	4.8420	4.8420
	Max	4.8476	4.8480	4.8470	4.8470	4.8460
	Average	4.8462	4.8450	4.8443	4.8443	4.8435

Limit: >4.8V



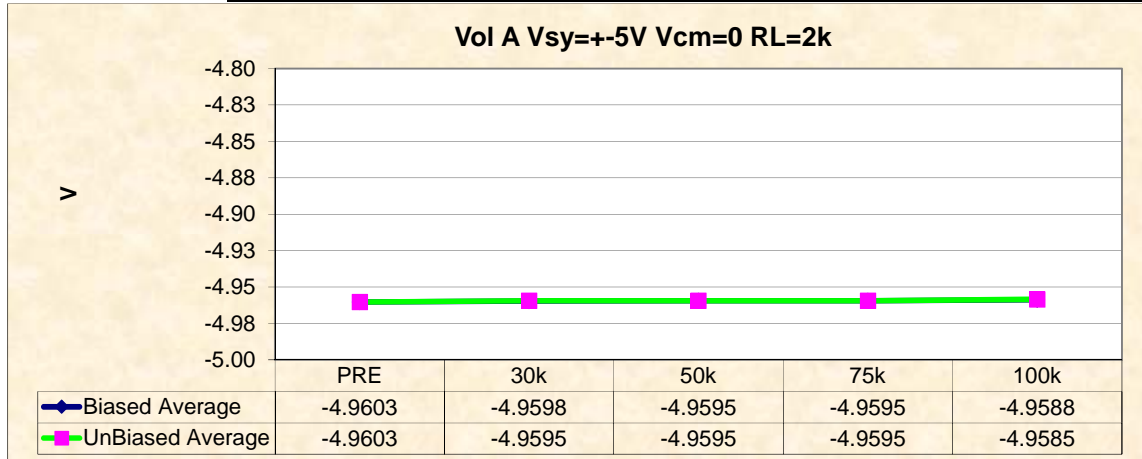
		Voh D Vsy=+-5V Vcm=0 RL=2k				
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	4.8470	0.0000	0.0000	4.8440	4.8440
	54	4.8420	0.0000	0.0000	4.8420	4.8420
Biased	8	4.8442	4.8420	4.8420	4.8410	4.8410
	9	4.8458	4.8440	4.8430	4.8430	4.8420
	55	4.8461	4.8450	4.8440	4.8440	4.8430
	56	4.8467	4.8450	4.8450	4.8440	4.8430
	Min	4.8442	4.8420	4.8420	4.8410	4.8410
	Max	4.8467	4.8450	4.8450	4.8440	4.8430
	Average	4.8457	4.8440	4.8435	4.8430	4.8423
UnBiased	10	4.8462	4.8450	4.8440	4.8440	4.8450
	11	4.8461	4.8470	4.8460	4.8460	4.8440
	57	4.8449	4.8440	4.8440	4.8440	4.8430
	58	4.8453	4.8440	4.8440	4.8440	4.8430
	Min	4.8449	4.8440	4.8440	4.8440	4.8430
	Max	4.8462	4.8470	4.8460	4.8460	4.8450
	Average	4.8456	4.8450	4.8445	4.8445	4.8438

Limit: >4.8V



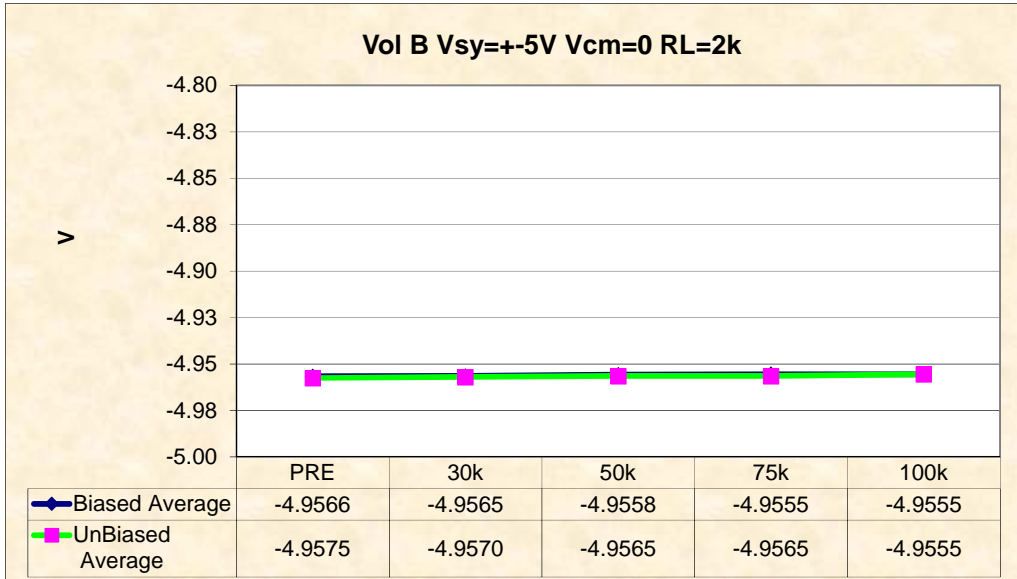
		Vol A Vsy=+5V Vcm=0 RL=2k				
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	-4.9590	0.0000	0.0000	-4.9610	-4.9600
	54	-4.9570	0.0000	0.0000	-4.9610	-4.9600
Biased	8	-4.9585	-4.9590	-4.9590	-4.9590	-4.9580
	9	-4.9615	-4.9600	-4.9600	-4.9600	-4.9590
	55	-4.9595	-4.9600	-4.9590	-4.9590	-4.9590
	56	-4.9617	-4.9600	-4.9600	-4.9600	-4.9590
	Min	-4.9617	-4.9600	-4.9600	-4.9600	-4.9590
	Max	-4.9585	-4.9590	-4.9590	-4.9590	-4.9580
	Average	-4.9603	-4.9598	-4.9595	-4.9595	-4.9588
UnBiased	10	-4.9610	-4.9600	-4.9600	-4.9600	-4.9570
	11	-4.9578	-4.9580	-4.9580	-4.9580	-4.9590
	57	-4.9606	-4.9600	-4.9600	-4.9600	-4.9590
	58	-4.9618	-4.9600	-4.9600	-4.9600	-4.9590
	Min	-4.9618	-4.9600	-4.9600	-4.9600	-4.9590
	Max	-4.9578	-4.9580	-4.9580	-4.9580	-4.9570
	Average	-4.9603	-4.9595	-4.9595	-4.9595	-4.9585

Limit: <-4.8V



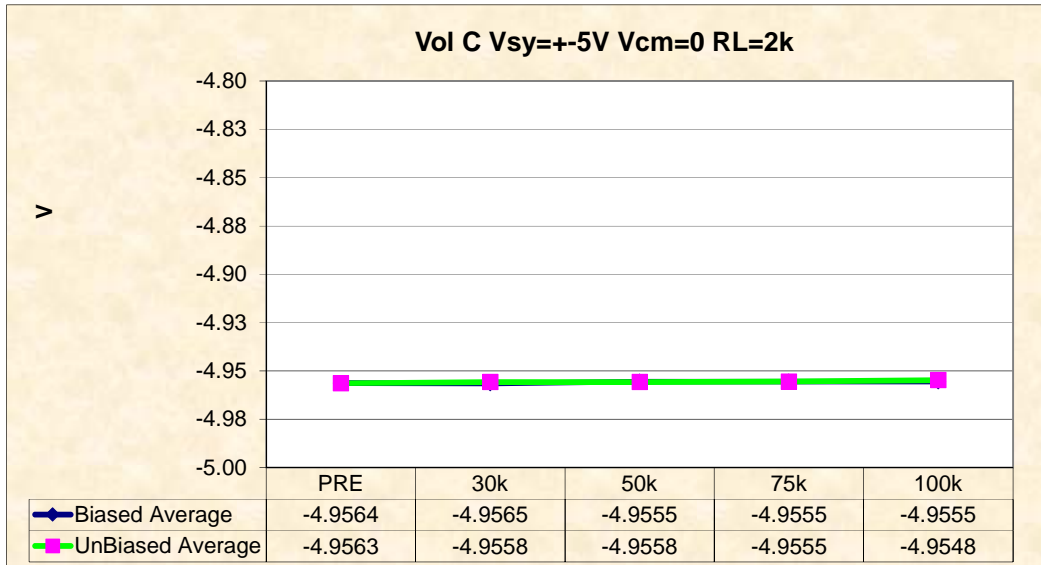
		Vol B Vsy=+-5V Vcm=0 RL=2k				
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	-4.9570	0.0000	0.0000	-4.9570	-4.9570
	54	-4.9560	0.0000	0.0000	-4.9570	-4.9570
Biased	14	-4.9551	-4.9560	-4.9550	-4.9550	-4.9550
	15	-4.9580	-4.9570	-4.9560	-4.9560	-4.9560
	42	-4.9551	-4.9560	-4.9550	-4.9550	-4.9550
	43	-4.9582	-4.9570	-4.9570	-4.9560	-4.9560
	Min	-4.9582	-4.9570	-4.9570	-4.9560	-4.9560
	Max	-4.9551	-4.9560	-4.9550	-4.9550	-4.9550
	Average	-4.9566	-4.9565	-4.9558	-4.9555	-4.9555
UnBiased	23	-4.9572	-4.9560	-4.9560	-4.9560	-4.9560
	24	-4.9580	-4.9580	-4.9570	-4.9570	-4.9550
	47	-4.9564	-4.9570	-4.9560	-4.9560	-4.9550
	48	-4.9583	-4.9570	-4.9570	-4.9570	-4.9560
	Min	-4.9583	-4.9580	-4.9570	-4.9570	-4.9560
	Max	-4.9564	-4.9560	-4.9560	-4.9560	-4.9550
	Average	-4.9575	-4.9570	-4.9565	-4.9565	-4.9555

Limit: <-4.8V



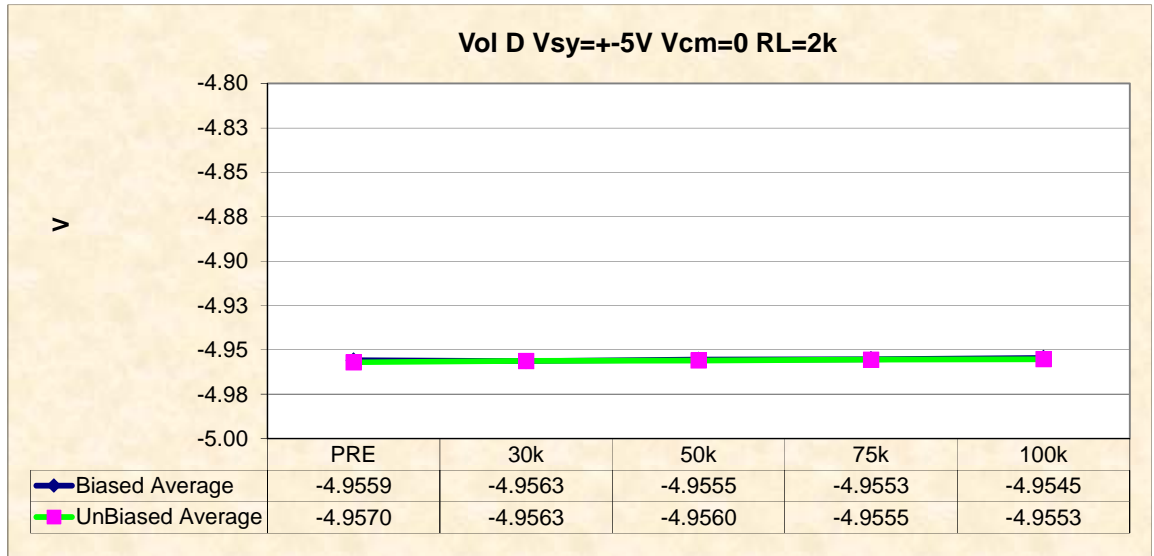
		Vol C Vsy=+-5V Vcm=0 RL=2k				
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	-4.9570	0.0000	0.0000	-4.9570	-4.9570
	54	-4.9560	0.0000	0.0000	-4.9570	-4.9560
Biased	8	-4.9549	-4.9560	-4.9550	-4.9550	-4.9550
	9	-4.9578	-4.9570	-4.9560	-4.9560	-4.9560
	55	-4.9550	-4.9560	-4.9550	-4.9550	-4.9550
	56	-4.9579	-4.9570	-4.9560	-4.9560	-4.9560
	Min	-4.9579	-4.9570	-4.9560	-4.9560	-4.9560
	Max	-4.9549	-4.9560	-4.9550	-4.9550	-4.9550
	Average	-4.9564	-4.9565	-4.9555	-4.9555	-4.9555
UnBiased	10	-4.9572	-4.9560	-4.9560	-4.9560	-4.9530
	11	-4.9535	-4.9540	-4.9540	-4.9540	-4.9550
	57	-4.9566	-4.9560	-4.9560	-4.9560	-4.9550
	58	-4.9581	-4.9570	-4.9570	-4.9560	-4.9560
	Min	-4.9581	-4.9570	-4.9570	-4.9560	-4.9560
	Max	-4.9535	-4.9540	-4.9540	-4.9540	-4.9530
	Average	-4.9563	-4.9558	-4.9558	-4.9555	-4.9548

Limit: <-4.8V



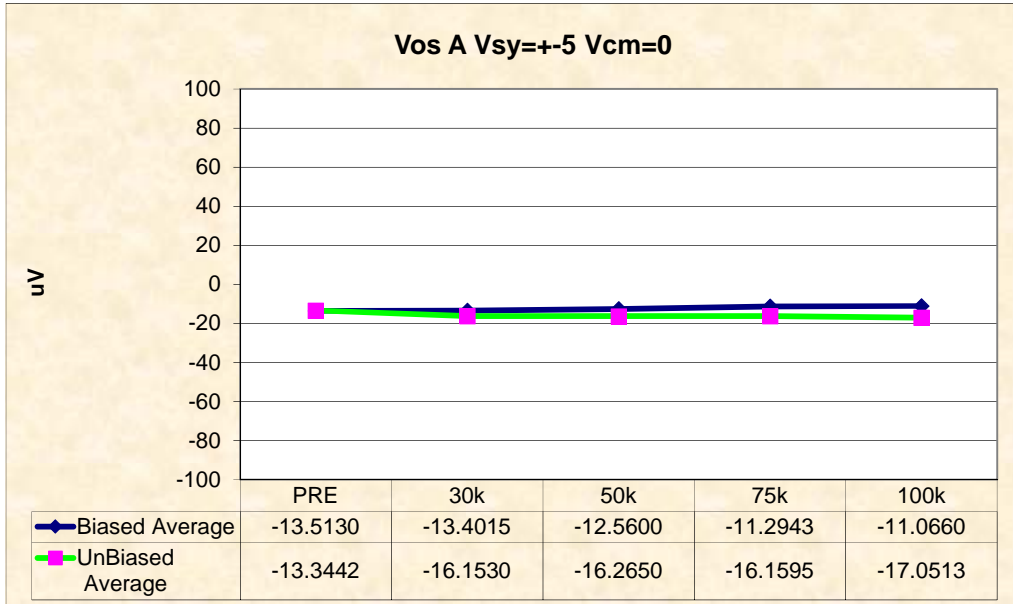
		Vol D Vsy=+-5V Vcm=0 RL=2k				
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	-4.9570	0.0000	0.0000	-4.9570	-4.9560
	54	-4.9560	0.0000	0.0000	-4.9560	-4.9560
Biased	8	-4.9542	-4.9550	-4.9550	-4.9540	-4.9540
	9	-4.9573	-4.9570	-4.9560	-4.9560	-4.9550
	55	-4.9547	-4.9560	-4.9550	-4.9550	-4.9540
	56	-4.9575	-4.9570	-4.9560	-4.9560	-4.9550
	Min	-4.9575	-4.9570	-4.9560	-4.9560	-4.9550
	Max	-4.9542	-4.9550	-4.9550	-4.9540	-4.9540
	Average	-4.9559	-4.9563	-4.9555	-4.9553	-4.9545
UnBiased	10	-4.9568	-4.9560	-4.9550	-4.9550	-4.9560
	11	-4.9573	-4.9570	-4.9570	-4.9560	-4.9550
	57	-4.9564	-4.9560	-4.9560	-4.9550	-4.9550
	58	-4.9576	-4.9560	-4.9560	-4.9560	-4.9550
	Min	-4.9576	-4.9570	-4.9570	-4.9560	-4.9560
	Max	-4.9564	-4.9560	-4.9550	-4.9550	-4.9550
	Average	-4.9570	-4.9563	-4.9560	-4.9555	-4.9553

Limit: <-4.8V



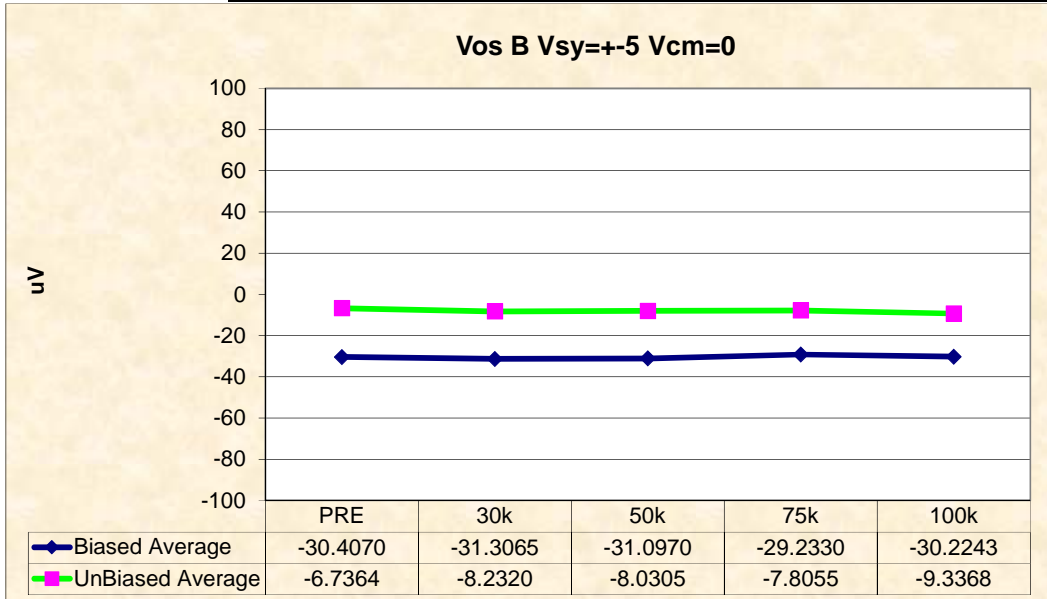
		Vos A Vsy=+5 Vcm=0				
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	-18.2030	0.0000	0.0000	-28.8380	-29.2650
	54	-19.2520	0.0000	0.0000	-20.3360	-20.4460
Biased	14	-0.4078	1.5640	3.1240	3.9910	4.6340
	15	-1.6320	-0.2340	0.0280	1.2770	1.8600
	42	-39.6687	-40.6750	-39.6350	-38.2470	-38.7510
	43	-12.3435	-14.2610	-13.7570	-12.1980	-12.0070
	Min	-39.6687	-40.6750	-39.6350	-38.2470	-38.7510
	Max	-0.4078	1.5640	3.1240	3.9910	4.6340
	Average	-13.5130	-13.4015	-12.5600	-11.2943	-11.0660
UnBiased	23	6.2980	3.9780	4.4530	4.8770	-30.0270
	24	-26.8548	-30.0400	-29.6710	-29.1530	3.6020
	47	-25.7572	-26.6590	-27.6310	-27.7880	-28.4180
	48	-7.0627	-11.8910	-12.2110	-12.5740	-13.3620
	Min	-26.8548	-30.0400	-29.6710	-29.1530	-30.0270
	Max	6.2980	3.9780	4.4530	4.8770	3.6020
	Average	-13.3442	-16.1530	-16.2650	-16.1595	-17.0513

Limit: +/-100uV



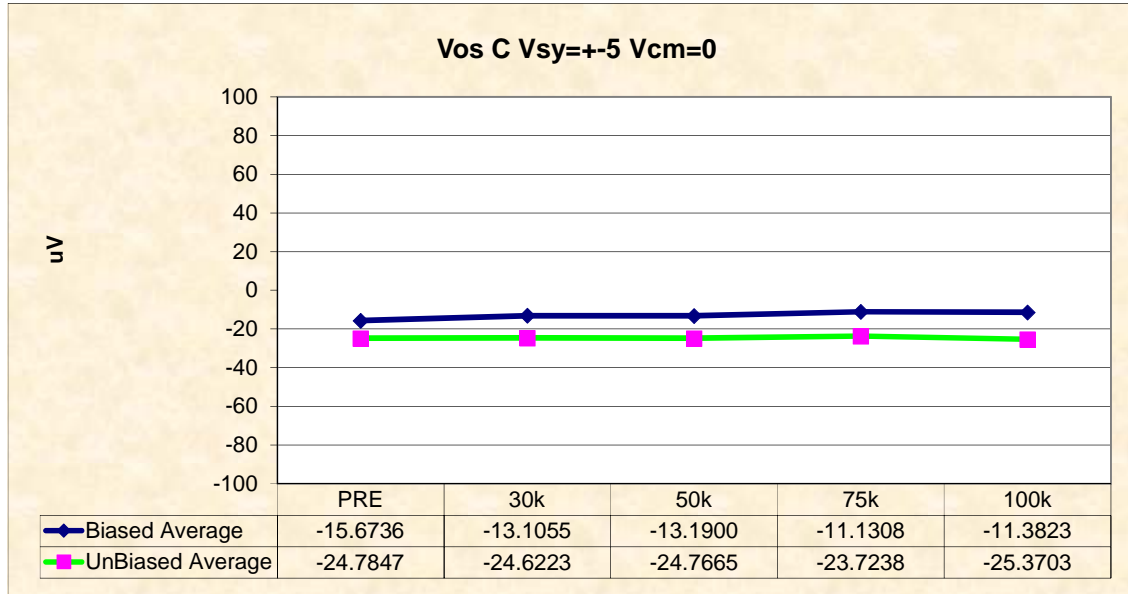
		Vos B Vsy=+-5 Vcm=0				
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	4.7840	0.0000	0.0000	-15.9350	-16.8720
	54	-21.1080	0.0000	0.0000	-21.8720	-23.2610
Biased	8	-41.8200	-42.3830	-41.0030	-39.9530	-40.7320
	9	-23.5500	-23.0750	-23.8490	-20.9610	-22.0990
	55	-33.7218	-34.6820	-34.7080	-32.5690	-33.4120
	56	-22.5363	-25.0860	-24.8280	-23.4490	-24.6540
	Min	-41.8200	-42.3830	-41.0030	-39.9530	-40.7320
	Max	-22.5363	-23.0750	-23.8490	-20.9610	-22.0990
	Average	-30.4070	-31.3065	-31.0970	-29.2330	-30.2243
UnBiased	10	-11.0520	-10.1540	-10.2210	-8.5140	-19.6500
	11	-17.9994	-16.5840	-16.4700	-17.1510	-10.4500
	57	-8.7002	-11.7660	-11.6200	-11.7950	-12.1250
	58	10.8059	5.5760	6.1890	6.2380	4.8780
	Min	-17.9994	-16.5840	-16.4700	-17.1510	-19.6500
	Max	10.8059	5.5760	6.1890	6.2380	4.8780
	Average	-6.7364	-8.2320	-8.0305	-7.8055	-9.3368

Limit: +/-100uV



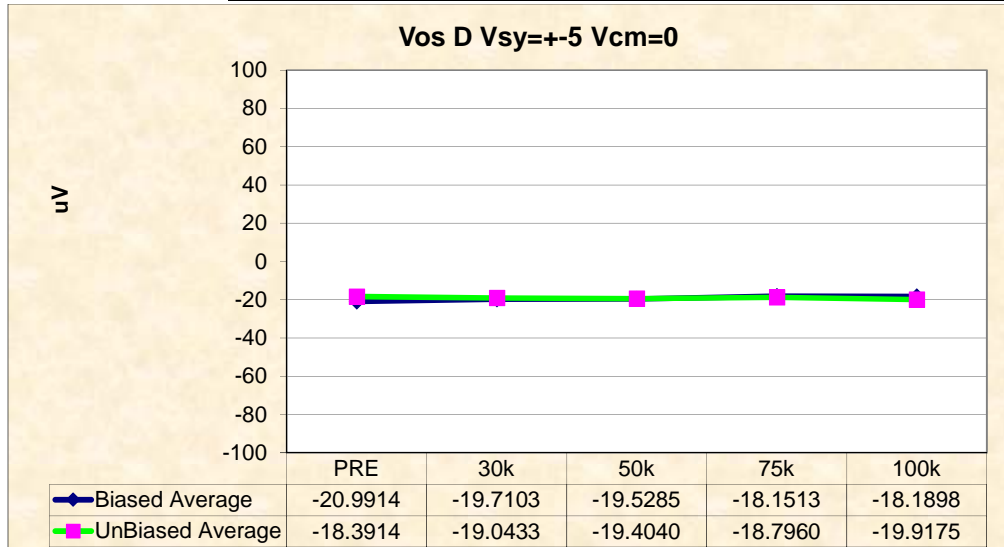
		Vos C Vsy=+-5 Vcm=0				
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	-51.1100	0.0000	0.0000	-34.2260	-34.9870
	54	-30.3440	0.0000	0.0000	-31.1590	-32.4050
Biased	8	-16.8030	-13.6740	-13.2060	-10.8370	-11.2700
	9	-8.2000	-4.9770	-5.3810	-3.4200	-3.8800
	55	-45.9625	-41.4300	-42.2750	-40.0000	-40.0430
	56	8.2712	7.6590	8.1020	9.7340	9.6640
	Min	-45.9625	-41.4300	-42.2750	-40.0000	-40.0430
	Max	8.2712	7.6590	8.1020	9.7340	9.6640
	Average	-15.6736	-13.1055	-13.1900	-11.1308	-11.3823
UnBiased	10	-31.2339	-31.7280	-32.7260	-30.5440	-45.6340
	11	-44.3593	-45.3970	-45.1320	-43.7960	-32.3720
	57	-6.0203	-2.5660	-2.6190	-2.0980	-3.3630
	58	-17.5254	-18.7980	-18.5890	-18.4570	-20.1120
	Min	-44.3593	-45.3970	-45.1320	-43.7960	-45.6340
	Max	-6.0203	-2.5660	-2.6190	-2.0980	-3.3630
	Average	-24.7847	-24.6223	-24.7665	-23.7238	-25.3703

Limit: +/-100uV



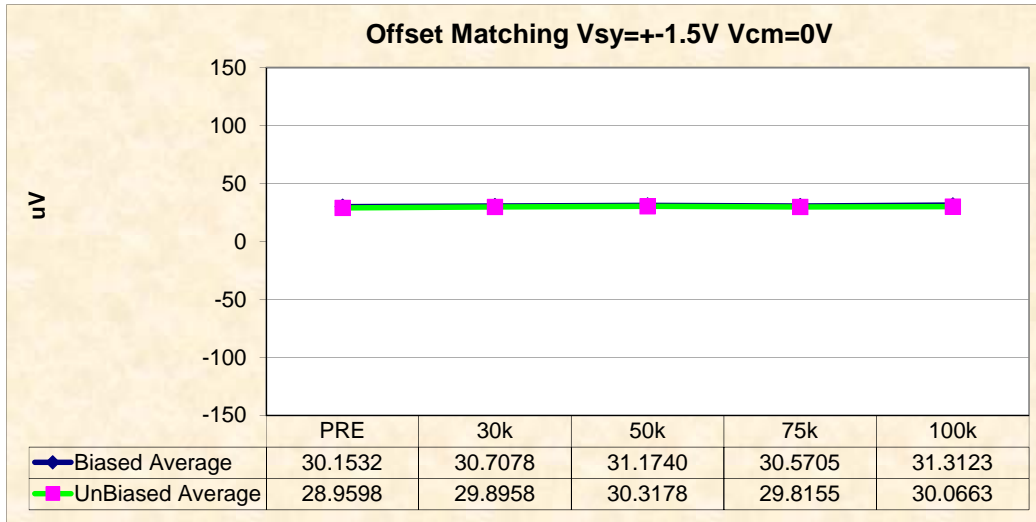
		Vos D Vsy=+5 Vcm=0				
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	-6.8910	0.0000	0.0000	-30.8430	-32.4630
	54	-1.7110	0.0000	0.0000	-2.3850	-3.8210
Biased	14	-4.9730	-4.2120	-3.3930	-1.5460	-2.4320
	15	-11.7060	-9.8680	-10.4140	-9.4100	-9.2100
	42	-32.1233	-28.9040	-28.9960	-27.6420	-27.4490
	43	-35.1634	-35.8570	-35.3110	-34.0070	-33.6680
	Min	-35.1634	-35.8570	-35.3110	-34.0070	-33.6680
	Max	-4.9730	-4.2120	-3.3930	-1.5460	-2.4320
	Average	-20.9914	-19.7103	-19.5285	-18.1513	-18.1898
UnBiased	23	-32.0696	-33.2480	-33.7250	-32.8140	-33.8510
	24	-35.2035	-33.3720	-33.3070	-32.4700	-34.7270
	47	-20.1420	-20.2080	-21.4150	-20.6790	-21.8770
	48	13.8495	10.6550	10.8310	10.7790	10.7850
	Min	-35.2035	-33.3720	-33.7250	-32.8140	-34.7270
	Max	13.8495	10.6550	10.8310	10.7790	10.7850
	Average	-18.3914	-19.0433	-19.4040	-18.7960	-19.9175

Limit: +/-100uV



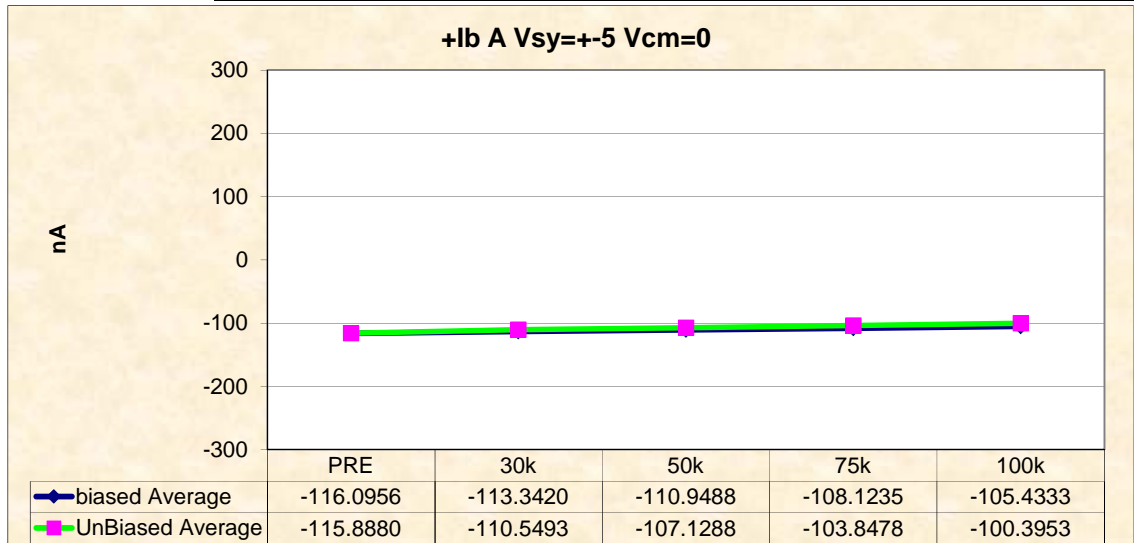
Offset Matching Vsy=+-1.5V Vcm=0V						
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	55.8940	0.0000	0.0000	18.2910	18.1150
	54	28.6330	0.0000	0.0000	28.7740	28.5840
Biased	8	41.4120	43.9470	44.1270	43.9440	45.3660
	9	21.9270	22.8420	23.8770	22.2390	23.9580
	55	13.8392	12.5260	13.2790	12.3580	12.5930
	56	43.4347	43.5160	43.4130	43.7410	43.3320
	Min	13.8392	12.5260	13.2790	12.3580	12.5930
	Max	43.4347	43.9470	44.1270	43.9440	45.3660
	Average	30.1532	30.7078	31.1740	30.5705	31.3123
UnBiased	10	38.3676	37.2260	38.1780	37.6910	25.9840
	11	26.3598	28.8120	28.6610	26.6450	38.3290
	57	19.7369	24.0930	25.0120	25.6900	25.0550
	58	31.3749	29.4520	29.4200	29.2360	30.8970
	Min	19.7369	24.0930	25.0120	25.6900	25.0550
	Max	38.3676	37.2260	38.1780	37.6910	38.3290
	Average	28.9598	29.8958	30.3178	29.8155	30.0663

Limit: <150uV



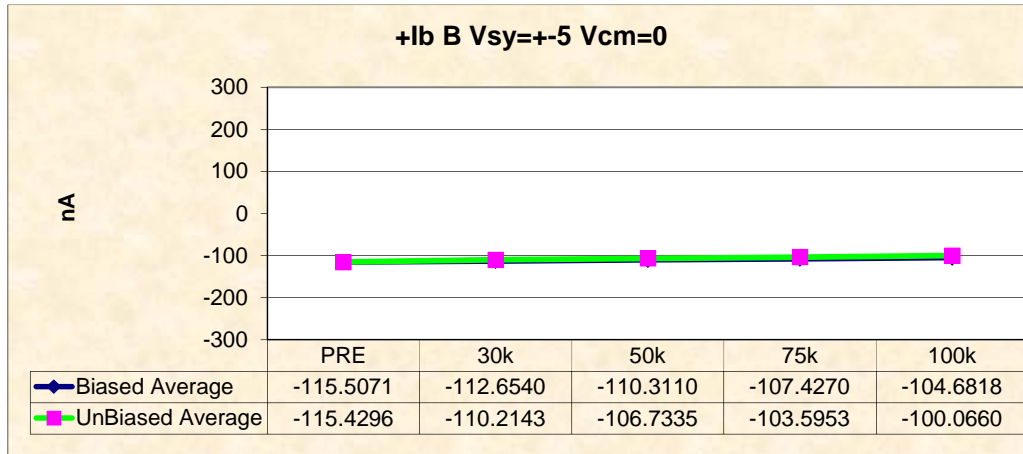
		+Ib A Vsy=+5 Vcm=0				
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	-103.6910	0.0000	0.0000	-100.2120	-99.6340
	54	-123.6440	0.0000	0.0000	-124.5730	-123.9910
Biased	8	-119.0470	-114.9820	-112.9090	-110.1080	-107.2660
	9	-115.6810	-112.8850	-110.0670	-106.9560	-104.5620
	55	-117.7753	-115.6930	-112.9760	-110.2840	-107.4040
	56	-111.8789	-109.8080	-107.8430	-105.1460	-102.5010
	Min	-119.0470	-115.6930	-112.9760	-110.2840	-107.4040
	Max	-111.8789	-109.8080	-107.8430	-105.1460	-102.5010
	Average	-116.0956	-113.3420	-110.9488	-108.1235	-105.4333
UnBiased	10	-117.7332	-107.6380	-103.8460	-100.2920	-97.3620
	11	-109.7291	-107.8720	-104.4780	-101.1610	-97.1280
	57	-122.2863	-118.0530	-114.2080	-111.0980	-107.8440
	58	-113.8035	-108.6340	-105.9830	-102.8400	-99.2470
	Min	-122.2863	-118.0530	-114.2080	-111.0980	-107.8440
	Max	-109.7291	-107.6380	-103.8460	-100.2920	-97.1280
	Average	-115.8880	-110.5493	-107.1288	-103.8478	-100.3953

Limit: +/-300nA



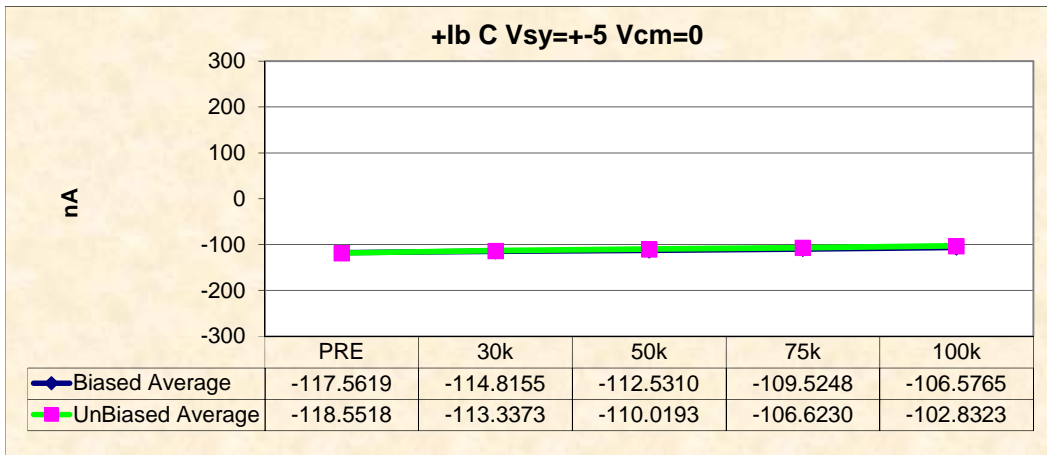
		+Ib B Vsy=+-5 Vcm=0				
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	-103.6140	0.0000	0.0000	-98.3280	-97.8810
	54	-122.7480	0.0000	0.0000	-123.8560	-123.2000
Biased	14	-119.6010	-115.4520	-113.7600	-110.4630	-107.9850
	15	-114.5350	-111.4690	-108.6830	-106.0260	-103.1390
	42	-115.8686	-113.9430	-111.1210	-108.1170	-105.3200
	43	-112.0239	-109.7520	-107.6800	-105.1020	-102.2830
	Min	-119.6010	-115.4520	-113.7600	-110.4630	-107.9850
	Max	-112.0239	-109.7520	-107.6800	-105.1020	-102.2830
	Average	-115.5071	-112.6540	-110.3110	-107.4270	-104.6818
UnBiased	23	-116.5393	-106.9430	-102.9180	-99.8420	-97.5050
	24	-109.8368	-107.5360	-104.2620	-101.3770	-96.3200
	47	-122.1385	-117.8410	-114.1390	-110.5950	-107.3250
	48	-113.2039	-108.5370	-105.6150	-102.5670	-99.1140
	Min	-122.1385	-117.8410	-114.1390	-110.5950	-107.3250
	Max	-109.8368	-106.9430	-102.9180	-99.8420	-96.3200
	Average	-115.4296	-110.2143	-106.7335	-103.5953	-100.0660

Limit: +/-300nA



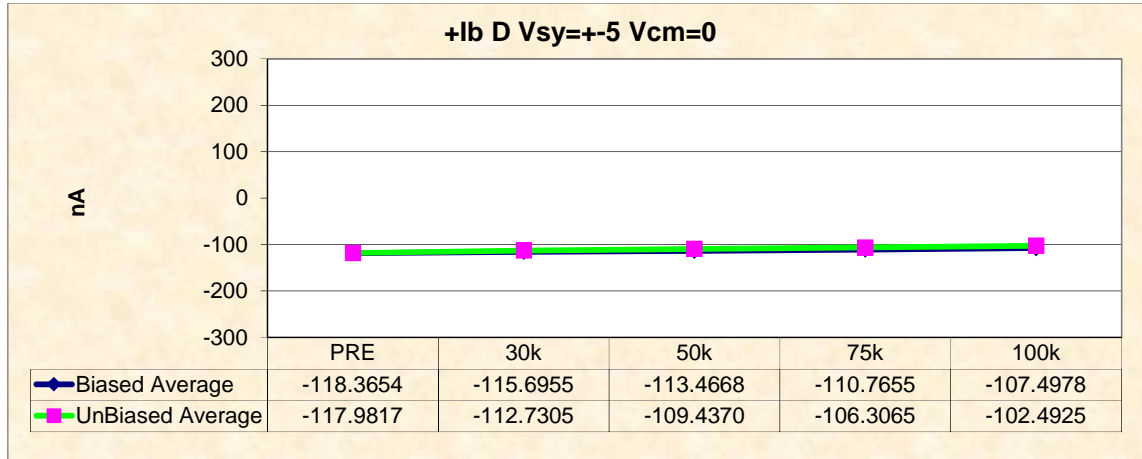
		+Ib C Vsy=+-5 Vcm=0				
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	-104.1700	0.0000	0.0000	-101.6900	-101.1590
	54	-125.8670	0.0000	0.0000	-126.5700	-126.0080
Biased	8	-120.9710	-116.9390	-115.0230	-111.9340	-108.6490
	9	-117.6670	-114.5390	-112.0200	-108.9020	-105.8460
	55	-118.7599	-116.5780	-114.0950	-111.0390	-108.6670
	56	-112.8496	-111.2060	-108.9860	-106.2240	-103.1440
	Min	-120.9710	-116.9390	-115.0230	-111.9340	-108.6670
	Max	-112.8496	-111.2060	-108.9860	-106.2240	-103.1440
	Average	-117.5619	-114.8155	-112.5310	-109.5248	-106.5765
UnBiased	10	-119.7868	-110.1700	-106.5900	-103.1400	-99.5000
	11	-112.7094	-110.6950	-107.1520	-103.9120	-99.7630
	57	-125.9735	-121.6950	-118.3290	-114.6230	-111.3060
	58	-115.7374	-110.7890	-108.0060	-104.8170	-100.7600
	Min	-125.9735	-121.6950	-118.3290	-114.6230	-111.3060
	Max	-112.7094	-110.1700	-106.5900	-103.1400	-99.5000
	Average	-118.5518	-113.3373	-110.0193	-106.6230	-102.8323

Limit: +/-300nA



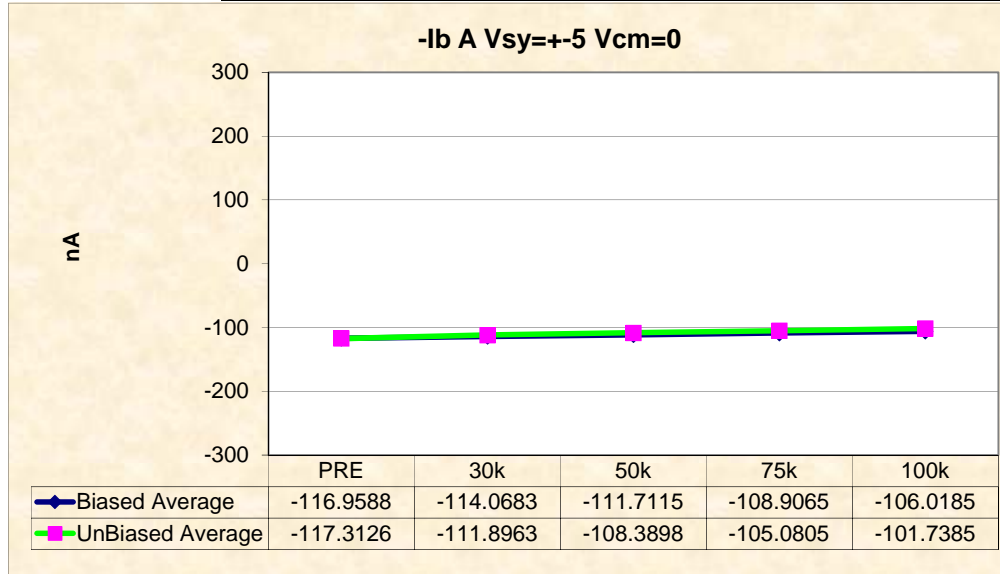
		+Ib D Vsy=+5 Vcm=0				
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	-104.0250	0.0000	0.0000	-104.0820	-103.4590
	54	-126.1650	0.0000	0.0000	-127.2400	-126.6550
Biased	8	-121.8430	-117.7990	-115.7910	-112.8030	-109.6070
	9	-117.5640	-114.7760	-112.3200	-109.4610	-105.9240
	55	-119.0681	-116.9690	-114.4380	-111.8990	-108.9410
	56	-114.9864	-113.2380	-111.3180	-108.8990	-105.5190
	Min	-121.8430	-117.7990	-115.7910	-112.8030	-109.6070
	Max	-114.9864	-113.2380	-111.3180	-108.8990	-105.5190
	Average	-118.3654	-115.6955	-113.4668	-110.7655	-107.4978
UnBiased	10	-119.0043	-109.2690	-105.4280	-102.4750	-100.5640
	11	-113.3747	-111.4240	-108.2300	-105.0850	-98.7290
	57	-126.4592	-122.1540	-118.5080	-115.3310	-111.9410
	58	-113.0887	-108.0750	-105.5820	-102.3350	-98.7360
	Min	-126.4592	-122.1540	-118.5080	-115.3310	-111.9410
	Max	-113.0887	-108.0750	-105.4280	-102.3350	-98.7290
	Average	-117.9817	-112.7305	-109.4370	-106.3065	-102.4925

Limit: +/-300nA



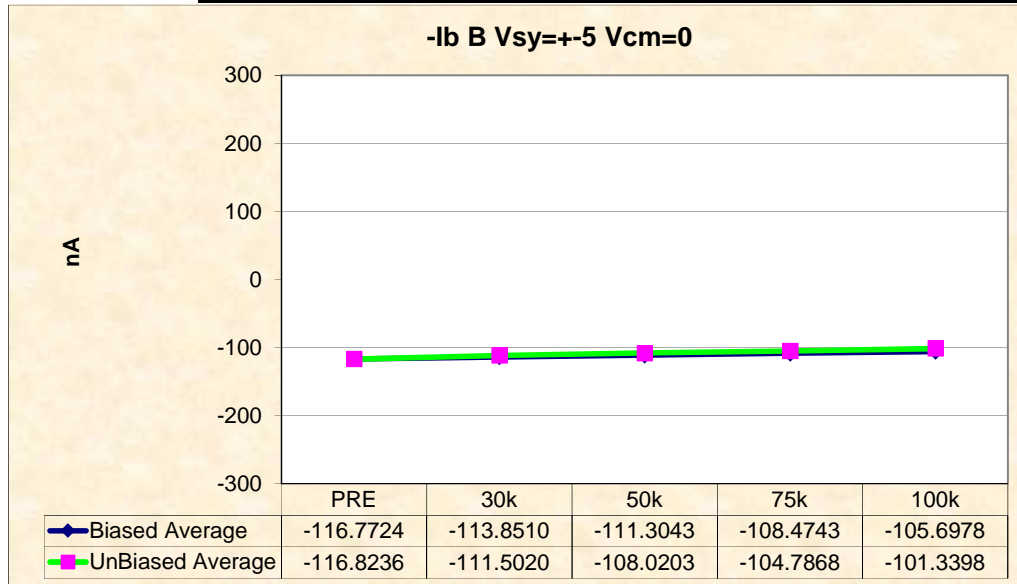
		-Ib A Vsy=+-5 Vcm=0				
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	-103.3990	0.0000	0.0000	-102.1680	-101.4120
	54	-125.2540	0.0000	0.0000	-126.0070	-125.4020
Biased	14	-120.0710	-116.1840	-114.1190	-111.2260	-108.1410
	15	-116.9730	-113.7370	-110.9370	-108.1640	-105.4400
	42	-119.0049	-116.4400	-113.9410	-111.2190	-108.2510
	43	-111.7864	-109.9120	-107.8490	-105.0170	-102.2420
	Min	-120.0710	-116.4400	-114.1190	-111.2260	-108.2510
	Max	-111.7864	-109.9120	-107.8490	-105.0170	-102.2420
	Average	-116.9588	-114.0683	-111.7115	-108.9065	-106.0185
UnBiased	23	-119.1275	-109.4350	-105.3490	-102.2090	-97.9370
	24	-110.9885	-108.5350	-104.9380	-101.7170	-99.1250
	47	-123.4754	-119.0930	-115.7340	-112.3560	-108.8170
	48	-115.6590	-110.5220	-107.5380	-104.0400	-101.0750
	Min	-123.4754	-119.0930	-115.7340	-112.3560	-108.8170
	Max	-110.9885	-108.5350	-104.9380	-101.7170	-97.9370
	Average	-117.3126	-111.8963	-108.3898	-105.0805	-101.7385

Limit: +/-300nA



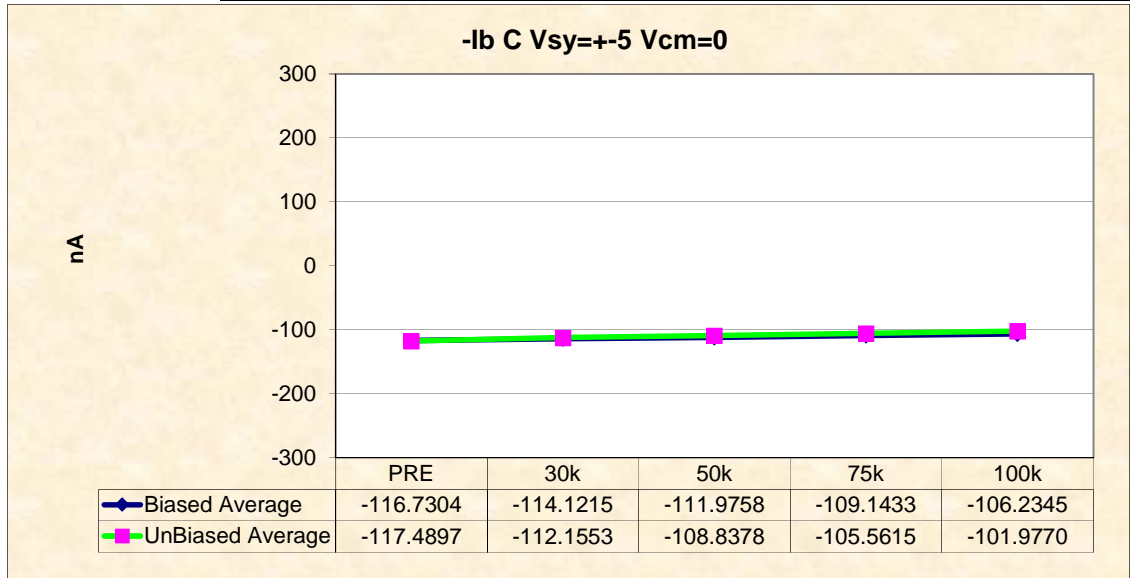
		-Ib B Vsy=+-5 Vcm=0				
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	-104.8410	0.0000	0.0000	-100.3180	-99.6760
	54	-122.9060	0.0000	0.0000	-123.9210	-123.2800
Biased	8	-120.3630	-116.1720	-113.8500	-111.0340	-108.1970
	9	-116.6500	-113.8210	-110.8180	-107.9600	-105.1480
	55	-117.3634	-114.7210	-112.1080	-109.1130	-106.4440
	56	-112.7134	-110.6900	-108.4410	-105.7900	-103.0020
	Min	-120.3630	-116.1720	-113.8500	-111.0340	-108.1970
	Max	-112.7134	-110.6900	-108.4410	-105.7900	-103.0020
	Average	-116.7724	-113.8510	-111.3043	-108.4743	-105.6978
UnBiased	10	-117.2094	-107.6770	-103.7820	-100.3820	-99.9960
	11	-112.1819	-109.8130	-106.3620	-103.0880	-97.0510
	57	-123.8910	-119.1270	-115.6880	-112.4910	-108.7940
	58	-114.0120	-109.3910	-106.2490	-103.1860	-99.5180
	Min	-123.8910	-119.1270	-115.6880	-112.4910	-108.7940
	Max	-112.1819	-107.6770	-103.7820	-100.3820	-97.0510
	Average	-116.8236	-111.5020	-108.0203	-104.7868	-101.3398

Limit: +/-300nA



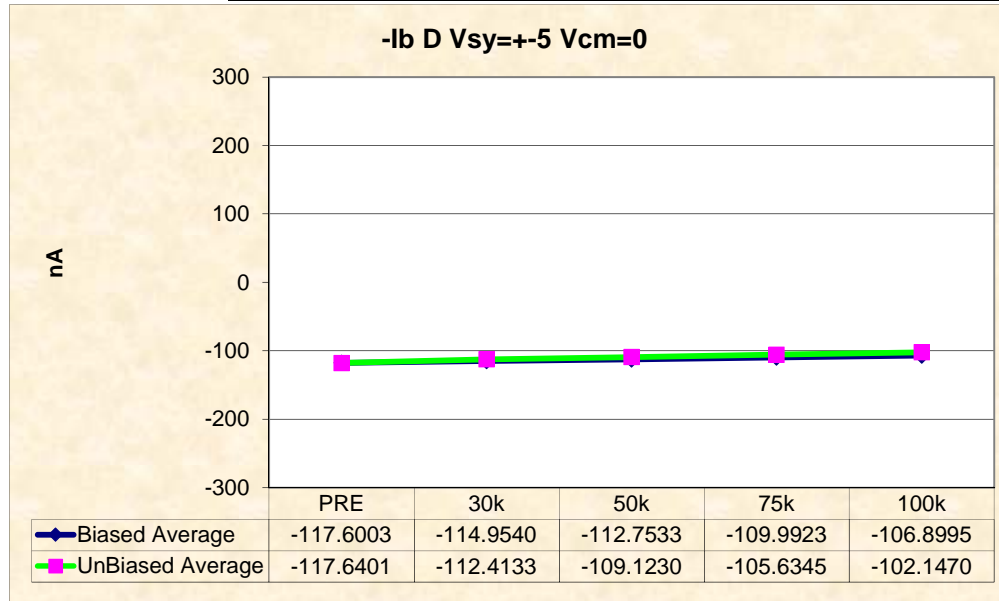
		-Ib C Vsy=+-5 Vcm=0				
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	-102.7940	0.0000	0.0000	-102.7380	-102.1410
	54	-125.1420	0.0000	0.0000	-125.8330	-125.1970
Biased	8	-118.3810	-114.2020	-112.4680	-109.6310	-106.8520
	9	-116.2480	-113.3270	-110.9430	-108.1580	-104.9080
	55	-117.7016	-116.0040	-113.6440	-110.9950	-108.0830
	56	-114.5912	-112.9530	-110.8480	-107.7890	-105.0950
	Min	-118.3810	-116.0040	-113.6440	-110.9950	-108.0830
	Max	-114.5912	-112.9530	-110.8480	-107.7890	-104.9080
	Average	-116.7304	-114.1215	-111.9758	-109.1433	-106.2345
UnBiased	10	-117.9347	-107.9940	-103.7210	-100.9260	-99.4980
	11	-111.8738	-109.8180	-106.7500	-103.0650	-97.5030
	57	-125.6888	-121.4770	-118.0050	-114.5450	-110.9390
	58	-114.4615	-109.3320	-106.8750	-103.7100	-99.9680
	Min	-125.6888	-121.4770	-118.0050	-114.5450	-110.9390
	Max	-111.8738	-107.9940	-103.7210	-100.9260	-97.5030
	Average	-117.4897	-112.1553	-108.8378	-105.5615	-101.9770

Limit: +/-300nA



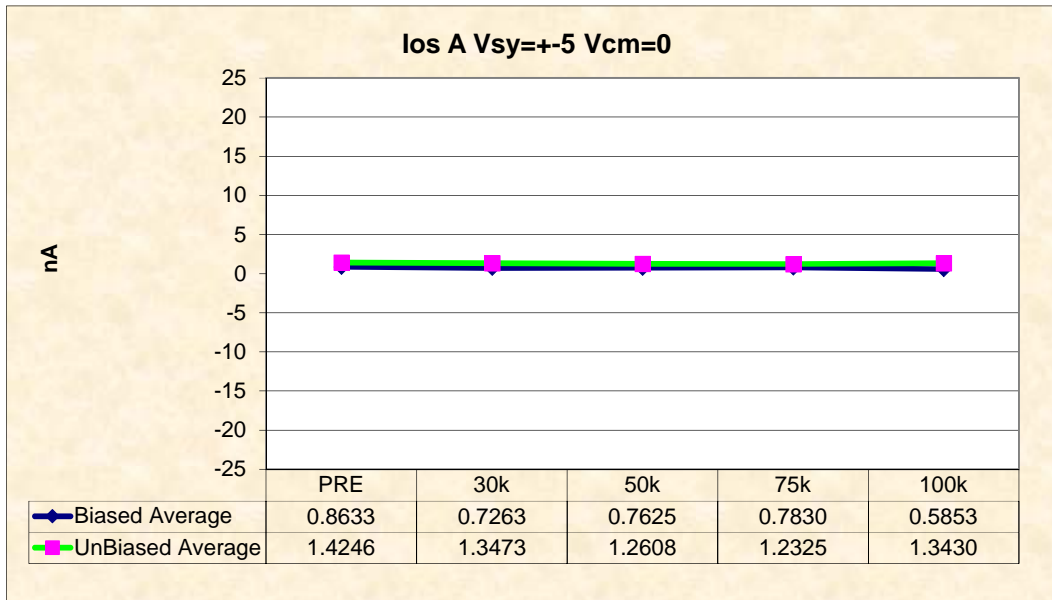
		-Ib D Vsy=+-5 Vcm=0				
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	-103.8180	0.0000	0.0000	-102.2130	-101.4940
	54	-126.8110	0.0000	0.0000	-127.6140	-126.8670
Biased	14	-121.4810	-117.6490	-115.8490	-112.9480	-109.6980
	15	-117.3910	-114.2530	-111.6760	-108.9520	-105.8160
	42	-117.5226	-115.6380	-113.3120	-110.5090	-107.6900
	43	-114.0065	-112.2760	-110.1760	-107.5600	-104.3940
	Min	-121.4810	-117.6490	-115.8490	-112.9480	-109.6980
	Max	-114.0065	-112.2760	-110.1760	-107.5600	-104.3940
	Average	-117.6003	-114.9540	-112.7533	-109.9923	-106.8995
UnBiased	23	-118.6868	-108.8150	-105.2940	-101.5540	-98.8250
	24	-111.7906	-109.5390	-105.9100	-102.4910	-98.4040
	47	-124.9291	-120.7310	-117.6060	-114.0550	-110.6460
	48	-115.1540	-110.5680	-107.6820	-104.4380	-100.7130
	Min	-124.9291	-120.7310	-117.6060	-114.0550	-110.6460
	Max	-111.7906	-108.8150	-105.2940	-101.5540	-98.4040
	Average	-117.6401	-112.4133	-109.1230	-105.6345	-102.1470

Limit: +/-300nA



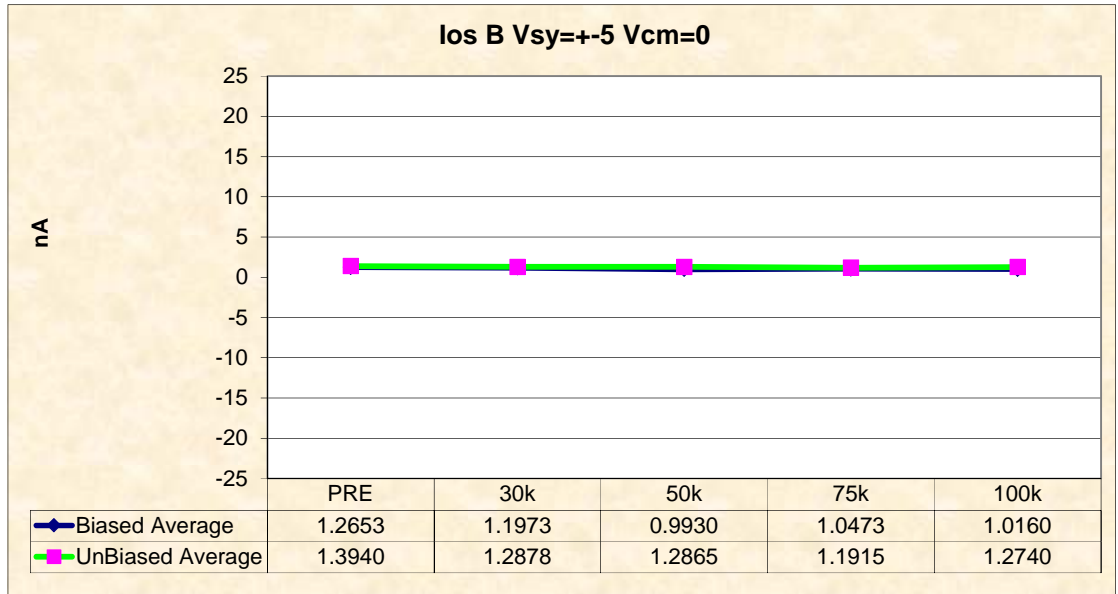
		Ios A Vsy=+-5 Vcm=0				
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	-0.2920	0.0000	0.0000	1.9560	1.7770
	54	1.6100	0.0000	0.0000	1.4330	1.4110
Biased	8	1.0240	1.2020	1.2100	1.1180	0.8750
	9	1.2920	0.8520	0.8690	1.2080	0.8780
	55	1.2296	0.7470	0.9650	0.9350	0.8470
	56	-0.0925	0.1040	0.0060	-0.1290	-0.2590
	Min	-0.0925	0.1040	0.0060	-0.1290	-0.2590
	Max	1.2920	1.2020	1.2100	1.2080	0.8780
	Average	0.8633	0.7263	0.7625	0.7830	0.5853
UnBiased	10	1.3942	1.7970	1.5030	1.9170	0.5750
	11	1.2594	0.6630	0.4590	0.5560	1.9970
	57	1.1891	1.0400	1.5260	1.2570	0.9730
	58	1.8555	1.8890	1.5550	1.2000	1.8270
	Min	1.1891	0.6630	0.4590	0.5560	0.5750
	Max	1.8555	1.8890	1.5550	1.9170	1.9970
	Average	1.4246	1.3473	1.2608	1.2325	1.3430

Limit: +/-25nA



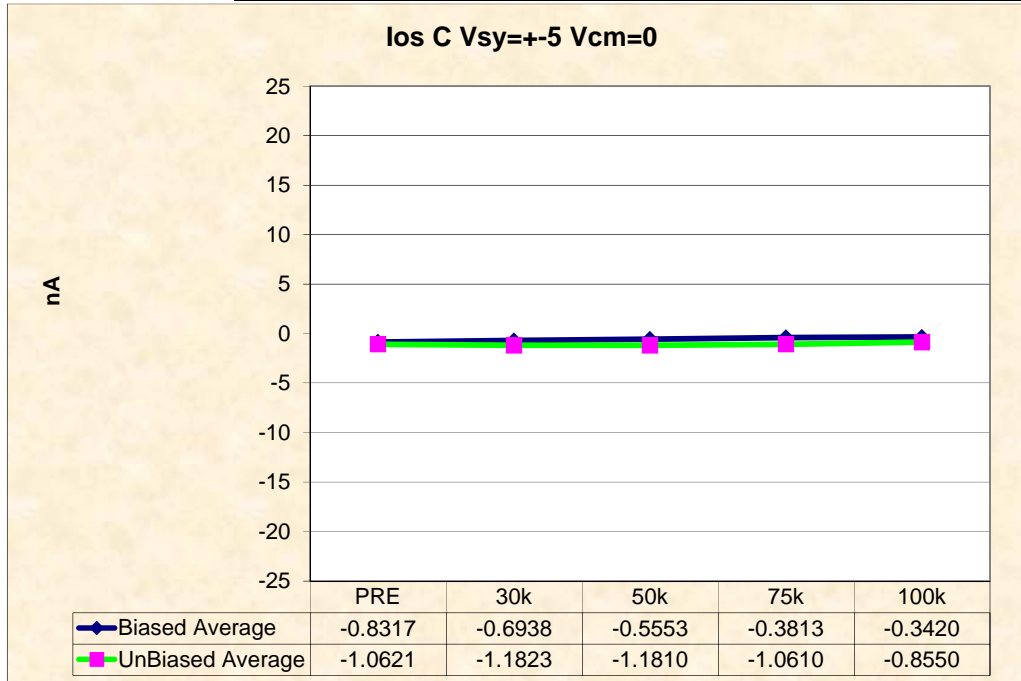
		Ios B Vsy=+-5 Vcm=0				
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	1.2270	0.0000	0.0000	1.9900	1.7950
	54	0.1580	0.0000	0.0000	0.0650	0.0800
Biased	8	0.7620	0.7200	0.0900	0.5710	0.2120
	9	2.1150	2.3520	2.1350	1.9340	2.0090
	55	1.4947	0.7790	0.9860	0.9960	1.1240
	56	0.6895	0.9380	0.7610	0.6880	0.7190
	Min	0.6895	0.7200	0.0900	0.5710	0.2120
	Max	2.1150	2.3520	2.1350	1.9340	2.0090
	Average	1.2653	1.1973	0.9930	1.0473	1.0160
UnBiased	10	0.6701	0.7330	0.8630	0.5400	2.4920
	11	2.3451	2.2770	2.1000	1.7110	0.7310
	57	1.7526	1.2860	1.5490	1.8970	1.4690
	58	0.8081	0.8550	0.6340	0.6180	0.4040
	Min	0.6701	0.7330	0.6340	0.5400	0.4040
	Max	2.3451	2.2770	2.1000	1.8970	2.4920
	Average	1.3940	1.2878	1.2865	1.1915	1.2740

Limit: +/-25nA



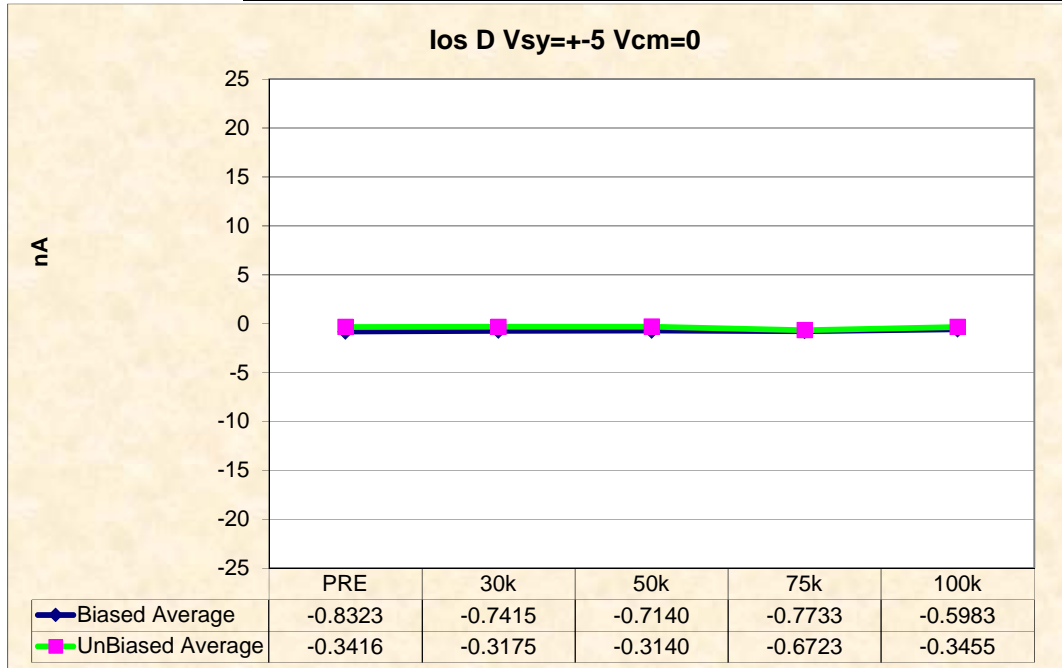
		Ios C Vsy=+-5 Vcm=0				
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	-1.3760	0.0000	0.0000	1.0480	0.9820
	54	-0.7250	0.0000	0.0000	-0.7370	-0.8110
Biased	14	-2.5900	-2.7370	-2.5550	-2.3030	-1.7970
	15	-1.4200	-1.2120	-1.0770	-0.7440	-0.9380
	42	-1.0584	-0.5740	-0.4510	-0.0430	-0.5840
	43	1.7416	1.7480	1.8620	1.5650	1.9510
	Min	-2.5900	-2.7370	-2.5550	-2.3030	-1.7970
	Max	1.7416	1.7480	1.8620	1.5650	1.9510
	Average	-0.8317	-0.6938	-0.5553	-0.3813	-0.3420
UnBiased	23	-1.8522	-2.1760	-2.8690	-2.2140	-0.0010
	24	-0.8356	-0.8770	-0.4010	-0.8470	-2.2600
	47	-0.2848	-0.2180	-0.3230	-0.0770	-0.3670
	48	-1.2759	-1.4580	-1.1310	-1.1060	-0.7920
	Min	-1.8522	-2.1760	-2.8690	-2.2140	-2.2600
	Max	-0.2848	-0.2180	-0.3230	-0.0770	-0.0010
	Average	-1.0621	-1.1823	-1.1810	-1.0610	-0.8550

Limit: +/-25nA



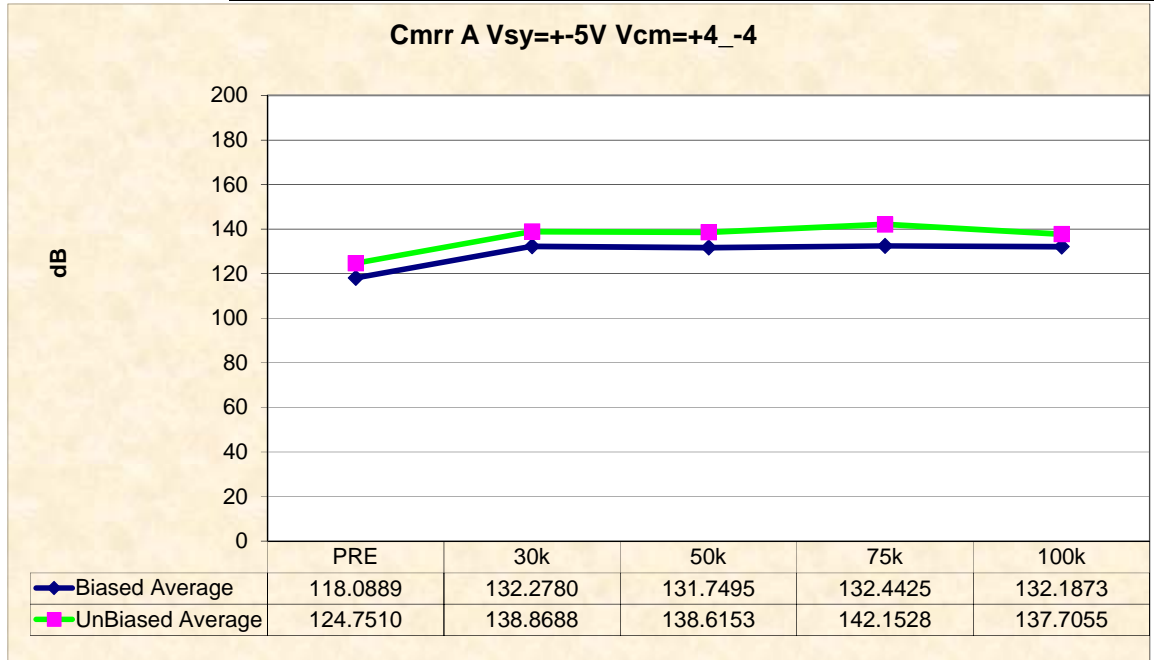
		Ios D Vsy=+5 Vcm=0				
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	-0.2060	0.0000	0.0000	-1.8690	-1.9660
	54	0.6470	0.0000	0.0000	0.3730	0.2110
Biased	8	-0.6320	-0.1500	0.0580	0.1450	0.0910
	9	-0.1720	-0.5230	-0.6450	-0.5090	-0.1070
	55	-1.5455	-1.3310	-1.1270	-1.3900	-1.2510
	56	-0.9799	-0.9620	-1.1420	-1.3390	-1.1260
	Min	-1.5455	-1.3310	-1.1420	-1.3900	-1.2510
	Max	-0.1720	-0.1500	0.0580	0.1450	0.0910
	Average	-0.8323	-0.7415	-0.7140	-0.7733	-0.5983
UnBiased	10	-0.3175	-0.4540	-0.1340	-0.9210	-1.7390
	11	-1.5841	-1.8850	-2.3200	-2.5940	-0.3250
	57	-1.5301	-1.4230	-0.9020	-1.2760	-1.2950
	58	2.0653	2.4920	2.1000	2.1020	1.9770
	Min	-1.5841	-1.8850	-2.3200	-2.5940	-1.7390
	Max	2.0653	2.4920	2.1000	2.1020	1.9770
	Average	-0.3416	-0.3175	-0.3140	-0.6723	-0.3455

Limit: +/-25nA



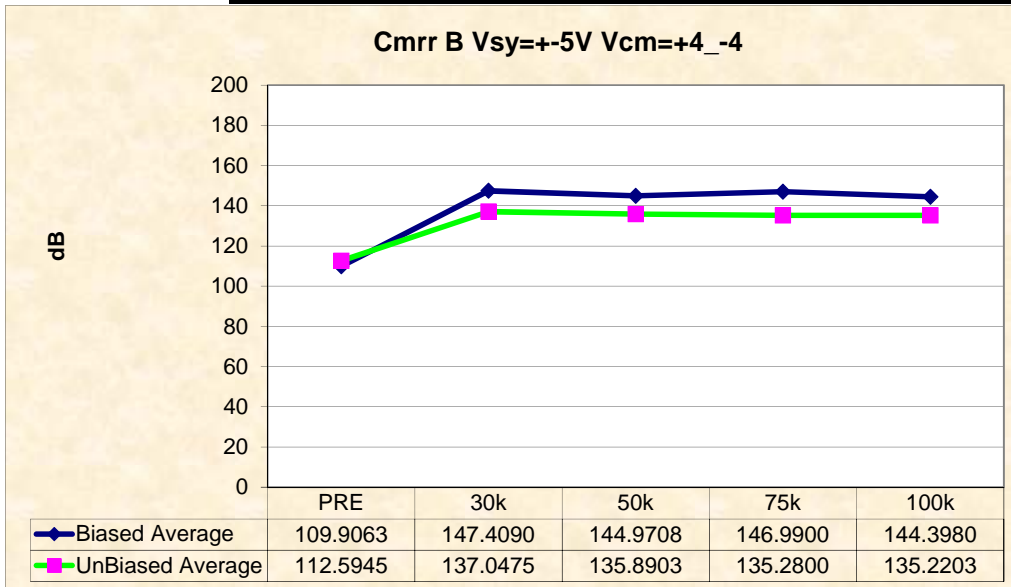
		Cmrr A Vsy=+5V Vcm=+4_-4				
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	130.8240	0.0000	0.0000	139.2100	139.0790
	54	133.6990	0.0000	0.0000	132.6820	132.8760
Biased	8	123.3224	130.8010	130.8820	130.9020	130.8920
	9	118.6175	142.7460	140.6670	143.2640	142.1770
	55	113.2237	126.5850	126.3740	126.3970	126.4720
	56	117.1918	128.9800	129.0750	129.2070	129.2080
	Min	113.2237	126.5850	126.3740	126.3970	126.4720
	Max	123.3224	142.7460	140.6670	143.2640	142.1770
	Average	118.0889	132.2780	131.7495	132.4425	132.1873
UnBiased	10	116.2472	133.0490	133.0330	132.3480	158.7270
	11	133.0493	161.8960	162.0290	175.8260	132.7830
	57	135.9217	128.7910	128.5230	128.4290	128.4490
	58	113.7859	131.7390	130.8760	132.0080	130.8630
	Min	113.7859	128.7910	128.5230	128.4290	128.4490
	Max	135.9217	161.8960	162.0290	175.8260	158.7270
	Average	124.7510	138.8688	138.6153	142.1528	137.7055

Limit: >98dB



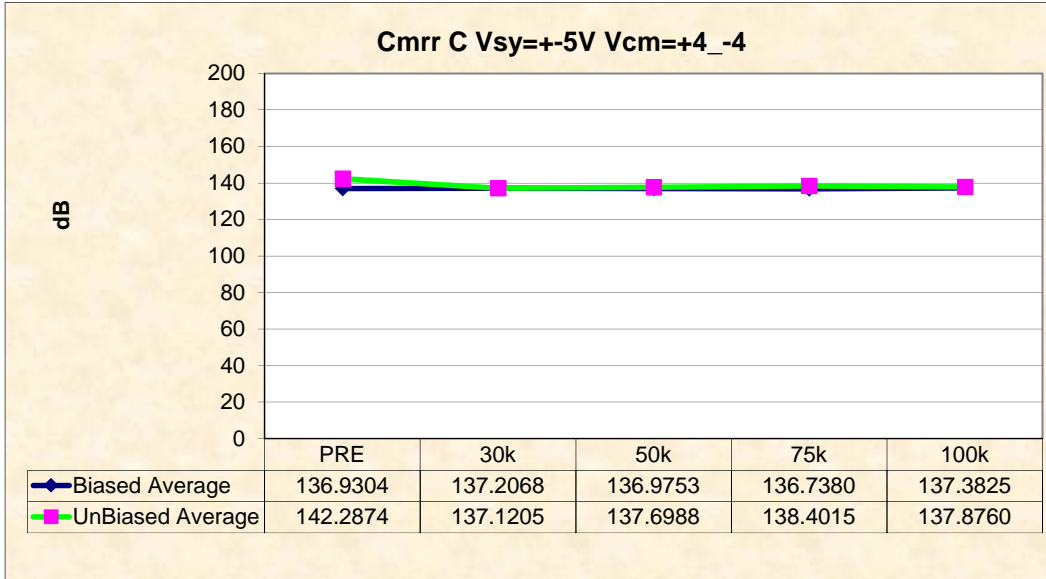
Cmrr B Vsy=+-5V Vcm=+4_-4						
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	140.6030	0.0000	0.0000	132.6870	132.4140
	54	151.6770	0.0000	0.0000	159.0540	156.7780
Biased	14	110.9949	142.7930	146.2410	142.7550	143.2280
	15	108.2425	127.0480	126.9820	127.3110	126.9690
	42	111.3862	175.3480	162.3010	172.1960	163.2100
	43	109.0017	144.4470	144.3590	145.6980	144.1850
	Min	108.2425	127.0480	126.9820	127.3110	126.9690
	Max	111.3862	175.3480	162.3010	172.1960	163.2100
	Average	109.9063	147.4090	144.9708	146.9900	144.3980
UnBiased	23	107.0693	138.2930	139.0930	136.3540	145.3970
	24	119.1218	152.7360	143.7240	147.6670	138.4000
	47	107.2821	132.0610	135.3900	131.8630	131.7640
	48	116.9048	125.1000	125.3540	125.2360	125.3200
	Min	107.0693	125.1000	125.3540	125.2360	125.3200
	Max	119.1218	152.7360	143.7240	147.6670	145.3970
	Average	112.5945	137.0475	135.8903	135.2800	135.2203

Limit: >98dB



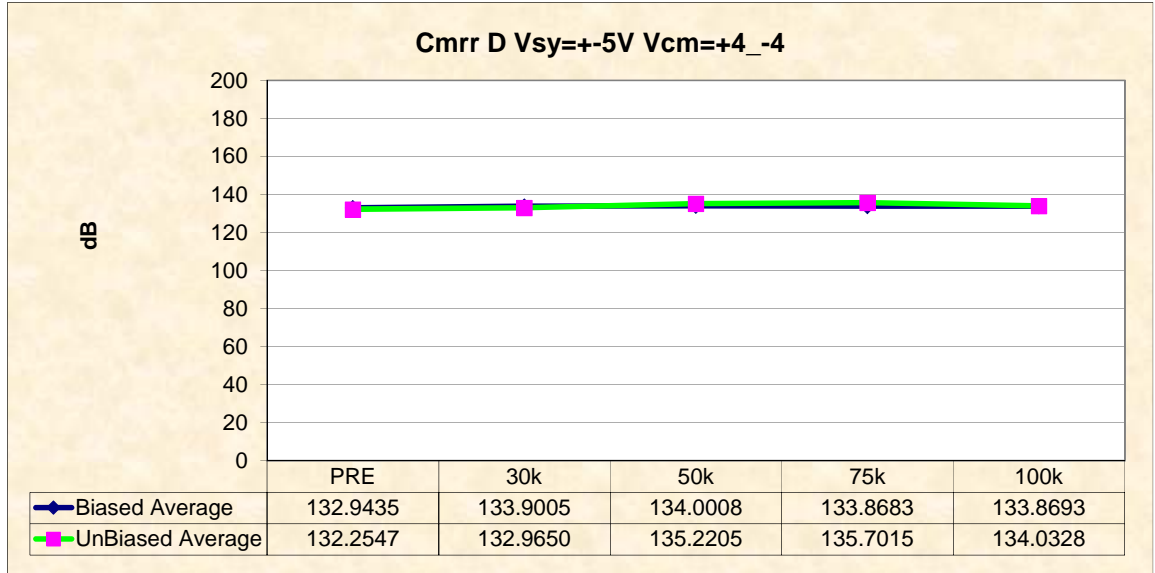
Cmrr C Vsy=+-5V Vcm=+4_-4						
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	124.7070	0.0000	0.0000	135.1880	135.7140
	54	126.6580	0.0000	0.0000	126.4620	126.2350
Biased	8	136.1706	138.9560	138.4660	138.7360	139.5240
	9	125.1220	125.9370	125.9410	125.9240	125.7800
	55	143.4893	138.0970	138.5940	138.5700	137.9940
	56	142.9399	145.8370	144.9000	143.7220	146.2320
	Min	125.1220	125.9370	125.9410	125.9240	125.7800
	Max	143.4893	145.8370	144.9000	143.7220	146.2320
	Average	136.9304	137.2068	136.9753	136.7380	137.3825
UnBiased	10	125.3200	126.0790	126.0000	126.0520	141.2900
	11	138.4511	141.0170	141.0780	141.0160	126.7510
	57	151.7959	142.3740	143.9440	145.2030	143.7190
	58	153.5825	139.0120	139.7730	141.3350	139.7440
	Min	125.3200	126.0790	126.0000	126.0520	126.7510
	Max	153.5825	142.3740	143.9440	145.2030	143.7190
	Average	142.2874	137.1205	137.6988	138.4015	137.8760

Limit: >98dB



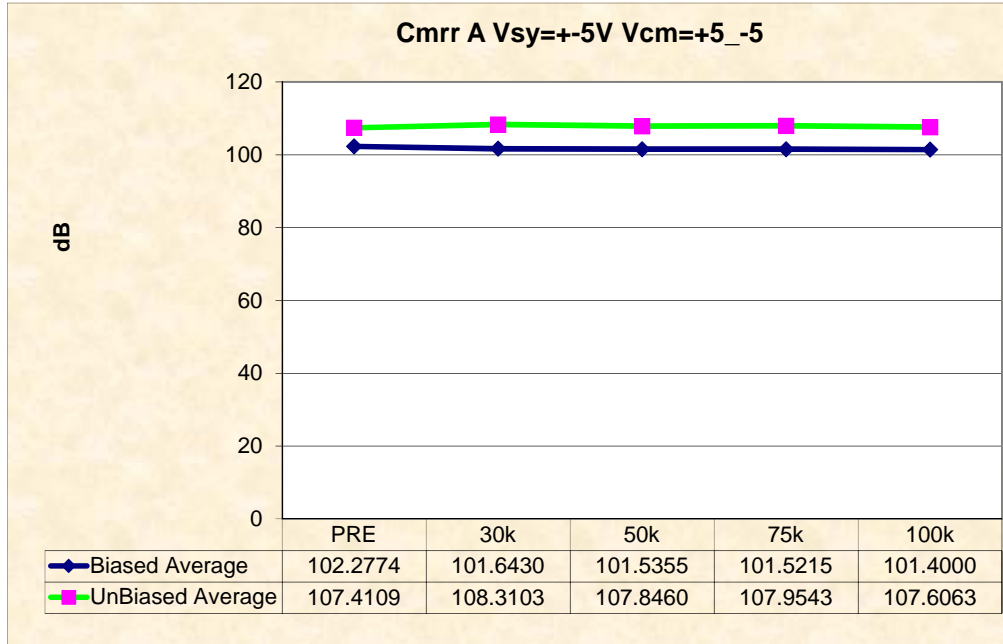
Cmrr D Vsy=+5V Vcm=+4_-4						
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	127.1570	0.0000	0.0000	120.8480	120.9420
	54	136.9150	0.0000	0.0000	138.6260	137.7410
Biased	8	144.4175	142.6780	143.6890	143.7170	142.3660
	9	123.0524	124.1590	124.2670	123.9800	124.1050
	55	132.8490	135.5600	135.3880	135.2600	136.0750
	56	131.4550	133.2050	132.6590	132.5160	132.9310
	Min	123.0524	124.1590	124.2670	123.9800	124.1050
	Max	144.4175	142.6780	143.6890	143.7170	142.3660
	Average	132.9435	133.9005	134.0008	133.8683	133.8693
UnBiased	10	119.6578	120.1560	120.1060	119.9690	157.6390
	11	152.6472	152.7270	161.0160	163.9270	120.1490
	57	126.9695	128.4550	128.6780	128.5880	128.0210
	58	129.7445	130.5220	131.0820	130.3220	130.3220
	Min	119.6578	120.1560	120.1060	119.9690	120.1490
	Max	152.6472	152.7270	161.0160	163.9270	157.6390
	Average	132.2547	132.9650	135.2205	135.7015	134.0328

Limit: >98dB



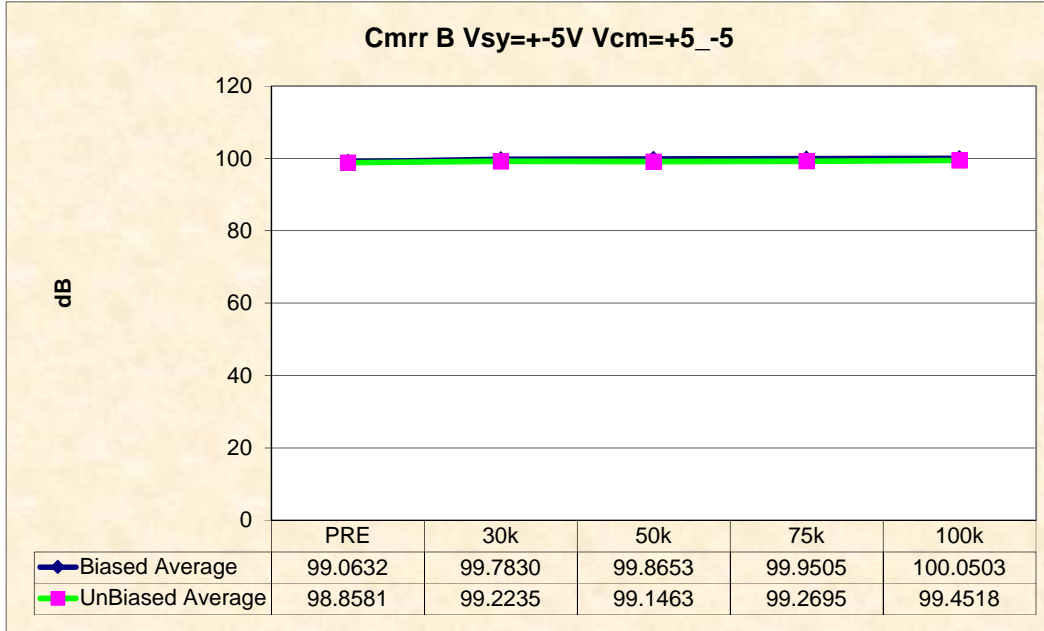
Cmrr A Vsy=+5V Vcm=+5_-5						
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	108.5290	0.0000	0.0000	110.4730	109.7570
	54	122.4380	0.0000	0.0000	124.3020	128.1920
Biased	14	111.5135	110.0420	109.8930	109.7200	109.3620
	15	99.8532	100.1630	100.2610	100.3050	100.5260
	42	97.9163	97.2980	97.0940	97.1390	96.9900
	43	99.8266	99.0690	98.8940	98.9220	98.7220
	Min	97.9163	97.2980	97.0940	97.1390	96.9900
	Max	111.5135	110.0420	109.8930	109.7200	109.3620
	Average	102.2774	101.6430	101.5355	101.5215	101.4000
UnBiased	23	100.3705	101.4060	101.5220	101.4160	116.2490
	24	121.3830	120.0680	117.9270	118.7050	101.5970
	47	111.8614	115.8900	116.0280	115.8000	116.9390
	48	96.0288	95.8770	95.9070	95.8960	95.6400
	Min	96.0288	95.8770	95.9070	95.8960	95.6400
	Max	121.3830	120.0680	117.9270	118.7050	116.9390
	Average	107.4109	108.3103	107.8460	107.9543	107.6063

Limit: >76dB



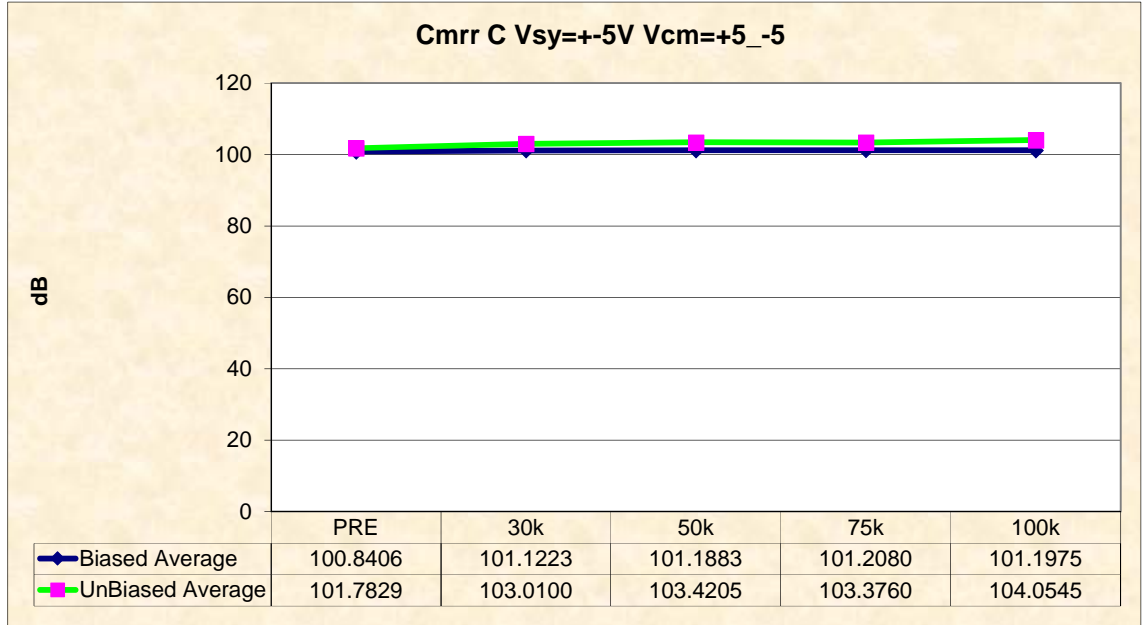
Cmrr B Vsy=+-5V Vcm=+5_-5						
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	94.5090	0.0000	0.0000	100.4960	100.6780
	54	101.5960	0.0000	0.0000	101.5630	101.7460
Biased	8	100.5350	101.3990	101.2460	101.5150	101.4940
	9	93.4608	93.8930	93.9540	93.9680	94.0470
	55	106.7650	107.7280	108.0660	108.1000	108.3440
	56	95.4920	96.1120	96.1950	96.2190	96.3160
	Min	93.4608	93.8930	93.9540	93.9680	94.0470
	Max	106.7650	107.7280	108.0660	108.1000	108.3440
	Average	99.0632	99.7830	99.8653	99.9505	100.0503
UnBiased	10	101.4450	101.9080	101.7510	101.9820	102.5780
	11	101.9190	102.1910	102.3040	102.2970	102.2020
	57	95.0431	95.2970	95.1130	95.3060	95.3850
	58	97.0254	97.4980	97.4170	97.4930	97.6420
	Min	95.0431	95.2970	95.1130	95.3060	95.3850
	Max	101.9190	102.1910	102.3040	102.2970	102.5780
	Average	98.8581	99.2235	99.1463	99.2695	99.4518

Limit: >76dB



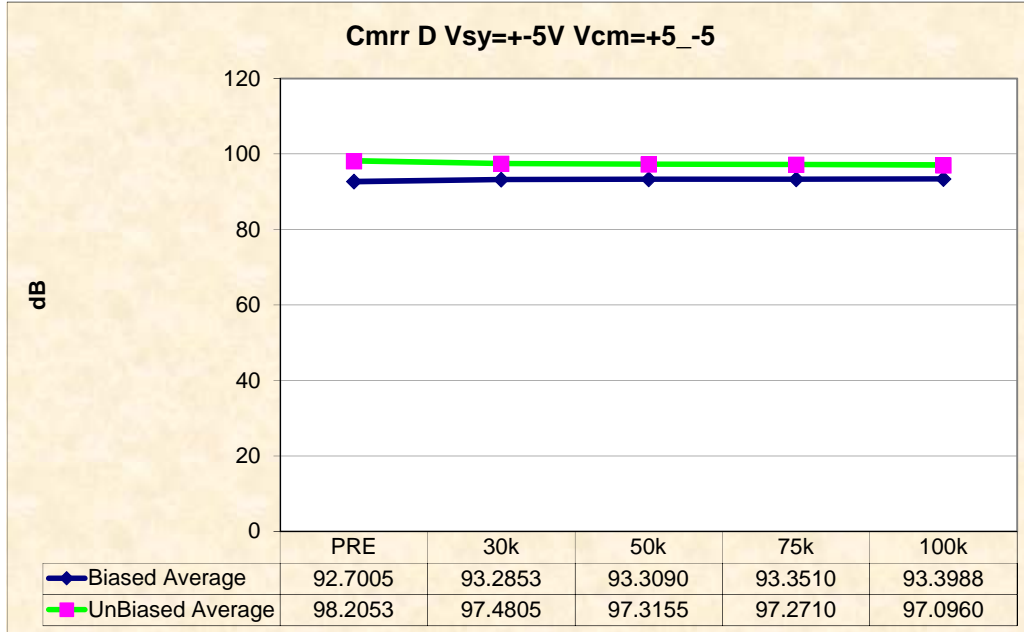
Cmrr C Vsy=+-5V Vcm=+5_-5						
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	96.9750	0.0000	0.0000	108.9900	108.5210
	54	94.4390	0.0000	0.0000	94.5390	94.5870
Biased	8	101.5329	102.5110	102.6930	102.7610	102.8560
	9	95.9081	96.5450	96.6620	96.6750	96.6810
	55	101.5772	101.9740	102.1320	102.2240	102.4010
	56	104.3442	103.4590	103.2660	103.1720	102.8520
	Min	95.9081	96.5450	96.6620	96.6750	96.6810
	Max	104.3442	103.4590	103.2660	103.1720	102.8560
	Average	100.8406	101.1223	101.1883	101.2080	101.1975
UnBiased	10	95.9198	96.7020	96.7060	96.7780	119.9330
	11	115.2216	116.7560	118.1610	117.7690	96.8030
	57	107.6489	110.0270	110.2610	110.3840	110.8930
	58	88.3414	88.5550	88.5540	88.5730	88.5890
	Min	88.3414	88.5550	88.5540	88.5730	88.5890
	Max	115.2216	116.7560	118.1610	117.7690	119.9330
	Average	101.7829	103.0100	103.4205	103.3760	104.0545

Limit: >76dB



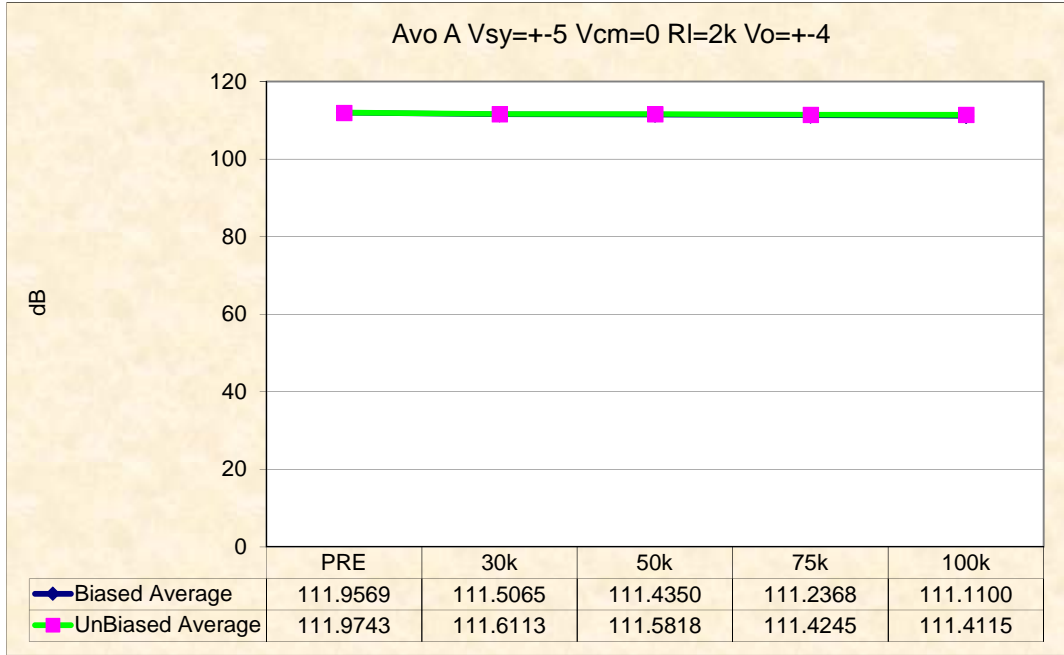
Cmrr D Vsy=+-5V Vcm=+5_-5						
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	94.1170	0.0000	0.0000	86.5960	86.5680
	54	96.8200	0.0000	0.0000	96.8910	96.9640
Biased	14	98.6189	99.4640	99.5640	99.6090	99.8050
	15	86.9179	87.2460	87.2090	87.2470	87.2300
	42	90.6429	90.9380	90.9440	90.9740	90.9770
	43	94.6224	95.4930	95.5190	95.5740	95.5830
	Min	86.9179	87.2460	87.2090	87.2470	87.2300
	Max	98.6189	99.4640	99.5640	99.6090	99.8050
	Average	92.7005	93.2853	93.3090	93.3510	93.3988
UnBiased	23	90.2679	90.5630	90.5700	90.5980	113.7670
	24	119.3672	115.4210	114.8070	114.5080	90.5940
	47	92.0006	92.5440	92.5270	92.5710	92.5830
	48	91.1854	91.3940	91.3580	91.4070	91.4400
	Min	90.2679	90.5630	90.5700	90.5980	90.5940
	Max	119.3672	115.4210	114.8070	114.5080	113.7670
	Average	98.2053	97.4805	97.3155	97.2710	97.0960

Limit: >76dB



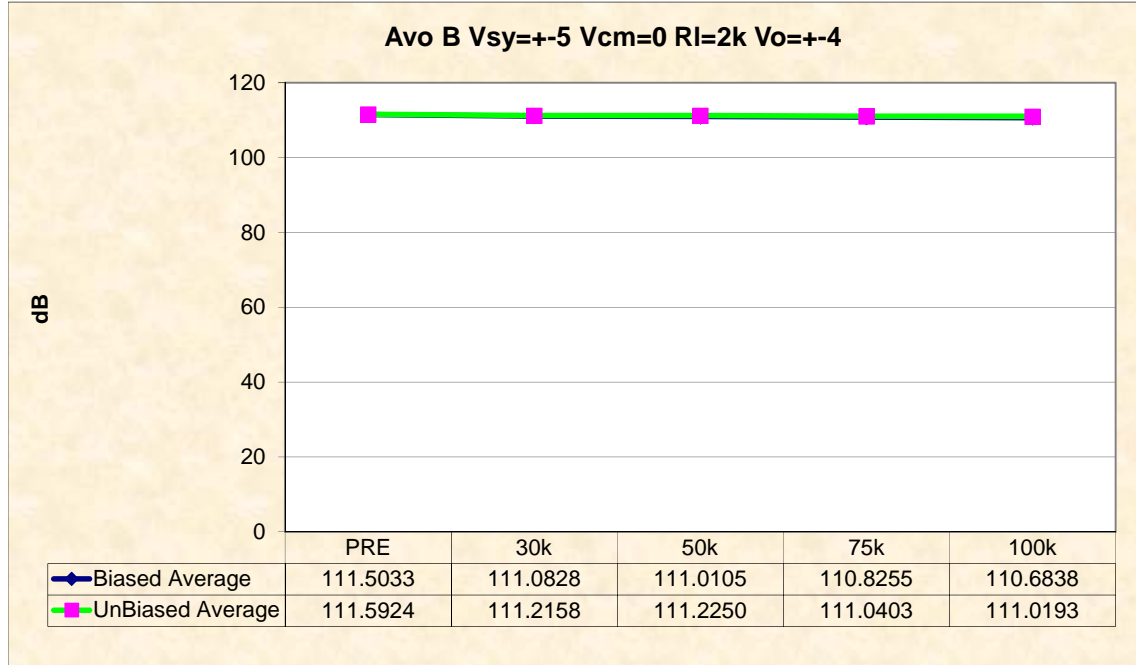
Avo A Vsy=+-5 Vcm=0 RI=2k Vo=+-4						
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	111.6360	0.0000	0.0000	111.7500	111.8400
	54	111.1050	0.0000	0.0000	111.0620	111.1690
Biased	8	112.0966	111.6940	111.5560	111.3540	111.2310
	9	111.9476	111.5170	111.4260	111.2370	111.1510
	55	111.7453	111.2660	111.2410	111.0380	110.8680
	56	112.0382	111.5490	111.5170	111.3180	111.1900
	Min	111.7453	111.2660	111.2410	111.0380	110.8680
	Max	112.0966	111.6940	111.5560	111.3540	111.2310
	Average	111.9569	111.5065	111.4350	111.2368	111.1100
UnBiased	10	111.8711	111.4900	111.4890	111.3190	111.1960
	11	111.6389	111.3480	111.3130	111.1890	111.2590
	57	111.8804	111.4590	111.4020	111.2390	111.2430
	58	112.5070	112.1480	112.1230	111.9510	111.9480
	Min	111.6389	111.3480	111.3130	111.1890	111.1960
	Max	112.5070	112.1480	112.1230	111.9510	111.9480
	Average	111.9743	111.6113	111.5818	111.4245	111.4115

Limit: >108dB



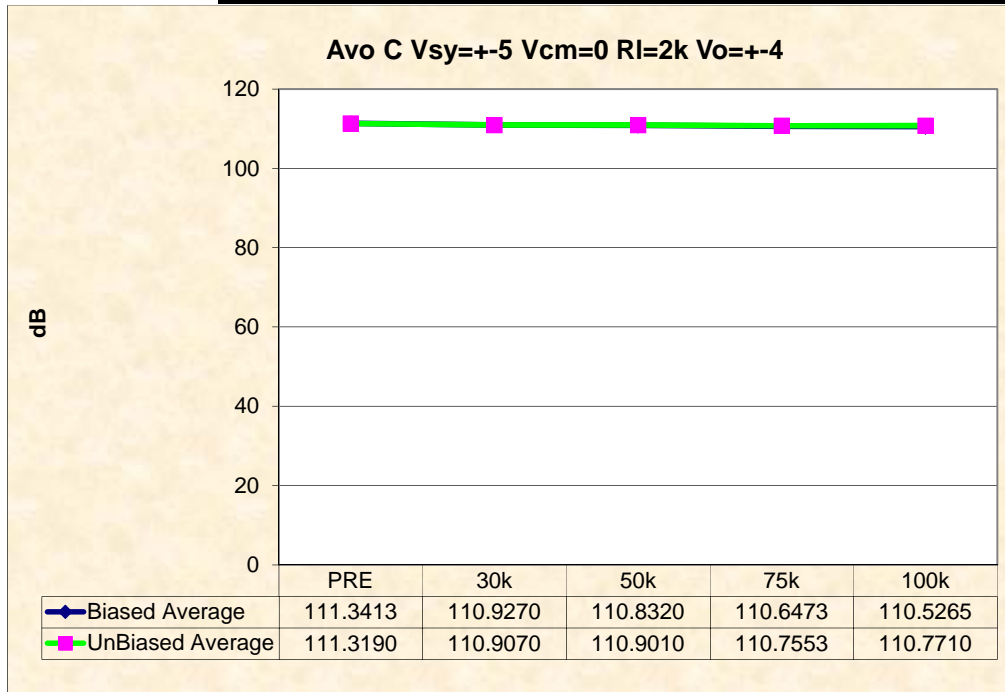
Avo B Vsy=+5 Vcm=0 RI=2k Vo=+-4						
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	111.4050	0.0000	0.0000	111.3980	111.4580
	54	111.4390	0.0000	0.0000	111.4640	111.5560
Biased	8	111.6622	111.3020	111.1410	111.0300	110.8620
	9	111.4651	111.0200	110.9610	110.7320	110.6490
	55	111.3552	110.8700	110.8620	110.6640	110.5300
	56	111.5310	111.1390	111.0780	110.8760	110.6940
	Min	111.3552	110.8700	110.8620	110.6640	110.5300
	Max	111.6622	111.3020	111.1410	111.0300	110.8620
	Average	111.5033	111.0828	111.0105	110.8255	110.6838
UnBiased	10	111.4220	110.9140	111.0040	110.8120	111.4510
	11	111.9796	111.6660	111.7390	111.4350	110.7910
	57	111.4697	111.0970	111.0710	110.8830	110.9470
	58	111.4986	111.1860	111.0860	111.0310	110.8880
	Min	111.4220	110.9140	111.0040	110.8120	110.7910
	Max	111.9796	111.6660	111.7390	111.4350	111.4510
	Average	111.5924	111.2158	111.2250	111.0403	111.0193

Limit: >108dB



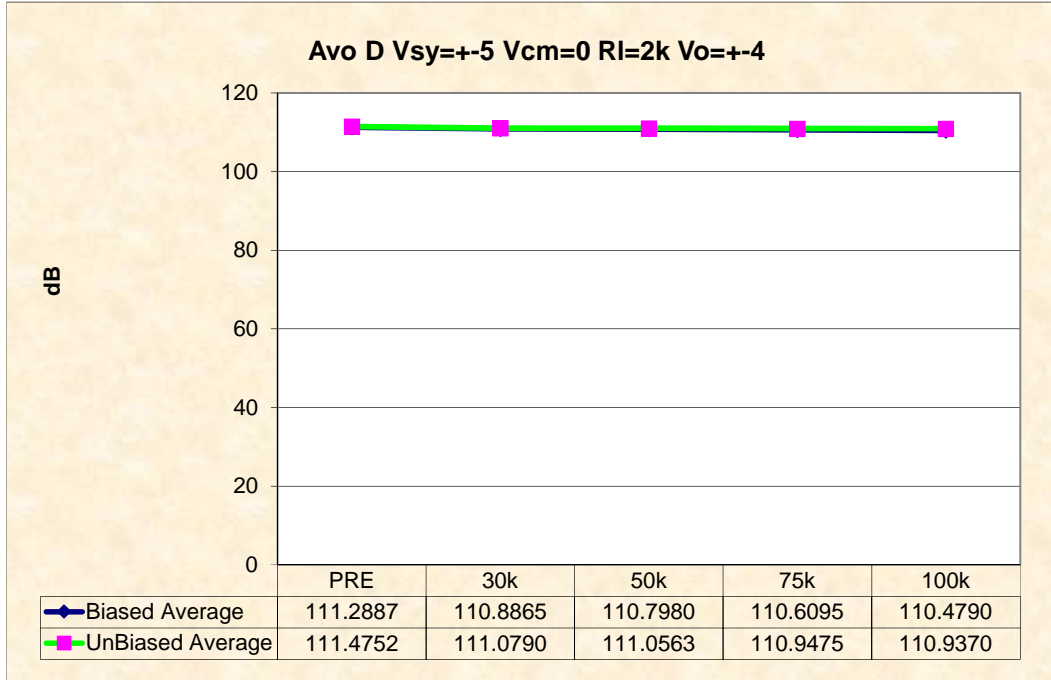
Avo C Vsy=+5 Vcm=0 RI=2k Vo=+-4						
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	111.1210	0.0000	0.0000	111.3180	111.4030
	54	111.0360	0.0000	0.0000	111.0540	111.1460
Biased	14	111.1411	110.8120	110.6860	110.5530	110.4180
	15	111.3771	110.8970	110.8290	110.6360	110.5570
	42	110.7991	110.3240	110.2560	110.0420	109.9200
	43	112.0480	111.6750	111.5570	111.3580	111.2110
	Min	110.7991	110.3240	110.2560	110.0420	109.9200
	Max	112.0480	111.6750	111.5570	111.3580	111.2110
	Average	111.3413	110.9270	110.8320	110.6473	110.5265
UnBiased	23	111.3416	110.7240	110.8400	110.6460	111.1820
	24	111.6335	111.3300	111.2850	111.1410	110.6720
	47	111.3335	110.9610	110.8710	110.7090	110.7980
	48	110.9676	110.6130	110.6080	110.5250	110.4320
	Min	110.9676	110.6130	110.6080	110.5250	110.4320
	Max	111.6335	111.3300	111.2850	111.1410	111.1820
	Average	111.3190	110.9070	110.9010	110.7553	110.7710

Limit: >108dB



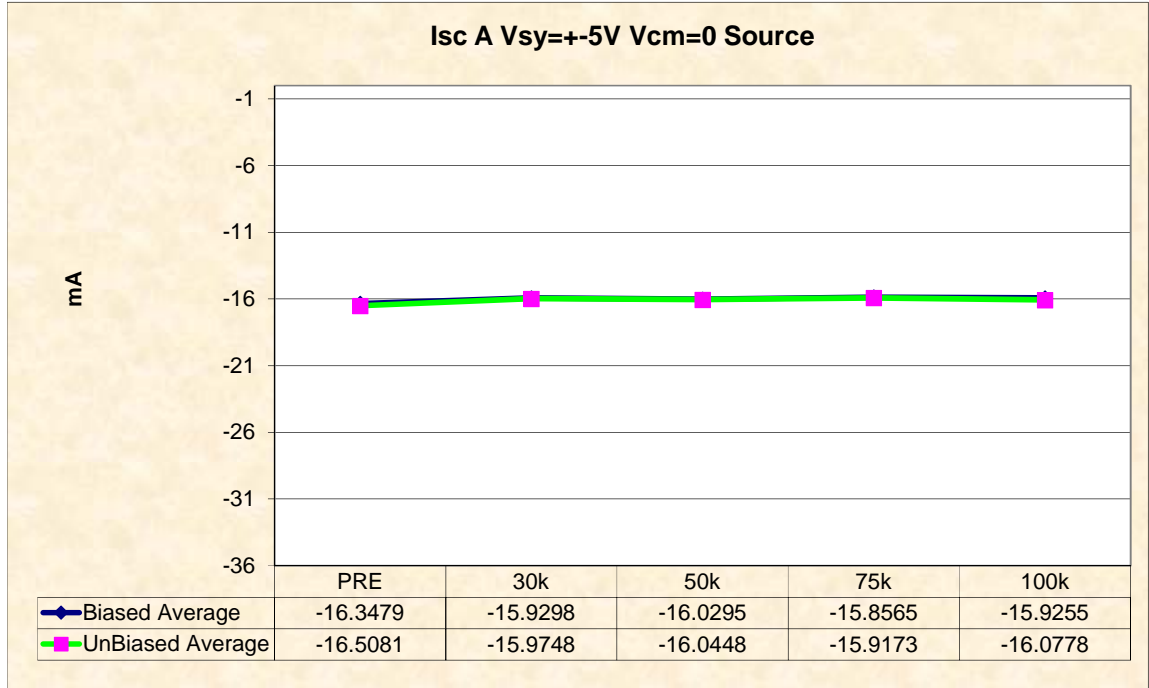
Avo D Vsy=+-5 Vcm=0 RI=2k Vo=+-4						
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	111.3290	0.0000	0.0000	110.8870	110.9790
	54	111.2500	0.0000	0.0000	111.3230	111.3750
Biased	8	111.6588	111.3290	111.1850	111.0100	110.8690
	9	110.9988	110.5620	110.5080	110.3430	110.1940
	55	110.9535	110.5330	110.4610	110.2380	110.1150
	56	111.5436	111.1220	111.0380	110.8470	110.7380
	Min	110.9535	110.5330	110.4610	110.2380	110.1150
	Max	111.6588	111.3290	111.1850	111.0100	110.8690
	Average	111.2887	110.8865	110.7980	110.6095	110.4790
UnBiased	10	111.3381	110.7970	110.8090	110.7710	111.5790
	11	112.0643	111.6990	111.7700	111.5830	110.7370
	57	111.2179	110.8350	110.7440	110.6100	110.6420
	58	111.2806	110.9850	110.9020	110.8260	110.7900
	Min	111.2179	110.7970	110.7440	110.6100	110.6420
	Max	112.0643	111.6990	111.7700	111.5830	111.5790
	Average	111.4752	111.0790	111.0563	110.9475	110.9370

Limit: >108dB



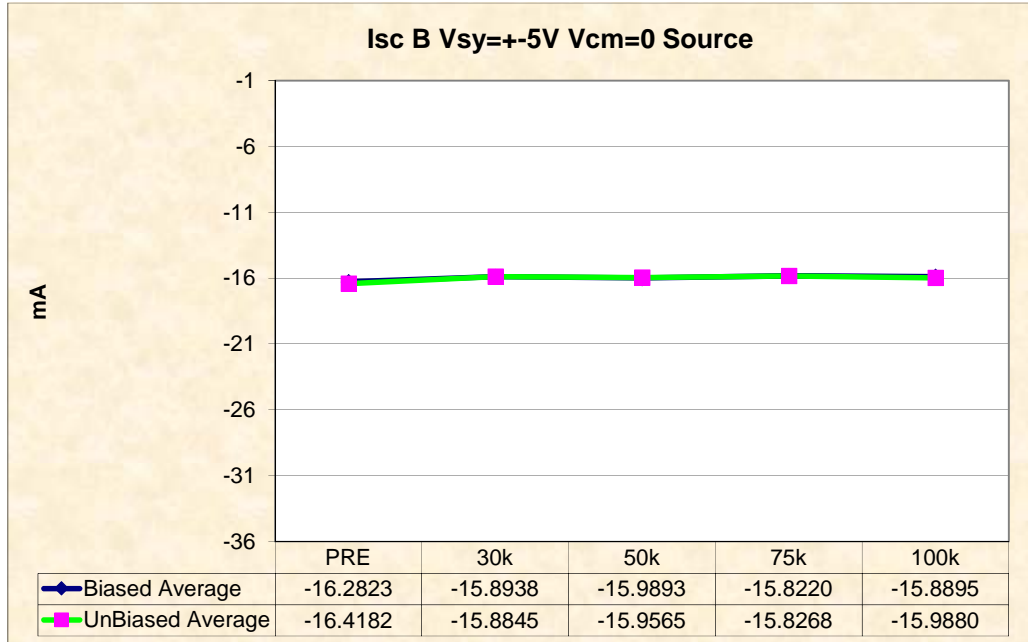
		Isc A Vsy=+-5V Vcm=0 Source				
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	-16.4920	0.0000	0.0000	-16.2380	-16.4870
	54	-15.5710	0.0000	0.0000	-15.4600	-15.6850
Biased	8	-16.0509	-15.8210	-15.7840	-15.6340	-15.6720
	9	-16.5423	-16.0630	-16.2500	-16.0390	-16.1260
	55	-16.3930	-15.8770	-16.0700	-15.8640	-15.9460
	56	-16.4055	-15.9580	-16.0140	-15.8890	-15.9580
	Min	-16.5423	-16.0630	-16.2500	-16.0390	-16.1260
	Max	-16.0509	-15.8210	-15.7840	-15.6340	-15.6720
	Average	-16.3479	-15.9298	-16.0295	-15.8565	-15.9255
UnBiased	10	-16.5236	-15.8890	-16.0200	-15.8770	-16.3130
	11	-16.8036	-16.1130	-16.2750	-16.1130	-16.0200
	57	-16.1069	-15.5970	-15.6840	-15.5660	-15.7090
	58	-16.5983	-16.3000	-16.2000	-16.1130	-16.2690
	Min	-16.8036	-16.3000	-16.2750	-16.1130	-16.3130
	Max	-16.1069	-15.5970	-15.6840	-15.5660	-15.7090
	Average	-16.5081	-15.9748	-16.0448	-15.9173	-16.0778

Limit: >-36mA



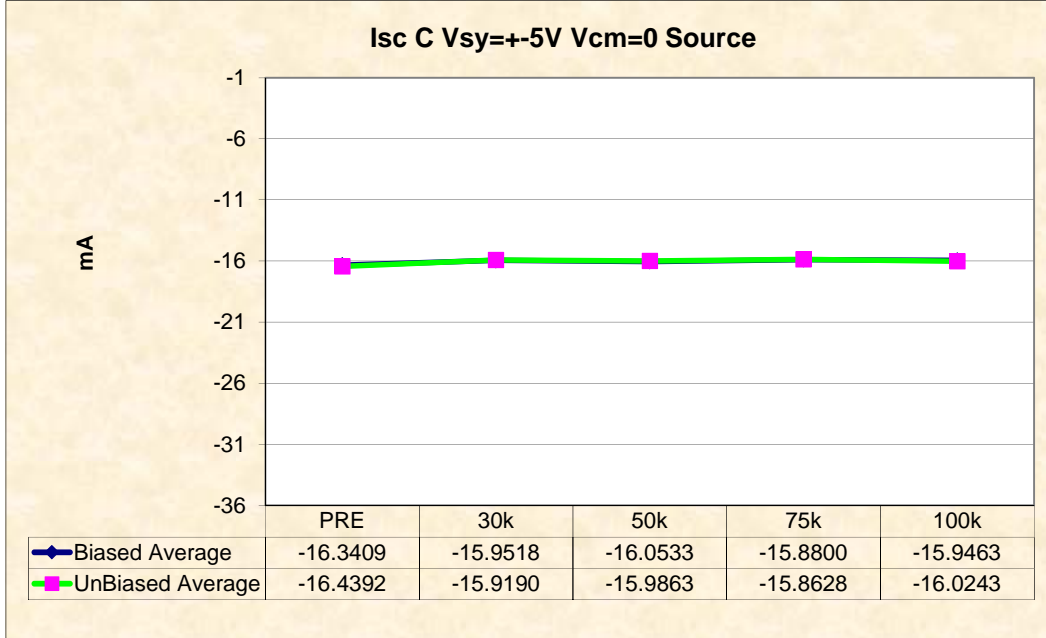
Isc B Vsy=+-5V Vcm=0 Source						
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	-16.3040	0.0000	0.0000	-16.2220	-16.4770
	54	-15.5040	0.0000	0.0000	-15.4100	-15.6400
Biased	14	-15.9996	-15.7780	-15.7410	-15.6100	-15.6400
	15	-16.3995	-15.9280	-16.1030	-15.8970	-15.9830
	42	-16.0870	-15.6100	-15.7970	-15.5970	-15.6770
	43	-16.6432	-16.2590	-16.3160	-16.1840	-16.2580
	Min	-16.6432	-16.2590	-16.3160	-16.1840	-16.2580
	Max	-15.9996	-15.6100	-15.7410	-15.5970	-15.6400
	Average	-16.2823	-15.8938	-15.9893	-15.8220	-15.8895
UnBiased	23	-16.4120	-15.7600	-15.8850	-15.7470	-16.2330
	24	-16.7182	-16.0280	-16.1910	-16.0280	-15.9020
	47	-16.0683	-15.5530	-15.6410	-15.5160	-15.6520
	48	-16.4745	-16.1970	-16.1090	-16.0160	-16.1650
	Min	-16.7182	-16.1970	-16.1910	-16.0280	-16.2330
	Max	-16.0683	-15.5530	-15.6410	-15.5160	-15.6520
	Average	-16.4182	-15.8845	-15.9565	-15.8268	-15.9880

Limit: >-36mA



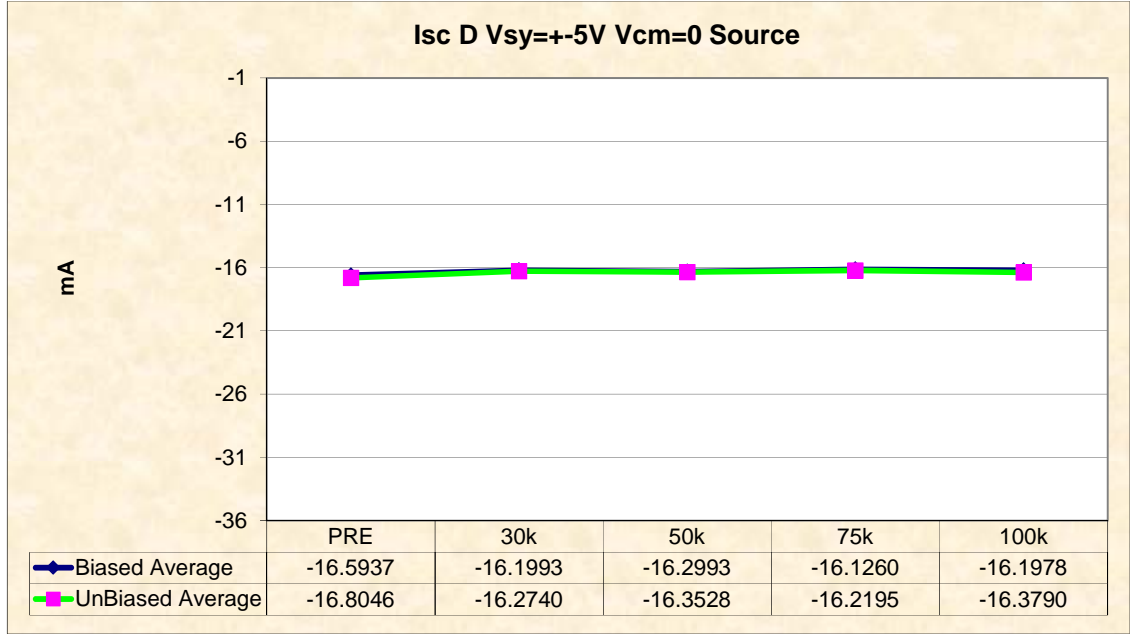
Isc C Vsy=+-5V Vcm=0 Source						
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	-16.6380	0.0000	0.0000	-16.3130	-16.5630
	54	-15.5070	0.0000		-15.4130	-15.6320
Biased	8	-16.2191	-16.0000	-15.9440	-15.8130	-15.8450
	9	-16.6500	-16.1940	-16.3870	-16.1690	-16.2570
	55	-16.0504	-15.5690	-15.7630	-15.5500	-15.6320
	56	-16.4439	-16.0440	-16.1190	-15.9880	-16.0510
	Min	-16.6500	-16.1940	-16.3870	-16.1690	-16.2570
	Max	-16.0504	-15.5690	-15.7630	-15.5500	-15.6320
	Average	-16.3409	-15.9518	-16.0533	-15.8800	-15.9463
UnBiased	10	-16.3752	-15.7190	-15.8440	-15.6940	-16.5070
	11	-16.9624	-16.3190	-16.4690	-16.3130	-15.8570
	57	-15.8818	-15.3820	-15.4690	-15.3690	-15.5010
	58	-16.5376	-16.2560	-16.1630	-16.0750	-16.2320
	Min	-16.9624	-16.3190	-16.4690	-16.3130	-16.5070
	Max	-15.8818	-15.3820	-15.4690	-15.3690	-15.5010
	Average	-16.4392	-15.9190	-15.9863	-15.8628	-16.0243

Limit: >-36mA



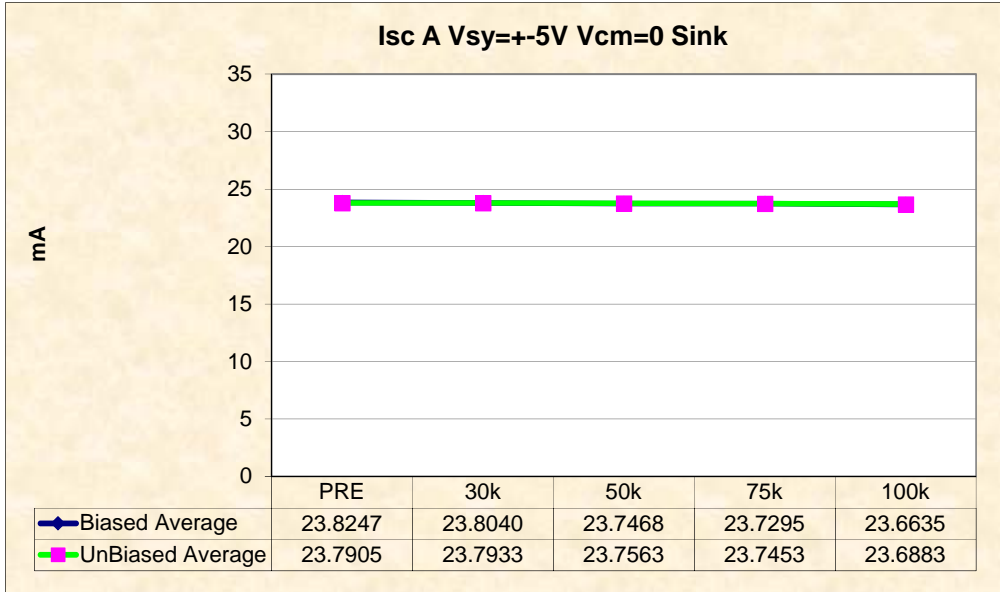
Isc D Vsy=+-5V Vcm=0 Source						
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	-16.8960	0.0000	0.0000	-16.2820	-16.5460
	54	-15.5460	0.0000	0.0000	-15.4630	-15.6900
Biased	8	-16.1702	-15.9630	-15.9200	-15.7820	-15.8150
	9	-16.8202	-16.3260	-16.5130	-16.3010	-16.3900
	55	-16.4452	-15.9950	-16.1760	-15.9700	-16.0650
	56	-16.9390	-16.5130	-16.5880	-16.4510	-16.5210
	Min	-16.9390	-16.5130	-16.5880	-16.4510	-16.5210
	Max	-16.1702	-15.9630	-15.9200	-15.7820	-15.8150
	Average	-16.5937	-16.1993	-16.2993	-16.1260	-16.1978
UnBiased	10	-17.1015	-16.4320	-16.5700	-16.4130	-16.6650
	11	-17.1327	-16.4630	-16.6450	-16.4700	-16.5710
	57	-16.3202	-15.8130	-15.8950	-15.7820	-15.9150
	58	-16.6640	-16.3880	-16.3010	-16.2130	-16.3650
	Min	-17.1327	-16.4630	-16.6450	-16.4700	-16.6650
	Max	-16.3202	-15.8130	-15.8950	-15.7820	-15.9150
	Average	-16.8046	-16.2740	-16.3528	-16.2195	-16.3790

Limit: >-36mA



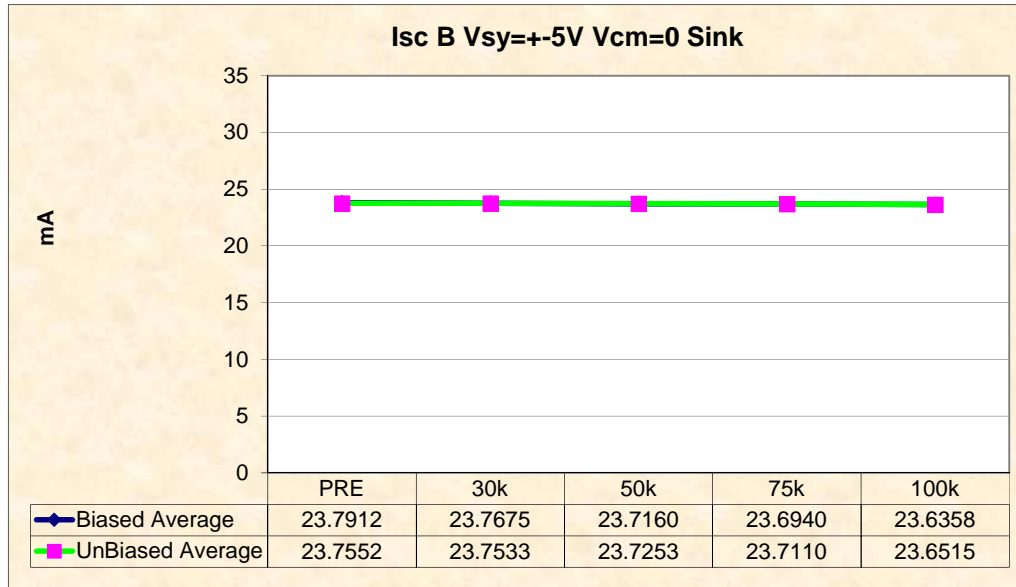
Isc A Vsy=+-5V Vcm=0 Sink						
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	23.9580	0.0000	0.0000	23.3890	23.3520
	54	23.6780	0.0000	0.0000	23.7060	23.6760
Biased	14	23.9211	23.8740	23.8430	23.8120	23.7570
	15	24.0206	24.0170	23.9490	23.9490	23.8750
	42	23.6412	23.6250	23.5510	23.5380	23.4710
	43	23.7158	23.7000	23.6440	23.6190	23.5510
	Min	23.6412	23.6250	23.5510	23.5380	23.4710
	Max	24.0206	24.0170	23.9490	23.9490	23.8750
	Average	23.8247	23.8040	23.7468	23.7295	23.6635
UnBiased	23	23.9024	23.9050	23.8620	23.8560	23.7260
	24	23.8029	23.8490	23.8000	23.7870	23.8060
	47	23.6536	23.6440	23.6070	23.5940	23.5390
	48	23.8029	23.7750	23.7560	23.7440	23.6820
	Min	23.6536	23.6440	23.6070	23.5940	23.5390
	Max	23.9024	23.9050	23.8620	23.8560	23.8060
	Average	23.7905	23.7933	23.7563	23.7453	23.6883

Limit: <35mA



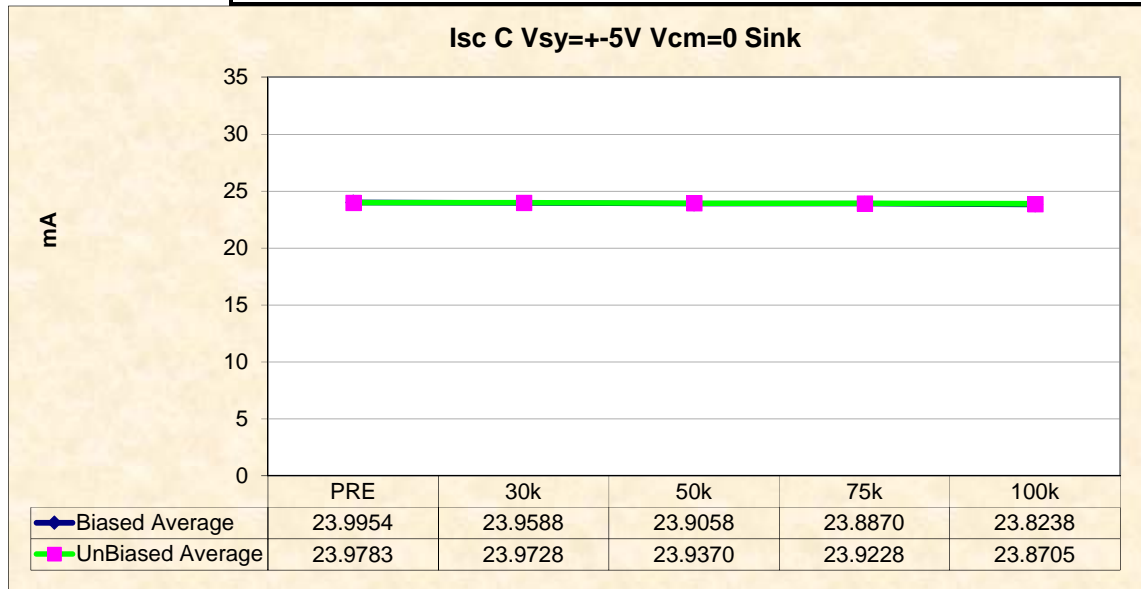
Isc B Vsy=+-5V Vcm=0 Sink						
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	23.9570	0.0000	0.0000	23.2080	23.1840
	54	23.6390	0.0000	0.0000	23.6390	23.6030
Biased	8	23.8740	23.8330	23.8020	23.7770	23.7220
	9	23.9739	23.9520	23.8890	23.8710	23.8090
	55	23.5865	23.5710	23.5020	23.4890	23.4280
	56	23.7302	23.7140	23.6710	23.6390	23.5840
	Min	23.5865	23.5710	23.5020	23.4890	23.4280
	Max	23.9739	23.9520	23.8890	23.8710	23.8090
	Average	23.7912	23.7675	23.7160	23.6940	23.6358
UnBiased	10	23.8302	23.8270	23.7890	23.7830	23.6720
	11	23.7490	23.7890	23.7520	23.7390	23.7220
	57	23.6240	23.6140	23.5830	23.5640	23.5150
	58	23.8177	23.7830	23.7770	23.7580	23.6970
	Min	23.6240	23.6140	23.5830	23.5640	23.5150
	Max	23.8302	23.8270	23.7890	23.7830	23.7220
	Average	23.7552	23.7533	23.7253	23.7110	23.6515

Limit: <35mA



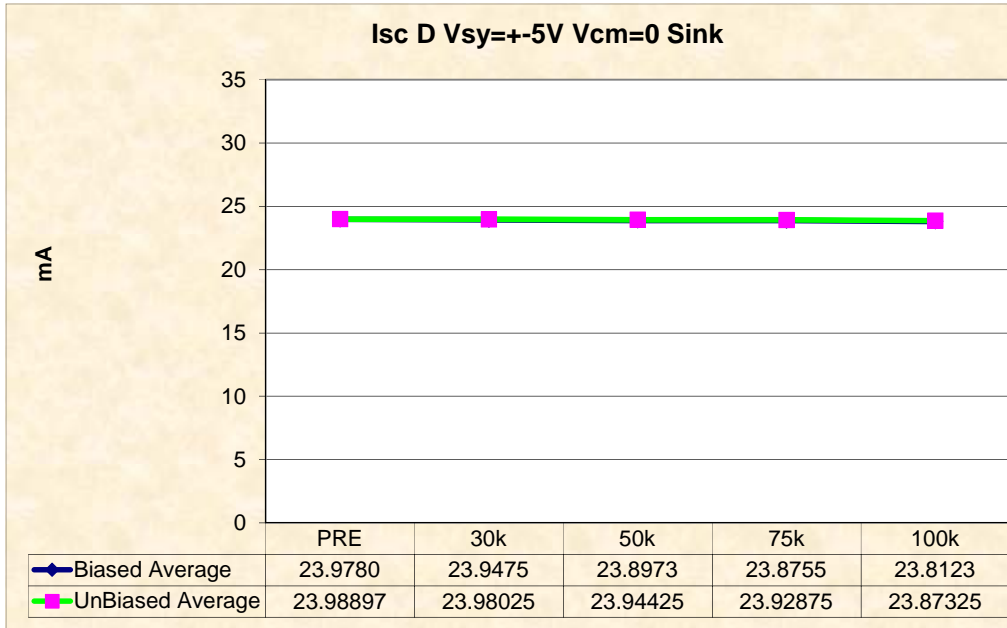
		Isc C Vsy=+-5V Vcm=0 Sink				
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	24.1250	0.0000	0.0000	23.6390	23.6070
	54	23.7620	0.0000	0.0000	23.7700	23.7440
Biased	8	24.1001	24.0380	24.0070	23.9880	23.9310
	9	24.1938	24.1700	24.1010	24.0950	24.0250
	55	23.8377	23.8070	23.7390	23.7200	23.6570
	56	23.8502	23.8200	23.7760	23.7450	23.6820
	Min	23.8377	23.8070	23.7390	23.7200	23.6570
	Max	24.1938	24.1700	24.1010	24.0950	24.0250
	Average	23.9954	23.9588	23.9058	23.8870	23.8238
UnBiased	10	24.0751	24.0510	24.0200	24.0070	23.9130
	11	23.9939	24.0380	23.9820	23.9630	23.9560
	57	23.8502	23.8390	23.7950	23.7820	23.7320
	58	23.9939	23.9630	23.9510	23.9390	23.8810
	Min	23.8502	23.8390	23.7950	23.7820	23.7320
	Max	24.0751	24.0510	24.0200	24.0070	23.9560
	Average	23.9783	23.9728	23.9370	23.9228	23.8705

Limit: <35mA



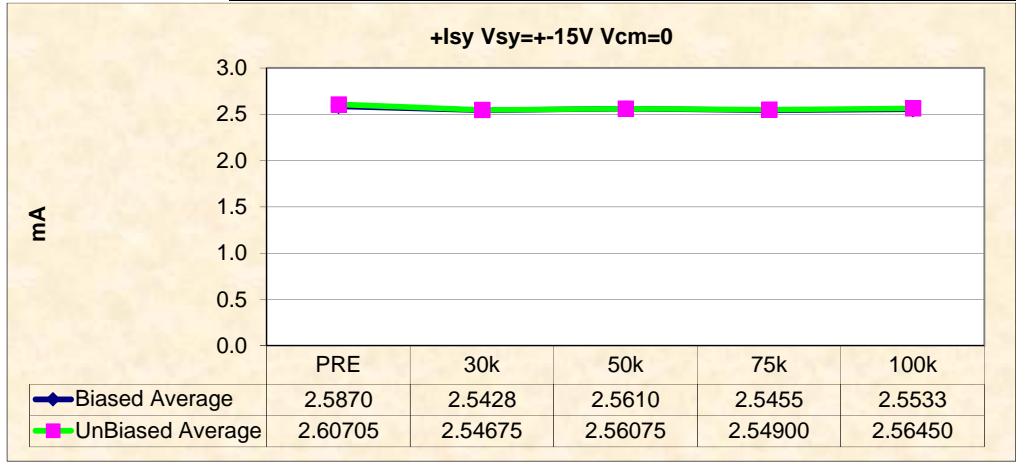
		Isc D Vsy=+-5V Vcm=0 Sink				
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	24.1730	0.0000	0.0000	23.6930	23.6670
	54	23.7980	0.0000	0.0000	23.7930	23.7670
Biased	14	24.0296	23.9800	23.9550	23.9300	23.8670
	15	24.2108	24.1930	24.1300	24.1050	24.0480
	42	23.8171	23.7870	23.7300	23.7180	23.6420
	43	23.8546	23.8300	23.7740	23.7490	23.6920
	Min	23.8171	23.7870	23.7300	23.7180	23.6420
	Max	24.2108	24.1930	24.1300	24.1050	24.0480
	Average	23.9780	23.9475	23.8973	23.8755	23.8123
UnBiased	23	24.1421	24.1300	24.0800	24.0740	23.8670
	24	23.9608	23.9930	23.9490	23.9300	24.0170
	47	23.8484	23.8300	23.7930	23.7740	23.7230
	48	24.00460	23.96800	23.95500	23.93700	23.88600
	Min	23.84835	23.83000	23.79300	23.77400	23.72300
	Max	24.14210	24.13000	24.08000	24.07400	24.01700
	Average	23.98897	23.98025	23.94425	23.92875	23.87325

Limit: <35mA



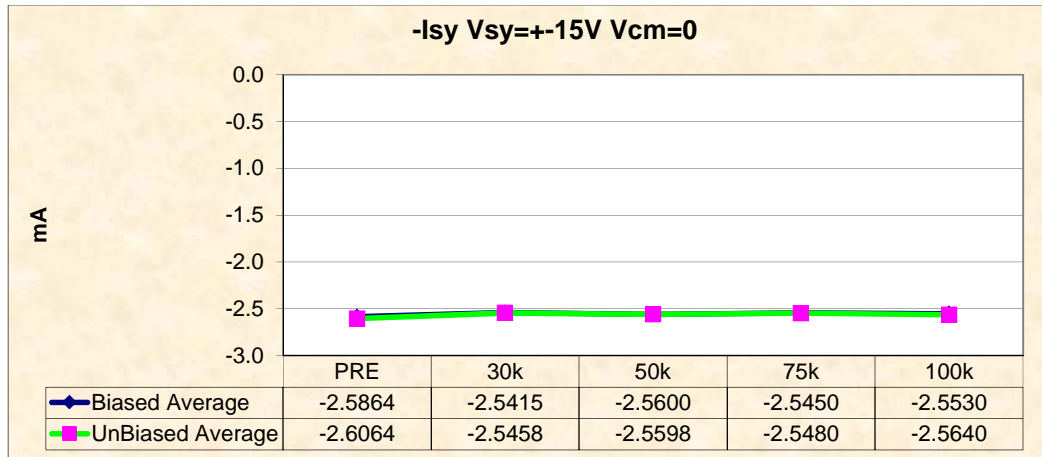
+Isy Vsy=+-15V Vcm=0						
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	2.5730	0.0000	0.0000	2.4690	2.4900
	54	2.5320	0.0000	0.0000	2.5210	2.5410
Biased	14	2.5937	2.5710	2.5730	2.5600	2.5650
	15	2.6193	2.5660	2.5950	2.5740	2.5850
	42	2.5634	2.5090	2.5370	2.5190	2.5280
	43	2.5715	2.5250	2.5390	2.5290	2.5350
	Min	2.5634	2.5090	2.5370	2.5190	2.5280
	Max	2.6193	2.5710	2.5950	2.5740	2.5850
	Average	2.5870	2.5428	2.5610	2.5455	2.5533
UnBiased	23	2.6236	2.5530	2.5730	2.5590	2.5840
	24	2.6298	2.5530	2.5780	2.5630	2.5730
	47	2.5835	2.5230	2.5380	2.5280	2.5400
	48	2.59129	2.55800	2.55400	2.54600	2.56100
	Min	2.58347	2.52300	2.53800	2.52800	2.54000
	Max	2.62978	2.55800	2.57800	2.56300	2.58400
	Average	2.60705	2.54675	2.56075	2.54900	2.56450

Limit: <3mA



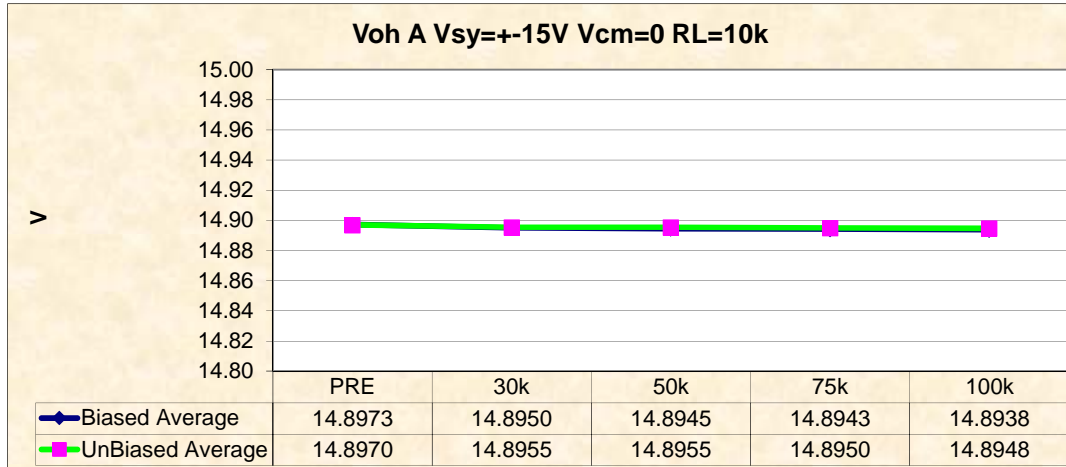
		-Isy Vsy=+-15V Vcm=0				
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	-2.5720	0.0000	0.0000	-2.4680	-2.4890
	54	-2.5310	0.0000	0.0000	-2.5200	-2.5400
Biased	8	-2.5929	-2.5690	-2.5720	-2.5590	-2.5650
	9	-2.6188	-2.5650	-2.5940	-2.5740	-2.5840
	55	-2.5630	-2.5080	-2.5360	-2.5180	-2.5280
	56	-2.5710	-2.5240	-2.5380	-2.5290	-2.5350
	Min	-2.6188	-2.5690	-2.5940	-2.5740	-2.5840
	Max	-2.5630	-2.5080	-2.5360	-2.5180	-2.5280
	Average	-2.5864	-2.5415	-2.5600	-2.5450	-2.5530
UnBiased	10	-2.6231	-2.5520	-2.5720	-2.5580	-2.5830
	11	-2.6291	-2.5530	-2.5770	-2.5620	-2.5730
	57	-2.5826	-2.5220	-2.5370	-2.5270	-2.5390
	58	-2.5907	-2.5560	-2.5530	-2.5450	-2.5610
	Min	-2.6291	-2.5560	-2.5770	-2.5620	-2.5830
	Max	-2.5826	-2.5220	-2.5370	-2.5270	-2.5390
Average	-2.6064	-2.5458	-2.5598	-2.5480	-2.5640	

Limit: >-3mA



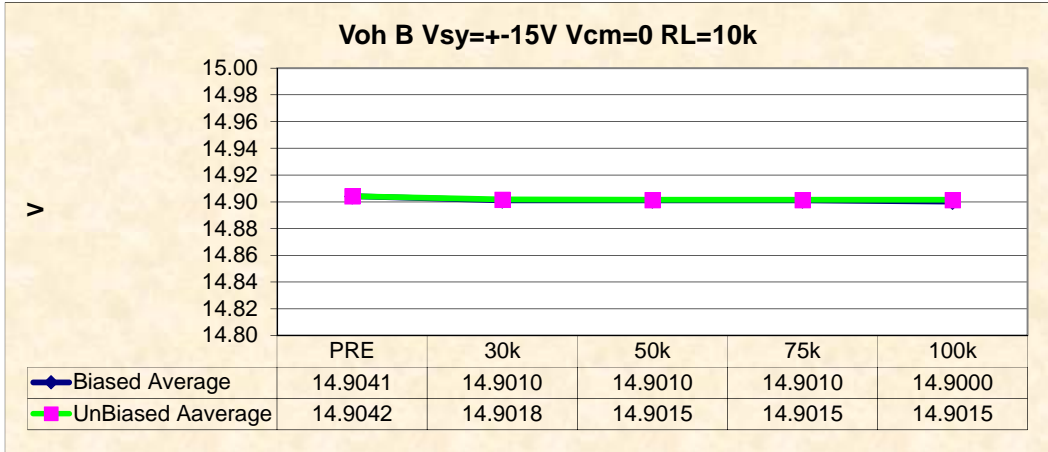
Voh A Vsy=+-15V Vcm=0 RL=10k						
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	14.9020	0.0000	0.0000	14.8960	14.8960
	54	14.8980	0.0000	0.0000	14.8940	14.8940
Biased	8	14.8966	14.8940	14.8940	14.8930	14.8930
	9	14.8976	14.8950	14.8950	14.8950	14.8940
	55	14.8981	14.8960	14.8950	14.8950	14.8950
	56	14.8967	14.8950	14.8940	14.8940	14.8930
	Min	14.8966	14.8940	14.8940	14.8930	14.8930
	Max	14.8981	14.8960	14.8950	14.8950	14.8950
	Average	14.8973	14.8950	14.8945	14.8943	14.8938
UnBiased	10	14.8967	14.8940	14.8940	14.8940	14.8960
	11	14.8977	14.8970	14.8970	14.8970	14.8940
	57	14.8963	14.8950	14.8950	14.8940	14.8940
	58	14.8974	14.8960	14.8960	14.8950	14.8950
	Min	14.8963	14.8940	14.8940	14.8940	14.8940
	Max	14.8977	14.8970	14.8970	14.8970	14.8960
	Average	14.8970	14.8955	14.8955	14.8950	14.8948

Limit: >14.8V



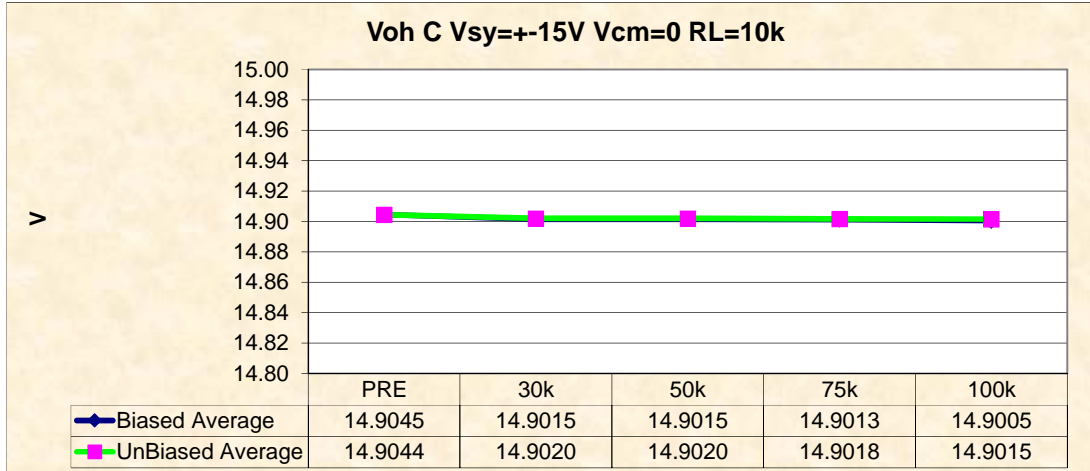
Voh B Vsy=+-15V Vcm=0 RL=10k						
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	14.9040	0.0000	0.0000	14.9040	14.9030
	54	14.9020	0.0000	0.0000	14.9010	14.9010
Biased	14	14.9032	14.9000	14.9000	14.9000	14.8990
	15	14.9042	14.9010	14.9010	14.9010	14.9000
	42	14.9040	14.9010	14.9010	14.9010	14.9000
	43	14.9051	14.9020	14.9020	14.9020	14.9010
	Min	14.9032	14.9000	14.9000	14.9000	14.8990
	Max	14.9051	14.9020	14.9020	14.9020	14.9010
	Average	14.9041	14.9010	14.9010	14.9010	14.9000
UnBiased	23	14.9040	14.9000	14.9000	14.9000	14.9030
	24	14.9046	14.9040	14.9030	14.9030	14.9000
	47	14.9038	14.9010	14.9010	14.9010	14.9010
	48	14.9045	14.9020	14.9020	14.9020	14.9020
	Min	14.9038	14.9000	14.9000	14.9000	14.9000
	Max	14.9046	14.9040	14.9030	14.9030	14.9030
	Average	14.9042	14.9018	14.9015	14.9015	14.9015

Limit: >14.8V



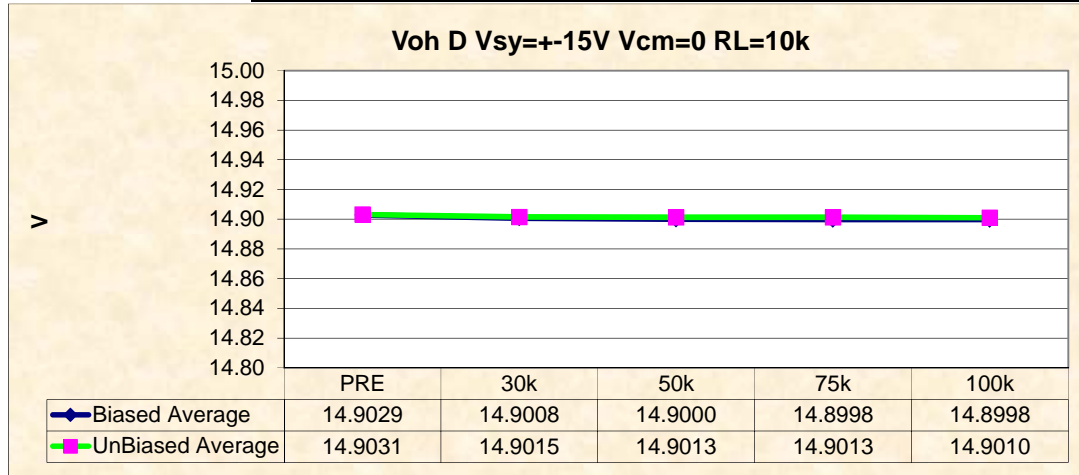
		Voh C sy=+-15V Vcm=0 RL=10k				
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	14.9050	0.0000	0.0000	14.9040	14.9030
	54	14.9010	0.0000	0.0000	14.9010	14.9010
Biased	8	14.9042	14.9010	14.9010	14.9010	14.9000
	9	14.9049	14.9020	14.9020	14.9020	14.9010
	55	14.9041	14.9010	14.9010	14.9010	14.9000
	56	14.9046	14.9020	14.9020	14.9010	14.9010
	Min	14.9041	14.9010	14.9010	14.9010	14.9000
	Max	14.9049	14.9020	14.9020	14.9020	14.9010
	Average	14.9045	14.9015	14.9015	14.9013	14.9005
UnBiased	10	14.9038	14.9000	14.9000	14.9000	14.9040
	11	14.9060	14.9050	14.9050	14.9050	14.9000
	57	14.9032	14.9010	14.9010	14.9000	14.9000
	58	14.9047	14.9020	14.9020	14.9020	14.9020
	Min	14.9032	14.9000	14.9000	14.9000	14.9000
	Max	14.9060	14.9050	14.9050	14.9050	14.9040
	Average	14.9044	14.9020	14.9020	14.9018	14.9015

Limit: >14.8V



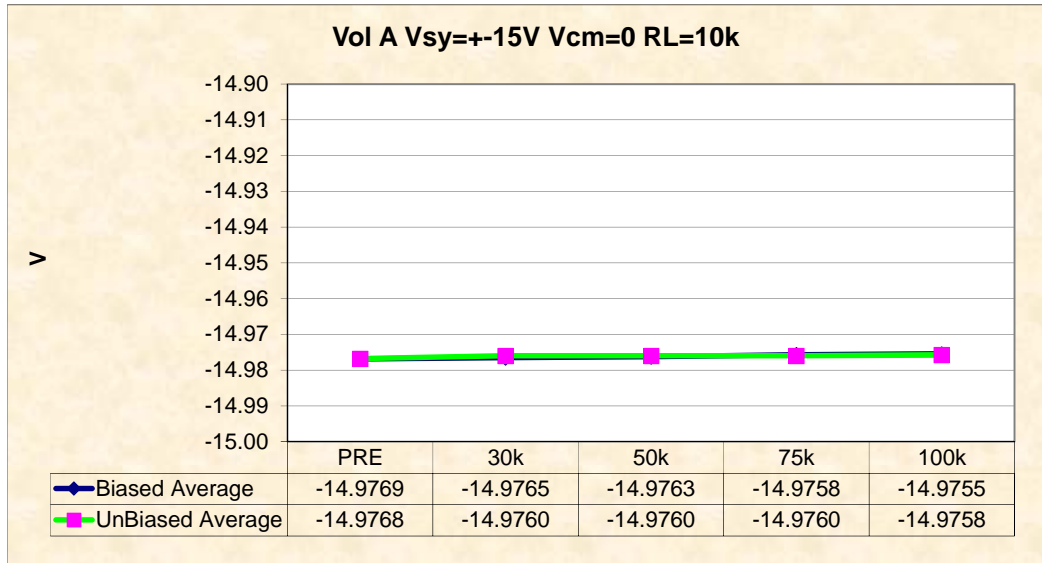
		Voh D Vsy=+15V Vcm=0 RL=10k				
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	14.9030	0.0000	0.0000	14.9010	14.9010
	54	14.8990	0.0000	0.0000	14.8990	14.8990
Biased	8	14.9014	14.8990	14.8990	14.8980	14.8980
	9	14.9032	14.9010	14.9000	14.9000	14.9000
	55	14.9030	14.9010	14.9000	14.9000	14.9000
	56	14.9040	14.9020	14.9010	14.9010	14.9010
	Min	14.9014	14.8990	14.8990	14.8980	14.8980
	Max	14.9040	14.9020	14.9010	14.9010	14.9010
	Average	14.9029	14.9008	14.9000	14.8998	14.8998
UnBiased	10	14.9038	14.9010	14.9010	14.9010	14.9030
	11	14.9039	14.9030	14.9030	14.9030	14.9010
	57	14.9023	14.9010	14.9000	14.9000	14.9000
	58	14.9025	14.9010	14.9010	14.9010	14.9000
	Min	14.9023	14.9010	14.9000	14.9000	14.9000
	Max	14.9039	14.9030	14.9030	14.9030	14.9030
	Average	14.9031	14.9015	14.9013	14.9013	14.9010

Limit: >14.8V



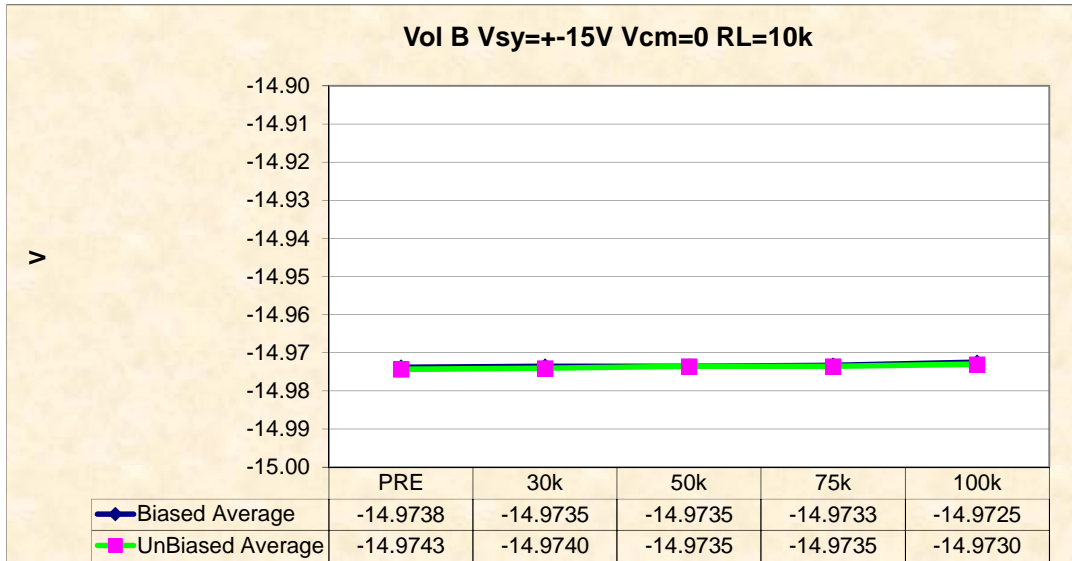
Vol A Vsy=+-15V Vcm=0 RL=10k						
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	-14.9750	0.0000	0.0000	-14.9770	-14.9770
	54	-14.9740	0.0000	0.0000	-14.9770	-14.9770
Biased	14	-14.9757	-14.9760	-14.9760	-14.9750	-14.9750
	15	-14.9776	-14.9770	-14.9760	-14.9760	-14.9760
	42	-14.9765	-14.9760	-14.9760	-14.9760	-14.9750
	43	-14.9777	-14.9770	-14.9770	-14.9760	-14.9760
	Min	-14.9777	-14.9770	-14.9770	-14.9760	-14.9760
	Max	-14.9757	-14.9760	-14.9760	-14.9750	-14.9750
	Average	-14.9769	-14.9765	-14.9763	-14.9758	-14.9755
UnBiased	23	-14.9772	-14.9760	-14.9760	-14.9760	-14.9750
	24	-14.9752	-14.9750	-14.9750	-14.9750	-14.9760
	47	-14.9769	-14.9760	-14.9760	-14.9760	-14.9760
	48	-14.9778	-14.9770	-14.9770	-14.9770	-14.9760
	Min	-14.9778	-14.9770	-14.9770	-14.9770	-14.9760
	Max	-14.9752	-14.9750	-14.9750	-14.9750	-14.9750
	Average	-14.9768	-14.9760	-14.9760	-14.9760	-14.9758

Limit: <-14.9V



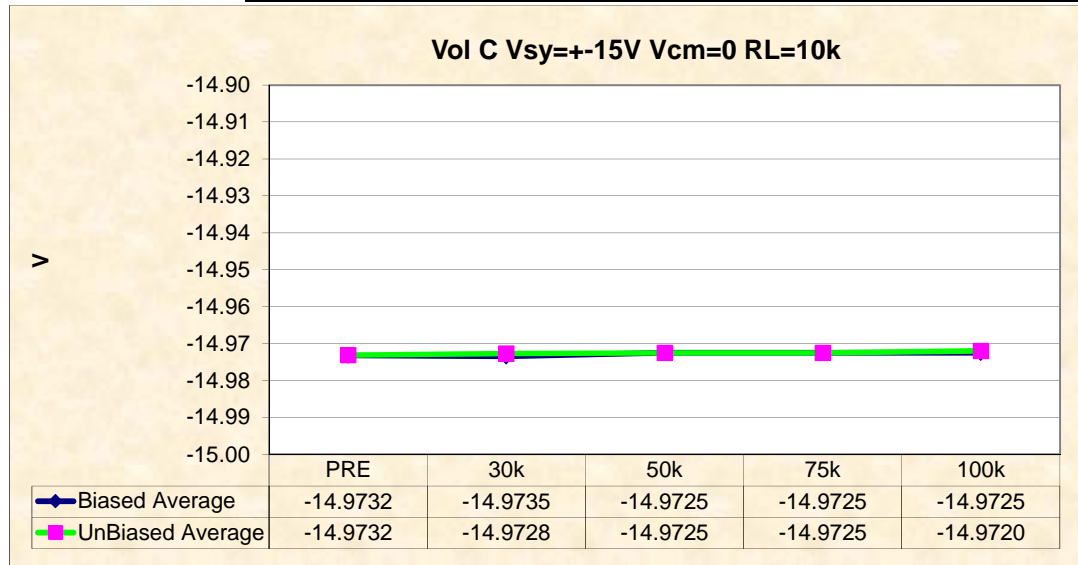
		Vol B Vsy=+-15V Vcm=0 RL=10k				
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	-14.9730	0.0000	0.0000	-14.9740	-14.9740
	54	-14.9730	0.0000	0.0000	-14.9740	-14.9740
Biased	8	-14.9728	-14.9730	-14.9730	-14.9730	-14.9720
	9	-14.9747	-14.9740	-14.9740	-14.9730	-14.9730
	55	-14.9728	-14.9730	-14.9730	-14.9730	-14.9720
	56	-14.9748	-14.9740	-14.9740	-14.9740	-14.9730
	Min	-14.9748	-14.9740	-14.9740	-14.9740	-14.9730
	Max	-14.9728	-14.9730	-14.9730	-14.9730	-14.9720
	Average	-14.9738	-14.9735	-14.9735	-14.9733	-14.9725
UnBiased	10	-14.9741	-14.9730	-14.9730	-14.9730	-14.9730
	11	-14.9747	-14.9750	-14.9740	-14.9740	-14.9730
	57	-14.9736	-14.9740	-14.9730	-14.9730	-14.9730
	58	-14.9749	-14.9740	-14.9740	-14.9740	-14.9730
	Min	-14.9749	-14.9750	-14.9740	-14.9740	-14.9730
	Max	-14.9736	-14.9730	-14.9730	-14.9730	-14.9730
	Average	-14.9743	-14.9740	-14.9735	-14.9735	-14.9730

Limit: <-14.9V



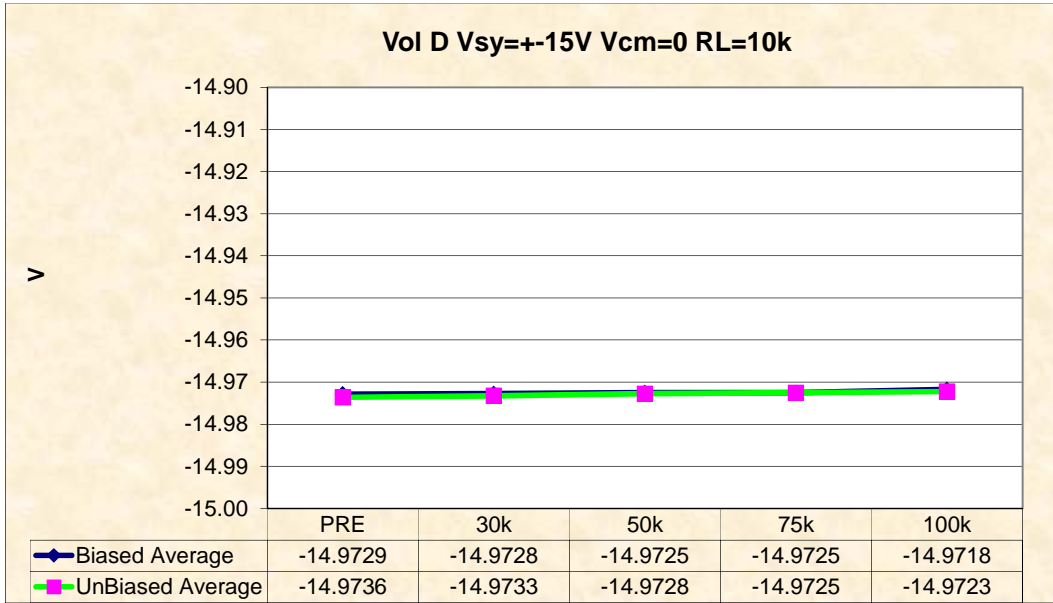
		Vol C Vsy=+15V Vcm=0 RL=10k				
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	-14.9730	0.0000	0.0000	-14.9740	-14.9730
	54	-14.9720	0.0000	0.0000	-14.9730	-14.9730
Biased	8	-14.9722	-14.9730	-14.9720	-14.9720	-14.9720
	9	-14.9741	-14.9740	-14.9730	-14.9730	-14.9730
	55	-14.9723	-14.9730	-14.9720	-14.9720	-14.9720
	56	-14.9743	-14.9740	-14.9730	-14.9730	-14.9730
	Min	-14.9743	-14.9740	-14.9730	-14.9730	-14.9730
	Max	-14.9722	-14.9730	-14.9720	-14.9720	-14.9720
	Average	-14.9732	-14.9735	-14.9725	-14.9725	-14.9725
UnBiased	10	-14.9738	-14.9730	-14.9730	-14.9730	-14.9710
	11	-14.9712	-14.9720	-14.9710	-14.9710	-14.9720
	57	-14.9733	-14.9730	-14.9730	-14.9730	-14.9720
	58	-14.9744	-14.9730	-14.9730	-14.9730	-14.9730
	Min	-14.9744	-14.9730	-14.9730	-14.9730	-14.9730
	Max	-14.9712	-14.9720	-14.9710	-14.9710	-14.9710
	Average	-14.9732	-14.9728	-14.9725	-14.9725	-14.9720

Limit: <-14.9V



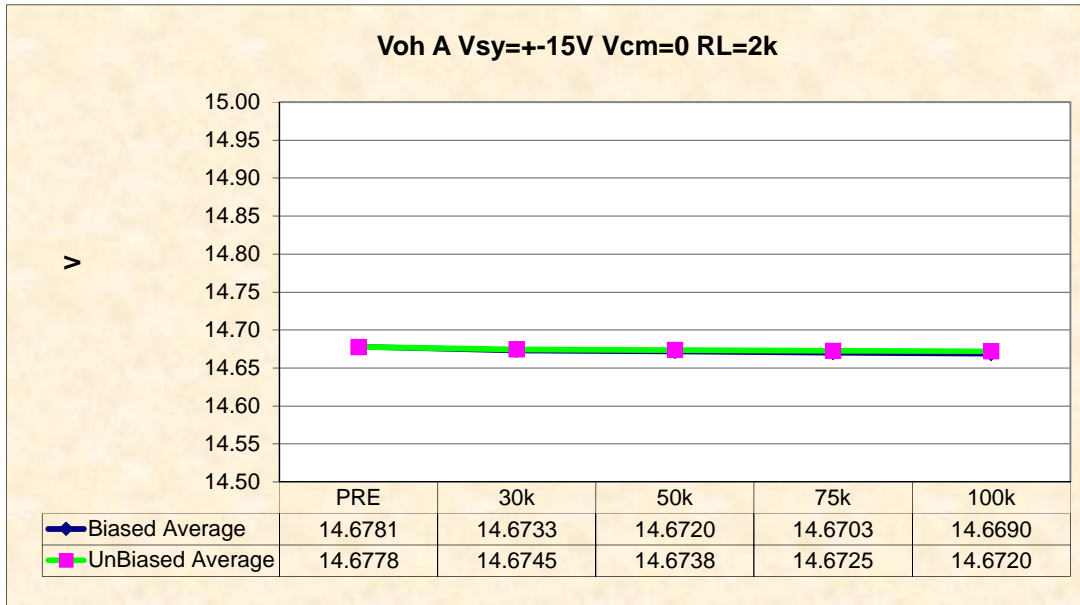
Vol D Vsy=+-15V Vcm=0 RL=10k						
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	-14.9730	0.0000	0.0000	-14.9730	-14.9730
	54	-14.9720	0.0000	0.0000	-14.9730	-14.9730
Biased	14	-14.9717	-14.9720	-14.9720	-14.9720	-14.9710
	15	-14.9738	-14.9730	-14.9730	-14.9730	-14.9720
	42	-14.9720	-14.9730	-14.9720	-14.9720	-14.9720
	43	-14.9740	-14.9730	-14.9730	-14.9730	-14.9720
	Min	-14.9740	-14.9730	-14.9730	-14.9730	-14.9720
	Max	-14.9717	-14.9720	-14.9720	-14.9720	-14.9710
	Average	-14.9729	-14.9728	-14.9725	-14.9725	-14.9718
UnBiased	23	-14.9734	-14.9730	-14.9720	-14.9720	-14.9730
	24	-14.9738	-14.9740	-14.9730	-14.9730	-14.9720
	47	-14.9732	-14.9730	-14.9730	-14.9720	-14.9720
	48	-14.9740	-14.9730	-14.9730	-14.9730	-14.9720
	Min	-14.9740	-14.9740	-14.9730	-14.9730	-14.9730
	Max	-14.9732	-14.9730	-14.9720	-14.9720	-14.9720
	Average	-14.9736	-14.9733	-14.9728	-14.9725	-14.9723

Limit: <-14.9V



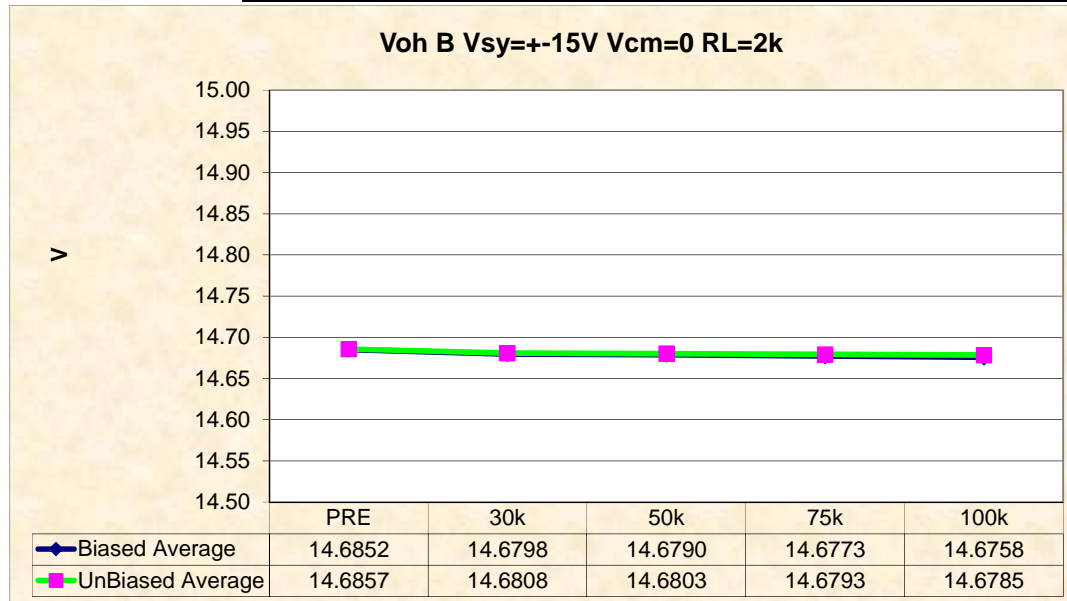
		Voh A Vsy=+-15V Vcm=0 RL=2k				
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	14.6840	0.0000	0.0000	14.6780	14.6790
	54	14.6700	0.0000	0.0000	14.6680	14.6680
Biased	8	14.6743	14.6700	14.6680	14.6660	14.6650
	9	14.6796	14.6750	14.6740	14.6720	14.6710
	55	14.6823	14.6770	14.6760	14.6750	14.6730
	56	14.6761	14.6710	14.6700	14.6680	14.6670
	Min	14.6743	14.6700	14.6680	14.6660	14.6650
	Max	14.6823	14.6770	14.6760	14.6750	14.6730
	Average	14.6781	14.6733	14.6720	14.6703	14.6690
UnBiased	10	14.6756	14.6710	14.6700	14.6690	14.6770
	11	14.6818	14.6800	14.6790	14.6770	14.6690
	57	14.6741	14.6700	14.6700	14.6690	14.6680
	58	14.6797	14.6770	14.6760	14.6750	14.6740
	Min	14.6741	14.6700	14.6700	14.6690	14.6680
	Max	14.6818	14.6800	14.6790	14.6770	14.6770
	Average	14.6778	14.6745	14.6738	14.6725	14.6720

Limit: >14.5V



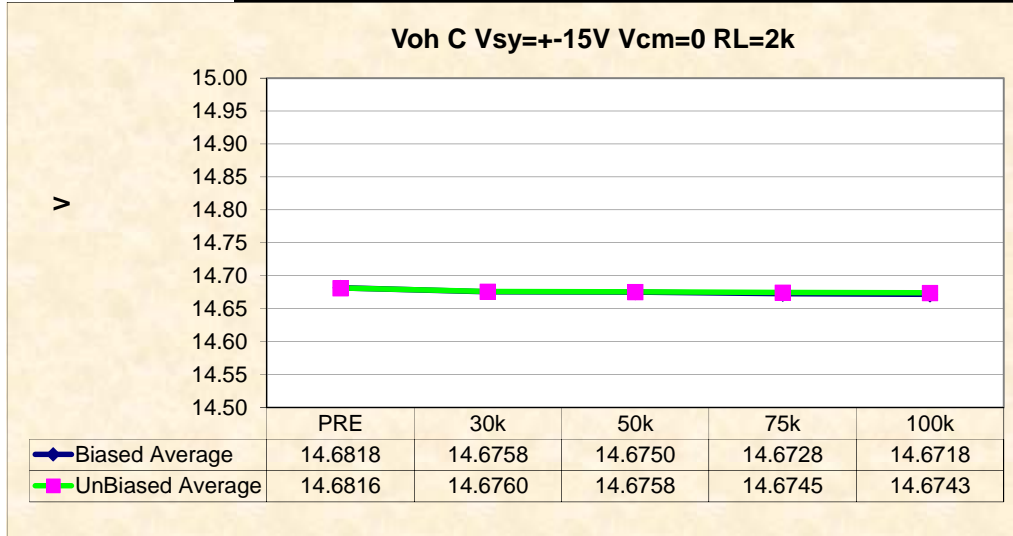
		Voh B Vsy=+-15V Vcm=0 RL=2k				
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	14.6870	0.0000	0.0000	14.6870	14.6880
	54	14.6750	0.0000	0.0000	14.6740	14.6750
Biased	8	14.6807	14.6760	14.6740	14.6720	14.6710
	9	14.6852	14.6790	14.6790	14.6770	14.6760
	55	14.6857	14.6800	14.6800	14.6780	14.6760
	56	14.6893	14.6840	14.6830	14.6820	14.6800
	Min	14.6807	14.6760	14.6740	14.6720	14.6710
	Max	14.6893	14.6840	14.6830	14.6820	14.6800
	Average	14.6852	14.6798	14.6790	14.6773	14.6758
UnBiased	10	14.6843	14.6770	14.6770	14.6760	14.6830
	11	14.6881	14.6850	14.6840	14.6830	14.6750
	57	14.6838	14.6780	14.6780	14.6770	14.6760
	58	14.6865	14.6830	14.6820	14.6810	14.6800
	Min	14.6838	14.6770	14.6770	14.6760	14.6750
	Max	14.6881	14.6850	14.6840	14.6830	14.6830
	Average	14.6857	14.6808	14.6803	14.6793	14.6785

Limit: >14.5V



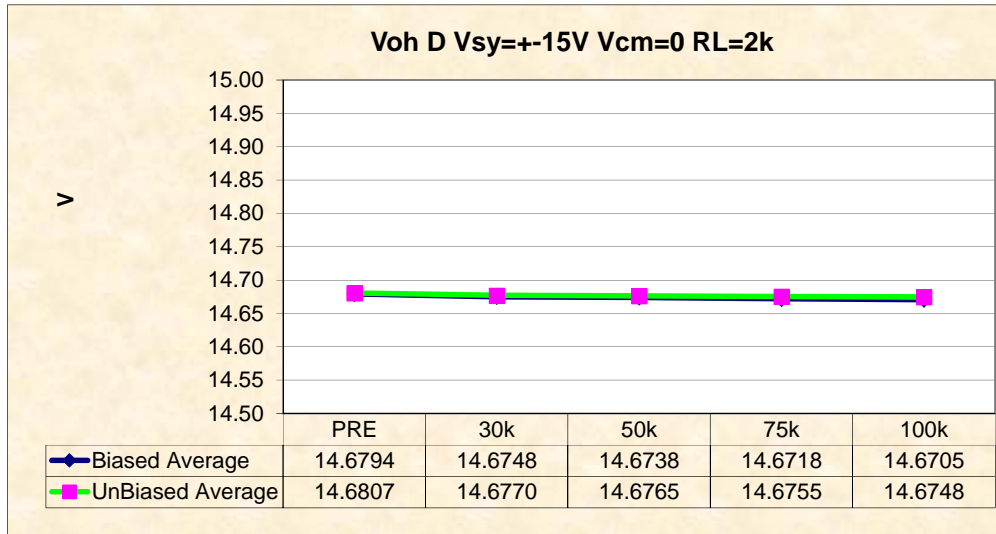
Voh C Vsy=+-15V Vcm=0 RL=2k						
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	14.6880	0.0000	0.0000	14.6820	14.6830
	54	14.6700	0.0000	0.0000	14.6700	14.6710
Biased	14	14.6788	14.6730	14.6720	14.6700	14.6690
	15	14.6831	14.6770	14.6770	14.6740	14.6730
	42	14.6816	14.6750	14.6740	14.6720	14.6710
	43	14.6835	14.6780	14.6770	14.6750	14.6740
	Min	14.6788	14.6730	14.6720	14.6700	14.6690
	Max	14.6835	14.6780	14.6770	14.6750	14.6740
	Average	14.6818	14.6758	14.6750	14.6728	14.6718
UnBiased	23	14.6785	14.6700	14.6700	14.6690	14.6830
	24	14.6880	14.6850	14.6850	14.6830	14.6690
	47	14.6761	14.6700	14.6700	14.6690	14.6680
	48	14.6836	14.6790	14.6780	14.6770	14.6770
	Min	14.6761	14.6700	14.6700	14.6690	14.6680
	Max	14.6880	14.6850	14.6850	14.6830	14.6830
Average	14.6816	14.6760	14.6758	14.6745	14.6743	

Limit: >14.5V



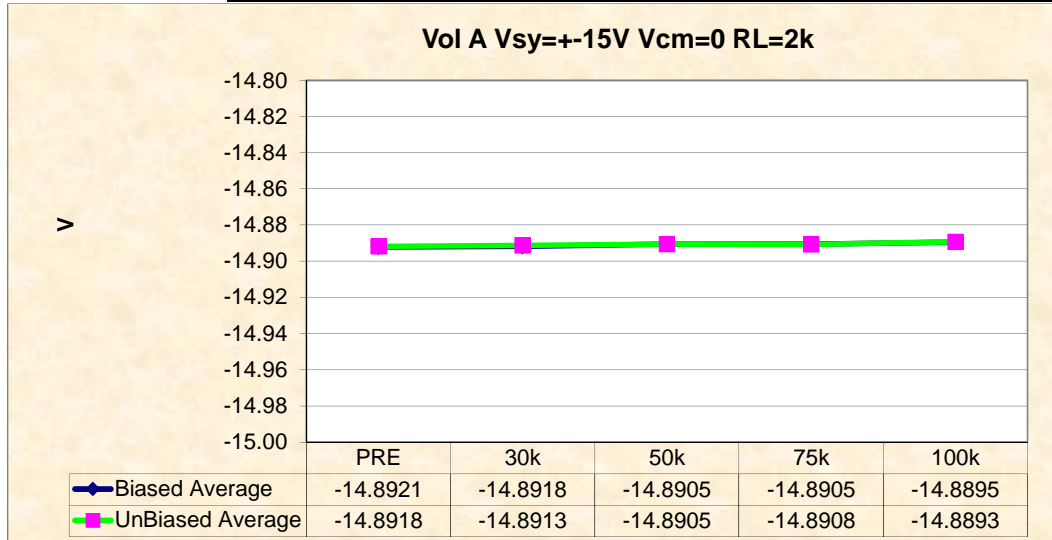
		Voh D Vsy=+-15V Vcm=0 RL=2k				
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	14.6850	0.0000	0.0000	14.6760	14.6760
	54	14.6660	0.0000	0.0000	14.6660	14.6660
Biased	8	14.6711	14.6660	14.6650	14.6630	14.6620
	9	14.6801	14.6750	14.6740	14.6720	14.6710
	55	14.6812	14.6770	14.6760	14.6740	14.6720
	56	14.6853	14.6810	14.6800	14.6780	14.6770
	Min	14.6711	14.6660	14.6650	14.6630	14.6620
	Max	14.6853	14.6810	14.6800	14.6780	14.6770
	Average	14.6794	14.6748	14.6738	14.6718	14.6705
UnBiased	10	14.6827	14.6770	14.6770	14.6760	14.6790
	11	14.6838	14.6820	14.6810	14.6800	14.6750
	57	14.6770	14.6730	14.6720	14.6710	14.6710
	58	14.6794	14.6760	14.6760	14.6750	14.6740
	Min	14.6770	14.6730	14.6720	14.6710	14.6710
	Max	14.6838	14.6820	14.6810	14.6800	14.6790
	Average	14.6807	14.6770	14.6765	14.6755	14.6748

Limit: >14.5V



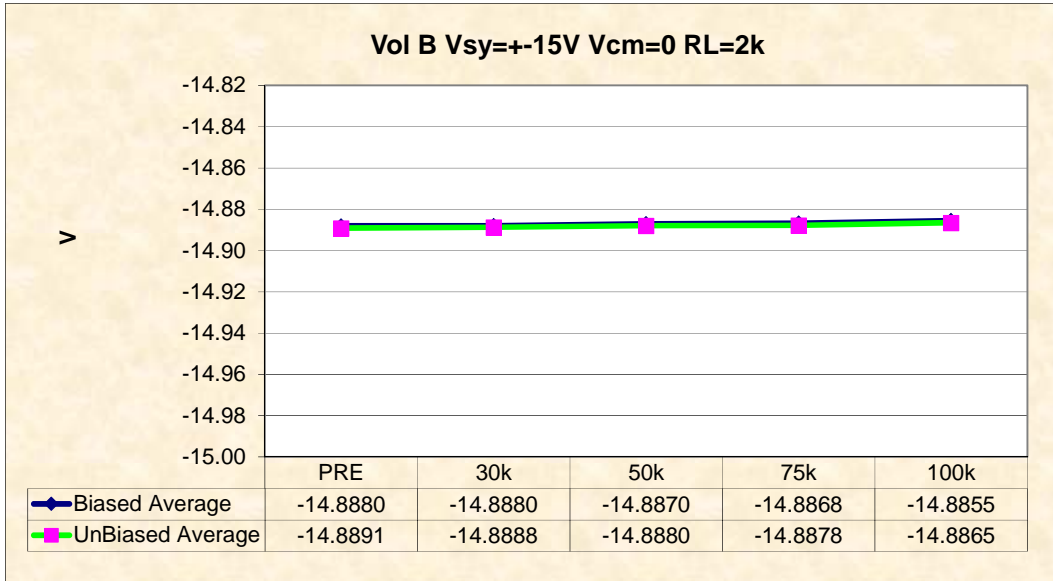
		Vol A Vsy=+-15V Vcm=0 RL=2k				
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	-14.8910	0.0000	0.0000	-14.8920	-14.8910
	54	-14.8890	0.0000	0.0000	-14.8920	-14.8920
Biased	8	-14.8896	-14.8900	-14.8900	-14.8900	-14.8890
	9	-14.8942	-14.8930	-14.8910	-14.8910	-14.8900
	55	-14.8908	-14.8920	-14.8900	-14.8900	-14.8890
	56	-14.8939	-14.8920	-14.8910	-14.8910	-14.8900
	Min	-14.8942	-14.8930	-14.8910	-14.8910	-14.8900
	Max	-14.8896	-14.8900	-14.8900	-14.8900	-14.8890
	Average	-14.8921	-14.8918	-14.8905	-14.8905	-14.8895
UnBiased	10	-14.8929	-14.8920	-14.8910	-14.8910	-14.8870
	11	-14.8880	-14.8890	-14.8880	-14.8890	-14.8900
	57	-14.8920	-14.8920	-14.8910	-14.8910	-14.8900
	58	-14.8943	-14.8920	-14.8920	-14.8920	-14.8900
	Min	-14.8943	-14.8920	-14.8920	-14.8920	-14.8900
	Max	-14.8880	-14.8890	-14.8880	-14.8890	-14.8870
	Average	-14.8918	-14.8913	-14.8905	-14.8908	-14.8893

Limit: <-14.8V



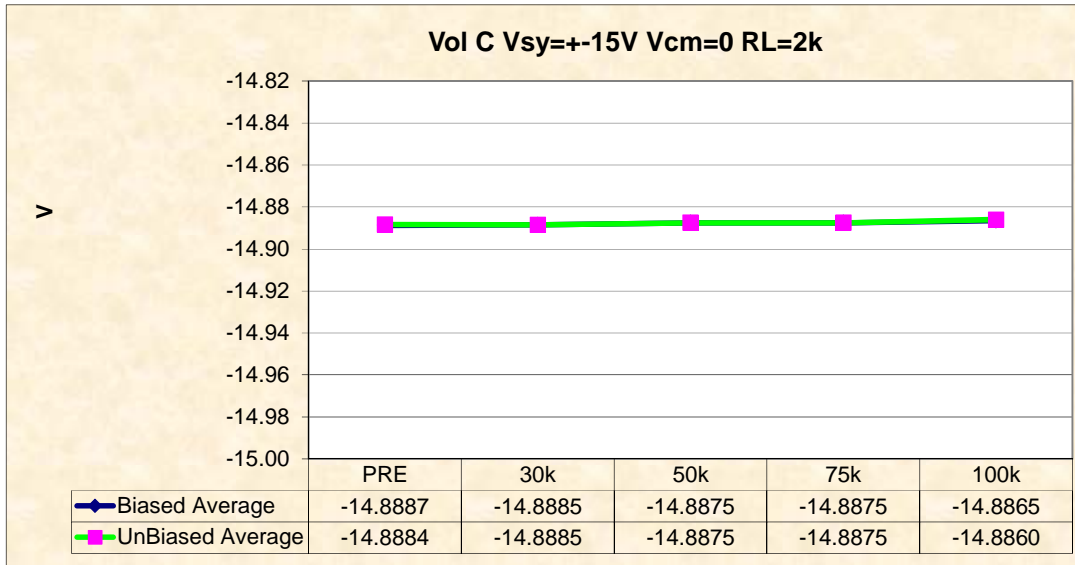
Vol B Vsy=+-15V Vcm=0 RL=2k						
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	-14.8890	0.0000	0.0000	-14.8880	-14.8870
	54	-14.8870	0.0000	0.0000	-14.8890	-14.8880
Biased	14	-14.8860	-14.8870	-14.8860	-14.8860	-14.8850
	15	-14.8904	-14.8890	-14.8880	-14.8880	-14.8860
	42	-14.8856	-14.8870	-14.8860	-14.8860	-14.8850
	43	-14.8902	-14.8890	-14.8880	-14.8870	-14.8860
	Min	-14.8904	-14.8890	-14.8880	-14.8880	-14.8860
	Max	-14.8856	-14.8870	-14.8860	-14.8860	-14.8850
	Average	-14.8880	-14.8880	-14.8870	-14.8868	-14.8855
UnBiased	23	-14.8887	-14.8880	-14.8870	-14.8870	-14.8870
	24	-14.8898	-14.8900	-14.8890	-14.8890	-14.8860
	47	-14.8873	-14.8880	-14.8870	-14.8870	-14.8860
	48	-14.8907	-14.8890	-14.8890	-14.8880	-14.8870
	Min	-14.8907	-14.8900	-14.8890	-14.8890	-14.8870
	Max	-14.8873	-14.8880	-14.8870	-14.8870	-14.8860
	Average	-14.8891	-14.8888	-14.8880	-14.8878	-14.8865

Limit: <-14.8V



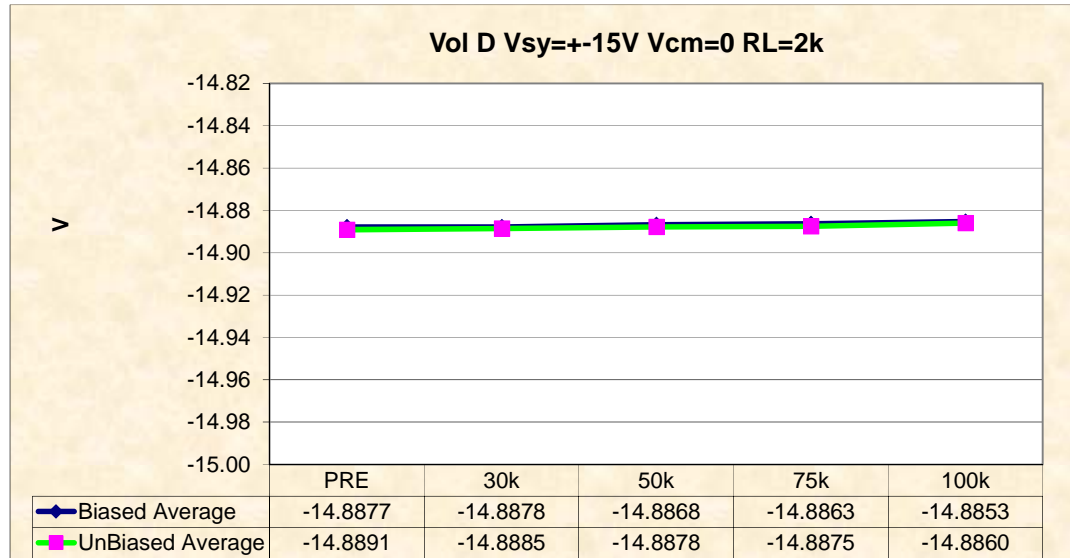
		Vol C Vsy=+-15V Vcm=0 RL=2k				
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	-14.8900	0.0000	0.0000	-14.8890	-14.8880
	54	-14.8880	0.0000	0.0000	-14.8890	-14.8880
Biased	8	-14.8865	-14.8870	-14.8870	-14.8870	-14.8860
	9	-14.8911	-14.8900	-14.8880	-14.8880	-14.8870
	55	-14.8865	-14.8880	-14.8870	-14.8870	-14.8860
	56	-14.8907	-14.8890	-14.8880	-14.8880	-14.8870
	Min	-14.8911	-14.8900	-14.8880	-14.8880	-14.8870
	Max	-14.8865	-14.8870	-14.8870	-14.8870	-14.8860
	Average	-14.8887	-14.8885	-14.8875	-14.8875	-14.8865
UnBiased	10	-14.8899	-14.8890	-14.8880	-14.8880	-14.8840
	11	-14.8838	-14.8860	-14.8850	-14.8850	-14.8870
	57	-14.8886	-14.8890	-14.8880	-14.8880	-14.8860
	58	-14.8913	-14.8900	-14.8890	-14.8890	-14.8870
	Min	-14.8913	-14.8900	-14.8890	-14.8890	-14.8870
	Max	-14.8838	-14.8860	-14.8850	-14.8850	-14.8840
	Average	-14.8884	-14.8885	-14.8875	-14.8875	-14.8860

Limit: <-14.8V



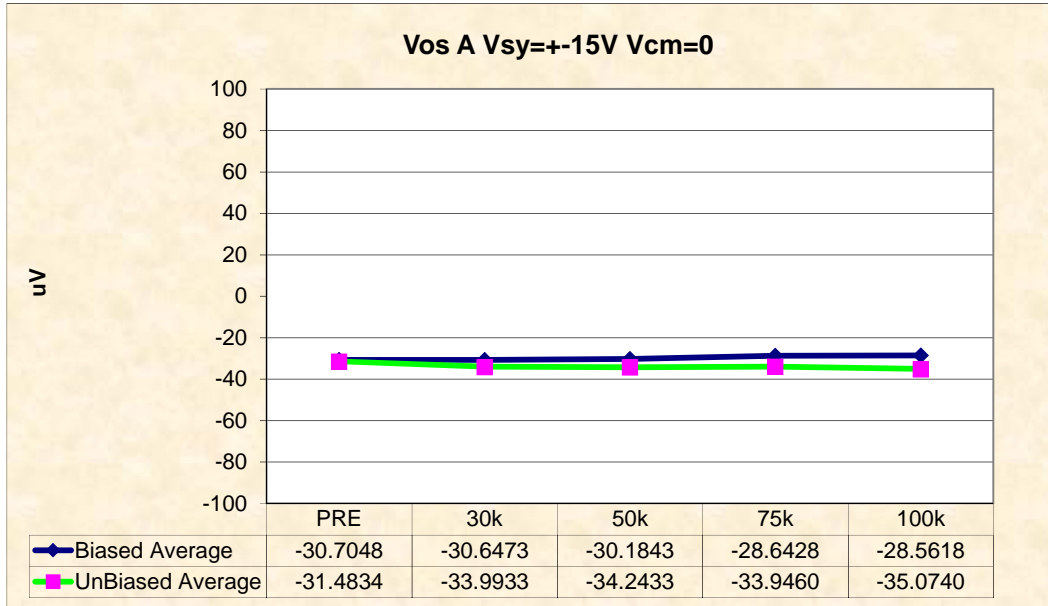
		Vol D Vsy=+-15V Vcm=0 RL=2k				
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	-14.8900	0.0000	0.0000	-14.8880	-14.8870
	54	-14.8870	0.0000	0.0000	-14.8880	-14.8880
Biased	8	-14.8852	-14.8860	-14.8860	-14.8850	-14.8840
	9	-14.8901	-14.8890	-14.8880	-14.8870	-14.8860
	55	-14.8858	-14.8870	-14.8860	-14.8860	-14.8850
	56	-14.8898	-14.8890	-14.8870	-14.8870	-14.8860
	Min	-14.8901	-14.8890	-14.8880	-14.8870	-14.8860
	Max	-14.8852	-14.8860	-14.8860	-14.8850	-14.8840
	Average	-14.8877	-14.8878	-14.8868	-14.8863	-14.8853
UnBiased	10	-14.8888	-14.8880	-14.8870	-14.8870	-14.8870
	11	-14.8894	-14.8900	-14.8890	-14.8880	-14.8860
	57	-14.8879	-14.8880	-14.8870	-14.8870	-14.8850
	58	-14.8901	-14.8880	-14.8880	-14.8880	-14.8860
	Min	-14.8901	-14.8900	-14.8890	-14.8880	-14.8870
	Max	-14.8879	-14.8880	-14.8870	-14.8870	-14.8850
	Average	-14.8891	-14.8885	-14.8878	-14.8875	-14.8860

Limit: <-14.8V



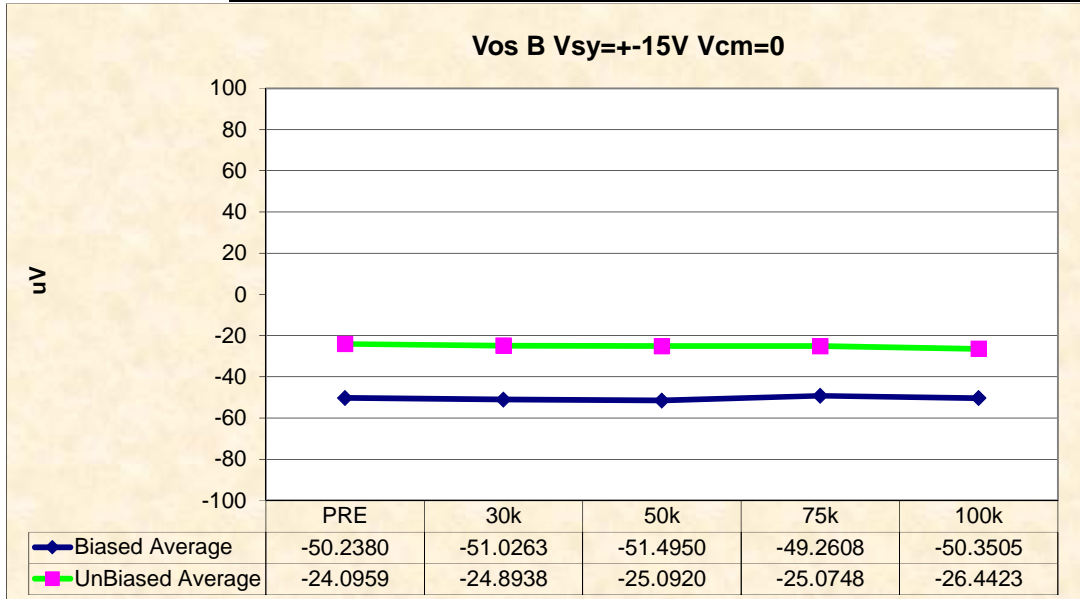
		Vos A Vsy=+-15V Vcm=0				
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	-38.3000	0.0000	0.0000	-44.1530	-44.8950
	54	-31.4020	0.0000	0.0000	-32.3950	-32.8430
Biased	14	-15.4522	-13.6890	-12.2380	-11.0740	-10.3460
	15	-17.8726	-16.4410	-16.7220	-15.0650	-14.8050
	42	-56.3801	-57.4810	-57.0740	-55.3860	-56.0470
	43	-33.1142	-34.9780	-34.7030	-33.0460	-33.0490
	Min	-56.3801	-57.4810	-57.0740	-55.3860	-56.0470
	Max	-15.4522	-13.6890	-12.2380	-11.0740	-10.3460
	Average	-30.7048	-30.6473	-30.1843	-28.6428	-28.5618
UnBiased	23	-13.3445	-15.6840	-15.0650	-14.6580	-50.4490
	24	-47.3176	-49.1880	-49.5500	-48.6560	-15.6810
	47	-42.8458	-43.6530	-44.9100	-44.8660	-45.4640
	48	-22.4257	-27.4480	-27.4480	-27.6040	-28.7020
	Min	-47.3176	-49.1880	-49.5500	-48.6560	-50.4490
	Max	-13.3445	-15.6840	-15.0650	-14.6580	-15.6810
	Average	-31.4834	-33.9933	-34.2433	-33.9460	-35.0740

Limit: +/-100uV



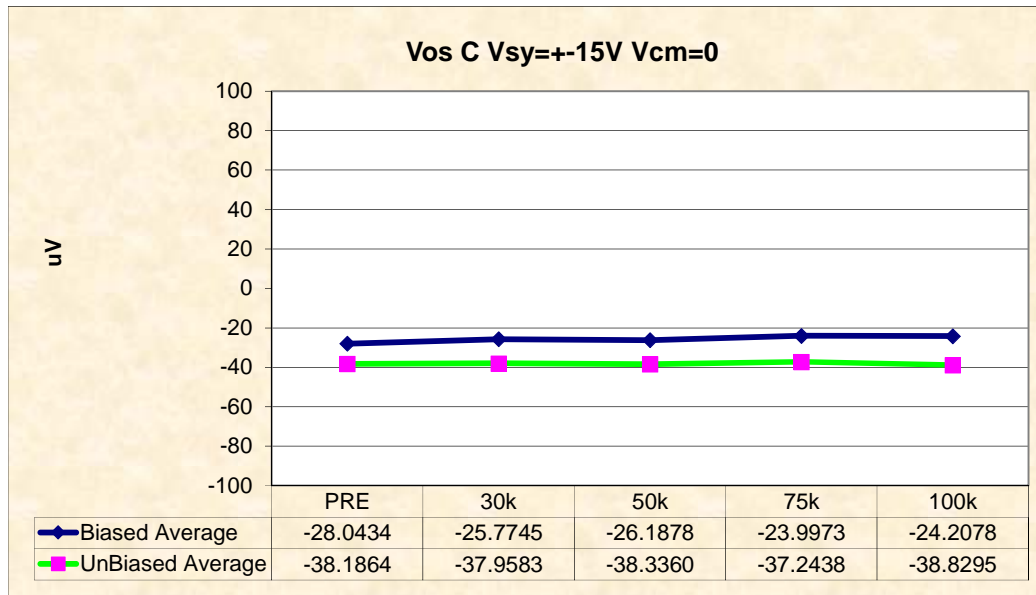
		Vos B Vsy=+-15V Vcm=0				
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	-13.4160	0.0000	0.0000	-33.3060	-34.3620
	54	-40.4420	0.0000	0.0000	-41.1420	-42.7060
Biased	8	-60.0748	-60.9410	-60.3100	-58.6540	-59.1860
	9	-45.9382	-45.1050	-46.6920	-43.2800	-44.9120
	55	-51.5504	-52.3790	-53.0170	-50.5670	-51.6050
	56	-43.3884	-45.6800	-45.9610	-44.5420	-45.6990
	Min	-60.0748	-60.9410	-60.3100	-58.6540	-59.1860
	Max	-43.3884	-45.1050	-45.9610	-43.2800	-44.9120
	Average	-50.2380	-51.0263	-51.4950	-49.2608	-50.3505
UnBiased	10	-28.6456	-26.7310	-27.5250	-25.9370	-36.8120
	11	-35.4014	-32.8620	-33.4740	-34.0430	-27.5310
	57	-24.2459	-26.5250	-27.1310	-27.1310	-27.3060
	58	-8.0908	-13.4570	-12.2380	-13.1880	-14.1200
	Min	-35.4014	-32.8620	-33.4740	-34.0430	-36.8120
	Max	-8.0908	-13.4570	-12.2380	-13.1880	-14.1200
	Average	-24.0959	-24.8938	-25.0920	-25.0748	-26.4423

Limit: +/-100uV



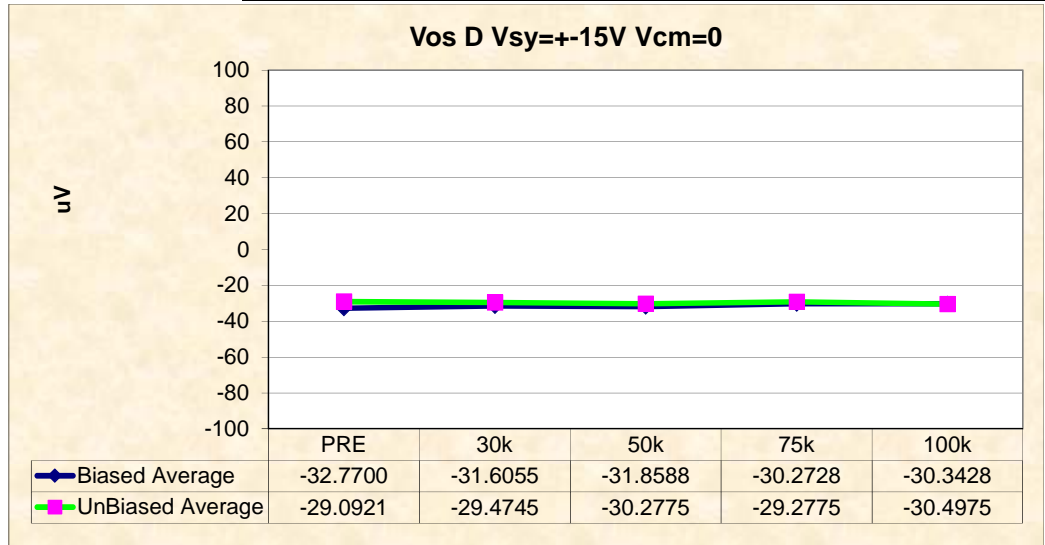
		Vos C Vsy=+-15V Vcm=0				
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	-63.6640	0.0000	0.0000	-44.3890	-45.1710
	54	-46.5910	0.0000	0.0000	-46.3490	-47.5560
Biased	8	-28.2556	-25.5640	-25.4450	-23.0800	-23.1870
	9	-23.2060	-20.4950	-21.1820	-18.7730	-19.3610
	55	-59.0340	-54.4820	-55.9920	-53.4770	-53.5360
	56	-1.6780	-2.5570	-2.1320	-0.6590	-0.7470
	Min	-59.0340	-54.4820	-55.9920	-53.4770	-53.5360
	Max	-1.6780	-2.5570	-2.1320	-0.6590	-0.7470
	Average	-28.0434	-25.7745	-26.1878	-23.9973	-24.2078
UnBiased	10	-43.7041	-44.2390	-45.6060	-43.0780	-58.7410
	11	-57.4299	-58.3270	-58.1020	-56.9850	-45.2520
	57	-20.3785	-16.7750	-17.0130	-16.8570	-17.4450
	58	-31.2330	-32.4920	-32.6230	-32.0550	-33.8800
	Min	-57.4299	-58.3270	-58.1020	-56.9850	-58.7410
	Max	-20.3785	-16.7750	-17.0130	-16.8570	-17.4450
	Average	-38.1864	-37.9583	-38.3360	-37.2438	-38.8295

Limit: +/-100uV



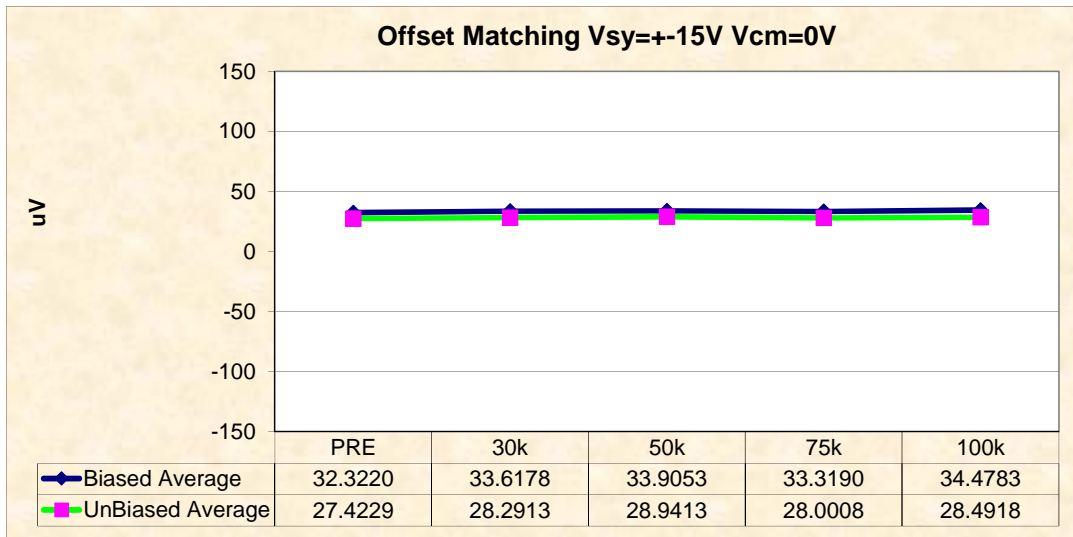
		Vos D Vsy=+-15V Vcm=0				
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	-17.3910	0.0000	0.0000	-36.5980	-38.3400
	54	-16.3540	0.0000	0.0000	-16.8610	-18.2960
Biased	14	-16.1833	-15.9610	-15.2800	-13.4490	-14.1340
	15	-22.3893	-20.7360	-21.8860	-20.5920	-20.5270
	42	-46.6319	-43.0470	-43.8160	-42.1470	-42.0330
	43	-45.8757	-46.6780	-46.4530	-44.9030	-44.6770
	Min	-46.6319	-46.6780	-46.4530	-44.9030	-44.6770
	Max	-16.1833	-15.9610	-15.2800	-13.4490	-14.1340
	Average	-32.7700	-31.6055	-31.8588	-30.2728	-30.3428
UnBiased	23	-40.9571	-41.3970	-42.4600	-41.2350	-45.0770
	24	-46.1006	-43.8540	-44.2970	-42.9850	-43.0770
	47	-32.9138	-32.4230	-34.4290	-33.4660	-34.4020
	48	3.6032	-0.2240	0.0760	0.5760	0.5660
	Min	-46.1006	-43.8540	-44.2970	-42.9850	-45.0770
	Max	3.6032	-0.2240	0.0760	0.5760	0.5660
	Average	-29.0921	-29.4745	-30.2775	-29.2775	-30.4975

Limit: +/-100uV



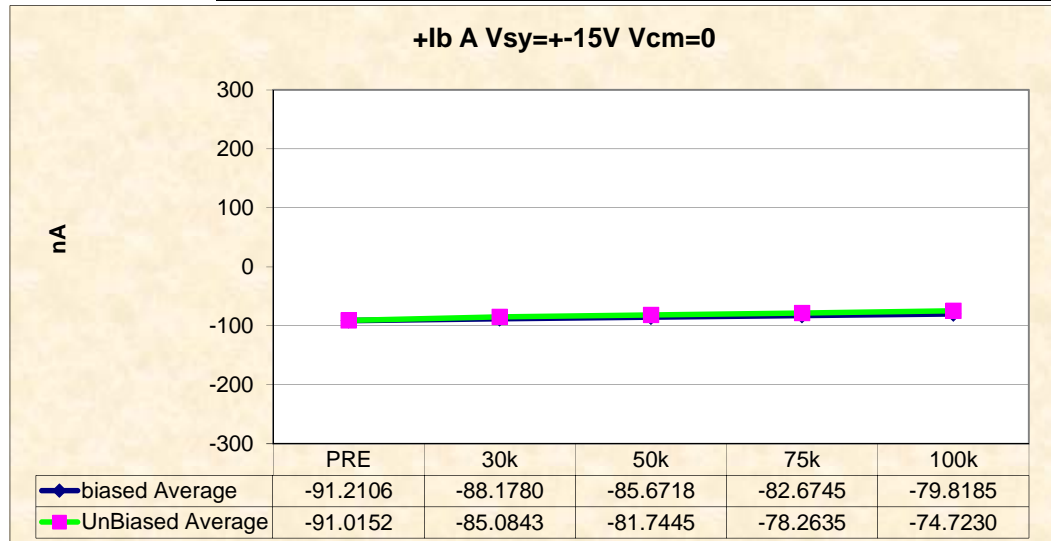
Offset Matching Vsy=+-15V Vcm=0V						
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	50.2480	0.0000	0.0000	11.0830	10.8090
	54	30.2360	0.0000	0.0000	29.4880	29.2600
Biased	8	44.6226	47.2520	48.0720	47.5790	48.8410
	9	28.0657	28.6640	29.9700	28.2150	30.1070
	55	12.4022	14.4330	13.2580	13.2380	14.0130
	56	44.1977	44.1220	44.3210	44.2440	44.9520
	Min	12.4022	14.4330	13.2580	13.2380	14.0130
	Max	44.6226	47.2520	48.0720	47.5790	48.8410
	Average	32.3220	33.6178	33.9053	33.3190	34.4783
UnBiased	10	30.3597	28.5550	30.5410	28.4200	21.9290
	11	22.0285	25.4650	24.6280	22.9420	29.5720
	57	22.4674	26.8770	27.8970	28.0100	28.0200
	58	34.8362	32.2680	32.6990	32.6310	34.4460
	Min	22.0285	25.4650	24.6280	22.9420	21.9290
	Max	34.8362	32.2680	32.6990	32.6310	34.4460
	Average	27.4229	28.2913	28.9413	28.0008	28.4918

Limit: <150uV



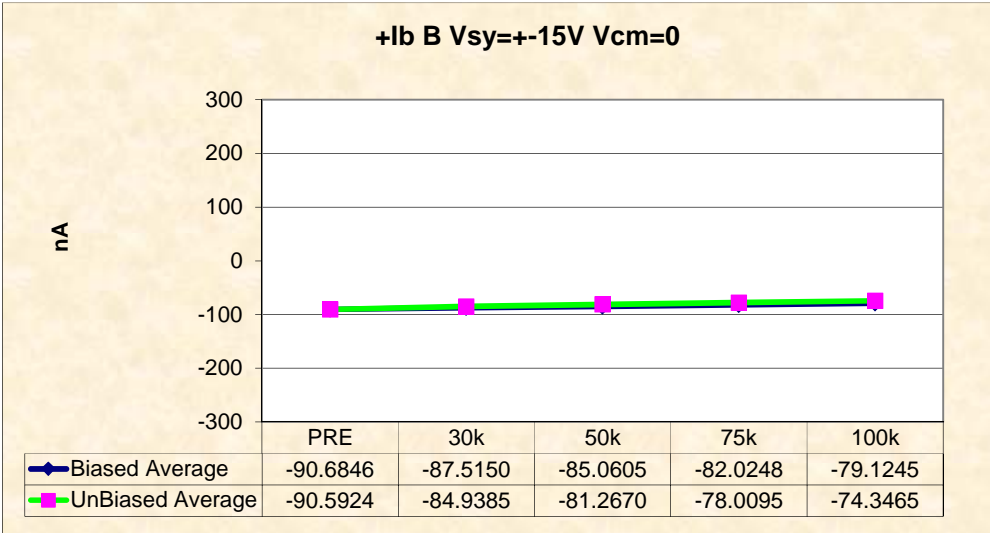
		+Ib A Vsy=+-15V Vcm=0				
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	-78.5940	0.0000	0.0000	-76.1830	-75.8030
	54	-98.4270	0.0000	0.0000	-99.4480	-99.0010
Biased	8	-93.9453	-89.6290	-87.3840	-84.4630	-81.5570
	9	-90.5993	-87.4650	-84.6570	-81.2360	-78.7370
	55	-92.9509	-90.6420	-87.7150	-84.8760	-81.8260
	56	-87.3470	-84.9760	-82.9310	-80.1230	-77.1540
	Min	-93.9453	-90.6420	-87.7150	-84.8760	-81.8260
	Max	-87.3470	-84.9760	-82.9310	-80.1230	-77.1540
	Average	-91.2106	-88.1780	-85.6718	-82.6745	-79.8185
UnBiased	10	-92.7507	-81.9870	-78.5470	-74.3560	-71.4750
	11	-85.0080	-82.2430	-78.9410	-75.6700	-71.4130
	57	-97.2601	-92.6630	-88.9480	-85.4510	-82.2140
	58	-89.0420	-83.4440	-80.5420	-77.5770	-73.7900
	Min	-97.2601	-92.6630	-88.9480	-85.4510	-82.2140
	Max	-85.0080	-81.9870	-78.5470	-74.3560	-71.4130
	Average	-91.0152	-85.0843	-81.7445	-78.2635	-74.7230

Limit: +/-300nA



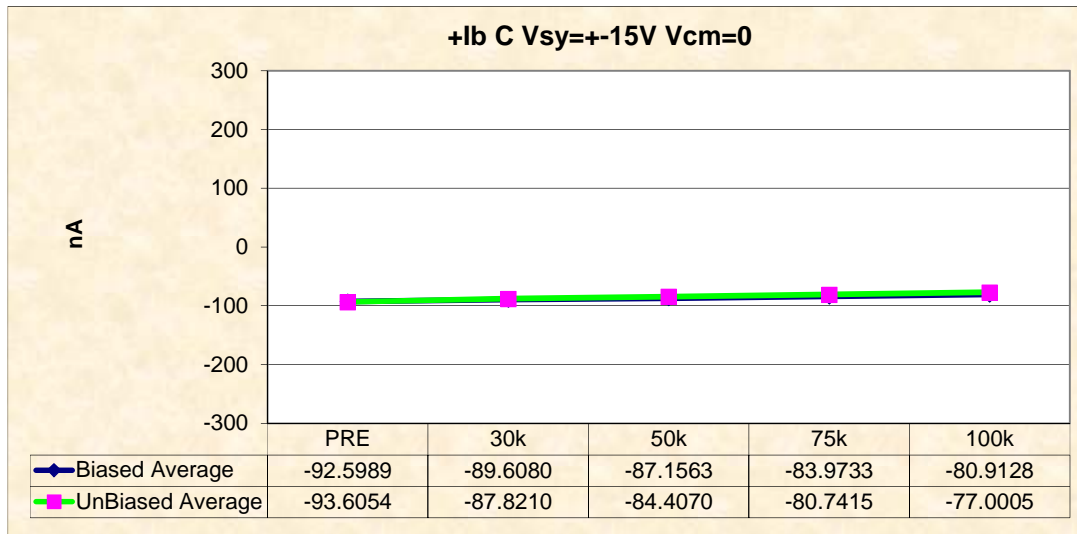
		+Ib B Vsy=+-15V Vcm=0				
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	-78.5990	0.0000	0.0000	-74.7250	-74.2910
	54	-97.8500	0.0000	0.0000	-98.9050	-98.3330
Biased	14	-94.4500	-90.1240	-88.0810	-84.7930	-82.3460
	15	-89.5628	-86.1370	-83.2810	-80.5440	-77.3840
	42	-91.1814	-88.9430	-86.1430	-82.8250	-79.7900
	43	-87.5442	-84.8560	-82.7370	-79.9370	-76.9780
	Min	-94.4500	-90.1240	-88.0810	-84.7930	-82.3460
	Max	-87.5442	-84.8560	-82.7370	-79.9370	-76.9780
	Average	-90.6846	-87.5150	-85.0605	-82.0248	-79.1245
UnBiased	23	-91.6439	-81.7810	-77.5880	-74.1130	-71.7840
	24	-84.9444	-82.1620	-78.5310	-75.6250	-70.6590
	47	-97.2310	-92.3490	-88.5990	-85.1250	-81.5590
	48	-88.5504	-83.4620	-80.3500	-77.1750	-73.3840
	Min	-97.2310	-92.3490	-88.5990	-85.1250	-81.5590
	Max	-84.9444	-81.7810	-77.5880	-74.1130	-70.6590
	Average	-90.5924	-84.9385	-81.2670	-78.0095	-74.3465

Limit: +/-300nA



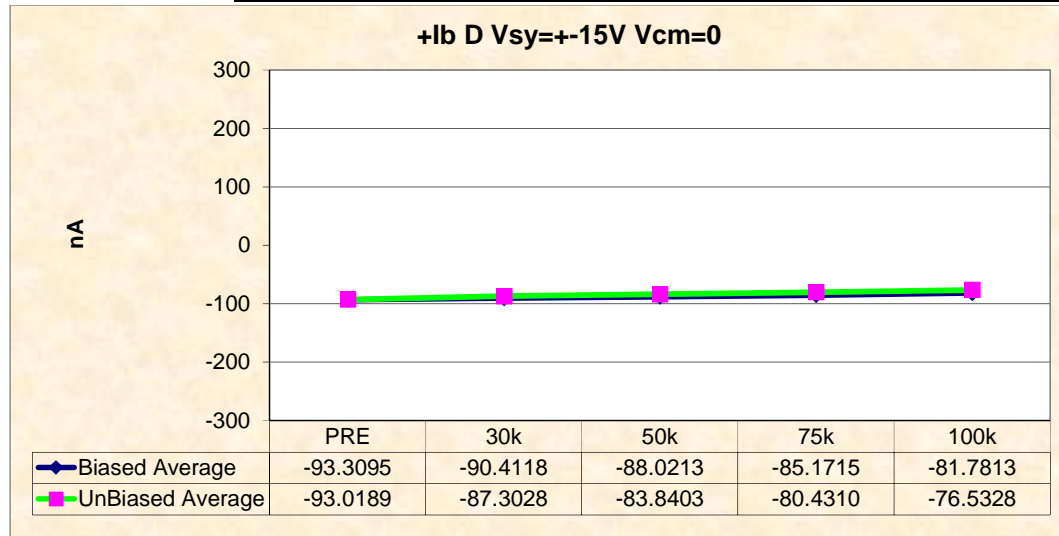
		+Ib C Vsy=+-15V Vcm=0				
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	-79.1800	0.0000	0.0000	-77.5660	-77.0380
	54	-100.8230	0.0000	0.0000	-101.6030	-101.1070
Biased	8	-95.7432	-91.5790	-89.4250	-86.1730	-82.9060
	9	-92.4975	-88.9950	-86.3290	-83.1710	-79.9280
	55	-93.9581	-91.5290	-88.9070	-85.6620	-83.0550
	56	-88.1969	-86.3290	-83.9640	-80.8870	-77.7620
	Min	-95.7432	-91.5790	-89.4250	-86.1730	-83.0550
	Max	-88.1969	-86.3290	-83.9640	-80.8870	-77.7620
	Average	-92.5989	-89.6080	-87.1563	-83.9733	-80.9128
UnBiased	10	-94.5573	-84.0510	-80.5750	-76.9540	-73.8980
	11	-88.0096	-85.3930	-81.8730	-78.2280	-73.3800
	57	-100.9239	-96.2100	-92.4900	-88.3520	-85.4210
	58	-90.9308	-85.6300	-82.6900	-79.4320	-75.3030
	Min	-100.9239	-96.2100	-92.4900	-88.3520	-85.4210
	Max	-88.0096	-84.0510	-80.5750	-76.9540	-73.3800
	Average	-93.6054	-87.8210	-84.4070	-80.7415	-77.0005

Limit: +/-300nA



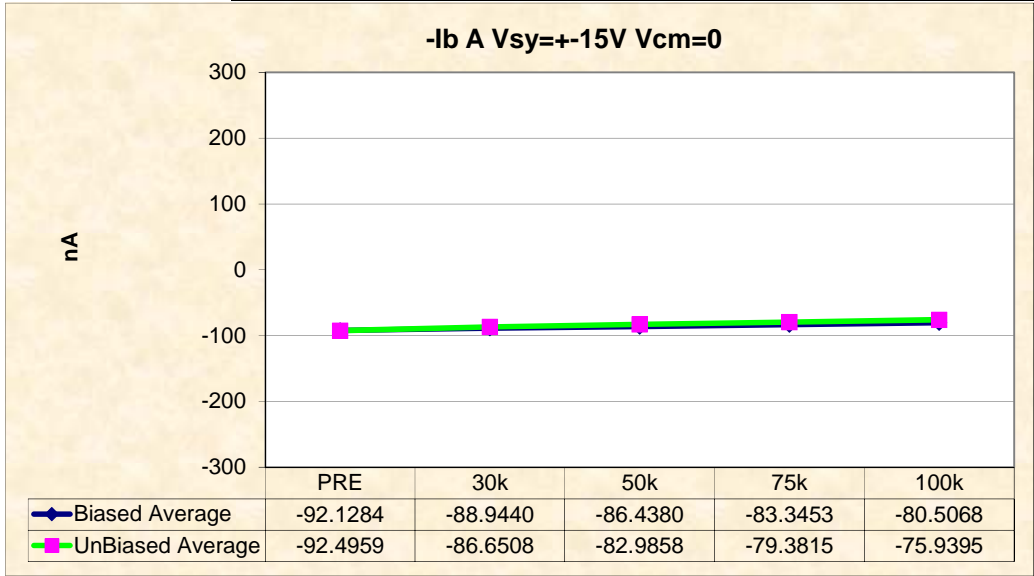
		+Ib D Vsy=+-15V Vcm=0				
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	-78.6180	0.0000	0.0000	-79.7470	-79.1920
	54	-100.9370	0.0000	0.0000	-102.0830	-101.5480
Biased	8	-96.5203	-92.3460	-90.1710	-87.1090	-83.6920
	9	-92.2893	-89.3840	-86.5960	-83.6340	-79.9170
	55	-94.1017	-91.7460	-89.2150	-86.3590	-83.3860
	56	-90.3269	-88.1710	-86.1030	-83.5840	-80.1300
	Min	-96.5203	-92.3460	-90.1710	-87.1090	-83.6920
	Max	-90.3269	-88.1710	-86.1030	-83.5840	-79.9170
	Average	-93.3095	-90.4118	-88.0213	-85.1715	-81.7813
UnBiased	10	-93.8954	-83.6280	-79.9090	-76.2970	-74.5170
	11	-88.4457	-85.7780	-82.2400	-79.2900	-72.5740
	57	-101.3013	-96.6400	-92.7340	-89.2900	-85.8980
	58	-88.4332	-83.1650	-80.4780	-76.8470	-73.1420
	Min	-101.3013	-96.6400	-92.7340	-89.2900	-85.8980
	Max	-88.4332	-83.1650	-79.9090	-76.2970	-72.5740
	Average	-93.0189	-87.3028	-83.8403	-80.4310	-76.5328

Limit: +/-300nA



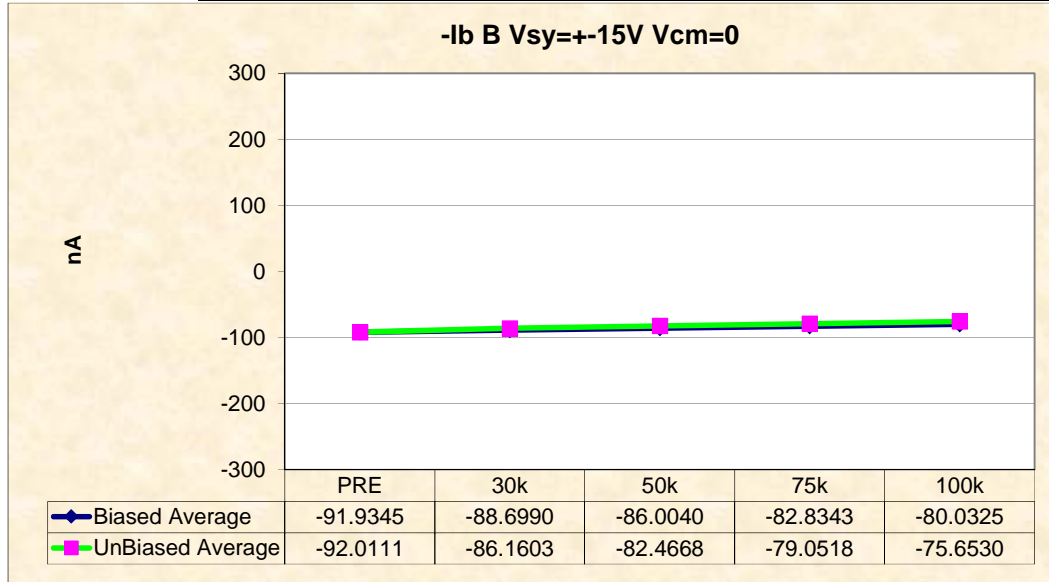
		-Ib A Vsy=+-15V Vcm=0				
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	-78.3320	0.0000	0.0000	-78.2280	-77.7170
	54	-99.9810	0.0000	0.0000	-100.9370	-100.2770
Biased	14	-94.9272	-90.8610	-88.6290	-85.3640	-82.1140
	15	-91.8876	-88.4030	-85.3950	-82.3180	-79.7250
	42	-94.2330	-91.3800	-88.7470	-85.8580	-82.8150
	43	-87.4659	-85.1320	-82.9810	-79.8410	-77.3730
	Min	-94.9272	-91.3800	-88.7470	-85.8580	-82.8150
	Max	-87.4659	-85.1320	-82.9810	-79.8410	-77.3730
	Average	-92.1284	-88.9440	-86.4380	-83.3453	-80.5068
UnBiased	23	-94.1204	-84.0570	-79.5790	-76.6200	-72.0880
	24	-86.1775	-82.9120	-79.3660	-75.6320	-73.1950
	47	-98.6047	-94.0320	-90.3110	-86.5960	-83.1840
	48	-91.0808	-85.6020	-82.6870	-78.6780	-75.2910
	Min	-98.6047	-94.0320	-90.3110	-86.5960	-83.1840
	Max	-86.1775	-82.9120	-79.3660	-75.6320	-72.0880
	Average	-92.4959	-86.6508	-82.9858	-79.3815	-75.9395

Limit: +/-300nA



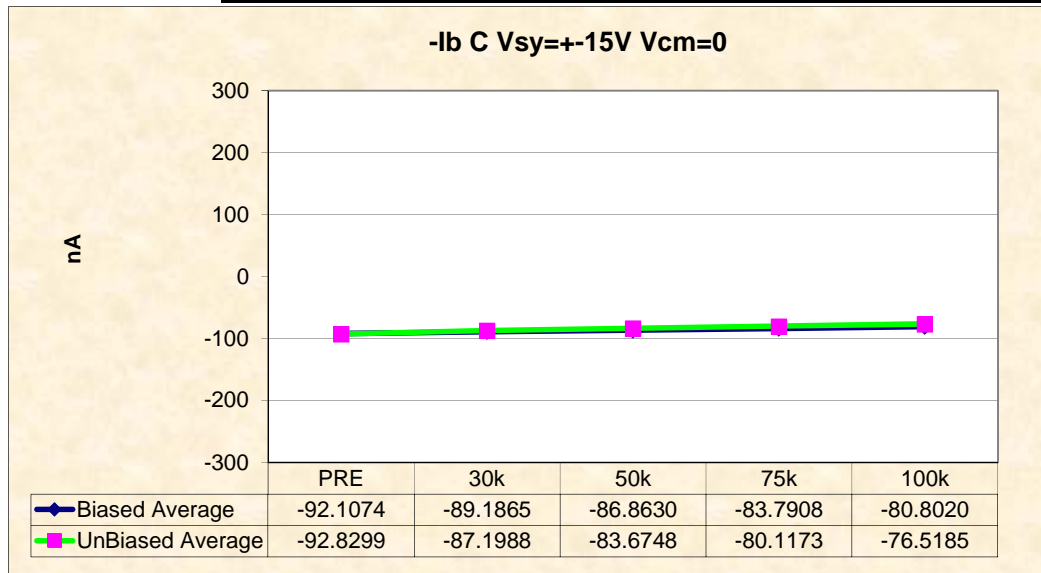
		-Ib B Vsy=+-15V Vcm=0				
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	-79.7790	0.0000	0.0000	-76.5310	-75.9840
	54	-97.7690	0.0000	0.0000	-98.8920	-98.2770
Biased	8	-95.1562	-90.7800	-88.5240	-85.2500	-82.1590
	9	-91.6127	-88.3680	-85.2180	-82.0310	-79.2590
	55	-92.7751	-89.8050	-86.8310	-83.5750	-81.0150
	56	-88.1941	-85.8430	-83.4430	-80.4810	-77.6970
	Min	-95.1562	-90.7800	-88.5240	-85.2500	-82.1590
	Max	-88.1941	-85.8430	-83.4430	-80.4810	-77.6970
	Average	-91.9345	-88.6990	-86.0040	-82.8343	-80.0325
UnBiased	10	-92.3814	-82.0870	-78.1940	-74.8440	-73.9590
	11	-87.3380	-84.3810	-80.6870	-77.1130	-71.3090
	57	-98.8247	-93.8860	-89.9300	-86.2870	-83.0280
	58	-89.5003	-84.2870	-81.0560	-77.9630	-74.3160
	Min	-98.8247	-93.8860	-89.9300	-86.2870	-83.0280
	Max	-87.3380	-82.0870	-78.1940	-74.8440	-71.3090
	Average	-92.0111	-86.1603	-82.4668	-79.0518	-75.6530

Limit: +/-300nA



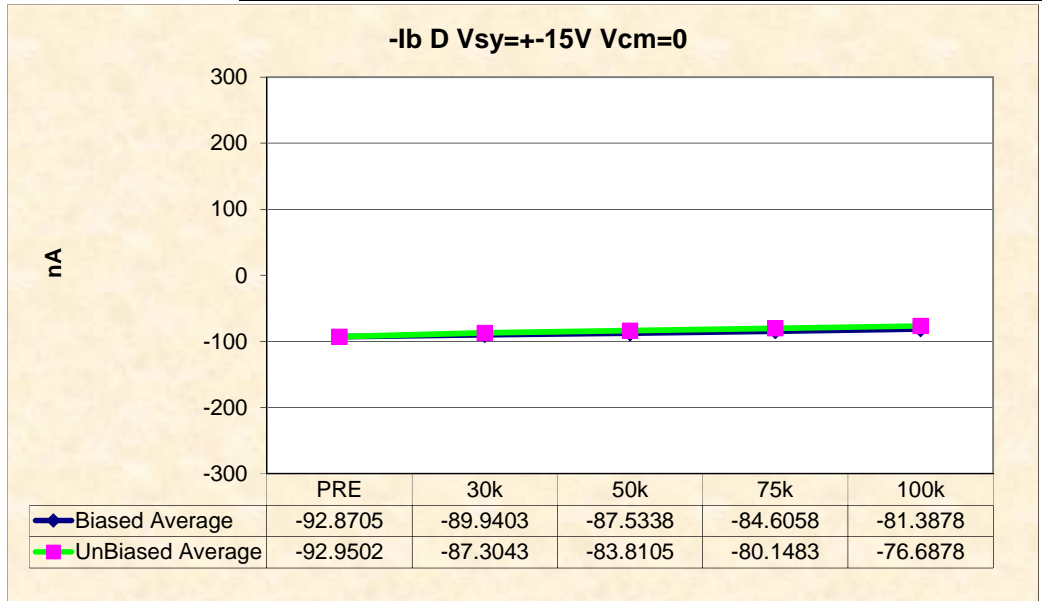
		-Ib C Vsy=+-15V Vcm=0				
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	-77.7510	0.0000	0.0000	-78.8580	-78.4180
	54	-100.0490	0.0000	0.0000	-100.7790	-100.3460
Biased	8	-93.7521	-89.2380	-87.3280	-84.3200	-81.3450
	9	-91.3303	-88.1830	-85.5180	-82.5220	-79.1790
	55	-92.9531	-91.0610	-88.5390	-85.6240	-82.7120
	56	-90.3940	-88.2640	-86.0670	-82.6970	-79.9720
	Min	-93.7521	-91.0610	-88.5390	-85.6240	-82.7120
	Max	-90.3940	-88.1830	-85.5180	-82.5220	-79.1790
	Average	-92.1074	-89.1865	-86.8630	-83.7908	-80.8020
UnBiased	10	-93.1654	-83.0030	-78.5330	-75.1820	-73.9550
	11	-87.3480	-84.7380	-81.2920	-77.8470	-72.0320
	57	-100.7741	-96.3220	-93.0140	-89.3250	-85.6520
	58	-90.0320	-84.7320	-81.8600	-78.1150	-74.4350
	Min	-100.7741	-96.3220	-93.0140	-89.3250	-85.6520
	Max	-87.3480	-83.0030	-78.5330	-75.1820	-72.0320
	Average	-92.8299	-87.1988	-83.6748	-80.1173	-76.5185

Limit: +/-300nA



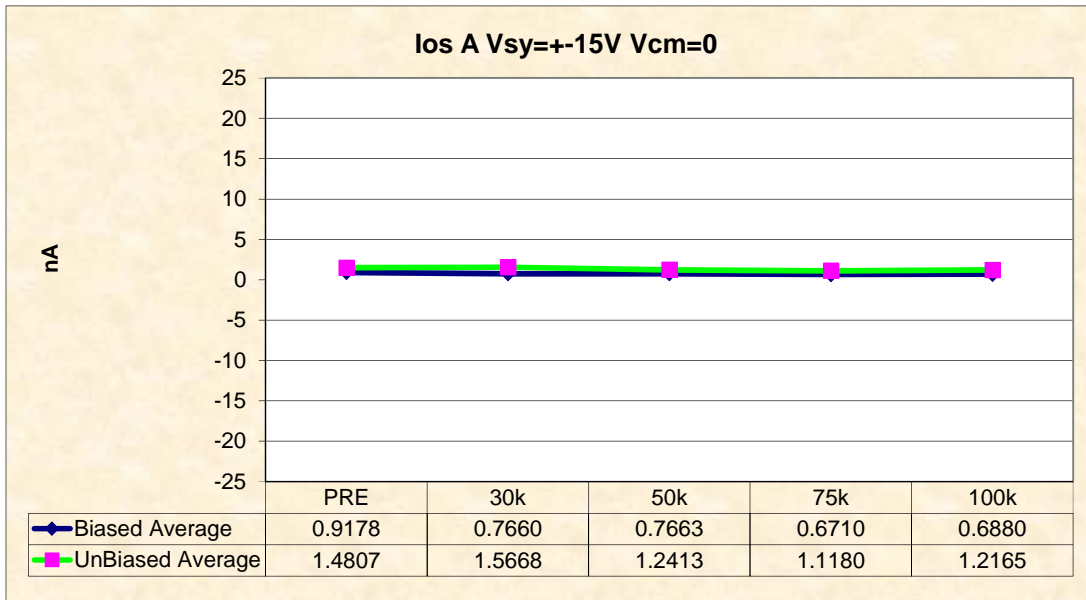
		-Ib D Vsy=+-15V Vcm=0				
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	-78.8060	0.0000	0.0000	-78.3220	-77.7920
	54	-101.8170	0.0000	0.0000	-102.5960	-101.9480
Biased	14	-96.5516	-92.5150	-90.4020	-87.3710	-84.0990
	15	-92.4330	-88.9280	-86.1400	-83.1780	-79.9920
	42	-92.9455	-90.7340	-88.2280	-85.3090	-82.2490
	43	-89.5519	-87.5840	-85.3650	-82.5650	-79.2110
	Min	-96.5516	-92.5150	-90.4020	-87.3710	-84.0990
	Max	-89.5519	-87.5840	-85.3650	-82.5650	-79.2110
	Average	-92.8705	-89.9403	-87.5338	-84.6058	-81.3878
UnBiased	23	-93.7205	-83.6030	-79.4900	-76.1660	-73.6170
	24	-87.2020	-84.5960	-80.6530	-77.1280	-72.7990
	47	-100.1951	-95.4150	-92.1770	-88.1960	-84.6990
	48	-90.6831	-85.6030	-82.9220	-79.1030	-75.6360
	Min	-100.1951	-95.4150	-92.1770	-88.1960	-84.6990
	Max	-87.2020	-83.6030	-79.4900	-76.1660	-72.7990
	Average	-92.9502	-87.3043	-83.8105	-80.1483	-76.6878

Limit: +/-300nA



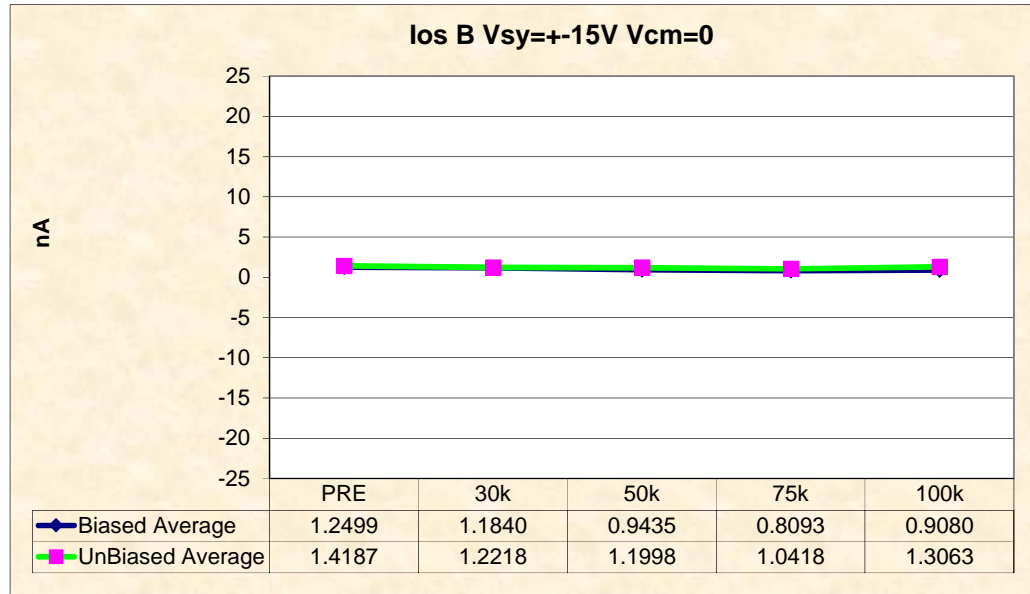
		Ios A Vsy=+-15V Vcm=0				
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	-0.2620	0.0000	0.0000	2.0450	1.9140
	54	1.5540	0.0000	0.0000	1.4890	1.2760
Biased	8	0.9819	1.2320	1.2450	0.9010	0.5570
	9	1.2884	0.9380	0.7380	1.0820	0.9880
	55	1.2821	0.7380	1.0320	0.9820	0.9880
	56	0.1188	0.1560	0.0500	-0.2810	0.2190
	Min	0.1188	0.1560	0.0500	-0.2810	0.2190
	Max	1.2884	1.2320	1.2450	1.0820	0.9880
	Average	0.9178	0.7660	0.7663	0.6710	0.6880
UnBiased	10	1.3697	2.0700	1.0320	2.2640	0.6130
	11	1.1695	0.6690	0.4250	-0.0380	1.7830
	57	1.3447	1.3700	1.3630	1.1450	0.9690
	58	2.0389	2.1580	2.1450	1.1010	1.5010
	Min	1.1695	0.6690	0.4250	-0.0380	0.6130
	Max	2.0389	2.1580	2.1450	2.2640	1.7830
	Average	1.4807	1.5668	1.2413	1.1180	1.2165

Limit: +/-25nA



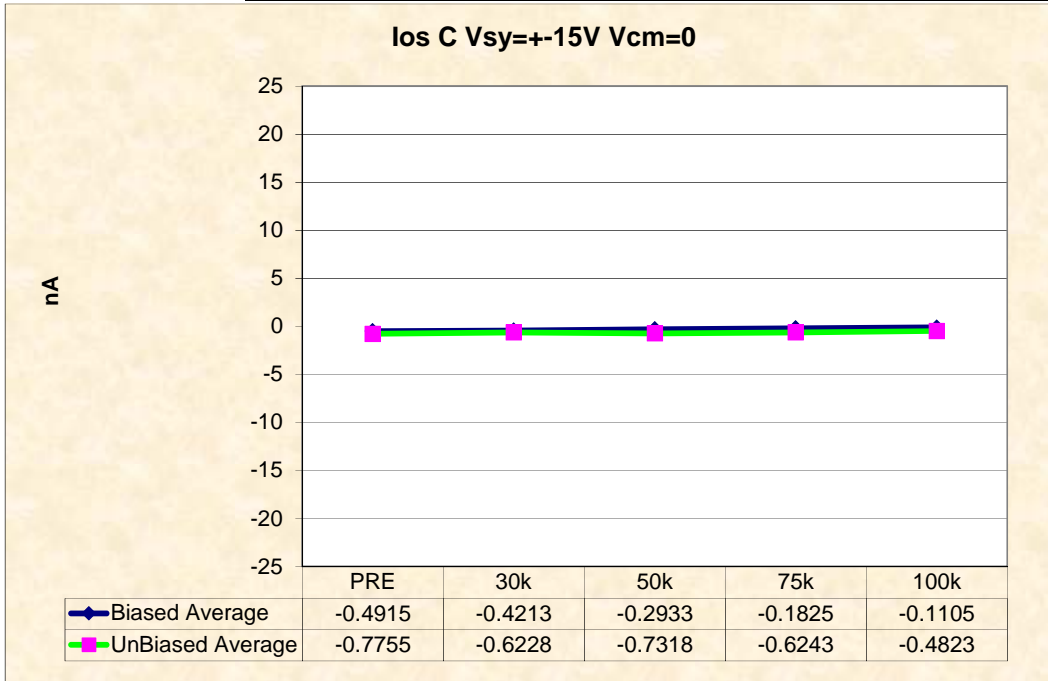
		Ios B Vsy=+-15V Vcm=0				
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	1.1800	0.0000	0.0000	1.8060	1.6940
	54	-0.0810	0.0000	0.0000	-0.0120	-0.0560
Biased	8	0.7062	0.6560	0.4440	0.4560	-0.1870
	9	2.0499	2.2310	1.9370	1.4870	1.8750
	55	1.5936	0.8620	0.6870	0.7500	1.2250
	56	0.6500	0.9870	0.7060	0.5440	0.7190
	Min	0.6500	0.6560	0.4440	0.4560	-0.1870
	Max	2.0499	2.2310	1.9370	1.4870	1.8750
	Average	1.2499	1.1840	0.9435	0.8093	0.9080
UnBiased	10	0.7375	0.3060	0.6060	0.7310	2.1750
	11	2.3936	2.2190	2.1560	1.4870	0.6500
	57	1.5936	1.5370	1.3310	1.1620	1.4690
	58	0.9499	0.8250	0.7060	0.7870	0.9310
	Min	0.7375	0.3060	0.6060	0.7310	0.6500
	Max	2.3936	2.2190	2.1560	1.4870	2.1750
	Average	1.4187	1.2218	1.1998	1.0418	1.3063

Limit: +/-25nA



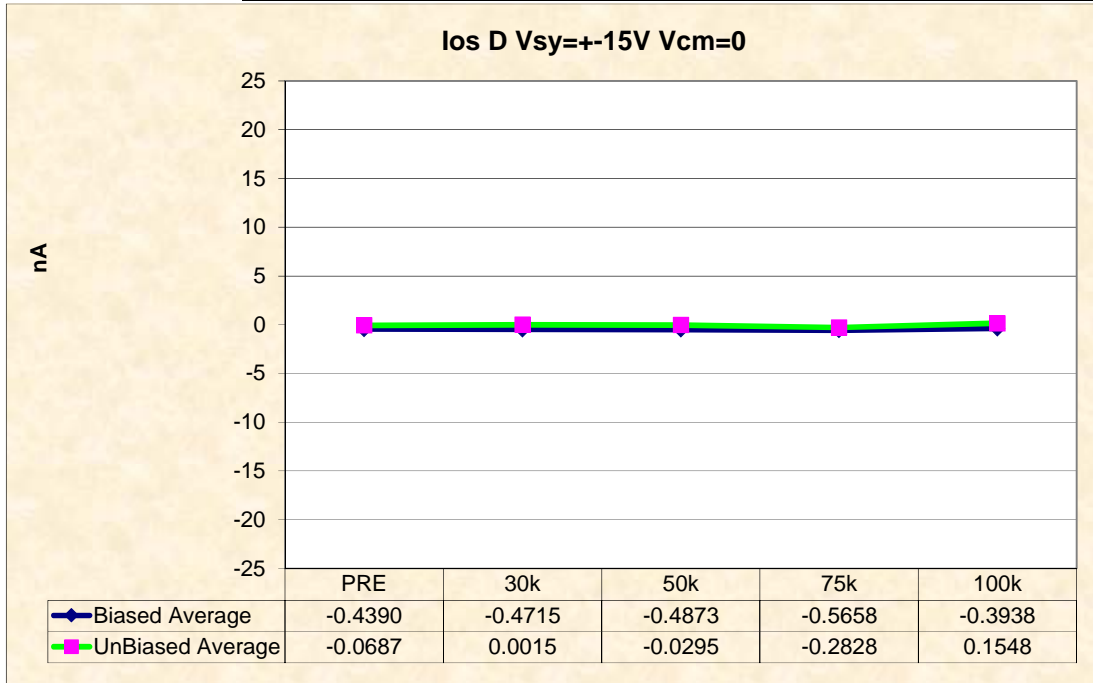
		Ios C Vsy=+-15V Vcm=0				
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	-1.4300	0.0000	0.0000	1.2920	1.3790
	54	-0.7740	0.0000	0.0000	-0.8240	-0.7620
Biased	14	-1.9911	-2.3410	-2.0970	-1.8540	-1.5600
	15	-1.1672	-0.8110	-0.8110	-0.6490	-0.7490
	42	-1.0049	-0.4680	-0.3680	-0.0370	-0.3430
	43	2.1971	1.9350	2.1030	1.8100	2.2100
	Min	-1.9911	-2.3410	-2.0970	-1.8540	-1.5600
	Max	2.1971	1.9350	2.1030	1.8100	2.2100
	Average	-0.4915	-0.4213	-0.2933	-0.1825	-0.1105
Un Biased	23	-1.3919	-1.0490	-2.0410	-1.7730	0.0560
	24	-0.6616	-0.6550	-0.5800	-0.3810	-1.3480
	47	-0.1498	0.1120	0.5240	0.9740	0.2310
	48	-0.8988	-0.8990	-0.8300	-1.3170	-0.8680
	Min	-1.3919	-1.0490	-2.0410	-1.7730	-1.3480
	Max	-0.1498	0.1120	0.5240	0.9740	0.2310
	Average	-0.7755	-0.6228	-0.7318	-0.6243	-0.4823

Limit: +/-25nA



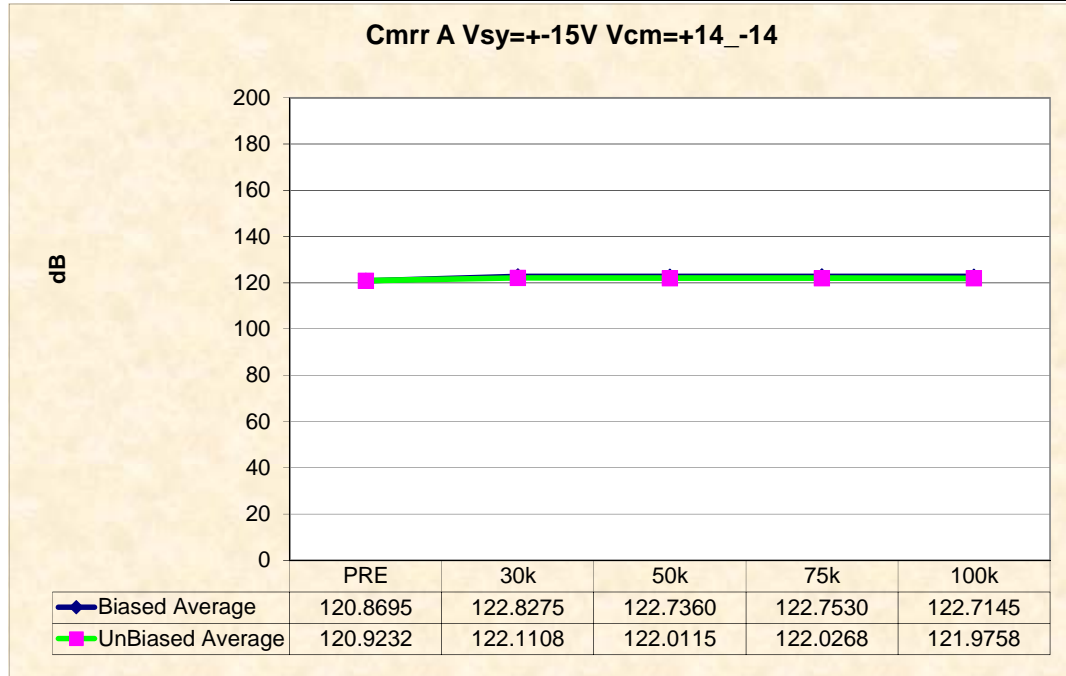
		Ios D Vsy=+15V Vcm=0				
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	0.1870	0.0000	0.0000	-1.4250	-1.4000
	54	0.8800	0.0000	0.0000	0.5120	0.4000
Biased	8	0.0312	0.1690	0.2310	0.2620	0.4060
	9	0.1437	-0.4560	-0.4560	-0.4560	0.0750
	55	-1.1562	-1.0120	-0.9870	-1.0500	-1.1370
	56	-0.7750	-0.5870	-0.7370	-1.0190	-0.9190
	Min	-1.1562	-1.0120	-0.9870	-1.0500	-1.1370
	Max	0.1437	0.1690	0.2310	0.2620	0.4060
	Average	-0.4390	-0.4715	-0.4873	-0.5658	-0.3938
UnBiased	10	-0.1750	-0.0250	-0.4190	-0.1310	-0.9000
	11	-1.2437	-1.1810	-1.5870	-2.1620	0.2250
	57	-1.1062	-1.2250	-0.5560	-1.0940	-1.2000
	58	2.2499	2.4370	2.4440	2.2560	2.4940
	Min	-1.2437	-1.2250	-1.5870	-2.1620	-1.2000
	Max	2.2499	2.4370	2.4440	2.2560	2.4940
	Average	-0.0687	0.0015	-0.0295	-0.2828	0.1548

Limit: +/-25nA



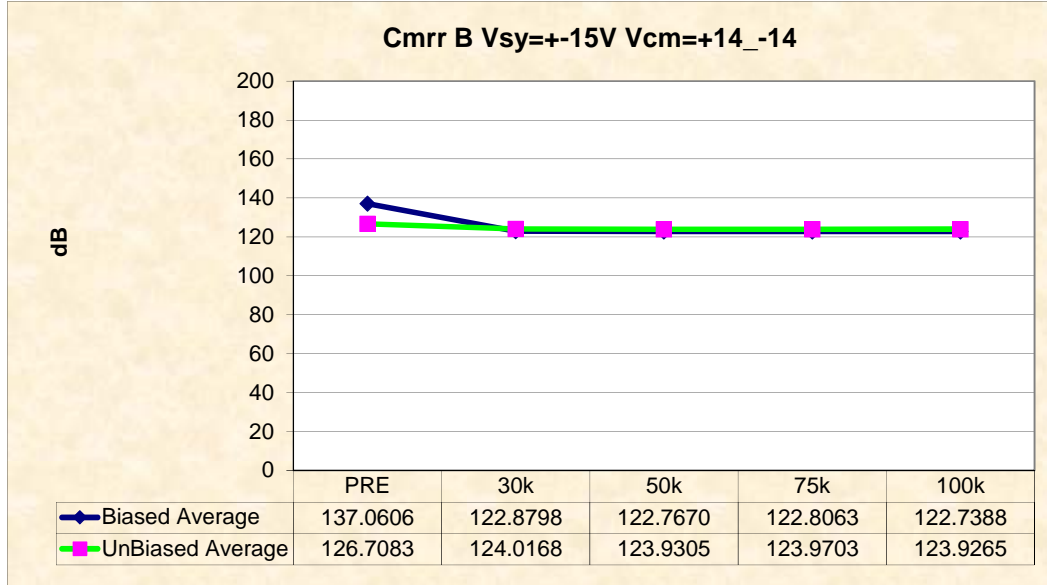
		Cmrr A Vsy=+-15V Vcm=+14_-14 dB				
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	121.8140	0.0000	0.0000	122.1400	122.0750
	54	128.6710	0.0000	0.0000	128.9830	128.9420
Biased	8	124.8816	126.1460	126.0920	126.0850	126.0650
	9	124.4715	121.8430	121.6910	121.7390	121.6720
	55	115.9060	121.6440	121.5450	121.5460	121.5230
	56	118.2188	121.6770	121.6160	121.6420	121.5980
	Min	115.9060	121.6440	121.5450	121.5460	121.5230
	Max	124.8816	126.1460	126.0920	126.0850	126.0650
	Average	120.8695	122.8275	122.7360	122.7530	122.7145
UnBiased	10	125.2814	121.8540	121.6290	121.6930	122.4340
	11	121.5943	122.6690	122.5340	122.5080	121.6570
	57	119.7759	121.3380	121.2840	121.2890	121.3380
	58	117.0413	122.5820	122.5990	122.6170	122.4740
	Min	117.0413	121.3380	121.2840	121.2890	121.3380
	Max	125.2814	122.6690	122.5990	122.6170	122.4740
	Average	120.9232	122.1108	122.0115	122.0268	121.9758

Limit: >106dB



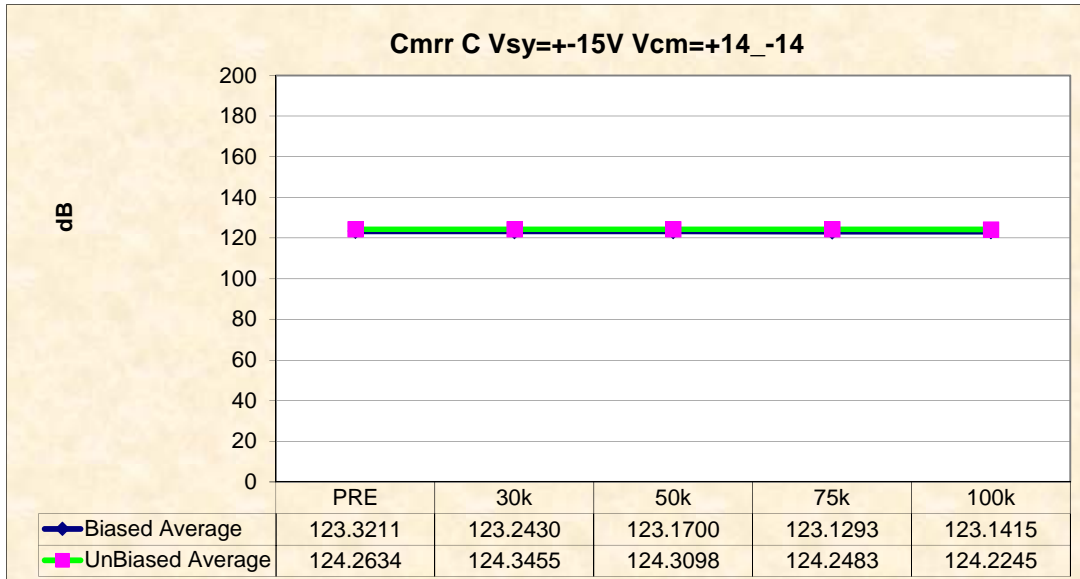
Cmrr B Vsy=+-15V Vcm=+14_-14 dB						
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	124.2080	0.0000	0.0000	121.6980	121.5790
	54	125.0830	0.0000	0.0000	125.0910	125.0110
Biased	14	133.3681	122.4500	122.3460	122.4270	122.3730
	15	145.4706	120.0300	119.9500	119.9590	119.8970
	42	136.7393	125.0390	124.8600	124.9040	124.8420
	43	132.6645	124.0000	123.9120	123.9350	123.8430
	Min	132.6645	120.0300	119.9500	119.9590	119.8970
	Max	145.4706	125.0390	124.8600	124.9040	124.8420
	Average	137.0606	122.8798	122.7670	122.8063	122.7388
UnBiased	23	129.8806	124.3600	124.3270	124.3130	123.1980
	24	125.4131	123.4270	123.2060	123.3510	124.2180
	47	126.3471	127.8970	127.7730	127.7610	128.0330
	48	125.1924	120.3830	120.4160	120.4560	120.2570
	Min	125.1924	120.3830	120.4160	120.4560	120.2570
	Max	129.8806	127.8970	127.7730	127.7610	128.0330
	Average	126.7083	124.0168	123.9305	123.9703	123.9265

Limit: >106dB



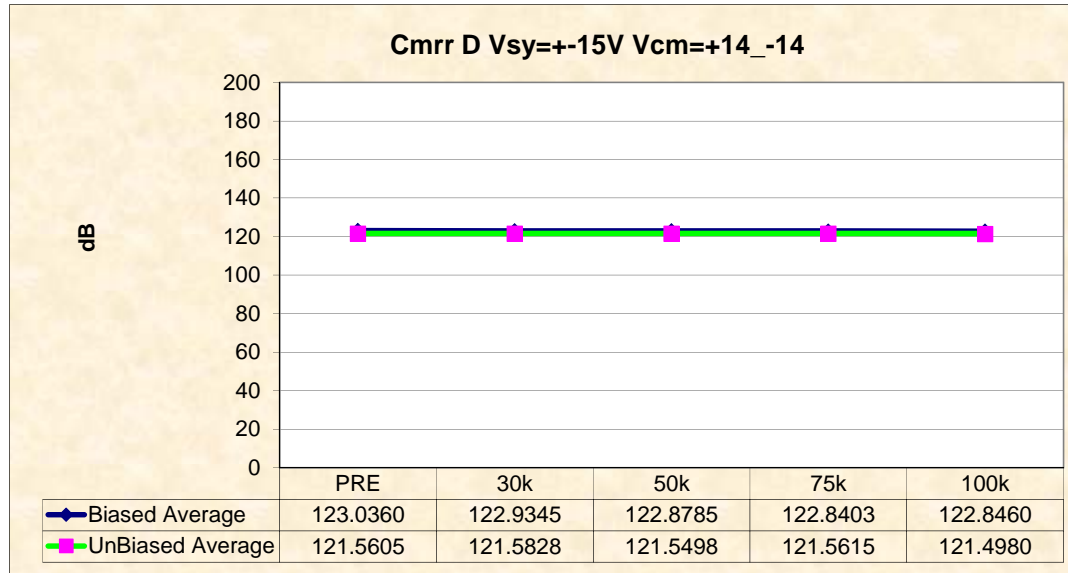
Cmrr C Vsy=+-15V Vcm=+14_-14 dB						
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	119.9040	0.0000	0.0000	122.1110	122.0530
	54	133.2180	0.0000	0.0000	133.4330	133.5450
Biased	8	122.5536	122.3480	122.3390	122.3160	122.3240
	9	120.3202	120.3170	120.2000	120.1990	120.1570
	55	126.2977	126.3190	126.2070	126.1320	126.1710
	56	124.1130	123.9880	123.9340	123.8700	123.9140
	Min	120.3202	120.3170	120.2000	120.1990	120.1570
	Max	126.2977	126.3190	126.2070	126.1320	126.1710
	Average	123.3211	123.2430	123.1700	123.1293	123.1415
UnBiased	10	120.7528	120.7230	120.6970	120.6620	123.4580
	11	123.4532	123.5900	123.5170	123.4260	120.6400
	57	125.6009	125.6220	125.6030	125.5930	125.4370
	58	127.2468	127.4470	127.4220	127.3120	127.3630
	Min	120.7528	120.7230	120.6970	120.6620	120.6400
	Max	127.2468	127.4470	127.4220	127.3120	127.3630
	Average	124.2634	124.3455	124.3098	124.2483	124.2245

Limit: >106dB



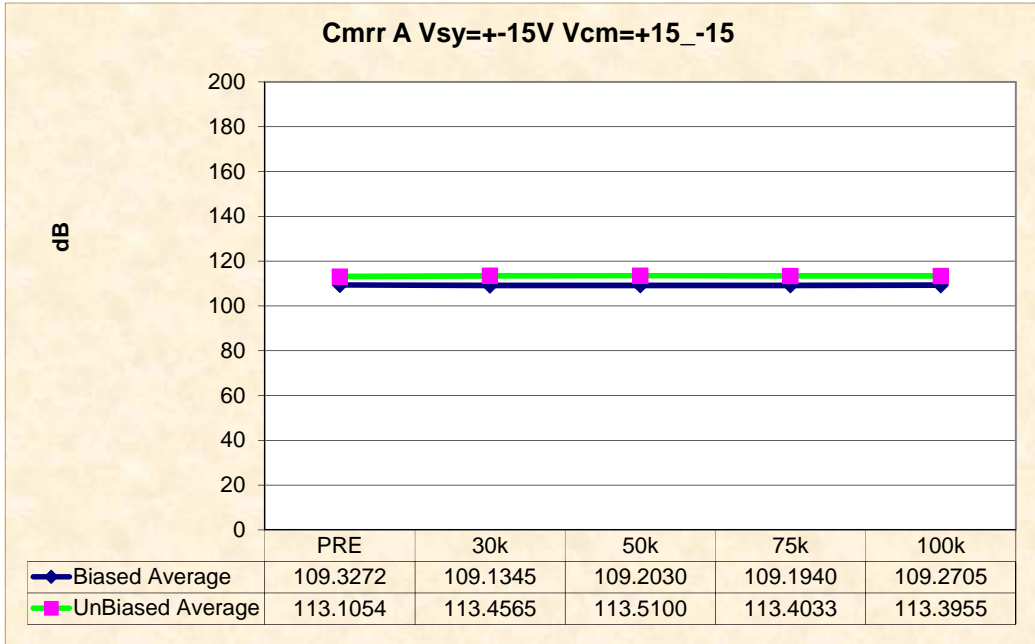
Cmrr D Vsy=+15V Vcm=+14_-14 dB						
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	120.2930	0.0000	0.0000	118.4610	118.4090
	54	128.5790	0.0000	0.0000	128.2970	128.4260
Biased	8	124.6357	124.2860	124.3130	124.2190	124.2330
	9	119.7302	119.7380	119.6700	119.6460	119.6320
	55	124.6146	124.5340	124.4700	124.4230	124.4560
	56	123.1634	123.1800	123.0610	123.0730	123.0630
	Min	119.7302	119.7380	119.6700	119.6460	119.6320
	Max	124.6357	124.5340	124.4700	124.4230	124.4560
	Average	123.0360	122.9345	122.8785	122.8403	122.8460
UnBiased	10	117.8278	117.8740	117.7880	117.8620	122.5130
	11	122.6391	122.7060	122.7140	122.5290	117.8010
	57	122.4082	122.5200	122.4100	122.4990	122.4380
	58	123.3671	123.2310	123.2870	123.3560	123.2400
	Min	117.8278	117.8740	117.7880	117.8620	117.8010
	Max	123.3671	123.2310	123.2870	123.3560	123.2400
	Average	121.5605	121.5828	121.5498	121.5615	121.4980

Limit: >106dB



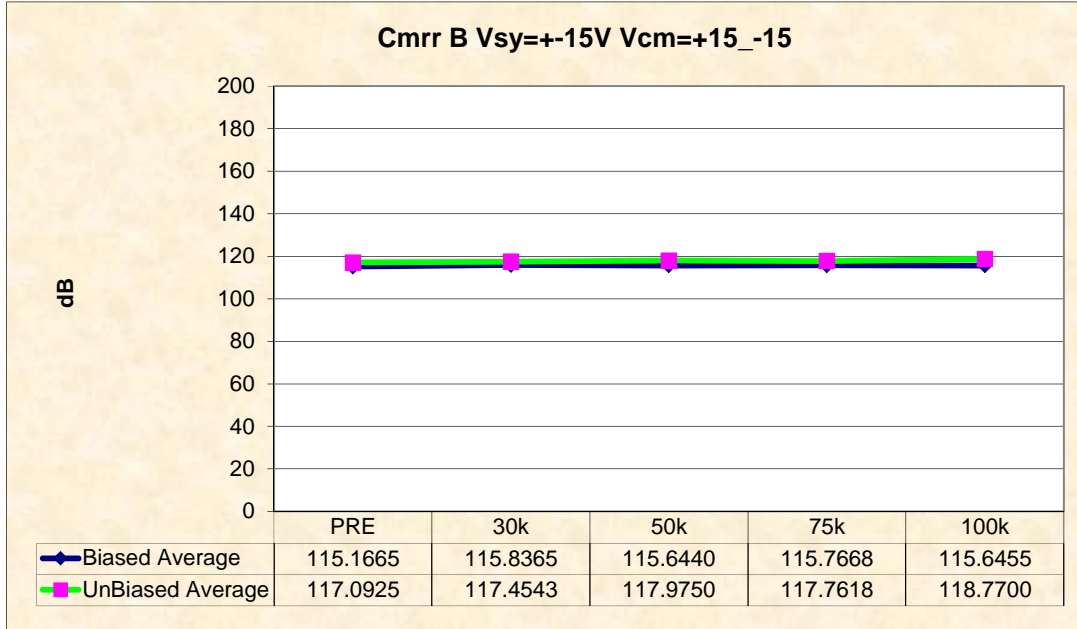
Cmrr A Vsy=+-15V Vcm=+15_-15 dB						
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	122.6530	0.0000	0.0000	110.3420	110.0270
	54	114.9460	0.0000	0.0000	114.8150	114.3440
Biased	14	110.3680	109.9660	109.8640	109.8980	109.7490
	15	117.8864	118.2470	118.9570	118.7860	119.4990
	42	103.9547	103.6180	103.4210	103.4940	103.3730
	43	105.0999	104.7070	104.5700	104.5980	104.4610
	Min	103.9547	103.6180	103.4210	103.4940	103.3730
	Max	117.8864	118.2470	118.9570	118.7860	119.4990
	Average	109.3272	109.1345	109.2030	109.1940	109.2705
UnBiased	23	119.4501	122.1790	123.0210	122.3130	111.7450
	24	112.2878	112.5010	112.0640	112.2490	123.4230
	47	118.1170	116.6410	116.4520	116.5120	116.0720
	48	102.5669	102.5050	102.5030	102.5390	102.3420
	Min	102.5669	102.5050	102.5030	102.5390	102.3420
	Max	119.4501	122.1790	123.0210	122.3130	123.4230
	Average	113.1054	113.4565	113.5100	113.4033	113.3955

Limit: >85dB



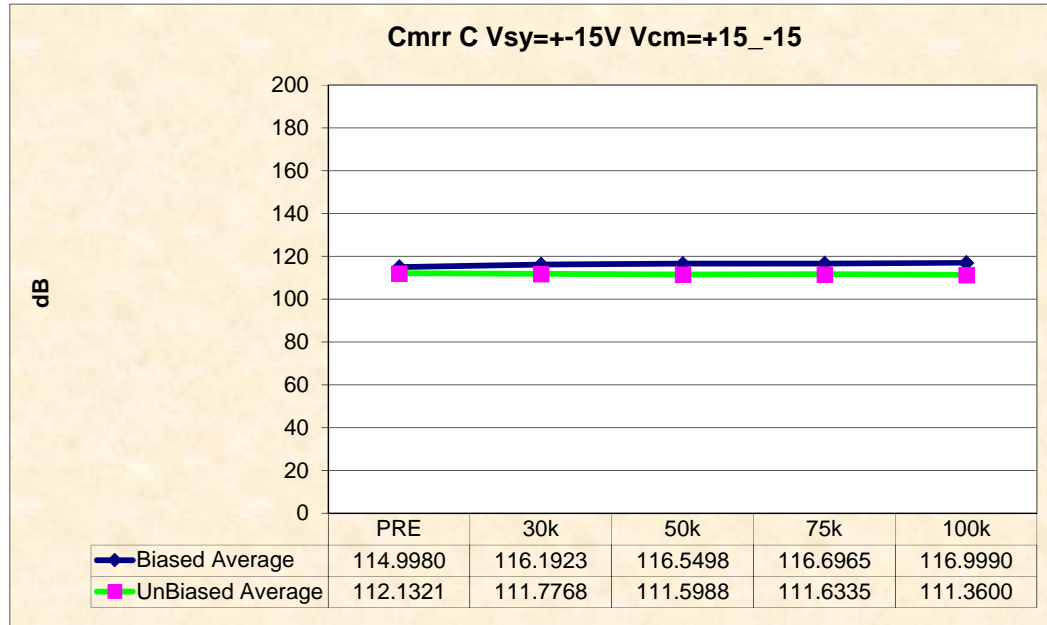
Cmrr B Vsy=+-15V Vcm=+15_-15 dB						
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	107.3310	0.0000	0.0000	118.7710	119.4720
	54	122.6940	0.0000	0.0000	122.3860	123.4020
Biased	8	120.1317	123.0980	122.9320	123.2510	123.2070
	9	105.9053	106.4200	106.6320	106.5820	106.6930
	55	125.6744	123.9820	123.0030	123.1850	122.5110
	56	108.9546	109.8460	110.0090	110.0490	110.1710
	Min	105.9053	106.4200	106.6320	106.5820	106.6930
	Max	125.6744	123.9820	123.0030	123.2510	123.2070
	Average	115.1665	115.8365	115.6440	115.7668	115.6455
UnBiased	10	122.5494	123.5670	124.0020	123.9000	128.4290
	11	125.8780	125.3040	126.8830	126.2740	125.1750
	57	108.2445	108.4770	108.5310	108.5060	108.6840
	58	111.6982	112.4690	112.4840	112.3670	112.7920
	Min	108.2445	108.4770	108.5310	108.5060	108.6840
	Max	125.8780	125.3040	126.8830	126.2740	128.4290
	Average	117.0925	117.4543	117.9750	117.7618	118.7700

Limit: >85dB



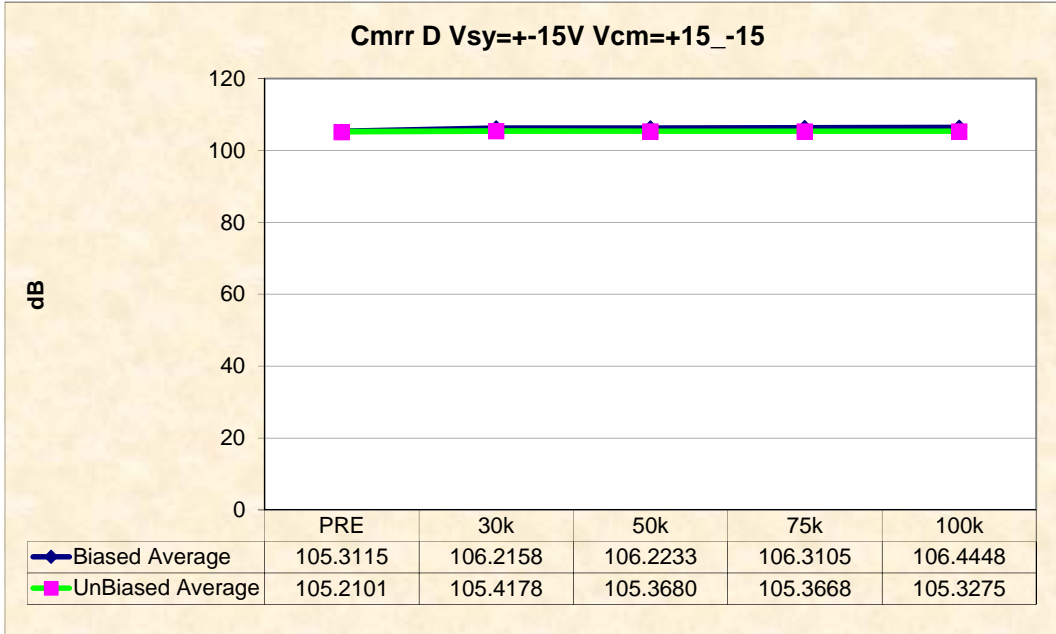
Cmrr C Vsy=+15V Vcm=+15_-15 dB						
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	110.9980	0.0000	0.0000	110.3190	110.0520
	54	107.1640	0.0000	0.0000	107.2820	107.3980
Biased	8	120.8526	124.1760	124.5720	125.0740	125.5690
	9	109.4482	110.3450	110.5890	110.5760	110.6570
	55	121.8653	122.7440	123.6900	123.8140	124.6290
	56	107.8258	107.5040	107.3480	107.3220	107.1410
	Min	107.8258	107.5040	107.3480	107.3220	107.1410
	Max	121.8653	124.1760	124.5720	125.0740	125.5690
	Average	114.9980	116.1923	116.5498	116.6965	116.9990
UnBiased	10	109.4767	110.6250	110.7650	110.7090	115.9310
	11	116.8426	116.9470	116.4290	116.5320	110.8240
	57	122.8809	119.9610	119.6470	119.7100	119.0760
	58	99.3281	99.5740	99.5540	99.5830	99.6090
	Min	99.3281	99.5740	99.5540	99.5830	99.6090
	Max	122.8809	119.9610	119.6470	119.7100	119.0760
	Average	112.1321	111.7768	111.5988	111.6335	111.3600

Limit: >85dB



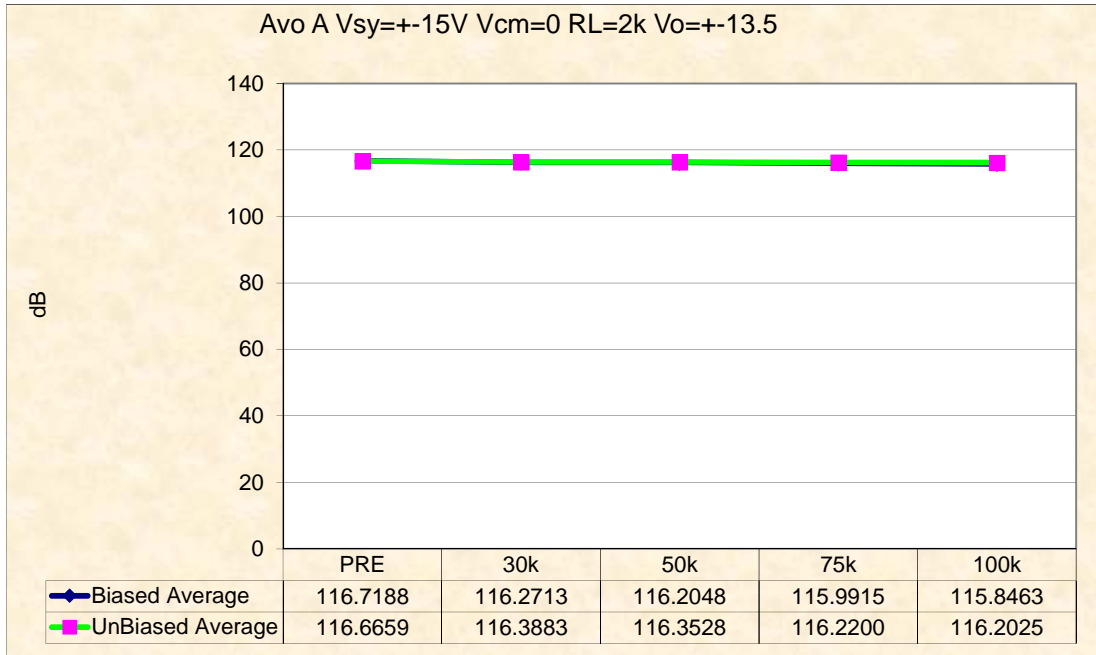
Cmrr D Vsy=+-15V Vcm=+15_-15 dB						
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	106.7130	0.0000	0.0000	97.1790	97.1670
	54	111.0790	0.0000	0.0000	111.1200	111.3530
Biased	14	114.1565	115.8400	115.8220	116.1090	116.5890
	15	97.6271	97.9880	97.9690	97.9830	97.9700
	42	102.0692	102.4080	102.4270	102.4490	102.4720
	43	107.3932	108.6270	108.6750	108.7010	108.7480
	Min	97.6271	97.9880	97.9690	97.9830	97.9700
	Max	114.1565	115.8400	115.8220	116.1090	116.5890
	Average	105.3115	106.2158	106.2233	106.3105	106.4448
UnBiased	23	101.6249	101.9300	101.9640	101.9810	111.4450
	24	112.3997	112.0180	111.8490	111.7510	102.0130
	47	103.8913	104.5560	104.5390	104.5800	104.6250
	48	102.9247	103.1670	103.1200	103.1550	103.2270
	Min	101.6249	101.9300	101.9640	101.9810	102.0130
	Max	112.3997	112.0180	111.8490	111.7510	111.4450
	Average	105.2101	105.4178	105.3680	105.3668	105.3275

Limit: >85dB



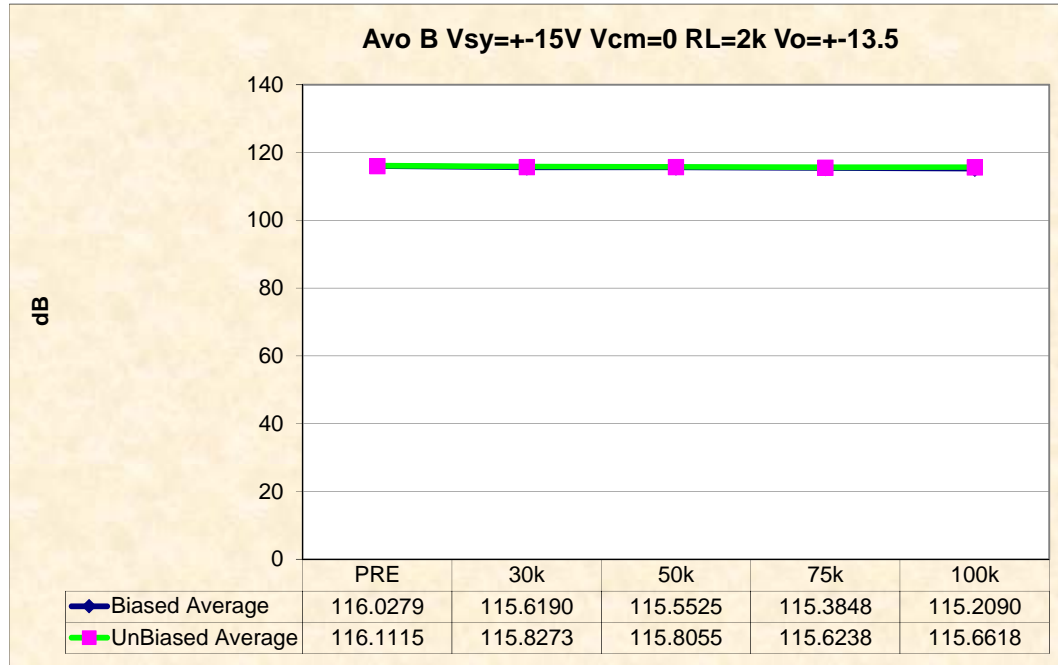
Avo A Vsy=+-15V Vcm=0 RL=2k Vo=+-13.5 dB						
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	116.4800	0.0000	0.0000	116.7680	116.8560
	54	115.5140	0.0000	0.0000	115.4640	115.5680
Biased	8	116.9976	116.6020	116.4380	116.2650	116.1160
	9	116.7003	116.2940	116.2000	115.9920	115.8890
	55	116.3738	115.8820	115.8860	115.6490	115.4670
	56	116.8035	116.3070	116.2950	116.0600	115.9130
	Min	116.3738	115.8820	115.8860	115.6490	115.4670
	Max	116.9976	116.6020	116.4380	116.2650	116.1160
	Average	116.7188	116.2713	116.2048	115.9915	115.8463
UnBiased	10	116.5580	116.3070	116.2660	116.2120	115.7910
	11	116.2204	115.9580	115.9740	115.8000	116.1600
	57	116.4024	116.0240	115.9930	115.8460	115.8410
	58	117.4827	117.2640	117.1780	117.0220	117.0180
	Min	116.2204	115.9580	115.9740	115.8000	115.7910
	Max	117.4827	117.2640	117.1780	117.0220	117.0180
	Average	116.6659	116.3883	116.3528	116.2200	116.2025

Limit: >110dB



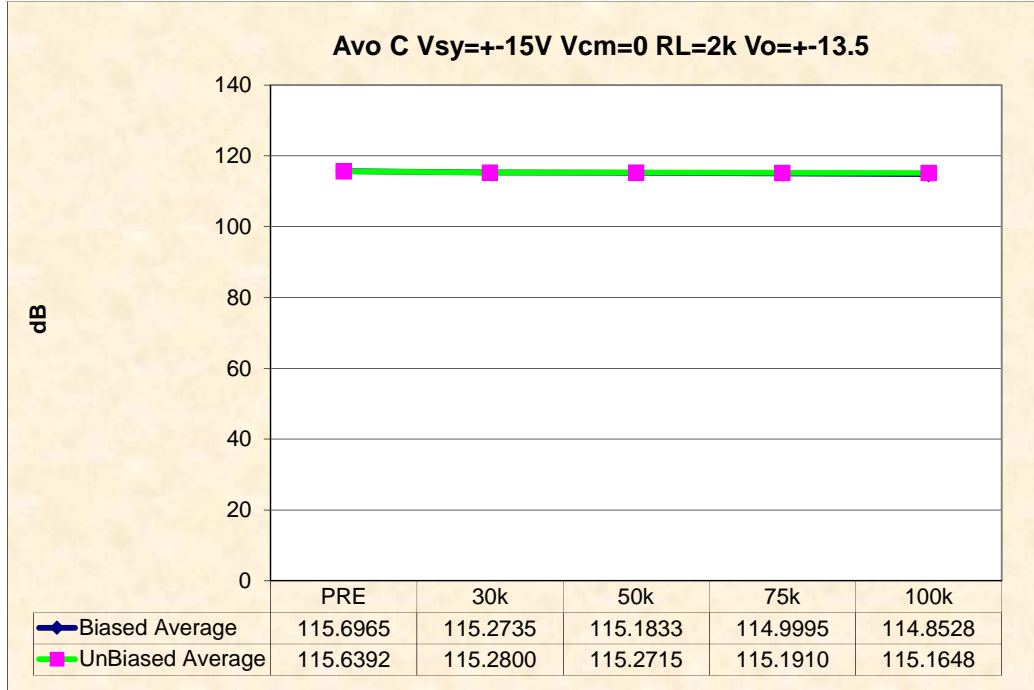
Avo B Vsy=+-15V Vcm=0 RL=2k Vo=+-13.5 dB						
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	116.0780	0.0000	0.0000	116.2820	116.3560
	54	115.9940	0.0000	0.0000	116.0410	116.1250
Biased	8	116.3047	115.9350	115.7760	115.6890	115.4900
	9	115.9770	115.5470	115.4800	115.2890	115.1280
	55	115.7930	115.3400	115.3600	115.1620	115.0000
	56	116.0368	115.6540	115.5940	115.3990	115.2180
	Min	115.7930	115.3400	115.3600	115.1620	115.0000
	Max	116.3047	115.9350	115.7760	115.6890	115.4900
	Average	116.0279	115.6190	115.5525	115.3848	115.2090
UnBiased	10	115.9067	115.5150	115.4650	115.4020	116.2640
	11	116.7488	116.5110	116.5480	116.2730	115.4200
	57	115.8979	115.6540	115.6360	115.3990	115.5370
	58	115.8924	115.6290	115.5730	115.4210	115.4260
	Min	115.8924	115.5150	115.4650	115.3990	115.4200
	Max	116.7488	116.5110	116.5480	116.2730	116.2640
	Average	116.1115	115.8273	115.8055	115.6238	115.6618

Limit: >110dB



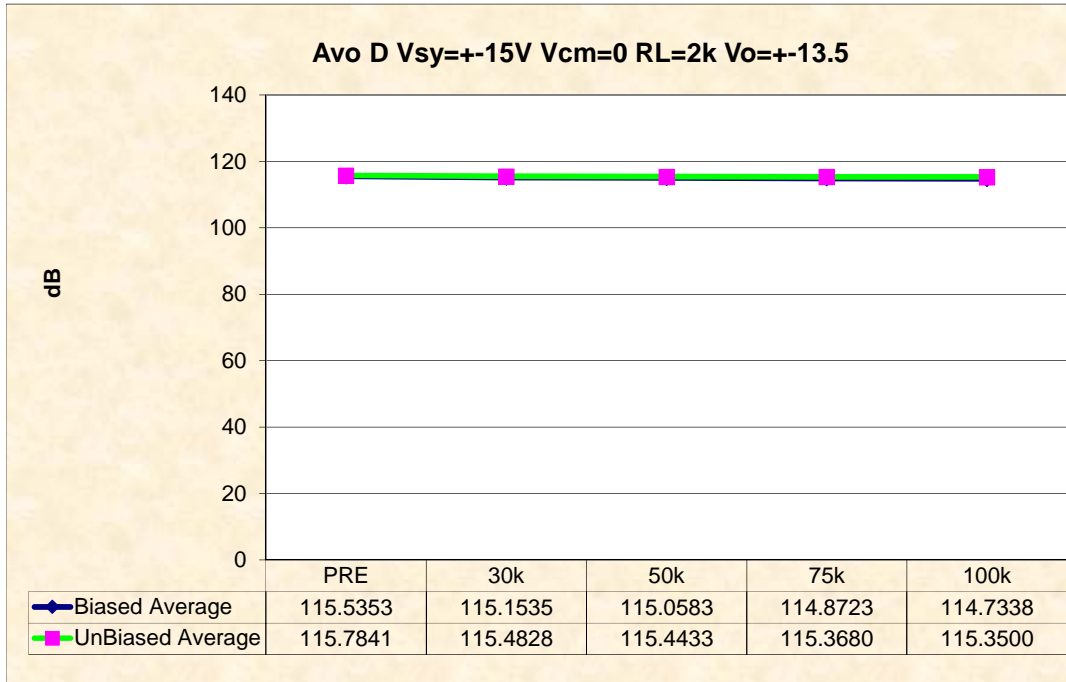
Avo C Vsy=+-15V Vcm=0 RL=2k Vo=+-13.5 dB						
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	115.7040	0.0000	0.0000	115.9940	116.0700
	54	115.3340	0.0000	0.0000	115.3440	115.4370
Biased	14	115.4150	115.0790	114.9390	114.8180	114.6970
	15	115.7461	115.2680	115.2010	114.9750	114.8640
	42	114.8893	114.3920	114.3440	114.1340	113.9850
	43	116.7354	116.3550	116.2490	116.0710	115.8650
	Min	114.8893	114.3920	114.3440	114.1340	113.9850
	Max	116.7354	116.3550	116.2490	116.0710	115.8650
	Average	115.6965	115.2735	115.1833	114.9995	114.8528
UnBiased	23	115.7224	115.2000	115.2190	115.1660	115.7610
	24	116.1504	115.8570	115.8200	115.7270	115.1430
	47	115.6285	115.2970	115.3100	115.1980	115.1100
	48	115.0557	114.7660	114.7370	114.6730	114.6450
	Min	115.0557	114.7660	114.7370	114.6730	114.6450
	Max	116.1504	115.8570	115.8200	115.7270	115.7610
	Average	115.6392	115.2800	115.2715	115.1910	115.1648

Limit: >110dB



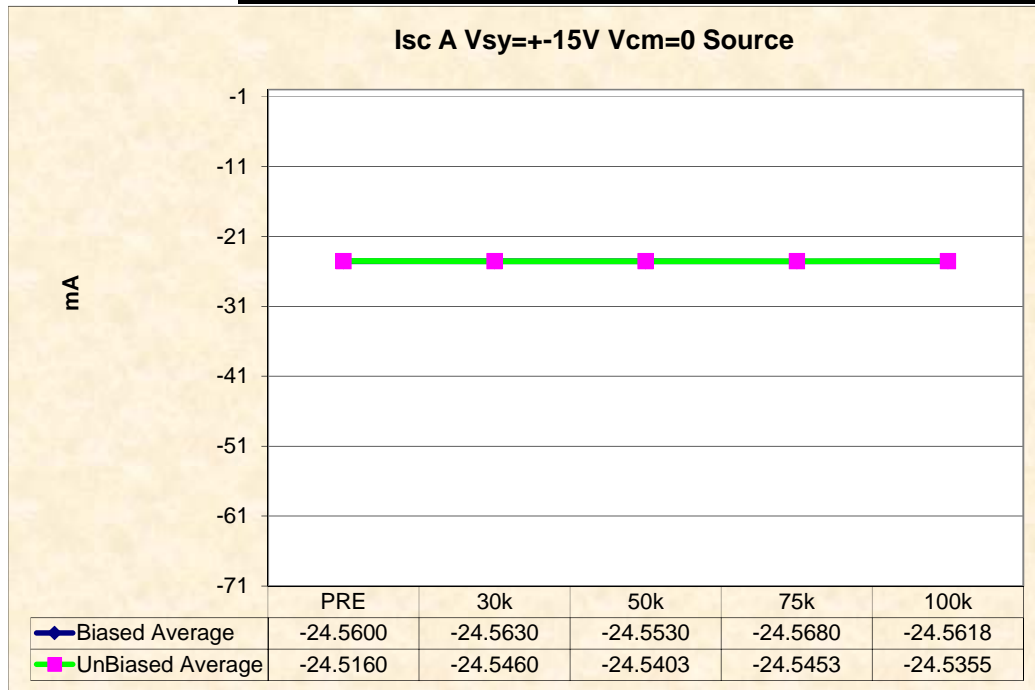
Avo D Vsy=+-15V Vcm=0 RL=2k Vo=+-13.5 dB						
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	115.7650	0.0000	0.0000	115.2440	115.3040
	54	115.6200	0.0000	0.0000	115.6540	115.7180
Biased	8	116.0252	115.7220	115.5760	115.4180	115.2580
	9	115.1193	114.6940	114.6690	114.4550	114.3160
	55	115.0648	114.6410	114.5490	114.3500	114.2240
	56	115.9319	115.5570	115.4390	115.2660	115.1370
	Min	115.0648	114.6410	114.5490	114.3500	114.2240
	Max	116.0252	115.7220	115.5760	115.4180	115.2580
	Average	115.5353	115.1535	115.0583	114.8723	114.7338
UnBiased	10	115.5919	115.2010	115.1900	115.1540	116.3100
	11	116.7335	116.4600	116.4340	116.3090	115.1650
	57	115.4152	115.0910	115.0160	114.9000	114.8610
	58	115.3958	115.1790	115.1330	115.1090	115.0640
	Min	115.3958	115.0910	115.0160	114.9000	114.8610
	Max	116.7335	116.4600	116.4340	116.3090	116.3100
	Average	115.7841	115.4828	115.4433	115.3680	115.3500

Limit: >110dB



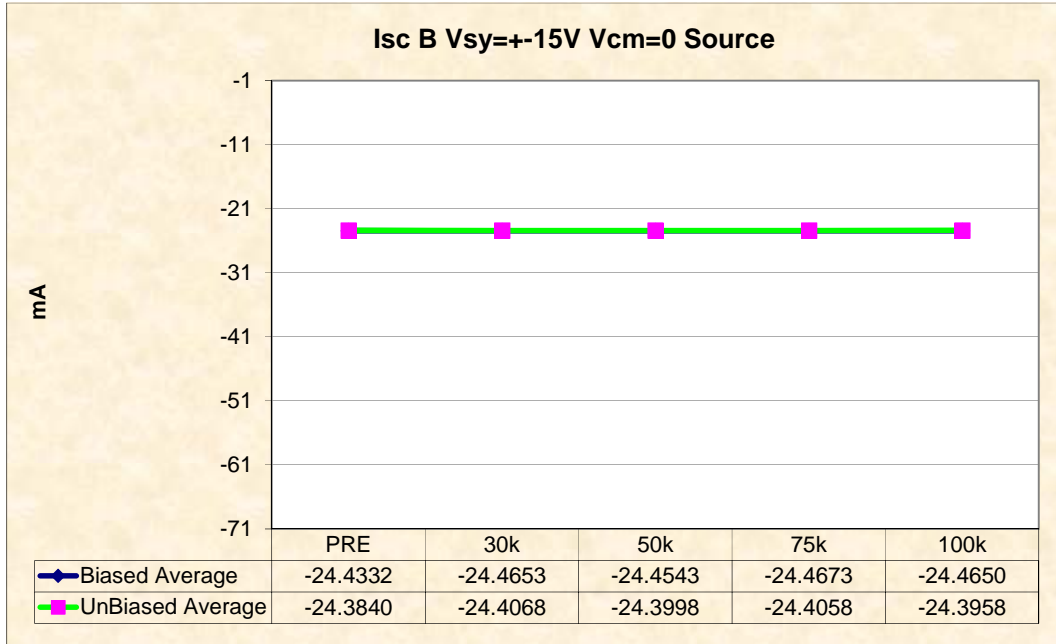
		Isc A Vsy=+-15V Vcm=0 Source				
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	-24.4190	0.0000	0.0000	-24.5570	-24.5390
	54	-24.7460	0.0000	0.0000	-24.7360	-24.7370
Biased	8	-24.5417	-24.5430	-24.5420	-24.5520	-24.5470
	9	-24.5348	-24.5650	-24.5490	-24.5700	-24.5590
	55	-24.9211	-24.9180	-24.9040	-24.9240	-24.9160
	56	-24.2425	-24.2260	-24.2170	-24.2260	-24.2250
	Min	-24.9211	-24.9180	-24.9040	-24.9240	-24.9160
	Max	-24.2425	-24.2260	-24.2170	-24.2260	-24.2250
	Average	-24.5600	-24.5630	-24.5530	-24.5680	-24.5618
UnBiased	10	-24.3986	-24.4620	-24.4530	-24.4620	-24.4010
	11	-24.4005	-24.4200	-24.4050	-24.4110	-24.4520
	57	-24.6238	-24.6590	-24.6540	-24.6590	-24.6480
	58	-24.6412	-24.6430	-24.6490	-24.6490	-24.6410
	Min	-24.6412	-24.6590	-24.6540	-24.6590	-24.6480
	Max	-24.3986	-24.4200	-24.4050	-24.4110	-24.4010
	Average	-24.5160	-24.5460	-24.5403	-24.5453	-24.5355

Limit: >-71mA



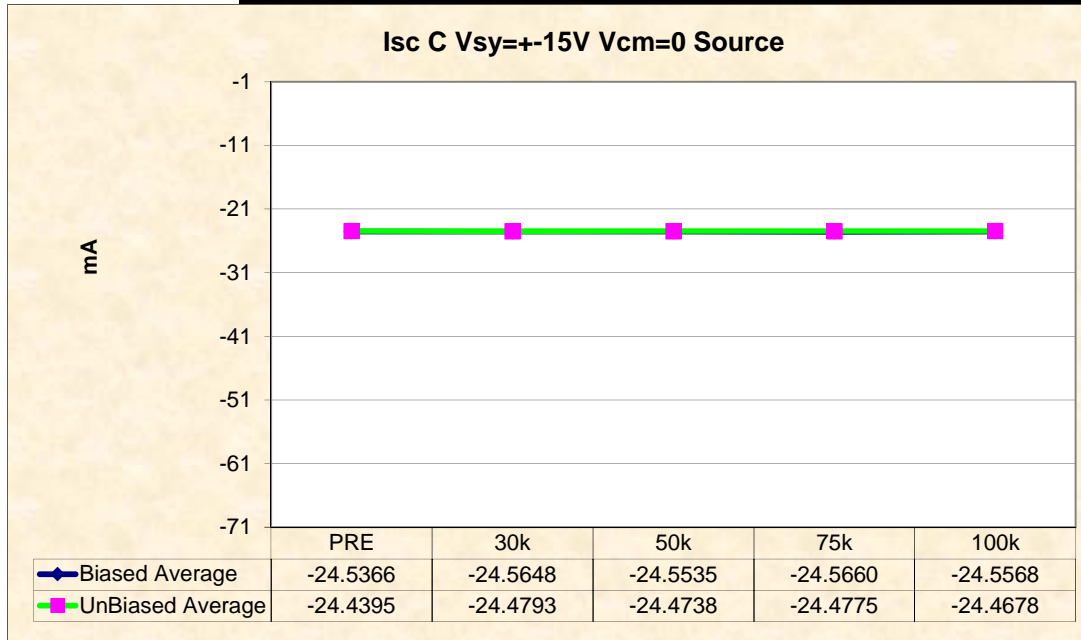
Isc B Vsy=+-15V Vcm=0 Source						
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	-24.1310	0.0000	0.0000	-24.6420	-24.6270
	54	-24.5670	0.0000	0.0000	-24.5720	-24.5780
Biased	14	-24.4785	-24.4940	-24.4940	-24.5040	-24.5040
	15	-24.2761	-24.3090	-24.2890	-24.3050	-24.2970
	42	-24.3454	-24.3700	-24.3570	-24.3700	-24.3710
	43	-24.6329	-24.6880	-24.6770	-24.6900	-24.6880
	Min	-24.6329	-24.6880	-24.6770	-24.6900	-24.6880
	Max	-24.2761	-24.3090	-24.2890	-24.3050	-24.2970
	Average	-24.4332	-24.4653	-24.4543	-24.4673	-24.4650
UnBiased	23	-24.2723	-24.3040	-24.2950	-24.3030	-24.2310
	24	-24.2230	-24.2490	-24.2330	-24.2420	-24.2970
	47	-24.5923	-24.6100	-24.6000	-24.6070	-24.5960
	48	-24.4486	-24.4640	-24.4710	-24.4710	-24.4590
	Min	-24.5923	-24.6100	-24.6000	-24.6070	-24.5960
	Max	-24.2230	-24.2490	-24.2330	-24.2420	-24.2310
	Average	-24.3840	-24.4068	-24.3998	-24.4058	-24.3958

Limit: >-71mA



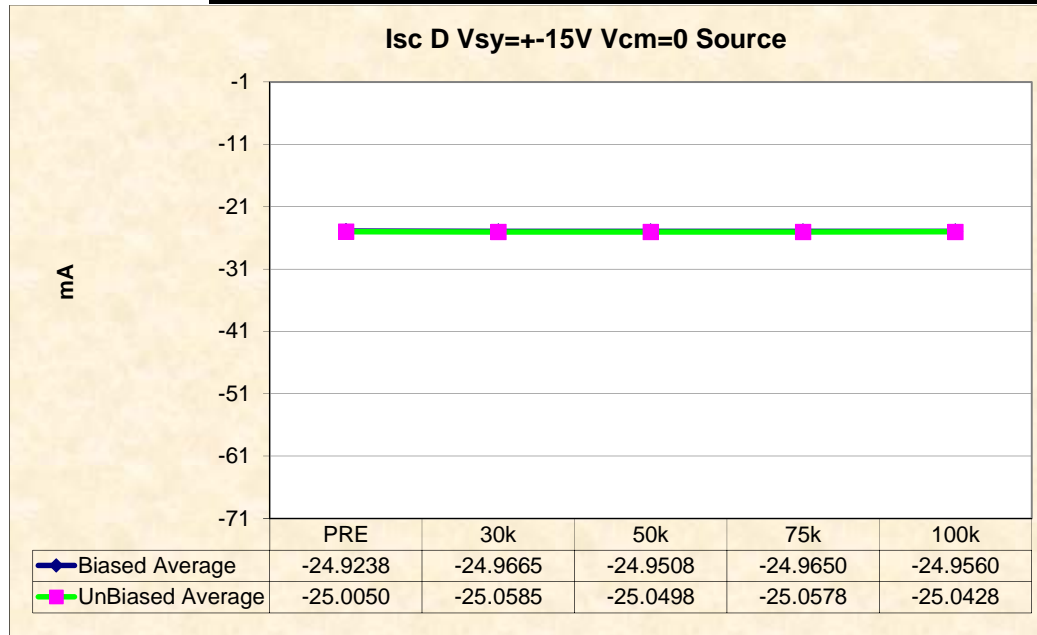
		Isc C Vsy=+-15V Vcm=0 Source				
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	-24.7740	0.0000	0.0000	-24.7180	-24.7020
	54	-24.5490	0.0000	0.0000	-24.5490	-24.5480
Biased	8	-24.8394	-24.8400	-24.8410	-24.8520	-24.8470
	9	-24.6689	-24.7250	-24.7000	-24.7230	-24.7060
	55	-24.2835	-24.2970	-24.2820	-24.2950	-24.2870
	56	-24.3547	-24.3970	-24.3910	-24.3940	-24.3870
	Min	-24.8394	-24.8400	-24.8410	-24.8520	-24.8470
	Max	-24.2835	-24.2970	-24.2820	-24.2950	-24.2870
	Average	-24.5366	-24.5648	-24.5535	-24.5660	-24.5568
UnBiased	10	-24.2710	-24.2950	-24.2870	-24.2910	-24.7360
	11	-24.6839	-24.7670	-24.7470	-24.7560	-24.2800
	57	-24.2604	-24.3020	-24.3000	-24.3020	-24.2970
	58	-24.5427	-24.5530	-24.5610	-24.5610	-24.5580
	Min	-24.6839	-24.7670	-24.7470	-24.7560	-24.7360
	Max	-24.2604	-24.2950	-24.2870	-24.2910	-24.2800
	Average	-24.4395	-24.4793	-24.4738	-24.4775	-24.4678

Limit: >-71mA



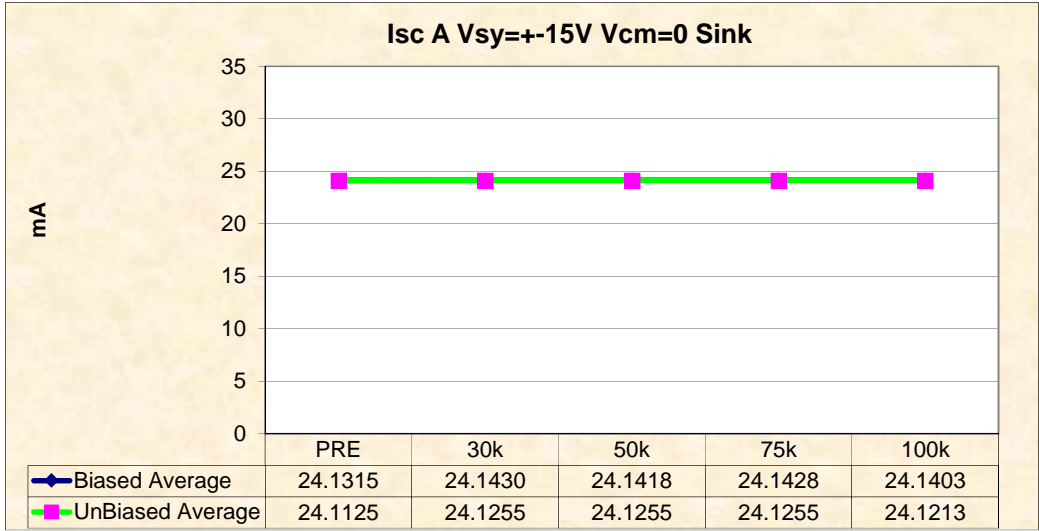
		Isc D Vsy=+-15V Vcm=0 Source				
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	-25.0780	0.0000	0.0000	-24.6290	-24.6150
	54	-24.6870	0.0000	0.0000	-24.7090	-24.7110
Biased	8	-24.6446	-24.6750	-24.6700	-24.6780	-24.6710
	9	-24.9489	-24.9770	-24.9540	-24.9770	-24.9600
	55	-24.9308	-25.0020	-24.9810	-24.9950	-24.9900
	56	-25.1708	-25.2120	-25.1980	-25.2100	-25.2030
	Min	-25.1708	-25.2120	-25.1980	-25.2100	-25.2030
	Max	-24.6446	-24.6750	-24.6700	-24.6780	-24.6710
	Average	-24.9238	-24.9665	-24.9508	-24.9650	-24.9560
UnBiased	10	-25.3521	-25.4120	-25.3990	-25.4130	-24.9720
	11	-24.9258	-25.0020	-24.9830	-24.9930	-25.3970
	57	-24.9871	-25.0330	-25.0220	-25.0300	-25.0170
	58	-24.7552	-24.7870	-24.7950	-24.7950	-24.7850
	Min	-25.3521	-25.4120	-25.3990	-25.4130	-25.3970
	Max	-24.7552	-24.7870	-24.7950	-24.7950	-24.7850
	Average	-25.0050	-25.0585	-25.0498	-25.0578	-25.0428

Limit: >-71mA



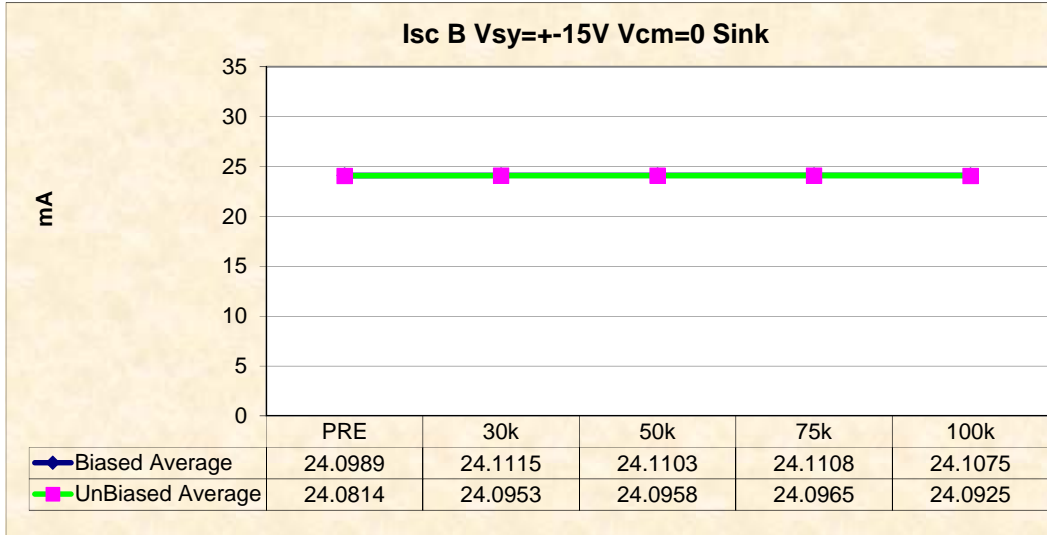
Isc A Vsy=+-15V Vcm=0 Sink						
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	24.2440	0.0000	0.0000	24.1470	24.1390
	54	24.3190	0.0000	0.0000	24.1900	24.1830
Biased	14	24.1351	24.1450	24.1450	24.1450	24.1450
	15	24.1226	24.1360	24.1310	24.1370	24.1330
	42	24.1320	24.1440	24.1420	24.1460	24.1380
	43	24.1363	24.1470	24.1490	24.1430	24.1450
	Min	24.1226	24.1360	24.1310	24.1370	24.1330
	Max	24.1363	24.1470	24.1490	24.1460	24.1450
	Average	24.1315	24.1430	24.1418	24.1428	24.1403
UnBiased	23	24.1139	24.1290	24.1280	24.1270	24.1340
	24	24.1276	24.1400	24.1390	24.1360	24.1230
	47	24.1139	24.1270	24.1260	24.1290	24.1220
	48	24.0947	24.1060	24.1090	24.1100	24.1060
	Min	24.0947	24.1060	24.1090	24.1100	24.1060
	Max	24.1276	24.1400	24.1390	24.1360	24.1340
	Average	24.1125	24.1255	24.1255	24.1255	24.1213

Limit: <35mA



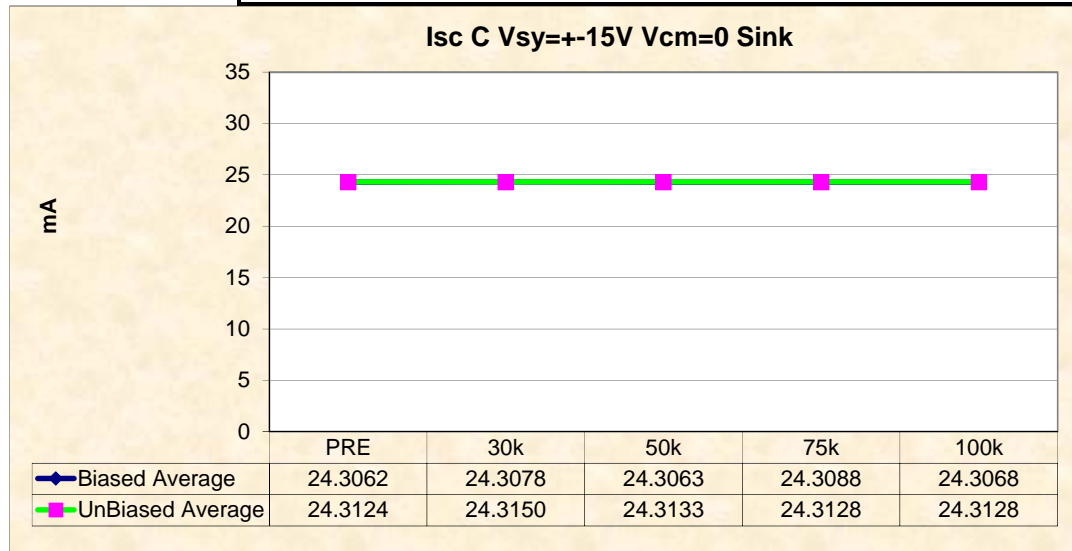
		Isc B Vsy=+-15V Vcm=0 Sink				
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	24.2390	0.0000	0.0000	23.9640	23.9640
	54	24.2650	0.0000	0.0000	24.1310	24.1230
Biased	8	24.0958	24.1060	24.1060	24.1060	24.1030
	9	24.0608	24.0750	24.0730	24.0740	24.0720
	55	24.0827	24.0940	24.0920	24.0940	24.0860
	56	24.1564	24.1710	24.1700	24.1690	24.1690
	Min	24.0608	24.0750	24.0730	24.0740	24.0720
	Max	24.1564	24.1710	24.1700	24.1690	24.1690
	Average	24.0989	24.1115	24.1103	24.1108	24.1075
UnBiased	10	24.0427	24.0580	24.0590	24.0600	24.0870
	11	24.0739	24.0910	24.0870	24.0900	24.0530
	57	24.0927	24.1050	24.1090	24.1060	24.1020
	58	24.1164	24.1270	24.1280	24.1300	24.1280
	Min	24.0427	24.0580	24.0590	24.0600	24.0530
	Max	24.1164	24.1270	24.1280	24.1300	24.1280
	Average	24.0814	24.0953	24.0958	24.0965	24.0925

Limit: <35mA



		Isc C Vsy=+-15V Vcm=0 Sink				
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	24.4200	0.0000	0.0000	24.4050	24.4020
	54	24.3950	0.0000	0.0000	24.2680	24.2660
Biased	8	24.3237	24.3230	24.3210	24.3250	24.3260
	9	24.2937	24.2970	24.2910	24.2980	24.2940
	55	24.3305	24.3300	24.3300	24.3330	24.3280
	56	24.2768	24.2810	24.2830	24.2790	24.2790
	Min	24.2768	24.2810	24.2830	24.2790	24.2790
	Max	24.3305	24.3300	24.3300	24.3330	24.3280
	Average	24.3062	24.3078	24.3063	24.3088	24.3068
UnBiased	10	24.2906	24.2910	24.2900	24.2910	24.2900
	11	24.3305	24.3360	24.3280	24.3280	24.2910
	57	24.3193	24.3250	24.3250	24.3250	24.3200
	58	24.3093	24.3080	24.3100	24.3070	24.3110
	Min	24.2906	24.2910	24.2900	24.2910	24.2910
	Max	24.3305	24.3360	24.3280	24.3280	24.3290
	Average	24.3124	24.3150	24.3133	24.3128	24.3128

Limit: <35mA



Isc D Vsy=+-15V Vcm=0 Sink						
Wafer	SN	PRE	30k	50k	75k	100k
Control	7	24.4700	0.0000	0.0000	24.4610	24.4550
	54	24.4340	0.0000	0.0000	24.2870	24.2850
Biased	14	24.2565	24.2620	24.2620	24.2600	24.2630
	15	24.3171	24.3200	24.3170	24.3170	24.3150
	42	24.3190	24.3200	24.3180	24.3220	24.3200
	43	24.2871	24.2870	24.2850	24.2820	24.2840
	Min	24.2565	24.2620	24.2620	24.2600	24.2630
	Max	24.3190	24.3200	24.3180	24.3220	24.3200
	Average	24.2949	24.2973	24.2955	24.2953	24.2955
UnBiased	23	24.3577	24.3610	24.3610	24.3590	24.2900
	24	24.2908	24.2910	24.2890	24.2900	24.3630
	47	24.3140	24.3120	24.3140	24.3110	24.3090
	48	24.31397	24.31500	24.31800	24.31500	24.31600
	Min	24.29085	24.29100	24.28900	24.29000	24.29000
	Max	24.35772	24.36100	24.36100	24.35900	24.36300
	Average	24.31913	24.31975	24.32050	24.31875	24.31950

Limit: <35mA

