**ADAR1000 Memory Map.** Note that the addresses assumes chip 0 addressing (AD0 = 0, AD1 = 0). The most significant digit in the register address needs to change accordingly, when addressing chip 1, 2 or 3.

| Addr | Name | Bits | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
|---|---|---|---|---|---|---|---|---|---|---|
| 0x1000 | BEAM_POSITION_0_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x1001 | BEAM_POSITION_0_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x1002 | BEAM_POSITION_0_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x1003 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1004 | BEAM_POSITION_0_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x1005 | BEAM_POSITION_0_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x1006 | BEAM_POSITION_0_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x1007 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1008 | BEAM_POSITION_0_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x1009 | BEAM_POSITION_0_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x100A | BEAM_POSITION_0_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x100B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x100C | BEAM_POSITION_0_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x100D | BEAM_POSITION_0_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x100E | BEAM_POSITION_0_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x100F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1010 | BEAM_POSITION_1_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x1011 | BEAM_POSITION_1_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x1012 | BEAM_POSITION_1_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x1013 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1014 | BEAM_POSITION_1_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x1015 | BEAM_POSITION_1_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x1016 | BEAM_POSITION_1_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x1017 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1018 | BEAM_POSITION_1_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x1019 | BEAM_POSITION_1_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x101A | BEAM_POSITION_1_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x101B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x101C | BEAM_POSITION_1_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x101D | BEAM_POSITION_1_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x101E | BEAM_POSITION_1_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x101F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1020 | BEAM_POSITION_2_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x1021 | BEAM_POSITION_2_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x1022 | BEAM_POSITION_2_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x1023 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1024 | BEAM_POSITION_2_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |

| Addr | Name | Bits | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
|---|---|---|---|---|---|---|---|---|---|---|
| 0x1025 | BEAM_POSITION_2_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x1026 | BEAM_POSITION_2_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x1027 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1028 | BEAM_POSITION_2_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x1029 | BEAM_POSITION_2_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x102A | BEAM_POSITION_2_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x102B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x102C | BEAM_POSITION_2_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x102D | BEAM_POSITION_2_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x102E | BEAM_POSITION_2_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x102F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1030 | BEAM_POSITION_3_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x1031 | BEAM_POSITION_3_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x1032 | BEAM_POSITION_3_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x1033 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1034 | BEAM_POSITION_3_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x1035 | BEAM_POSITION_3_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x1036 | BEAM_POSITION_3_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x1037 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1038 | BEAM_POSITION_3_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x1039 | BEAM_POSITION_3_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x103A | BEAM_POSITION_3_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x103B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x103C | BEAM_POSITION_3_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x103D | BEAM_POSITION_3_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x103E | BEAM_POSITION_3_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x103F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1040 | BEAM_POSITION_4_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x1041 | BEAM_POSITION_4_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x1042 | BEAM_POSITION_4_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x1043 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1044 | BEAM_POSITION_4_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x1045 | BEAM_POSITION_4_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x1046 | BEAM_POSITION_4_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x1047 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1048 | BEAM_POSITION_4_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x1049 | BEAM_POSITION_4_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x104A | BEAM_POSITION_4_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x104B | RESERVED | [7:0] | RESERVED | | | | | | | |

| Addr | Name | Bits | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
|------|------|------|-------|-------|-------|-------|-------|-------|-------|-------|
| 0x104C | BEAM_POSITION_4_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x104D | BEAM_POSITION_4_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x104E | BEAM_POSITION_4_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x104F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1050 | BEAM_POSITION_5_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x1051 | BEAM_POSITION_5_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x1052 | BEAM_POSITION_5_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x1053 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1054 | BEAM_POSITION_5_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x1055 | BEAM_POSITION_5_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x1056 | BEAM_POSITION_5_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x1057 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1058 | BEAM_POSITION_5_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x1059 | BEAM_POSITION_5_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x105A | BEAM_POSITION_5_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x105B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x105C | BEAM_POSITION_5_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x105D | BEAM_POSITION_5_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x105E | BEAM_POSITION_5_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x105F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1060 | BEAM_POSITION_6_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x1061 | BEAM_POSITION_6_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x1062 | BEAM_POSITION_6_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x1063 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1064 | BEAM_POSITION_6_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x1065 | BEAM_POSITION_6_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x1066 | BEAM_POSITION_6_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x1067 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1068 | BEAM_POSITION_6_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x1069 | BEAM_POSITION_6_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x106A | BEAM_POSITION_6_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x106B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x106C | BEAM_POSITION_6_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x106D | BEAM_POSITION_6_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x106E | BEAM_POSITION_6_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x106F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1070 | BEAM_POSITION_7_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x1071 | BEAM_POSITION_7_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x1072 | BEAM_POSITION_7_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |

| Addr | Name | Bits | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
|------|------|------|-------|-------|-------|-------|-------|-------|-------|-------|
| 0x1073 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1074 | BEAM_POSITION_7_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x1075 | BEAM_POSITION_7_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x1076 | BEAM_POSITION_7_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x1077 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1078 | BEAM_POSITION_7_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x1079 | BEAM_POSITION_7_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x107A | BEAM_POSITION_7_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x107B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x107C | BEAM_POSITION_7_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x107D | BEAM_POSITION_7_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x107E | BEAM_POSITION_7_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x107F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1080 | BEAM_POSITION_8_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x1081 | BEAM_POSITION_8_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x1082 | BEAM_POSITION_8_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x1083 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1084 | BEAM_POSITION_8_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x1085 | BEAM_POSITION_8_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x1086 | BEAM_POSITION_8_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x1087 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1088 | BEAM_POSITION_8_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x1089 | BEAM_POSITION_8_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x108A | BEAM_POSITION_8_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x108B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x108C | BEAM_POSITION_8_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x108D | BEAM_POSITION_8_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x108E | BEAM_POSITION_8_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x108F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1090 | BEAM_POSITION_9_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x1091 | BEAM_POSITION_9_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x1092 | BEAM_POSITION_9_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x1093 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1094 | BEAM_POSITION_9_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x1095 | BEAM_POSITION_9_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x1096 | BEAM_POSITION_9_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x1097 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1098 | BEAM_POSITION_9_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x1099 | BEAM_POSITION_9_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |

| Addr | Name | Bits | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
|------|------|------|-------|-------|-------|-------|-------|-------|-------|-------|
| 0x109A | BEAM_POSITION_9_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x109B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x109C | BEAM_POSITION_9_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x109D | BEAM_POSITION_9_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x109E | BEAM_POSITION_9_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x109F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x10A0 | BEAM_POSITION_10_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x10A1 | BEAM_POSITION_10_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x10A2 | BEAM_POSITION_10_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x10A3 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x10A4 | BEAM_POSITION_10_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x10A5 | BEAM_POSITION_10_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x10A6 | BEAM_POSITION_10_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x10A7 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x10A8 | BEAM_POSITION_10_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x10A9 | BEAM_POSITION_10_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x10AA | BEAM_POSITION_10_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x10AB | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x10AC | BEAM_POSITION_10_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x10AD | BEAM_POSITION_10_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x10AE | BEAM_POSITION_10_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x10AF | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x10B0 | BEAM_POSITION_11_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x10B1 | BEAM_POSITION_11_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x10B2 | BEAM_POSITION_11_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x10B3 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x10B4 | BEAM_POSITION_11_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x10B5 | BEAM_POSITION_11_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x10B6 | BEAM_POSITION_11_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x10B7 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x10B8 | BEAM_POSITION_11_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x10B9 | BEAM_POSITION_11_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x10BA | BEAM_POSITION_11_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x10BB | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x10BC | BEAM_POSITION_11_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x10BD | BEAM_POSITION_11_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x10BE | BEAM_POSITION_11_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x10BF | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x10C0 | BEAM_POSITION_12_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |

| Addr | Name | Bits | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
|---|---|---|---|---|---|---|---|---|---|---|
| 0x10C1 | BEAM_POSITION_12_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x10C2 | BEAM_POSITION_12_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x10C3 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x10C4 | BEAM_POSITION_12_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x10C5 | BEAM_POSITION_12_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x10C6 | BEAM_POSITION_12_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x10C7 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x10C8 | BEAM_POSITION_12_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x10C9 | BEAM_POSITION_12_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x10CA | BEAM_POSITION_12_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x10CB | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x10CC | BEAM_POSITION_12_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x10CD | BEAM_POSITION_12_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x10CE | BEAM_POSITION_12_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x10CF | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x10D0 | BEAM_POSITION_13_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x10D1 | BEAM_POSITION_13_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x10D2 | BEAM_POSITION_13_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x10D3 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x10D4 | BEAM_POSITION_13_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x10D5 | BEAM_POSITION_13_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x10D6 | BEAM_POSITION_13_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x10D7 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x10D8 | BEAM_POSITION_13_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x10D9 | BEAM_POSITION_13_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x10DA | BEAM_POSITION_13_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x10DB | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x10DC | BEAM_POSITION_13_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x10DD | BEAM_POSITION_13_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x10DE | BEAM_POSITION_13_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x10DF | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x10E0 | BEAM_POSITION_14_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x10E1 | BEAM_POSITION_14_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x10E2 | BEAM_POSITION_14_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x10E3 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x10E4 | BEAM_POSITION_14_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x10E5 | BEAM_POSITION_14_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x10E6 | BEAM_POSITION_14_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x10E7 | RESERVED | [7:0] | RESERVED | | | | | | | |

| Addr | Name | Bits | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
|------|------|------|-------|-------|-------|-------|-------|-------|-------|-------|
| 0x10E8 | BEAM_POSITION_14_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x10E9 | BEAM_POSITION_14_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x10EA | BEAM_POSITION_14_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x10EB | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x10EC | BEAM_POSITION_14_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x10ED | BEAM_POSITION_14_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x10EE | BEAM_POSITION_14_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x10EF | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x10F0 | BEAM_POSITION_15_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x10F1 | BEAM_POSITION_15_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x10F2 | BEAM_POSITION_15_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x10F3 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x10F4 | BEAM_POSITION_15_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x10F5 | BEAM_POSITION_15_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x10F6 | BEAM_POSITION_15_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x10F7 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x10F8 | BEAM_POSITION_15_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x10F9 | BEAM_POSITION_15_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x10FA | BEAM_POSITION_15_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x10FB | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x10FC | BEAM_POSITION_15_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x10FD | BEAM_POSITION_15_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x10FE | BEAM_POSITION_15_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x10FF | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1100 | BEAM_POSITION_16_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x1101 | BEAM_POSITION_16_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x1102 | BEAM_POSITION_16_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x1103 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1104 | BEAM_POSITION_16_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x1105 | BEAM_POSITION_16_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x1106 | BEAM_POSITION_16_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x1107 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1108 | BEAM_POSITION_16_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x1109 | BEAM_POSITION_16_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x110A | BEAM_POSITION_16_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x110B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x110C | BEAM_POSITION_16_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x110D | BEAM_POSITION_16_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x110E | BEAM_POSITION_16_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |

| Addr | Name | Bits | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
|------|------|------|-------|-------|-------|-------|-------|-------|-------|-------|
| 0x110F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1110 | BEAM_POSITION_17_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x1111 | BEAM_POSITION_17_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x1112 | BEAM_POSITION_17_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x1113 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1114 | BEAM_POSITION_17_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x1115 | BEAM_POSITION_17_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x1116 | BEAM_POSITION_17_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x1117 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1118 | BEAM_POSITION_17_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x1119 | BEAM_POSITION_17_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x111A | BEAM_POSITION_17_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x111B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x111C | BEAM_POSITION_17_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x111D | BEAM_POSITION_17_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x111E | BEAM_POSITION_17_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x111F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1120 | BEAM_POSITION_18_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x1121 | BEAM_POSITION_18_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x1122 | BEAM_POSITION_18_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x1123 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1124 | BEAM_POSITION_18_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x1125 | BEAM_POSITION_18_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x1126 | BEAM_POSITION_18_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x1127 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1128 | BEAM_POSITION_18_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x1129 | BEAM_POSITION_18_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x112A | BEAM_POSITION_18_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x112B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x112C | BEAM_POSITION_18_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x112D | BEAM_POSITION_18_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x112E | BEAM_POSITION_18_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x112F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1130 | BEAM_POSITION_19_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x1131 | BEAM_POSITION_19_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x1132 | BEAM_POSITION_19_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x1133 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1134 | BEAM_POSITION_19_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x1135 | BEAM_POSITION_19_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |

| Addr | Name | Bits | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
|------|------|------|-------|-------|-------|-------|-------|-------|-------|-------|
| 0x1136 | BEAM_POSITION_19_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x1137 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1138 | BEAM_POSITION_19_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x1139 | BEAM_POSITION_19_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x113A | BEAM_POSITION_19_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x113B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x113C | BEAM_POSITION_19_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x113D | BEAM_POSITION_19_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x113E | BEAM_POSITION_19_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x113F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1140 | BEAM_POSITION_20_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x1141 | BEAM_POSITION_20_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x1142 | BEAM_POSITION_20_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x1143 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1144 | BEAM_POSITION_20_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x1145 | BEAM_POSITION_20_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x1146 | BEAM_POSITION_20_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x1147 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1148 | BEAM_POSITION_20_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x1149 | BEAM_POSITION_20_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x114A | BEAM_POSITION_20_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x114B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x114C | BEAM_POSITION_20_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x114D | BEAM_POSITION_20_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x114E | BEAM_POSITION_20_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x114F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1150 | BEAM_POSITION_21_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x1151 | BEAM_POSITION_21_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x1152 | BEAM_POSITION_21_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x1153 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1154 | BEAM_POSITION_21_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x1155 | BEAM_POSITION_21_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x1156 | BEAM_POSITION_21_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x1157 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1158 | BEAM_POSITION_21_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x1159 | BEAM_POSITION_21_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x115A | BEAM_POSITION_21_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x115B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x115C | BEAM_POSITION_21_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |

| Addr | Name | Bits | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
|------|------|------|-------|-------|-------|-------|-------|-------|-------|-------|
| 0x115D | BEAM_POSITION_21_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x115E | BEAM_POSITION_21_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x115F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1160 | BEAM_POSITION_22_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x1161 | BEAM_POSITION_22_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x1162 | BEAM_POSITION_22_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x1163 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1164 | BEAM_POSITION_22_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x1165 | BEAM_POSITION_22_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x1166 | BEAM_POSITION_22_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x1167 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1168 | BEAM_POSITION_22_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x1169 | BEAM_POSITION_22_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x116A | BEAM_POSITION_22_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x116B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x116C | BEAM_POSITION_22_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x116D | BEAM_POSITION_22_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x116E | BEAM_POSITION_22_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x116F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1170 | BEAM_POSITION_23_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x1171 | BEAM_POSITION_23_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x1172 | BEAM_POSITION_23_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x1173 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1174 | BEAM_POSITION_23_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x1175 | BEAM_POSITION_23_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x1176 | BEAM_POSITION_23_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x1177 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1178 | BEAM_POSITION_23_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x1179 | BEAM_POSITION_23_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x117A | BEAM_POSITION_23_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x117B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x117C | BEAM_POSITION_23_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x117D | BEAM_POSITION_23_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x117E | BEAM_POSITION_23_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x117F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1180 | BEAM_POSITION_24_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x1181 | BEAM_POSITION_24_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x1182 | BEAM_POSITION_24_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x1183 | RESERVED | [7:0] | RESERVED | | | | | | | |

| Addr | Name | Bits | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
|---|---|---|---|---|---|---|---|---|---|---|
| 0x1184 | BEAM_POSITION_24_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x1185 | BEAM_POSITION_24_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x1186 | BEAM_POSITION_24_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x1187 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1188 | BEAM_POSITION_24_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x1189 | BEAM_POSITION_24_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x118A | BEAM_POSITION_24_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x118B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x118C | BEAM_POSITION_24_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x118D | BEAM_POSITION_24_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x118E | BEAM_POSITION_24_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x118F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1190 | BEAM_POSITION_25_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x1191 | BEAM_POSITION_25_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x1192 | BEAM_POSITION_25_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x1193 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1194 | BEAM_POSITION_25_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x1195 | BEAM_POSITION_25_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x1196 | BEAM_POSITION_25_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x1197 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1198 | BEAM_POSITION_25_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x1199 | BEAM_POSITION_25_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x119A | BEAM_POSITION_25_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x119B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x119C | BEAM_POSITION_25_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x119D | BEAM_POSITION_25_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x119E | BEAM_POSITION_25_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x119F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x11A0 | BEAM_POSITION_26_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x11A1 | BEAM_POSITION_26_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x11A2 | BEAM_POSITION_26_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x11A3 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x11A4 | BEAM_POSITION_26_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x11A5 | BEAM_POSITION_26_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x11A6 | BEAM_POSITION_26_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x11A7 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x11A8 | BEAM_POSITION_26_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x11A9 | BEAM_POSITION_26_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x11AA | BEAM_POSITION_26_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |

| Addr | Name | Bits | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
|---|---|---|---|---|---|---|---|---|---|---|
| 0x11AB | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x11AC | BEAM_POSITION_26_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x11AD | BEAM_POSITION_26_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x11AE | BEAM_POSITION_26_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x11AF | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x11B0 | BEAM_POSITION_27_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x11B1 | BEAM_POSITION_27_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x11B2 | BEAM_POSITION_27_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x11B3 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x11B4 | BEAM_POSITION_27_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x11B5 | BEAM_POSITION_27_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x11B6 | BEAM_POSITION_27_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x11B7 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x11B8 | BEAM_POSITION_27_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x11B9 | BEAM_POSITION_27_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x11BA | BEAM_POSITION_27_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x11BB | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x11BC | BEAM_POSITION_27_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x11BD | BEAM_POSITION_27_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x11BE | BEAM_POSITION_27_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x11BF | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x11C0 | BEAM_POSITION_28_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x11C1 | BEAM_POSITION_28_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x11C2 | BEAM_POSITION_28_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x11C3 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x11C4 | BEAM_POSITION_28_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x11C5 | BEAM_POSITION_28_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x11C6 | BEAM_POSITION_28_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x11C7 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x11C8 | BEAM_POSITION_28_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x11C9 | BEAM_POSITION_28_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x11CA | BEAM_POSITION_28_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x11CB | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x11CC | BEAM_POSITION_28_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x11CD | BEAM_POSITION_28_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x11CE | BEAM_POSITION_28_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x11CF | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x11D0 | BEAM_POSITION_29_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x11D1 | BEAM_POSITION_29_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |

| Addr | Name | Bits | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
|------|------|------|-------|-------|-------|-------|-------|-------|-------|-------|
| 0x11D2 | BEAM_POSITION_29_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x11D3 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x11D4 | BEAM_POSITION_29_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x11D5 | BEAM_POSITION_29_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x11D6 | BEAM_POSITION_29_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x11D7 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x11D8 | BEAM_POSITION_29_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x11D9 | BEAM_POSITION_29_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x11DA | BEAM_POSITION_29_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x11DB | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x11DC | BEAM_POSITION_29_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x11DD | BEAM_POSITION_29_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x11DE | BEAM_POSITION_29_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x11DF | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x11E0 | BEAM_POSITION_30_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x11E1 | BEAM_POSITION_30_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x11E2 | BEAM_POSITION_30_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x11E3 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x11E4 | BEAM_POSITION_30_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x11E5 | BEAM_POSITION_30_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x11E6 | BEAM_POSITION_30_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x11E7 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x11E8 | BEAM_POSITION_30_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x11E9 | BEAM_POSITION_30_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x11EA | BEAM_POSITION_30_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x11EB | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x11EC | BEAM_POSITION_30_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x11ED | BEAM_POSITION_30_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x11EE | BEAM_POSITION_30_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x11EF | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x11F0 | BEAM_POSITION_31_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x11F1 | BEAM_POSITION_31_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x11F2 | BEAM_POSITION_31_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x11F3 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x11F4 | BEAM_POSITION_31_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x11F5 | BEAM_POSITION_31_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x11F6 | BEAM_POSITION_31_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x11F7 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x11F8 | BEAM_POSITION_31_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |

| Addr | Name | Bits | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
|------|------|------|-------|-------|-------|-------|-------|-------|-------|-------|
| 0x11F9 | BEAM_POSITION_31_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x11FA | BEAM_POSITION_31_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x11FB | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x11FC | BEAM_POSITION_31_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x11FD | BEAM_POSITION_31_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x11FE | BEAM_POSITION_31_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x11FF | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1200 | BEAM_POSITION_32_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x1201 | BEAM_POSITION_32_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x1202 | BEAM_POSITION_32_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x1203 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1204 | BEAM_POSITION_32_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x1205 | BEAM_POSITION_32_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x1206 | BEAM_POSITION_32_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x1207 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1208 | BEAM_POSITION_32_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x1209 | BEAM_POSITION_32_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x120A | BEAM_POSITION_32_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x120B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x120C | BEAM_POSITION_32_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x120D | BEAM_POSITION_32_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x120E | BEAM_POSITION_32_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x120F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1210 | BEAM_POSITION_33_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x1211 | BEAM_POSITION_33_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x1212 | BEAM_POSITION_33_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x1213 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1214 | BEAM_POSITION_33_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x1215 | BEAM_POSITION_33_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x1216 | BEAM_POSITION_33_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x1217 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1218 | BEAM_POSITION_33_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x1219 | BEAM_POSITION_33_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x121A | BEAM_POSITION_33_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x121B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x121C | BEAM_POSITION_33_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x121D | BEAM_POSITION_33_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x121E | BEAM_POSITION_33_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x121F | RESERVED | [7:0] | RESERVED | | | | | | | |

| Addr | Name | Bits | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
|---|---|---|---|---|---|---|---|---|---|---|
| 0x1220 | BEAM_POSITION_34_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x1221 | BEAM_POSITION_34_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x1222 | BEAM_POSITION_34_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x1223 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1224 | BEAM_POSITION_34_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x1225 | BEAM_POSITION_34_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x1226 | BEAM_POSITION_34_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x1227 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1228 | BEAM_POSITION_34_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x1229 | BEAM_POSITION_34_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x122A | BEAM_POSITION_34_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x122B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x122C | BEAM_POSITION_34_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x122D | BEAM_POSITION_34_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x122E | BEAM_POSITION_34_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x122F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1230 | BEAM_POSITION_35_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x1231 | BEAM_POSITION_35_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x1232 | BEAM_POSITION_35_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x1233 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1234 | BEAM_POSITION_35_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x1235 | BEAM_POSITION_35_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x1236 | BEAM_POSITION_35_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x1237 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1238 | BEAM_POSITION_35_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x1239 | BEAM_POSITION_35_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x123A | BEAM_POSITION_35_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x123B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x123C | BEAM_POSITION_35_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x123D | BEAM_POSITION_35_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x123E | BEAM_POSITION_35_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x123F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1240 | BEAM_POSITION_36_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x1241 | BEAM_POSITION_36_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x1242 | BEAM_POSITION_36_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x1243 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1244 | BEAM_POSITION_36_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x1245 | BEAM_POSITION_36_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x1246 | BEAM_POSITION_36_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |

| Addr | Name | Bits | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
|---|---|---|---|---|---|---|---|---|---|---|
| 0x1247 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1248 | BEAM_POSITION_36_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x1249 | BEAM_POSITION_36_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x124A | BEAM_POSITION_36_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x124B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x124C | BEAM_POSITION_36_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x124D | BEAM_POSITION_36_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x124E | BEAM_POSITION_36_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x124F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1250 | BEAM_POSITION_37_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x1251 | BEAM_POSITION_37_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x1252 | BEAM_POSITION_37_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x1253 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1254 | BEAM_POSITION_37_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x1255 | BEAM_POSITION_37_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x1256 | BEAM_POSITION_37_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x1257 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1258 | BEAM_POSITION_37_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x1259 | BEAM_POSITION_37_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x125A | BEAM_POSITION_37_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x125B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x125C | BEAM_POSITION_37_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x125D | BEAM_POSITION_37_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x125E | BEAM_POSITION_37_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x125F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1260 | BEAM_POSITION_38_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x1261 | BEAM_POSITION_38_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x1262 | BEAM_POSITION_38_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x1263 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1264 | BEAM_POSITION_38_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x1265 | BEAM_POSITION_38_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x1266 | BEAM_POSITION_38_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x1267 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1268 | BEAM_POSITION_38_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x1269 | BEAM_POSITION_38_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x126A | BEAM_POSITION_38_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x126B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x126C | BEAM_POSITION_38_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x126D | BEAM_POSITION_38_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |

| Addr | Name | Bits | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
|---|---|---|---|---|---|---|---|---|---|---|
| 0x126E | BEAM_POSITION_38_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x126F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1270 | BEAM_POSITION_39_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x1271 | BEAM_POSITION_39_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x1272 | BEAM_POSITION_39_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x1273 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1274 | BEAM_POSITION_39_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x1275 | BEAM_POSITION_39_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x1276 | BEAM_POSITION_39_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x1277 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1278 | BEAM_POSITION_39_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x1279 | BEAM_POSITION_39_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x127A | BEAM_POSITION_39_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x127B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x127C | BEAM_POSITION_39_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x127D | BEAM_POSITION_39_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x127E | BEAM_POSITION_39_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x127F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1280 | BEAM_POSITION_40_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x1281 | BEAM_POSITION_40_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x1282 | BEAM_POSITION_40_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x1283 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1284 | BEAM_POSITION_40_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x1285 | BEAM_POSITION_40_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x1286 | BEAM_POSITION_40_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x1287 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1288 | BEAM_POSITION_40_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x1289 | BEAM_POSITION_40_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x128A | BEAM_POSITION_40_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x128B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x128C | BEAM_POSITION_40_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x128D | BEAM_POSITION_40_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x128E | BEAM_POSITION_40_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x128F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1290 | BEAM_POSITION_41_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x1291 | BEAM_POSITION_41_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x1292 | BEAM_POSITION_41_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x1293 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1294 | BEAM_POSITION_41_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |

| Addr | Name | Bits | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
|------|------|------|-------|-------|-------|-------|-------|-------|-------|-------|
| 0x1295 | BEAM_POSITION_41_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x1296 | BEAM_POSITION_41_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x1297 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1298 | BEAM_POSITION_41_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x1299 | BEAM_POSITION_41_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x129A | BEAM_POSITION_41_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x129B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x129C | BEAM_POSITION_41_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x129D | BEAM_POSITION_41_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x129E | BEAM_POSITION_41_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x129F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x12A0 | BEAM_POSITION_42_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x12A1 | BEAM_POSITION_42_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x12A2 | BEAM_POSITION_42_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x12A3 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x12A4 | BEAM_POSITION_42_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x12A5 | BEAM_POSITION_42_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x12A6 | BEAM_POSITION_42_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x12A7 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x12A8 | BEAM_POSITION_42_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x12A9 | BEAM_POSITION_42_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x12AA | BEAM_POSITION_42_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x12AB | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x12AC | BEAM_POSITION_42_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x12AD | BEAM_POSITION_42_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x12AE | BEAM_POSITION_42_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x12AF | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x12B0 | BEAM_POSITION_43_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x12B1 | BEAM_POSITION_43_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x12B2 | BEAM_POSITION_43_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x12B3 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x12B4 | BEAM_POSITION_43_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x12B5 | BEAM_POSITION_43_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x12B6 | BEAM_POSITION_43_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x12B7 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x12B8 | BEAM_POSITION_43_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x12B9 | BEAM_POSITION_43_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x12BA | BEAM_POSITION_43_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x12BB | RESERVED | [7:0] | RESERVED | | | | | | | |

| Addr | Name | Bits | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
|------|------|------|-------|-------|-------|-------|-------|-------|-------|-------|
| 0x12BC | BEAM_POSITION_43_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x12BD | BEAM_POSITION_43_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x12BE | BEAM_POSITION_43_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x12BF | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x12C0 | BEAM_POSITION_44_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x12C1 | BEAM_POSITION_44_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x12C2 | BEAM_POSITION_44_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x12C3 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x12C4 | BEAM_POSITION_44_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x12C5 | BEAM_POSITION_44_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x12C6 | BEAM_POSITION_44_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x12C7 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x12C8 | BEAM_POSITION_44_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x12C9 | BEAM_POSITION_44_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x12CA | BEAM_POSITION_44_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x12CB | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x12CC | BEAM_POSITION_44_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x12CD | BEAM_POSITION_44_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x12CE | BEAM_POSITION_44_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x12CF | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x12D0 | BEAM_POSITION_45_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x12D1 | BEAM_POSITION_45_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x12D2 | BEAM_POSITION_45_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x12D3 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x12D4 | BEAM_POSITION_45_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x12D5 | BEAM_POSITION_45_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x12D6 | BEAM_POSITION_45_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x12D7 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x12D8 | BEAM_POSITION_45_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x12D9 | BEAM_POSITION_45_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x12DA | BEAM_POSITION_45_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x12DB | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x12DC | BEAM_POSITION_45_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x12DD | BEAM_POSITION_45_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x12DE | BEAM_POSITION_45_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x12DF | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x12E0 | BEAM_POSITION_46_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x12E1 | BEAM_POSITION_46_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x12E2 | BEAM_POSITION_46_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |

| Addr | Name | Bits | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
|---|---|---|---|---|---|---|---|---|---|---|
| 0x12E3 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x12E4 | BEAM_POSITION_46_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x12E5 | BEAM_POSITION_46_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x12E6 | BEAM_POSITION_46_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x12E7 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x12E8 | BEAM_POSITION_46_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x12E9 | BEAM_POSITION_46_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x12EA | BEAM_POSITION_46_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x12EB | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x12EC | BEAM_POSITION_46_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x12ED | BEAM_POSITION_46_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x12EE | BEAM_POSITION_46_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x12EF | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x12F0 | BEAM_POSITION_47_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x12F1 | BEAM_POSITION_47_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x12F2 | BEAM_POSITION_47_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x12F3 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x12F4 | BEAM_POSITION_47_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x12F5 | BEAM_POSITION_47_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x12F6 | BEAM_POSITION_47_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x12F7 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x12F8 | BEAM_POSITION_47_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x12F9 | BEAM_POSITION_47_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x12FA | BEAM_POSITION_47_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x12FB | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x12FC | BEAM_POSITION_47_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x12FD | BEAM_POSITION_47_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x12FE | BEAM_POSITION_47_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x12FF | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1300 | BEAM_POSITION_48_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x1301 | BEAM_POSITION_48_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x1302 | BEAM_POSITION_48_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x1303 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1304 | BEAM_POSITION_48_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x1305 | BEAM_POSITION_48_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x1306 | BEAM_POSITION_48_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x1307 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1308 | BEAM_POSITION_48_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x1309 | BEAM_POSITION_48_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |

| Addr | Name | Bits | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
|---|---|---|---|---|---|---|---|---|---|---|
| 0x130A | BEAM_POSITION_48_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x130B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x130C | BEAM_POSITION_48_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x130D | BEAM_POSITION_48_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x130E | BEAM_POSITION_48_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x130F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1310 | BEAM_POSITION_49_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x1311 | BEAM_POSITION_49_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x1312 | BEAM_POSITION_49_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x1313 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1314 | BEAM_POSITION_49_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x1315 | BEAM_POSITION_49_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x1316 | BEAM_POSITION_49_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x1317 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1318 | BEAM_POSITION_49_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x1319 | BEAM_POSITION_49_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x131A | BEAM_POSITION_49_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x131B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x131C | BEAM_POSITION_49_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x131D | BEAM_POSITION_49_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x131E | BEAM_POSITION_49_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x131F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1320 | BEAM_POSITION_50_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x1321 | BEAM_POSITION_50_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x1322 | BEAM_POSITION_50_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x1323 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1324 | BEAM_POSITION_50_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x1325 | BEAM_POSITION_50_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x1326 | BEAM_POSITION_50_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x1327 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1328 | BEAM_POSITION_50_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x1329 | BEAM_POSITION_50_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x132A | BEAM_POSITION_50_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x132B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x132C | BEAM_POSITION_50_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x132D | BEAM_POSITION_50_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x132E | BEAM_POSITION_50_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x132F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1330 | BEAM_POSITION_51_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |

| Addr | Name | Bits | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
|---|---|---|---|---|---|---|---|---|---|---|
| 0x1331 | BEAM_POSITION_51_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x1332 | BEAM_POSITION_51_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x1333 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1334 | BEAM_POSITION_51_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x1335 | BEAM_POSITION_51_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x1336 | BEAM_POSITION_51_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x1337 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1338 | BEAM_POSITION_51_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x1339 | BEAM_POSITION_51_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x133A | BEAM_POSITION_51_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x133B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x133C | BEAM_POSITION_51_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x133D | BEAM_POSITION_51_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x133E | BEAM_POSITION_51_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x133F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1340 | BEAM_POSITION_52_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x1341 | BEAM_POSITION_52_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x1342 | BEAM_POSITION_52_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x1343 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1344 | BEAM_POSITION_52_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x1345 | BEAM_POSITION_52_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x1346 | BEAM_POSITION_52_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x1347 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1348 | BEAM_POSITION_52_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x1349 | BEAM_POSITION_52_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x134A | BEAM_POSITION_52_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x134B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x134C | BEAM_POSITION_52_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x134D | BEAM_POSITION_52_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x134E | BEAM_POSITION_52_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x134F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1350 | BEAM_POSITION_53_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x1351 | BEAM_POSITION_53_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x1352 | BEAM_POSITION_53_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x1353 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1354 | BEAM_POSITION_53_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x1355 | BEAM_POSITION_53_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x1356 | BEAM_POSITION_53_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x1357 | RESERVED | [7:0] | RESERVED | | | | | | | |

| Addr | Name | Bits | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
|---|---|---|---|---|---|---|---|---|---|---|
| 0x1358 | BEAM_POSITION_53_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x1359 | BEAM_POSITION_53_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x135A | BEAM_POSITION_53_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x135B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x135C | BEAM_POSITION_53_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x135D | BEAM_POSITION_53_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x135E | BEAM_POSITION_53_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x135F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1360 | BEAM_POSITION_54_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x1361 | BEAM_POSITION_54_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x1362 | BEAM_POSITION_54_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x1363 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1364 | BEAM_POSITION_54_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x1365 | BEAM_POSITION_54_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x1366 | BEAM_POSITION_54_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x1367 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1368 | BEAM_POSITION_54_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x1369 | BEAM_POSITION_54_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x136A | BEAM_POSITION_54_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x136B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x136C | BEAM_POSITION_54_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x136D | BEAM_POSITION_54_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x136E | BEAM_POSITION_54_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x136F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1370 | BEAM_POSITION_55_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x1371 | BEAM_POSITION_55_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x1372 | BEAM_POSITION_55_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x1373 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1374 | BEAM_POSITION_55_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x1375 | BEAM_POSITION_55_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x1376 | BEAM_POSITION_55_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x1377 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1378 | BEAM_POSITION_55_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x1379 | BEAM_POSITION_55_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x137A | BEAM_POSITION_55_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x137B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x137C | BEAM_POSITION_55_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x137D | BEAM_POSITION_55_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x137E | BEAM_POSITION_55_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |

| Addr | Name | Bits | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
|---|---|---|---|---|---|---|---|---|---|---|
| 0x137F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1380 | BEAM_POSITION_56_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x1381 | BEAM_POSITION_56_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x1382 | BEAM_POSITION_56_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x1383 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1384 | BEAM_POSITION_56_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x1385 | BEAM_POSITION_56_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x1386 | BEAM_POSITION_56_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x1387 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1388 | BEAM_POSITION_56_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x1389 | BEAM_POSITION_56_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x138A | BEAM_POSITION_56_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x138B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x138C | BEAM_POSITION_56_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x138D | BEAM_POSITION_56_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x138E | BEAM_POSITION_56_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x138F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1390 | BEAM_POSITION_57_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x1391 | BEAM_POSITION_57_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x1392 | BEAM_POSITION_57_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x1393 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1394 | BEAM_POSITION_57_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x1395 | BEAM_POSITION_57_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x1396 | BEAM_POSITION_57_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x1397 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1398 | BEAM_POSITION_57_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x1399 | BEAM_POSITION_57_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x139A | BEAM_POSITION_57_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x139B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x139C | BEAM_POSITION_57_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x139D | BEAM_POSITION_57_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x139E | BEAM_POSITION_57_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x139F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x13A0 | BEAM_POSITION_58_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x13A1 | BEAM_POSITION_58_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x13A2 | BEAM_POSITION_58_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x13A3 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x13A4 | BEAM_POSITION_58_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x13A5 | BEAM_POSITION_58_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |

| Addr | Name | Bits | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
|------|------|------|-------|-------|-------|-------|-------|-------|-------|-------|
| 0x13A6 | BEAM_POSITION_58_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x13A7 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x13A8 | BEAM_POSITION_58_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x13A9 | BEAM_POSITION_58_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x13AA | BEAM_POSITION_58_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x13AB | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x13AC | BEAM_POSITION_58_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x13AD | BEAM_POSITION_58_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x13AE | BEAM_POSITION_58_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x13AF | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x13B0 | BEAM_POSITION_59_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x13B1 | BEAM_POSITION_59_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x13B2 | BEAM_POSITION_59_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x13B3 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x13B4 | BEAM_POSITION_59_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x13B5 | BEAM_POSITION_59_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x13B6 | BEAM_POSITION_59_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x13B7 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x13B8 | BEAM_POSITION_59_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x13B9 | BEAM_POSITION_59_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x13BA | BEAM_POSITION_59_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x13BB | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x13BC | BEAM_POSITION_59_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x13BD | BEAM_POSITION_59_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x13BE | BEAM_POSITION_59_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x13BF | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x13C0 | BEAM_POSITION_60_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x13C1 | BEAM_POSITION_60_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x13C2 | BEAM_POSITION_60_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x13C3 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x13C4 | BEAM_POSITION_60_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x13C5 | BEAM_POSITION_60_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x13C6 | BEAM_POSITION_60_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x13C7 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x13C8 | BEAM_POSITION_60_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x13C9 | BEAM_POSITION_60_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x13CA | BEAM_POSITION_60_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x13CB | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x13CC | BEAM_POSITION_60_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |

| Addr | Name | Bits | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
|---|---|---|---|---|---|---|---|---|---|---|
| 0x13CD | BEAM_POSITION_60_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x13CE | BEAM_POSITION_60_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x13CF | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x13D0 | BEAM_POSITION_61_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x13D1 | BEAM_POSITION_61_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x13D2 | BEAM_POSITION_61_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x13D3 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x13D4 | BEAM_POSITION_61_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x13D5 | BEAM_POSITION_61_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x13D6 | BEAM_POSITION_61_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x13D7 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x13D8 | BEAM_POSITION_61_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x13D9 | BEAM_POSITION_61_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x13DA | BEAM_POSITION_61_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x13DB | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x13DC | BEAM_POSITION_61_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x13DD | BEAM_POSITION_61_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x13DE | BEAM_POSITION_61_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x13DF | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x13E0 | BEAM_POSITION_62_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x13E1 | BEAM_POSITION_62_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x13E2 | BEAM_POSITION_62_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x13E3 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x13E4 | BEAM_POSITION_62_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x13E5 | BEAM_POSITION_62_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x13E6 | BEAM_POSITION_62_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x13E7 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x13E8 | BEAM_POSITION_62_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x13E9 | BEAM_POSITION_62_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x13EA | BEAM_POSITION_62_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x13EB | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x13EC | BEAM_POSITION_62_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x13ED | BEAM_POSITION_62_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x13EE | BEAM_POSITION_62_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x13EF | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x13F0 | BEAM_POSITION_63_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x13F1 | BEAM_POSITION_63_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x13F2 | BEAM_POSITION_63_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x13F3 | RESERVED | [7:0] | RESERVED | | | | | | | |

| Addr | Name | Bits | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
|---|---|---|---|---|---|---|---|---|---|---|
| 0x13F4 | BEAM_POSITION_63_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x13F5 | BEAM_POSITION_63_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x13F6 | BEAM_POSITION_63_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x13F7 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x13F8 | BEAM_POSITION_63_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x13F9 | BEAM_POSITION_63_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x13FA | BEAM_POSITION_63_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x13FB | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x13FC | BEAM_POSITION_63_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x13FD | BEAM_POSITION_63_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x13FE | BEAM_POSITION_63_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x13FF | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1400 | BEAM_POSITION_64_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x1401 | BEAM_POSITION_64_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x1402 | BEAM_POSITION_64_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x1403 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1404 | BEAM_POSITION_64_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x1405 | BEAM_POSITION_64_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x1406 | BEAM_POSITION_64_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x1407 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1408 | BEAM_POSITION_64_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x1409 | BEAM_POSITION_64_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x140A | BEAM_POSITION_64_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x140B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x140C | BEAM_POSITION_64_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x140D | BEAM_POSITION_64_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x140E | BEAM_POSITION_64_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x140F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1410 | BEAM_POSITION_65_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x1411 | BEAM_POSITION_65_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x1412 | BEAM_POSITION_65_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x1413 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1414 | BEAM_POSITION_65_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x1415 | BEAM_POSITION_65_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x1416 | BEAM_POSITION_65_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x1417 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1418 | BEAM_POSITION_65_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x1419 | BEAM_POSITION_65_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x141A | BEAM_POSITION_65_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |

| Addr | Name | Bits | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
|------|------|------|-------|-------|-------|-------|-------|-------|-------|-------|
| 0x141B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x141C | BEAM_POSITION_65_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x141D | BEAM_POSITION_65_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x141E | BEAM_POSITION_65_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x141F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1420 | BEAM_POSITION_66_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x1421 | BEAM_POSITION_66_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x1422 | BEAM_POSITION_66_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x1423 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1424 | BEAM_POSITION_66_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x1425 | BEAM_POSITION_66_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x1426 | BEAM_POSITION_66_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x1427 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1428 | BEAM_POSITION_66_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x1429 | BEAM_POSITION_66_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x142A | BEAM_POSITION_66_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x142B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x142C | BEAM_POSITION_66_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x142D | BEAM_POSITION_66_CH4_RX_PHASE_I | | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x142E | BEAM_POSITION_66_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x142F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1430 | BEAM_POSITION_67_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x1431 | BEAM_POSITION_67_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x1432 | BEAM_POSITION_67_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x1433 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1434 | BEAM_POSITION_67_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x1435 | BEAM_POSITION_67_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x1436 | BEAM_POSITION_67_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x1437 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1438 | BEAM_POSITION_67_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x1439 | BEAM_POSITION_67_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x143A | BEAM_POSITION_67_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x143B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x143C | BEAM_POSITION_67_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x143D | BEAM_POSITION_67_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x143E | BEAM_POSITION_67_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x143F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1440 | BEAM_POSITION_68_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x1441 | BEAM_POSITION_68_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |

| Addr | Name | Bits | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
|---|---|---|---|---|---|---|---|---|---|---|
| 0x1442 | BEAM_POSITION_68_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x1443 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1444 | BEAM_POSITION_68_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x1445 | BEAM_POSITION_68_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x1446 | BEAM_POSITION_68_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x1447 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1448 | BEAM_POSITION_68_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x1449 | BEAM_POSITION_68_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x144A | BEAM_POSITION_68_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x144B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x144C | BEAM_POSITION_68_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x144D | BEAM_POSITION_68_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x144E | BEAM_POSITION_68_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x144F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1450 | BEAM_POSITION_69_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x1451 | BEAM_POSITION_69_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x1452 | BEAM_POSITION_69_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x1453 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1454 | BEAM_POSITION_69_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x1455 | BEAM_POSITION_69_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x1456 | BEAM_POSITION_69_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x1457 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1458 | BEAM_POSITION_69_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x1459 | BEAM_POSITION_69_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x145A | BEAM_POSITION_69_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x145B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x145C | BEAM_POSITION_69_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x145D | BEAM_POSITION_69_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x145E | BEAM_POSITION_69_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x145F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1460 | BEAM_POSITION_70_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x1461 | BEAM_POSITION_70_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x1462 | BEAM_POSITION_70_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x1463 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1464 | BEAM_POSITION_70_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x1465 | BEAM_POSITION_70_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x1466 | BEAM_POSITION_70_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x1467 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1468 | BEAM_POSITION_70_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |

| Addr | Name | Bits | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
|------|------|------|-------|-------|-------|-------|-------|-------|-------|-------|
| 0x1469 | BEAM_POSITION_70_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x146A | BEAM_POSITION_70_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x146B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x146C | BEAM_POSITION_70_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x146D | BEAM_POSITION_70_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x146E | BEAM_POSITION_70_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x146F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1470 | BEAM_POSITION_71_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x1471 | BEAM_POSITION_71_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x1472 | BEAM_POSITION_71_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x1473 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1474 | BEAM_POSITION_71_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x1475 | BEAM_POSITION_71_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x1476 | BEAM_POSITION_71_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x1477 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1478 | BEAM_POSITION_71_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x1479 | BEAM_POSITION_71_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x147A | BEAM_POSITION_71_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x147B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x147C | BEAM_POSITION_71_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x147D | BEAM_POSITION_71_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x147E | BEAM_POSITION_71_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x147F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1480 | BEAM_POSITION_72_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x1481 | BEAM_POSITION_72_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x1482 | BEAM_POSITION_72_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x1483 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1484 | BEAM_POSITION_72_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x1485 | BEAM_POSITION_72_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x1486 | BEAM_POSITION_72_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x1487 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1488 | BEAM_POSITION_72_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x1489 | BEAM_POSITION_72_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x148A | BEAM_POSITION_72_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x148B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x148C | BEAM_POSITION_72_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x148D | BEAM_POSITION_72_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x148E | BEAM_POSITION_72_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x148F | RESERVED | [7:0] | RESERVED | | | | | | | |

| Addr | Name | Bits | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
|---|---|---|---|---|---|---|---|---|---|---|
| 0x1490 | BEAM_POSITION_73_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x1491 | BEAM_POSITION_73_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x1492 | BEAM_POSITION_73_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x1493 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1494 | BEAM_POSITION_73_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x1495 | BEAM_POSITION_73_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x1496 | BEAM_POSITION_73_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x1497 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1498 | BEAM_POSITION_73_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x1499 | BEAM_POSITION_73_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x149A | BEAM_POSITION_73_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x149B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x149C | BEAM_POSITION_73_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x149D | BEAM_POSITION_73_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x149E | BEAM_POSITION_73_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x149F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x14A0 | BEAM_POSITION_74_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x14A1 | BEAM_POSITION_74_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x14A2 | BEAM_POSITION_74_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x14A3 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x14A4 | BEAM_POSITION_74_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x14A5 | BEAM_POSITION_74_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x14A6 | BEAM_POSITION_74_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x14A7 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x14A8 | BEAM_POSITION_74_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x14A9 | BEAM_POSITION_74_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x14AA | BEAM_POSITION_74_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x14AB | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x14AC | BEAM_POSITION_74_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x14AD | BEAM_POSITION_74_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x14AE | BEAM_POSITION_74_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x14AF | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x14B0 | BEAM_POSITION_75_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x14B1 | BEAM_POSITION_75_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x14B2 | BEAM_POSITION_75_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x14B3 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x14B4 | BEAM_POSITION_75_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x14B5 | BEAM_POSITION_75_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x14B6 | BEAM_POSITION_75_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |

| Addr | Name | Bits | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
|------|------|------|-------|-------|-------|-------|-------|-------|-------|-------|
| 0x14B7 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x14B8 | BEAM_POSITION_75_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x14B9 | BEAM_POSITION_75_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x14BA | BEAM_POSITION_75_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x14BB | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x14BC | BEAM_POSITION_75_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x14BD | BEAM_POSITION_75_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x14BE | BEAM_POSITION_75_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x14BF | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x14C0 | BEAM_POSITION_76_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x14C1 | BEAM_POSITION_76_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x14C2 | BEAM_POSITION_76_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x14C3 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x14C4 | BEAM_POSITION_76_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x14C5 | BEAM_POSITION_76_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x14C6 | BEAM_POSITION_76_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x14C7 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x14C8 | BEAM_POSITION_76_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x14C9 | BEAM_POSITION_76_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x14CA | BEAM_POSITION_76_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x14CB | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x14CC | BEAM_POSITION_76_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x14CD | BEAM_POSITION_76_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x14CE | BEAM_POSITION_76_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x14CF | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x14D0 | BEAM_POSITION_77_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x14D1 | BEAM_POSITION_77_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x14D2 | BEAM_POSITION_77_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x14D3 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x14D4 | BEAM_POSITION_77_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x14D5 | BEAM_POSITION_77_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x14D6 | BEAM_POSITION_77_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x14D7 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x14D8 | BEAM_POSITION_77_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x14D9 | BEAM_POSITION_77_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x14DA | BEAM_POSITION_77_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x14DB | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x14DC | BEAM_POSITION_77_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x14DD | BEAM_POSITION_77_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |

| Addr | Name | Bits | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
|------|------|------|-------|-------|-------|-------|-------|-------|-------|-------|
| 0x14DE | BEAM_POSITION_77_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x14DF | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x14E0 | BEAM_POSITION_78_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x14E1 | BEAM_POSITION_78_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x14E2 | BEAM_POSITION_78_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x14E3 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x14E4 | BEAM_POSITION_78_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x14E5 | BEAM_POSITION_78_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x14E6 | BEAM_POSITION_78_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x14E7 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x14E8 | BEAM_POSITION_78_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x14E9 | BEAM_POSITION_78_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x14EA | BEAM_POSITION_78_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x14EB | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x14EC | BEAM_POSITION_78_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x14ED | BEAM_POSITION_78_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x14EE | BEAM_POSITION_78_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x14EF | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x14F0 | BEAM_POSITION_79_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x14F1 | BEAM_POSITION_79_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x14F2 | BEAM_POSITION_79_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x14F3 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x14F4 | BEAM_POSITION_79_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x14F5 | BEAM_POSITION_79_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x14F6 | BEAM_POSITION_79_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x14F7 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x14F8 | BEAM_POSITION_79_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x14F9 | BEAM_POSITION_79_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x14FA | BEAM_POSITION_79_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x14FB | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x14FC | BEAM_POSITION_79_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x14FD | BEAM_POSITION_79_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x14FE | BEAM_POSITION_79_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x14FF | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1500 | BEAM_POSITION_80_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x1501 | BEAM_POSITION_80_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x1502 | BEAM_POSITION_80_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x1503 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1504 | BEAM_POSITION_80_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |

| Addr | Name | Bits | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
|---|---|---|---|---|---|---|---|---|---|---|
| 0x1505 | BEAM_POSITION_80_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x1506 | BEAM_POSITION_80_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x1507 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1508 | BEAM_POSITION_80_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x1509 | BEAM_POSITION_80_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x150A | BEAM_POSITION_80_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x150B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x150C | BEAM_POSITION_80_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x150D | BEAM_POSITION_80_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x150E | BEAM_POSITION_80_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x150F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1510 | BEAM_POSITION_81_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x1511 | BEAM_POSITION_81_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x1512 | BEAM_POSITION_81_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x1513 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1514 | BEAM_POSITION_81_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x1515 | BEAM_POSITION_81_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x1516 | BEAM_POSITION_81_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x1517 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1518 | BEAM_POSITION_81_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x1519 | BEAM_POSITION_81_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x151A | BEAM_POSITION_81_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x151B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x151C | BEAM_POSITION_81_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x151D | BEAM_POSITION_81_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x151E | BEAM_POSITION_81_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x151F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1520 | BEAM_POSITION_82_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x1521 | BEAM_POSITION_82_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x1522 | BEAM_POSITION_82_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x1523 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1524 | BEAM_POSITION_82_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x1525 | BEAM_POSITION_82_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x1526 | BEAM_POSITION_82_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x1527 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1528 | BEAM_POSITION_82_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x1529 | BEAM_POSITION_82_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x152A | BEAM_POSITION_82_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x152B | RESERVED | [7:0] | RESERVED | | | | | | | |

| Addr | Name | Bits | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
|---|---|---|---|---|---|---|---|---|---|---|
| 0x152C | BEAM_POSITION_82_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x152D | BEAM_POSITION_82_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x152E | BEAM_POSITION_82_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x152F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1530 | BEAM_POSITION_83_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x1531 | BEAM_POSITION_83_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x1532 | BEAM_POSITION_83_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x1533 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1534 | BEAM_POSITION_83_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x1535 | BEAM_POSITION_83_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x1536 | BEAM_POSITION_83_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x1537 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1538 | BEAM_POSITION_83_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x1539 | BEAM_POSITION_83_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x153A | BEAM_POSITION_83_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x153B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x153C | BEAM_POSITION_83_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x153D | BEAM_POSITION_83_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x153E | BEAM_POSITION_83_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x153F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1540 | BEAM_POSITION_84_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x1541 | BEAM_POSITION_84_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x1542 | BEAM_POSITION_84_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x1543 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1544 | BEAM_POSITION_84_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x1545 | BEAM_POSITION_84_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x1546 | BEAM_POSITION_84_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x1547 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1548 | BEAM_POSITION_84_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x1549 | BEAM_POSITION_84_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x154A | BEAM_POSITION_84_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x154B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x154C | BEAM_POSITION_84_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x154D | BEAM_POSITION_84_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x154E | BEAM_POSITION_84_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x154F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1550 | BEAM_POSITION_85_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x1551 | BEAM_POSITION_85_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x1552 | BEAM_POSITION_85_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |

| Addr | Name | Bits | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
|------|------|------|-------|-------|-------|-------|-------|-------|-------|-------|
| 0x1553 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1554 | BEAM_POSITION_85_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x1555 | BEAM_POSITION_85_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x1556 | BEAM_POSITION_85_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x1557 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1558 | BEAM_POSITION_85_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x1559 | BEAM_POSITION_85_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x155A | BEAM_POSITION_85_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x155B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x155C | BEAM_POSITION_85_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x155D | BEAM_POSITION_85_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x155E | BEAM_POSITION_85_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x155F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1560 | BEAM_POSITION_86_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x1561 | BEAM_POSITION_86_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x1562 | BEAM_POSITION_86_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x1563 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1564 | BEAM_POSITION_86_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x1565 | BEAM_POSITION_86_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x1566 | BEAM_POSITION_86_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x1567 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1568 | BEAM_POSITION_86_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x1569 | BEAM_POSITION_86_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x156A | BEAM_POSITION_86_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x156B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x156C | BEAM_POSITION_86_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x156D | BEAM_POSITION_86_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x156E | BEAM_POSITION_86_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x156F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1570 | BEAM_POSITION_87_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x1571 | BEAM_POSITION_87_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x1572 | BEAM_POSITION_87_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x1573 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1574 | BEAM_POSITION_87_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x1575 | BEAM_POSITION_87_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x1576 | BEAM_POSITION_87_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x1577 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1578 | BEAM_POSITION_87_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x1579 | BEAM_POSITION_87_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |

| Addr | Name | Bits | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
|---|---|---|---|---|---|---|---|---|---|---|
| 0x157A | BEAM_POSITION_87_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x157B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x157C | BEAM_POSITION_87_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x157D | BEAM_POSITION_87_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x157E | BEAM_POSITION_87_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x157F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1580 | BEAM_POSITION_88_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x1581 | BEAM_POSITION_88_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x1582 | BEAM_POSITION_88_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x1583 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1584 | BEAM_POSITION_88_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x1585 | BEAM_POSITION_88_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x1586 | BEAM_POSITION_88_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x1587 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1588 | BEAM_POSITION_88_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x1589 | BEAM_POSITION_88_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x158A | BEAM_POSITION_88_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x158B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x158C | BEAM_POSITION_88_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x158D | BEAM_POSITION_88_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x158E | BEAM_POSITION_88_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x158F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1590 | BEAM_POSITION_89_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x1591 | BEAM_POSITION_89_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x1592 | BEAM_POSITION_89_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x1593 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1594 | BEAM_POSITION_89_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x1595 | BEAM_POSITION_89_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x1596 | BEAM_POSITION_89_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x1597 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1598 | BEAM_POSITION_89_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x1599 | BEAM_POSITION_89_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x159A | BEAM_POSITION_89_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x159B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x159C | BEAM_POSITION_89_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x159D | BEAM_POSITION_89_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x159E | BEAM_POSITION_89_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x159F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x15A0 | BEAM_POSITION_90_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |

| Addr | Name | Bits | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
|---|---|---|---|---|---|---|---|---|---|---|
| 0x15A1 | BEAM_POSITION_90_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x15A2 | BEAM_POSITION_90_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x15A3 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x15A4 | BEAM_POSITION_90_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x15A5 | BEAM_POSITION_90_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x15A6 | BEAM_POSITION_90_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x15A7 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x15A8 | BEAM_POSITION_90_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x15A9 | BEAM_POSITION_90_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x15AA | BEAM_POSITION_90_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x15AB | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x15AC | BEAM_POSITION_90_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x15AD | BEAM_POSITION_90_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x15AE | BEAM_POSITION_90_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x15AF | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x15B0 | BEAM_POSITION_91_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x15B1 | BEAM_POSITION_91_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x15B2 | BEAM_POSITION_91_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x15B3 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x15B4 | BEAM_POSITION_91_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x15B5 | BEAM_POSITION_91_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x15B6 | BEAM_POSITION_91_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x15B7 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x15B8 | BEAM_POSITION_91_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x15B9 | BEAM_POSITION_91_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x15BA | BEAM_POSITION_91_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x15BB | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x15BC | BEAM_POSITION_91_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x15BD | BEAM_POSITION_91_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x15BE | BEAM_POSITION_91_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x15BF | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x15C0 | BEAM_POSITION_92_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x15C1 | BEAM_POSITION_92_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x15C2 | BEAM_POSITION_92_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x15C3 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x15C4 | BEAM_POSITION_92_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x15C5 | BEAM_POSITION_92_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x15C6 | BEAM_POSITION_92_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x15C7 | RESERVED | [7:0] | RESERVED | | | | | | | |

| Addr | Name | Bits | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
|---|---|---|---|---|---|---|---|---|---|---|
| 0x15C8 | BEAM_POSITION_92_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x15C9 | BEAM_POSITION_92_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x15CA | BEAM_POSITION_92_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x15CB | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x15CC | BEAM_POSITION_92_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x15CD | BEAM_POSITION_92_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x15CE | BEAM_POSITION_92_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x15CF | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x15D0 | BEAM_POSITION_93_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x15D1 | BEAM_POSITION_93_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x15D2 | BEAM_POSITION_93_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x15D3 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x15D4 | BEAM_POSITION_93_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x15D5 | BEAM_POSITION_93_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x15D6 | BEAM_POSITION_93_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x15D7 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x15D8 | BEAM_POSITION_93_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x15D9 | BEAM_POSITION_93_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x15DA | BEAM_POSITION_93_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x15DB | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x15DC | BEAM_POSITION_93_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x15DD | BEAM_POSITION_93_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x15DE | BEAM_POSITION_93_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x15DF | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x15E0 | BEAM_POSITION_94_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x15E1 | BEAM_POSITION_94_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x15E2 | BEAM_POSITION_94_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x15E3 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x15E4 | BEAM_POSITION_94_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x15E5 | BEAM_POSITION_94_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x15E6 | BEAM_POSITION_94_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x15E7 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x15E8 | BEAM_POSITION_94_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x15E9 | BEAM_POSITION_94_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x15EA | BEAM_POSITION_94_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x15EB | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x15EC | BEAM_POSITION_94_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x15ED | BEAM_POSITION_94_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x15EE | BEAM_POSITION_94_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |

| Addr | Name | Bits | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
|---|---|---|---|---|---|---|---|---|---|---|
| 0x15EF | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x15F0 | BEAM_POSITION_95_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x15F1 | BEAM_POSITION_95_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x15F2 | BEAM_POSITION_95_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x15F3 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x15F4 | BEAM_POSITION_95_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x15F5 | BEAM_POSITION_95_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x15F6 | BEAM_POSITION_95_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x15F7 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x15F8 | BEAM_POSITION_95_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x15F9 | BEAM_POSITION_95_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x15FA | BEAM_POSITION_95_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x15FB | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x15FC | BEAM_POSITION_95_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x15FD | BEAM_POSITION_95_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x15FE | BEAM_POSITION_95_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x15FF | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1600 | BEAM_POSITION_96_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x1601 | BEAM_POSITION_96_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x1602 | BEAM_POSITION_96_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x1603 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1604 | BEAM_POSITION_96_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x1605 | BEAM_POSITION_96_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x1606 | BEAM_POSITION_96_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x1607 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1608 | BEAM_POSITION_96_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x1609 | BEAM_POSITION_96_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x160A | BEAM_POSITION_96_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x160B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x160C | BEAM_POSITION_96_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x160D | BEAM_POSITION_96_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x160E | BEAM_POSITION_96_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x160F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1610 | BEAM_POSITION_97_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x1611 | BEAM_POSITION_97_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x1612 | BEAM_POSITION_97_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x1613 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1614 | BEAM_POSITION_97_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x1615 | BEAM_POSITION_97_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |

| Addr | Name | Bits | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
|---|---|---|---|---|---|---|---|---|---|---|
| 0x1616 | BEAM_POSITION_97_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x1617 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1618 | BEAM_POSITION_97_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x1619 | BEAM_POSITION_97_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x161A | BEAM_POSITION_97_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x161B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x161C | BEAM_POSITION_97_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x161D | BEAM_POSITION_97_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x161E | BEAM_POSITION_97_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x161F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1620 | BEAM_POSITION_98_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x1621 | BEAM_POSITION_98_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x1622 | BEAM_POSITION_98_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x1623 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1624 | BEAM_POSITION_98_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x1625 | BEAM_POSITION_98_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x1626 | BEAM_POSITION_98_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x1627 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1628 | BEAM_POSITION_98_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x1629 | BEAM_POSITION_98_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x162A | BEAM_POSITION_98_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x162B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x162C | BEAM_POSITION_98_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x162D | BEAM_POSITION_98_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x162E | BEAM_POSITION_98_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x162F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1630 | BEAM_POSITION_99_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x1631 | BEAM_POSITION_99_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x1632 | BEAM_POSITION_99_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x1633 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1634 | BEAM_POSITION_99_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x1635 | BEAM_POSITION_99_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x1636 | BEAM_POSITION_99_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x1637 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1638 | BEAM_POSITION_99_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x1639 | BEAM_POSITION_99_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x163A | BEAM_POSITION_99_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x163B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x163C | BEAM_POSITION_99_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |

| Addr | Name | Bits | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
|------|------|------|-------|-------|-------|-------|-------|-------|-------|-------|
| 0x163D | BEAM_POSITION_99_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x163E | BEAM_POSITION_99_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x163F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1640 | BEAM_POSITION_100_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x1641 | BEAM_POSITION_100_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x1642 | BEAM_POSITION_100_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x1643 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1644 | BEAM_POSITION_100_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x1645 | BEAM_POSITION_100_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x1646 | BEAM_POSITION_100_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x1647 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1648 | BEAM_POSITION_100_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x1649 | BEAM_POSITION_100_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x164A | BEAM_POSITION_100_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x164B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x164C | BEAM_POSITION_100_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x164D | BEAM_POSITION_100_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x164E | BEAM_POSITION_100_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x164F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1650 | BEAM_POSITION_101_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x1651 | BEAM_POSITION_101_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x1652 | BEAM_POSITION_101_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x1653 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1654 | BEAM_POSITION_101_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x1655 | BEAM_POSITION_101_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x1656 | BEAM_POSITION_101_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x1657 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1658 | BEAM_POSITION_101_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x1659 | BEAM_POSITION_101_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x165A | BEAM_POSITION_101_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x165B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x165C | BEAM_POSITION_101_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x165D | BEAM_POSITION_101_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x165E | BEAM_POSITION_101_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x165F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1660 | BEAM_POSITION_102_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x1661 | BEAM_POSITION_102_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x1662 | BEAM_POSITION_102_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x1663 | RESERVED | [7:0] | RESERVED | | | | | | | |

| Addr | Name | Bits | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
|------|------|------|-------|-------|-------|-------|-------|-------|-------|-------|
| 0x1664 | BEAM_POSITION_102_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x1665 | BEAM_POSITION_102_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x1666 | BEAM_POSITION_102_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x1667 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1668 | BEAM_POSITION_102_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x1669 | BEAM_POSITION_102_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x166A | BEAM_POSITION_102_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x166B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x166C | BEAM_POSITION_102_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x166D | BEAM_POSITION_102_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x166E | BEAM_POSITION_102_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x166F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1670 | BEAM_POSITION_103_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x1671 | BEAM_POSITION_103_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x1672 | BEAM_POSITION_103_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x1673 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1674 | BEAM_POSITION_103_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x1675 | BEAM_POSITION_103_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x1676 | BEAM_POSITION_103_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x1677 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1678 | BEAM_POSITION_103_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x1679 | BEAM_POSITION_103_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x167A | BEAM_POSITION_103_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x167B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x167C | BEAM_POSITION_103_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x167D | BEAM_POSITION_103_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x167E | BEAM_POSITION_103_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x167F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1680 | BEAM_POSITION_104_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x1681 | BEAM_POSITION_104_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x1682 | BEAM_POSITION_104_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x1683 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1684 | BEAM_POSITION_104_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x1685 | BEAM_POSITION_104_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x1686 | BEAM_POSITION_104_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x1687 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1688 | BEAM_POSITION_104_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x1689 | BEAM_POSITION_104_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x168A | BEAM_POSITION_104_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |

| Addr | Name | Bits | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
|---|---|---|---|---|---|---|---|---|---|---|
| 0x168B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x168C | BEAM_POSITION_104_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x168D | BEAM_POSITION_104_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x168E | BEAM_POSITION_104_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x168F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1690 | BEAM_POSITION_105_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x1691 | BEAM_POSITION_105_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x1692 | BEAM_POSITION_105_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x1693 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1694 | BEAM_POSITION_105_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x1695 | BEAM_POSITION_105_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x1696 | BEAM_POSITION_105_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x1697 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1698 | BEAM_POSITION_105_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x1699 | BEAM_POSITION_105_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x169A | BEAM_POSITION_105_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x169B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x169C | BEAM_POSITION_105_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x169D | BEAM_POSITION_105_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x169E | BEAM_POSITION_105_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x169F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x16A0 | BEAM_POSITION_106_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x16A1 | BEAM_POSITION_106_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x16A2 | BEAM_POSITION_106_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x16A3 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x16A4 | BEAM_POSITION_106_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x16A5 | BEAM_POSITION_106_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x16A6 | BEAM_POSITION_106_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x16A7 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x16A8 | BEAM_POSITION_106_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x16A9 | BEAM_POSITION_106_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x16AA | BEAM_POSITION_106_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x16AB | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x16AC | BEAM_POSITION_106_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x16AD | BEAM_POSITION_106_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x16AE | BEAM_POSITION_106_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x16AF | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x16B0 | BEAM_POSITION_107_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x16B1 | BEAM_POSITION_107_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |

| Addr | Name | Bits | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
|------|------|------|-------|-------|-------|-------|-------|-------|-------|-------|
| 0x16B2 | BEAM_POSITION_107_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x16B3 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x16B4 | BEAM_POSITION_107_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x16B5 | BEAM_POSITION_107_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x16B6 | BEAM_POSITION_107_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x16B7 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x16B8 | BEAM_POSITION_107_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x16B9 | BEAM_POSITION_107_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x16BA | BEAM_POSITION_107_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x16BB | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x16BC | BEAM_POSITION_107_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x16BD | BEAM_POSITION_107_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x16BE | BEAM_POSITION_107_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x16BF | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x16C0 | BEAM_POSITION_108_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x16C1 | BEAM_POSITION_108_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x16C2 | BEAM_POSITION_108_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x16C3 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x16C4 | BEAM_POSITION_108_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x16C5 | BEAM_POSITION_108_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x16C6 | BEAM_POSITION_108_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x16C7 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x16C8 | BEAM_POSITION_108_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x16C9 | BEAM_POSITION_108_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x16CA | BEAM_POSITION_108_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x16CB | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x16CC | BEAM_POSITION_108_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x16CD | BEAM_POSITION_108_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x16CE | BEAM_POSITION_108_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x16CF | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x16D0 | BEAM_POSITION_109_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x16D1 | BEAM_POSITION_109_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x16D2 | BEAM_POSITION_109_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x16D3 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x16D4 | BEAM_POSITION_109_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x16D5 | BEAM_POSITION_109_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x16D6 | BEAM_POSITION_109_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x16D7 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x16D8 | BEAM_POSITION_109_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |

| Addr | Name | Bits | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
|------|------|------|-------|-------|-------|-------|-------|-------|-------|-------|
| 0x16D9 | BEAM_POSITION_109_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x16DA | BEAM_POSITION_109_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x16DB | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x16DC | BEAM_POSITION_109_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x16DD | BEAM_POSITION_109_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x16DE | BEAM_POSITION_109_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x16DF | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x16E0 | BEAM_POSITION_110_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x16E1 | BEAM_POSITION_110_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x16E2 | BEAM_POSITION_110_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x16E3 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x16E4 | BEAM_POSITION_110_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x16E5 | BEAM_POSITION_110_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x16E6 | BEAM_POSITION_110_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x16E7 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x16E8 | BEAM_POSITION_110_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x16E9 | BEAM_POSITION_110_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x16EA | BEAM_POSITION_110_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x16EB | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x16EC | BEAM_POSITION_110_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x16ED | BEAM_POSITION_110_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x16EE | BEAM_POSITION_110_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x16EF | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x16F0 | BEAM_POSITION_111_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x16F1 | BEAM_POSITION_111_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x16F2 | BEAM_POSITION_111_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x16F3 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x16F4 | BEAM_POSITION_111_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x16F5 | BEAM_POSITION_111_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x16F6 | BEAM_POSITION_111_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x16F7 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x16F8 | BEAM_POSITION_111_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x16F9 | BEAM_POSITION_111_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x16FA | BEAM_POSITION_111_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x16FB | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x16FC | BEAM_POSITION_111_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x16FD | BEAM_POSITION_111_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x16FE | BEAM_POSITION_111_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x16FF | RESERVED | [7:0] | RESERVED | | | | | | | |

| Addr | Name | Bits | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
|------|------|------|-------|-------|-------|-------|-------|-------|-------|-------|
| 0x1700 | BEAM_POSITION_112_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x1701 | BEAM_POSITION_112_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x1702 | BEAM_POSITION_112_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x1703 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1704 | BEAM_POSITION_112_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x1705 | BEAM_POSITION_112_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x1706 | BEAM_POSITION_112_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x1707 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1708 | BEAM_POSITION_112_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x1709 | BEAM_POSITION_112_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x170A | BEAM_POSITION_112_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x170B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x170C | BEAM_POSITION_112_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x170D | BEAM_POSITION_112_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x170E | BEAM_POSITION_112_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x170F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1710 | BEAM_POSITION_113_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x1711 | BEAM_POSITION_113_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x1712 | BEAM_POSITION_113_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x1713 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1714 | BEAM_POSITION_113_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x1715 | BEAM_POSITION_113_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x1716 | BEAM_POSITION_113_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x1717 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1718 | BEAM_POSITION_113_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x1719 | BEAM_POSITION_113_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x171A | BEAM_POSITION_113_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x171B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x171C | BEAM_POSITION_113_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x171D | BEAM_POSITION_113_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x171E | BEAM_POSITION_113_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x171F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1720 | BEAM_POSITION_114_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x1721 | BEAM_POSITION_114_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x1722 | BEAM_POSITION_114_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x1723 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1724 | BEAM_POSITION_114_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x1725 | BEAM_POSITION_114_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x1726 | BEAM_POSITION_114_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |

| Addr | Name | Bits | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
|---|---|---|---|---|---|---|---|---|---|---|
| 0x1727 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1728 | BEAM_POSITION_114_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x1729 | BEAM_POSITION_114_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x172A | BEAM_POSITION_114_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x172B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x172C | BEAM_POSITION_114_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x172D | BEAM_POSITION_114_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x172E | BEAM_POSITION_114_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x172F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1730 | BEAM_POSITION_115_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x1731 | BEAM_POSITION_115_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x1732 | BEAM_POSITION_115_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x1733 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1734 | BEAM_POSITION_115_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x1735 | BEAM_POSITION_115_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x1736 | BEAM_POSITION_115_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x1737 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1738 | BEAM_POSITION_115_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x1739 | BEAM_POSITION_115_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x173A | BEAM_POSITION_115_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x173B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x173C | BEAM_POSITION_115_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x173D | BEAM_POSITION_115_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x173E | BEAM_POSITION_115_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x173F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1740 | BEAM_POSITION_116_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x1741 | BEAM_POSITION_116_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x1742 | BEAM_POSITION_116_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x1743 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1744 | BEAM_POSITION_116_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x1745 | BEAM_POSITION_116_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x1746 | BEAM_POSITION_116_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x1747 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1748 | BEAM_POSITION_116_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x1749 | BEAM_POSITION_116_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x174A | BEAM_POSITION_116_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x174B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x174C | BEAM_POSITION_116_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x174D | BEAM_POSITION_116_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |

| Addr | Name | Bits | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
|---|---|---|---|---|---|---|---|---|---|---|
| 0x174E | BEAM_POSITION_116_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x174F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1750 | BEAM_POSITION_117_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x1751 | BEAM_POSITION_117_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x1752 | BEAM_POSITION_117_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x1753 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1754 | BEAM_POSITION_117_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x1755 | BEAM_POSITION_117_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x1756 | BEAM_POSITION_117_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x1757 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1758 | BEAM_POSITION_117_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x1759 | BEAM_POSITION_117_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x175A | BEAM_POSITION_117_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x175B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x175C | BEAM_POSITION_117_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x175D | BEAM_POSITION_117_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x175E | BEAM_POSITION_117_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x175F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1760 | BEAM_POSITION_118_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x1761 | BEAM_POSITION_118_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x1762 | BEAM_POSITION_118_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x1763 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1764 | BEAM_POSITION_118_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x1765 | BEAM_POSITION_118_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x1766 | BEAM_POSITION_118_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x1767 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1768 | BEAM_POSITION_118_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x1769 | BEAM_POSITION_118_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x176A | BEAM_POSITION_118_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x176B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x176C | BEAM_POSITION_118_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x176D | BEAM_POSITION_118_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x176E | BEAM_POSITION_118_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x176F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1770 | BEAM_POSITION_119_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x1771 | BEAM_POSITION_119_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x1772 | BEAM_POSITION_119_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x1773 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1774 | BEAM_POSITION_119_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |

| Addr | Name | Bits | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
|------|------|------|-------|-------|-------|-------|-------|-------|-------|-------|
| 0x1775 | BEAM_POSITION_119_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x1776 | BEAM_POSITION_119_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x1777 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1778 | BEAM_POSITION_119_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x1779 | BEAM_POSITION_119_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x177A | BEAM_POSITION_119_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x177B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x177C | BEAM_POSITION_119_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x177D | BEAM_POSITION_119_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x177E | BEAM_POSITION_119_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x177F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1780 | BEAM_POSITION_120_CH1_RX_GAIN | [7:0] | CH1_ATTN_RX | RX_VGA_CH1 | | | | | | |
| 0x1781 | BEAM_POSITION_120_CH1_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH1_POL_I | RX_VM_CH1_GAIN_I | | | | |
| 0x1782 | BEAM_POSITION_120_CH1_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH1_POL_Q | RX_VM_CH1_GAIN_Q | | | | |
| 0x1783 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1784 | BEAM_POSITION_120_CH2_RX_GAIN | [7:0] | CH2_ATTN_RX | RX_VGA_CH2 | | | | | | |
| 0x1785 | BEAM_POSITION_120_CH2_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH2_POL_I | RX_VM_CH2_GAIN_I | | | | |
| 0x1786 | BEAM_POSITION_120_CH2_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH2_POL_Q | RX_VM_CH2_GAIN_Q | | | | |
| 0x1787 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1788 | BEAM_POSITION_120_CH3_RX_GAIN | [7:0] | CH3_ATTN_RX | RX_VGA_CH3 | | | | | | |
| 0x1789 | BEAM_POSITION_120_CH3_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH3_POL_I | RX_VM_CH3_GAIN_I | | | | |
| 0x178A | BEAM_POSITION_120_CH3_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH3_POL_Q | RX_VM_CH3_GAIN_Q | | | | |
| 0x178B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x178C | BEAM_POSITION_120_CH4_RX_GAIN | [7:0] | CH4_ATTN_RX | RX_VGA_CH4 | | | | | | |
| 0x178D | BEAM_POSITION_120_CH4_RX_PHASE_I | [7:0] | RESERVED | | RX_VM_CH4_POL_I | RX_VM_CH4_GAIN_I | | | | |
| 0x178E | BEAM_POSITION_120_CH4_RX_PHASE_Q | [7:0] | RESERVED | | RX_VM_CH4_POL_Q | RX_VM_CH4_GAIN_Q | | | | |
| 0x178F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1790 | RX_BIASSETTING_1_LNA_BIAS_OFF | [7:0] | EXT_LNA_BIAS_OFF | | | | | | | |
| 0x1791 | RX_BIASSETTING_1_LNA_BIAS_ON | [7:0] | EXT_LNA_BIAS_ON | | | | | | | |
| 0x1792 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1793 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1794 | RX_BIASSETTING_1_BIAS_CURRENT_RX | [7:0] | RESERVED | RX_VM_BIAS | | | RX_VGA_BIAS | | | |
| 0x1795 | RX_BIASSETTING_1_BIAS_CURRENT_RX_LNA | [7:0] | RESERVED | | | | LNA_BIAS | | | |
| 0x1796 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1797 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1798 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1799 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x179A | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x179B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x179C | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x179D | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x179E | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x179F | RESERVED | [7:0] | RESERVED | | | | | | | |

| Addr | Name | Bits | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
|------|------|------|-------|-------|-------|-------|-------|-------|-------|-------|
| 0x17A0 | RX_BIASSETTING_2_LNA_BIAS_OFF | [7:0] | EXT_LNA_BIAS_OFF | | | | | | | |
| 0x17A1 | RX_BIASSETTING_2_LNA_BIAS_ON | [7:0] | EXT_LNA_BIAS_ON | | | | | | | |
| 0x17A2 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x17A3 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x17A4 | RX_BIASSETTING_2_BIAS_CURRENT_RX | [7:0] | RESERVED | RX_VM_BIAS | | | RX_VGA_BIAS | | | |
| 0x17A5 | RX_BIASSETTING_2_BIAS_CURRENT_RX_LNA | [7:0] | RESERVED | | | | LNA_BIAS | | | |
| 0x17A6 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x17A7 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x17A8 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x17A9 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x17AA | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x17AB | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x17AC | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x17AD | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x17AE | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x17AF | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x17B0 | RX_BIASSETTING_3_LNA_BIAS_OFF | [7:0] | EXT_LNA_BIAS_OFF | | | | | | | |
| 0x17B1 | RX_BIASSETTING_3_LNA_BIAS_ON | [7:0] | EXT_LNA_BIAS_ON | | | | | | | |
| 0x17B2 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x17B3 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x17B4 | RX_BIASSETTING_3_BIAS_CURRENT_RX | [7:0] | RESERVED | RX_VM_BIAS | | | RX_VGA_BIAS | | | |
| 0x17B5 | RX_BIASSETTING_3_BIAS_CURRENT_RX_LNA | [7:0] | RESERVED | | | | LNA_BIAS | | | |
| 0x17B6 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x17B7 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x17B8 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x17B9 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x17BA | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x17BB | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x17BC | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x17BD | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x17BE | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x17BF | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x17C0 | RX_BIASSETTING_4_LNA_BIAS_OFF | [7:0] | EXT_LNA_BIAS_OFF | | | | | | | |
| 0x17C1 | RX_BIASSETTING_4_LNA_BIAS_ON | [7:0] | EXT_LNA_BIAS_ON | | | | | | | |
| 0x17C2 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x17C3 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x17C4 | RX_BIASSETTING_4_BIAS_CURRENT_RX | [7:0] | RESERVED | RX_VM_BIAS | | | RX_VGA_BIAS | | | |
| 0x17C5 | RX_BIASSETTING_4_BIAS_CURRENT_RX_LNA | [7:0] | RESERVED | | | | LNA_BIAS | | | |
| 0x17C6 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x17C7 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x17C8 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x17C9 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x17CA | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x17CB | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x17CC | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x17CD | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x17CE | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x17CF | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x17D0 | RX_BIASSETTING_5_LNA_BIAS_OFF | [7:0] | EXT_LNA_BIAS_OFF | | | | | | | |
| 0x17D1 | RX_BIASSETTING_5_LNA_BIAS_ON | [7:0] | EXT_LNA_BIAS_ON | | | | | | | |
| 0x17D2 | RESERVED | [7:0] | RESERVED | | | | | | | |

| Addr | Name | Bits | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
|---|---|---|---|---|---|---|---|---|---|---|
| 0x17D3 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x17D4 | RX_BIASSETTING_5_BIAS_CURRENT_RX | [7:0] | RESERVED | | RX_VM_BIAS | | | RX_VGA_BIAS | | |
| 0x17D5 | RX_BIASSETTING_5_BIAS_CURRENT_RX_LNA | [7:0] | RESERVED | | | | LNA_BIAS | | | |
| 0x17D6 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x17D7 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x17D8 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x17D9 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x17DA | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x17DB | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x17DC | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x17DD | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x17DE | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x17DF | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x17E0 | RX_BIASSETTING_6_LNA_BIAS_OFF | [7:0] | EXT_LNA_BIAS_OFF | | | | | | | |
| 0x17E1 | RX_BIASSETTING_6_LNA_BIAS_ON | [7:0] | EXT_LNA_BIAS_ON | | | | | | | |
| 0x17E2 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x17E3 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x17E4 | RX_BIASSETTING_6_BIAS_CURRENT_RX | [7:0] | RESERVED | | RX_VM_BIAS | | | RX_VGA_BIAS | | |
| 0x17E5 | RX_BIASSETTING_6_BIAS_CURRENT_RX_LNA | [7:0] | RESERVED | | | | LNA_BIAS | | | |
| 0x17E6 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x17E7 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x17E8 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x17E9 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x17EA | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x17EB | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x17EC | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x17ED | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x17EE | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x17EF | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x17F0 | RX_BIASSETTING_7_LNA_BIAS_OFF | [7:0] | EXT_LNA_BIAS_OFF | | | | | | | |
| 0x17F1 | RX_BIASSETTING_7_LNA_BIAS_ON | [7:0] | EXT_LNA_BIAS_ON | | | | | | | |
| 0x17F2 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x17F3 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x17F4 | RX_BIASSETTING_7_BIAS_CURRENT_RX | [7:0] | RESERVED | | RX_VM_BIAS | | | RX_VGA_BIAS | | |
| 0x17F5 | RX_BIASSETTING_7_BIAS_CURRENT_RX_LNA | [7:0] | RESERVED | | | | LNA_BIAS | | | |
| 0x17F6 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x17F7 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x17F8 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x17F9 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x17FA | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x17FB | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x17FC | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x17FD | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x17FE | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x17FF | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1800 | BEAM_POSITION_0_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x1801 | BEAM_POSITION_0_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x1802 | BEAM_POSITION_0_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x1803 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1804 | BEAM_POSITION_0_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x1805 | BEAM_POSITION_0_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |

| Addr | Name | Bits | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
|---|---|---|---|---|---|---|---|---|---|---|
| 0x1806 | BEAM_POSITION_0_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x1807 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1808 | BEAM_POSITION_0_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x1809 | BEAM_POSITION_0_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x180A | BEAM_POSITION_0_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x180B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x180C | BEAM_POSITION_0_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x180D | BEAM_POSITION_0_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x180E | BEAM_POSITION_0_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x180F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1810 | BEAM_POSITION_1_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x1811 | BEAM_POSITION_1_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x1812 | BEAM_POSITION_1_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x1813 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1814 | BEAM_POSITION_1_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x1815 | BEAM_POSITION_1_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x1816 | BEAM_POSITION_1_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x1817 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1818 | BEAM_POSITION_1_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x1819 | BEAM_POSITION_1_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x181A | BEAM_POSITION_1_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x181B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x181C | BEAM_POSITION_1_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x181D | BEAM_POSITION_1_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x181E | BEAM_POSITION_1_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x181F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1820 | BEAM_POSITION_2_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x1821 | BEAM_POSITION_2_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x1822 | BEAM_POSITION_2_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x1823 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1824 | BEAM_POSITION_2_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x1825 | BEAM_POSITION_2_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x1826 | BEAM_POSITION_2_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x1827 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1828 | BEAM_POSITION_2_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x1829 | BEAM_POSITION_2_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x182A | BEAM_POSITION_2_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x182B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x182C | BEAM_POSITION_2_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |

| Addr | Name | Bits | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
|---|---|---|---|---|---|---|---|---|---|---|
| 0x182D | BEAM_POSITION_2_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x182E | BEAM_POSITION_2_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x182F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1830 | BEAM_POSITION_3_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x1831 | BEAM_POSITION_3_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x1832 | BEAM_POSITION_3_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x1833 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1834 | BEAM_POSITION_3_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x1835 | BEAM_POSITION_3_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x1836 | BEAM_POSITION_3_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x1837 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1838 | BEAM_POSITION_3_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x1839 | BEAM_POSITION_3_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x183A | BEAM_POSITION_3_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x183B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x183C | BEAM_POSITION_3_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x183D | BEAM_POSITION_3_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x183E | BEAM_POSITION_3_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x183F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1840 | BEAM_POSITION_4_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x1841 | BEAM_POSITION_4_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x1842 | BEAM_POSITION_4_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x1843 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1844 | BEAM_POSITION_4_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x1845 | BEAM_POSITION_4_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x1846 | BEAM_POSITION_4_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x1847 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1848 | BEAM_POSITION_4_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x1849 | BEAM_POSITION_4_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x184A | BEAM_POSITION_4_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x184B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x184C | BEAM_POSITION_4_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x184D | BEAM_POSITION_4_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x184E | BEAM_POSITION_4_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x184F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1850 | BEAM_POSITION_5_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x1851 | BEAM_POSITION_5_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x1852 | BEAM_POSITION_5_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x1853 | RESERVED | [7:0] | RESERVED | | | | | | | |

| Addr | Name | Bits | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
|---|---|---|---|---|---|---|---|---|---|---|
| 0x1854 | BEAM_POSITION_5_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x1855 | BEAM_POSITION_5_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x1856 | BEAM_POSITION_5_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x1857 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1858 | BEAM_POSITION_5_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x1859 | BEAM_POSITION_5_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x185A | BEAM_POSITION_5_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x185B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x185C | BEAM_POSITION_5_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x185D | BEAM_POSITION_5_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x185E | BEAM_POSITION_5_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x185F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1860 | BEAM_POSITION_6_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x1861 | BEAM_POSITION_6_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x1862 | BEAM_POSITION_6_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x1863 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1864 | BEAM_POSITION_6_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x1865 | BEAM_POSITION_6_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x1866 | BEAM_POSITION_6_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x1867 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1868 | BEAM_POSITION_6_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x1869 | BEAM_POSITION_6_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x186A | BEAM_POSITION_6_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x186B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x186C | BEAM_POSITION_6_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x186D | BEAM_POSITION_6_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x186E | BEAM_POSITION_6_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x186F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1870 | BEAM_POSITION_7_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x1871 | BEAM_POSITION_7_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x1872 | BEAM_POSITION_7_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x1873 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1874 | BEAM_POSITION_7_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x1875 | BEAM_POSITION_7_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x1876 | BEAM_POSITION_7_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x1877 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1878 | BEAM_POSITION_7_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x1879 | BEAM_POSITION_7_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x187A | BEAM_POSITION_7_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |

| Addr | Name | Bits | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
|------|------|------|-------|-------|-------|-------|-------|-------|-------|-------|
| 0x187B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x187C | BEAM_POSITION_7_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x187D | BEAM_POSITION_7_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x187E | BEAM_POSITION_7_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x187F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1880 | BEAM_POSITION_8_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x1881 | BEAM_POSITION_8_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x1882 | BEAM_POSITION_8_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x1883 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1884 | BEAM_POSITION_8_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x1885 | BEAM_POSITION_8_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x1886 | BEAM_POSITION_8_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x1887 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1888 | BEAM_POSITION_8_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x1889 | BEAM_POSITION_8_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x188A | BEAM_POSITION_8_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x188B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x188C | BEAM_POSITION_8_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x188D | BEAM_POSITION_8_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x188E | BEAM_POSITION_8_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x188F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1890 | BEAM_POSITION_9_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x1891 | BEAM_POSITION_9_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x1892 | BEAM_POSITION_9_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x1893 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1894 | BEAM_POSITION_9_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x1895 | BEAM_POSITION_9_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x1896 | BEAM_POSITION_9_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x1897 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1898 | BEAM_POSITION_9_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x1899 | BEAM_POSITION_9_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x189A | BEAM_POSITION_9_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x189B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x189C | BEAM_POSITION_9_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x189D | BEAM_POSITION_9_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x189E | BEAM_POSITION_9_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x189F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x18A0 | BEAM_POSITION_10_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x18A1 | BEAM_POSITION_10_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |

| Addr | Name | Bits | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
|------|------|------|-------|-------|-------|-------|-------|-------|-------|-------|
| 0x18A2 | BEAM_POSITION_10_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x18A3 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x18A4 | BEAM_POSITION_10_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x18A5 | BEAM_POSITION_10_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x18A6 | BEAM_POSITION_10_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x18A7 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x18A8 | BEAM_POSITION_10_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x18A9 | BEAM_POSITION_10_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x18AA | BEAM_POSITION_10_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x18AB | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x18AC | BEAM_POSITION_10_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x18AD | BEAM_POSITION_10_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x18AE | BEAM_POSITION_10_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x18AF | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x18B0 | BEAM_POSITION_11_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x18B1 | BEAM_POSITION_11_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x18B2 | BEAM_POSITION_11_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x18B3 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x18B4 | BEAM_POSITION_11_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x18B5 | BEAM_POSITION_11_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x18B6 | BEAM_POSITION_11_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x18B7 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x18B8 | BEAM_POSITION_11_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x18B9 | BEAM_POSITION_11_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x18BA | BEAM_POSITION_11_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x18BB | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x18BC | BEAM_POSITION_11_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x18BD | BEAM_POSITION_11_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x18BE | BEAM_POSITION_11_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x18BF | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x18C0 | BEAM_POSITION_12_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x18C1 | BEAM_POSITION_12_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x18C2 | BEAM_POSITION_12_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x18C3 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x18C4 | BEAM_POSITION_12_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x18C5 | BEAM_POSITION_12_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x18C6 | BEAM_POSITION_12_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x18C7 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x18C8 | BEAM_POSITION_12_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |

| Addr | Name | Bits | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
|---|---|---|---|---|---|---|---|---|---|---|
| 0x18C9 | BEAM_POSITION_12_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x18CA | BEAM_POSITION_12_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x18CB | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x18CC | BEAM_POSITION_12_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x18CD | BEAM_POSITION_12_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x18CE | BEAM_POSITION_12_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x18CF | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x18D0 | BEAM_POSITION_13_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x18D1 | BEAM_POSITION_13_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x18D2 | BEAM_POSITION_13_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x18D3 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x18D4 | BEAM_POSITION_13_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x18D5 | BEAM_POSITION_13_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x18D6 | BEAM_POSITION_13_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x18D7 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x18D8 | BEAM_POSITION_13_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x18D9 | BEAM_POSITION_13_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x18DA | BEAM_POSITION_13_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x18DB | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x18DC | BEAM_POSITION_13_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x18DD | BEAM_POSITION_13_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x18DE | BEAM_POSITION_13_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x18DF | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x18E0 | BEAM_POSITION_14_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x18E1 | BEAM_POSITION_14_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x18E2 | BEAM_POSITION_14_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x18E3 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x18E4 | BEAM_POSITION_14_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x18E5 | BEAM_POSITION_14_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x18E6 | BEAM_POSITION_14_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x18E7 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x18E8 | BEAM_POSITION_14_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x18E9 | BEAM_POSITION_14_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x18EA | BEAM_POSITION_14_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x18EB | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x18EC | BEAM_POSITION_14_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x18ED | BEAM_POSITION_14_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x18EE | BEAM_POSITION_14_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x18EF | RESERVED | [7:0] | RESERVED | | | | | | | |

| Addr | Name | Bits | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
|---|---|---|---|---|---|---|---|---|---|---|
| 0x18F0 | BEAM_POSITION_15_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x18F1 | BEAM_POSITION_15_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x18F2 | BEAM_POSITION_15_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x18F3 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x18F4 | BEAM_POSITION_15_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x18F5 | BEAM_POSITION_15_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x18F6 | BEAM_POSITION_15_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x18F7 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x18F8 | BEAM_POSITION_15_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x18F9 | BEAM_POSITION_15_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x18FA | BEAM_POSITION_15_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x18FB | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x18FC | BEAM_POSITION_15_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x18FD | BEAM_POSITION_15_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x18FE | BEAM_POSITION_15_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x18FF | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1900 | BEAM_POSITION_16_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x1901 | BEAM_POSITION_16_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x1902 | BEAM_POSITION_16_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x1903 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1904 | BEAM_POSITION_16_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x1905 | BEAM_POSITION_16_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x1906 | BEAM_POSITION_16_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x1907 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1908 | BEAM_POSITION_16_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x1909 | BEAM_POSITION_16_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x190A | BEAM_POSITION_16_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x190B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x190C | BEAM_POSITION_16_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x190D | BEAM_POSITION_16_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x190E | BEAM_POSITION_16_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x190F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1910 | BEAM_POSITION_17_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x1911 | BEAM_POSITION_17_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x1912 | BEAM_POSITION_17_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x1913 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1914 | BEAM_POSITION_17_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x1915 | BEAM_POSITION_17_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x1916 | BEAM_POSITION_17_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |

| Addr | Name | Bits | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
|---|---|---|---|---|---|---|---|---|---|---|
| 0x1917 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1918 | BEAM_POSITION_17_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x1919 | BEAM_POSITION_17_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x191A | BEAM_POSITION_17_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x191B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x191C | BEAM_POSITION_17_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x191D | BEAM_POSITION_17_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x191E | BEAM_POSITION_17_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x191F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1920 | BEAM_POSITION_18_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x1921 | BEAM_POSITION_18_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x1922 | BEAM_POSITION_18_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x1923 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1924 | BEAM_POSITION_18_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x1925 | BEAM_POSITION_18_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x1926 | BEAM_POSITION_18_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x1927 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1928 | BEAM_POSITION_18_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x1929 | BEAM_POSITION_18_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x192A | BEAM_POSITION_18_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x192B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x192C | BEAM_POSITION_18_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x192D | BEAM_POSITION_18_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x192E | BEAM_POSITION_18_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x192F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1930 | BEAM_POSITION_19_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x1931 | BEAM_POSITION_19_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x1932 | BEAM_POSITION_19_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x1933 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1934 | BEAM_POSITION_19_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x1935 | BEAM_POSITION_19_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x1936 | BEAM_POSITION_19_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x1937 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1938 | BEAM_POSITION_19_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x1939 | BEAM_POSITION_19_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x193A | BEAM_POSITION_19_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x193B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x193C | BEAM_POSITION_19_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x193D | BEAM_POSITION_19_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |

| Addr | Name | Bits | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
|------|------|------|-------|-------|-------|-------|-------|-------|-------|-------|
| 0x193E | BEAM_POSITION_19_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x193F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1940 | BEAM_POSITION_20_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x1941 | BEAM_POSITION_20_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x1942 | BEAM_POSITION_20_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x1943 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1944 | BEAM_POSITION_20_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x1945 | BEAM_POSITION_20_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x1946 | BEAM_POSITION_20_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x1947 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1948 | BEAM_POSITION_20_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x1949 | BEAM_POSITION_20_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x194A | BEAM_POSITION_20_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x194B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x194C | BEAM_POSITION_20_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x194D | BEAM_POSITION_20_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x194E | BEAM_POSITION_20_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x194F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1950 | BEAM_POSITION_21_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x1951 | BEAM_POSITION_21_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x1952 | BEAM_POSITION_21_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x1953 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1954 | BEAM_POSITION_21_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x1955 | BEAM_POSITION_21_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x1956 | BEAM_POSITION_21_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x1957 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1958 | BEAM_POSITION_21_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x1959 | BEAM_POSITION_21_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x195A | BEAM_POSITION_21_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x195B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x195C | BEAM_POSITION_21_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x195D | BEAM_POSITION_21_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x195E | BEAM_POSITION_21_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x195F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1960 | BEAM_POSITION_22_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x1961 | BEAM_POSITION_22_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x1962 | BEAM_POSITION_22_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x1963 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1964 | BEAM_POSITION_22_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |

| Addr | Name | Bits | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
|---|---|---|---|---|---|---|---|---|---|---|
| 0x1965 | BEAM_POSITION_22_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x1966 | BEAM_POSITION_22_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x1967 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1968 | BEAM_POSITION_22_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x1969 | BEAM_POSITION_22_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x196A | BEAM_POSITION_22_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x196B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x196C | BEAM_POSITION_22_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x196D | BEAM_POSITION_22_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x196E | BEAM_POSITION_22_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x196F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1970 | BEAM_POSITION_23_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x1971 | BEAM_POSITION_23_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x1972 | BEAM_POSITION_23_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x1973 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1974 | BEAM_POSITION_23_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x1975 | BEAM_POSITION_23_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x1976 | BEAM_POSITION_23_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x1977 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1978 | BEAM_POSITION_23_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x1979 | BEAM_POSITION_23_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x197A | BEAM_POSITION_23_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x197B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x197C | BEAM_POSITION_23_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x197D | BEAM_POSITION_23_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x197E | BEAM_POSITION_23_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x197F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1980 | BEAM_POSITION_24_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x1981 | BEAM_POSITION_24_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x1982 | BEAM_POSITION_24_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x1983 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1984 | BEAM_POSITION_24_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x1985 | BEAM_POSITION_24_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x1986 | BEAM_POSITION_24_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x1987 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1988 | BEAM_POSITION_24_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x1989 | BEAM_POSITION_24_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x198A | BEAM_POSITION_24_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x198B | RESERVED | [7:0] | RESERVED | | | | | | | |

| Addr | Name | Bits | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
|---|---|---|---|---|---|---|---|---|---|---|
| 0x198C | BEAM_POSITION_24_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x198D | BEAM_POSITION_24_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x198E | BEAM_POSITION_24_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x198F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1990 | BEAM_POSITION_25_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x1991 | BEAM_POSITION_25_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x1992 | BEAM_POSITION_25_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x1993 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1994 | BEAM_POSITION_25_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x1995 | BEAM_POSITION_25_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x1996 | BEAM_POSITION_25_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x1997 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1998 | BEAM_POSITION_25_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x1999 | BEAM_POSITION_25_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x199A | BEAM_POSITION_25_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x199B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x199C | BEAM_POSITION_25_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x199D | BEAM_POSITION_25_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x199E | BEAM_POSITION_25_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x199F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x19A0 | BEAM_POSITION_26_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x19A1 | BEAM_POSITION_26_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x19A2 | BEAM_POSITION_26_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x19A3 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x19A4 | BEAM_POSITION_26_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x19A5 | BEAM_POSITION_26_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x19A6 | BEAM_POSITION_26_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x19A7 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x19A8 | BEAM_POSITION_26_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x19A9 | BEAM_POSITION_26_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x19AA | BEAM_POSITION_26_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x19AB | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x19AC | BEAM_POSITION_26_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x19AD | BEAM_POSITION_26_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x19AE | BEAM_POSITION_26_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x19AF | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x19B0 | BEAM_POSITION_27_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x19B1 | BEAM_POSITION_27_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x19B2 | BEAM_POSITION_27_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |

| Addr | Name | Bits | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
|---|---|---|---|---|---|---|---|---|---|---|
| 0x19B3 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x19B4 | BEAM_POSITION_27_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x19B5 | BEAM_POSITION_27_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x19B6 | BEAM_POSITION_27_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x19B7 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x19B8 | BEAM_POSITION_27_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x19B9 | BEAM_POSITION_27_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x19BA | BEAM_POSITION_27_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x19BB | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x19BC | BEAM_POSITION_27_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x19BD | BEAM_POSITION_27_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x19BE | BEAM_POSITION_27_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x19BF | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x19C0 | BEAM_POSITION_28_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x19C1 | BEAM_POSITION_28_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x19C2 | BEAM_POSITION_28_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x19C3 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x19C4 | BEAM_POSITION_28_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x19C5 | BEAM_POSITION_28_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x19C6 | BEAM_POSITION_28_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x19C7 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x19C8 | BEAM_POSITION_28_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x19C9 | BEAM_POSITION_28_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x19CA | BEAM_POSITION_28_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x19CB | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x19CC | BEAM_POSITION_28_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x19CD | BEAM_POSITION_28_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x19CE | BEAM_POSITION_28_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x19CF | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x19D0 | BEAM_POSITION_29_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x19D1 | BEAM_POSITION_29_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x19D2 | BEAM_POSITION_29_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x19D3 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x19D4 | BEAM_POSITION_29_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x19D5 | BEAM_POSITION_29_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x19D6 | BEAM_POSITION_29_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x19D7 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x19D8 | BEAM_POSITION_29_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x19D9 | BEAM_POSITION_29_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |

| Addr | Name | Bits | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
|---|---|---|---|---|---|---|---|---|---|---|
| 0x19DA | BEAM_POSITION_29_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x19DB | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x19DC | BEAM_POSITION_29_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x19DD | BEAM_POSITION_29_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x19DE | BEAM_POSITION_29_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x19DF | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x19E0 | BEAM_POSITION_30_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x19E1 | BEAM_POSITION_30_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x19E2 | BEAM_POSITION_30_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x19E3 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x19E4 | BEAM_POSITION_30_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x19E5 | BEAM_POSITION_30_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x19E6 | BEAM_POSITION_30_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x19E7 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x19E8 | BEAM_POSITION_30_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x19E9 | BEAM_POSITION_30_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x19EA | BEAM_POSITION_30_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x19EB | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x19EC | BEAM_POSITION_30_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x19ED | BEAM_POSITION_30_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x19EE | BEAM_POSITION_30_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x19EF | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x19F0 | BEAM_POSITION_31_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x19F1 | BEAM_POSITION_31_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x19F2 | BEAM_POSITION_31_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x19F3 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x19F4 | BEAM_POSITION_31_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x19F5 | BEAM_POSITION_31_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x19F6 | BEAM_POSITION_31_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x19F7 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x19F8 | BEAM_POSITION_31_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x19F9 | BEAM_POSITION_31_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x19FA | BEAM_POSITION_31_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x19FB | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x19FC | BEAM_POSITION_31_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x19FD | BEAM_POSITION_31_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x19FE | BEAM_POSITION_31_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x19FF | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1A00 | BEAM_POSITION_32_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |

| Addr | Name | Bits | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
|---|---|---|---|---|---|---|---|---|---|---|
| 0x1A01 | BEAM_POSITION_32_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x1A02 | BEAM_POSITION_32_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x1A03 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1A04 | BEAM_POSITION_32_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x1A05 | BEAM_POSITION_32_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x1A06 | BEAM_POSITION_32_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x1A07 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1A08 | BEAM_POSITION_32_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x1A09 | BEAM_POSITION_32_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x1A0A | BEAM_POSITION_32_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x1A0B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1A0C | BEAM_POSITION_32_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x1A0D | BEAM_POSITION_32_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x1A0E | BEAM_POSITION_32_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x1A0F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1A10 | BEAM_POSITION_33_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x1A11 | BEAM_POSITION_33_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x1A12 | BEAM_POSITION_33_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x1A13 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1A14 | BEAM_POSITION_33_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x1A15 | BEAM_POSITION_33_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x1A16 | BEAM_POSITION_33_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x1A17 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1A18 | BEAM_POSITION_33_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x1A19 | BEAM_POSITION_33_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x1A1A | BEAM_POSITION_33_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x1A1B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1A1C | BEAM_POSITION_33_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x1A1D | BEAM_POSITION_33_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x1A1E | BEAM_POSITION_33_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x1A1F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1A20 | BEAM_POSITION_34_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x1A21 | BEAM_POSITION_34_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x1A22 | BEAM_POSITION_34_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x1A23 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1A24 | BEAM_POSITION_34_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x1A25 | BEAM_POSITION_34_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x1A26 | BEAM_POSITION_34_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x1A27 | RESERVED | [7:0] | RESERVED | | | | | | | |

| Addr | Name | Bits | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
|---|---|---|---|---|---|---|---|---|---|---|
| 0x1A28 | BEAM_POSITION_34_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x1A29 | BEAM_POSITION_34_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x1A2A | BEAM_POSITION_34_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x1A2B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1A2C | BEAM_POSITION_34_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x1A2D | BEAM_POSITION_34_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x1A2E | BEAM_POSITION_34_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x1A2F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1A30 | BEAM_POSITION_35_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x1A31 | BEAM_POSITION_35_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x1A32 | BEAM_POSITION_35_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x1A33 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1A34 | BEAM_POSITION_35_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x1A35 | BEAM_POSITION_35_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x1A36 | BEAM_POSITION_35_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x1A37 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1A38 | BEAM_POSITION_35_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x1A39 | BEAM_POSITION_35_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x1A3A | BEAM_POSITION_35_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x1A3B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1A3C | BEAM_POSITION_35_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x1A3D | BEAM_POSITION_35_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x1A3E | BEAM_POSITION_35_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x1A3F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1A40 | BEAM_POSITION_36_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x1A41 | BEAM_POSITION_36_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x1A42 | BEAM_POSITION_36_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x1A43 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1A44 | BEAM_POSITION_36_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x1A45 | BEAM_POSITION_36_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x1A46 | BEAM_POSITION_36_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x1A47 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1A48 | BEAM_POSITION_36_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x1A49 | BEAM_POSITION_36_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x1A4A | BEAM_POSITION_36_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x1A4B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1A4C | BEAM_POSITION_36_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x1A4D | BEAM_POSITION_36_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x1A4E | BEAM_POSITION_36_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |

| Addr | Name | Bits | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
|------|------|------|-------|-------|-------|-------|-------|-------|-------|-------|
| 0x1A4F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1A50 | BEAM_POSITION_37_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x1A51 | BEAM_POSITION_37_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x1A52 | BEAM_POSITION_37_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x1A53 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1A54 | BEAM_POSITION_37_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x1A55 | BEAM_POSITION_37_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x1A56 | BEAM_POSITION_37_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x1A57 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1A58 | BEAM_POSITION_37_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x1A59 | BEAM_POSITION_37_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x1A5A | BEAM_POSITION_37_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x1A5B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1A5C | BEAM_POSITION_37_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x1A5D | BEAM_POSITION_37_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x1A5E | BEAM_POSITION_37_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x1A5F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1A60 | BEAM_POSITION_38_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x1A61 | BEAM_POSITION_38_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x1A62 | BEAM_POSITION_38_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x1A63 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1A64 | BEAM_POSITION_38_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x1A65 | BEAM_POSITION_38_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x1A66 | BEAM_POSITION_38_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x1A67 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1A68 | BEAM_POSITION_38_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x1A69 | BEAM_POSITION_38_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x1A6A | BEAM_POSITION_38_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x1A6B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1A6C | BEAM_POSITION_38_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x1A6D | BEAM_POSITION_38_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x1A6E | BEAM_POSITION_38_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x1A6F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1A70 | BEAM_POSITION_39_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x1A71 | BEAM_POSITION_39_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x1A72 | BEAM_POSITION_39_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x1A73 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1A74 | BEAM_POSITION_39_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x1A75 | BEAM_POSITION_39_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |

| Addr | Name | Bits | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
|---|---|---|---|---|---|---|---|---|---|---|
| 0x1A76 | BEAM_POSITION_39_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x1A77 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1A78 | BEAM_POSITION_39_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x1A79 | BEAM_POSITION_39_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x1A7A | BEAM_POSITION_39_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x1A7B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1A7C | BEAM_POSITION_39_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x1A7D | BEAM_POSITION_39_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x1A7E | BEAM_POSITION_39_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x1A7F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1A80 | BEAM_POSITION_40_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x1A81 | BEAM_POSITION_40_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x1A82 | BEAM_POSITION_40_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x1A83 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1A84 | BEAM_POSITION_40_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x1A85 | BEAM_POSITION_40_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x1A86 | BEAM_POSITION_40_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x1A87 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1A88 | BEAM_POSITION_40_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x1A89 | BEAM_POSITION_40_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x1A8A | BEAM_POSITION_40_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x1A8B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1A8C | BEAM_POSITION_40_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x1A8D | BEAM_POSITION_40_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x1A8E | BEAM_POSITION_40_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x1A8F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1A90 | BEAM_POSITION_41_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x1A91 | BEAM_POSITION_41_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x1A92 | BEAM_POSITION_41_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x1A93 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1A94 | BEAM_POSITION_41_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x1A95 | BEAM_POSITION_41_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x1A96 | BEAM_POSITION_41_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x1A97 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1A98 | BEAM_POSITION_41_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x1A99 | BEAM_POSITION_41_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x1A9A | BEAM_POSITION_41_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x1A9B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1A9C | BEAM_POSITION_41_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |

| Addr | Name | Bits | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
|---|---|---|---|---|---|---|---|---|---|---|
| 0x1A9D | BEAM_POSITION_41_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x1A9E | BEAM_POSITION_41_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x1A9F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1AA0 | BEAM_POSITION_42_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x1AA1 | BEAM_POSITION_42_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x1AA2 | BEAM_POSITION_42_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x1AA3 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1AA4 | BEAM_POSITION_42_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x1AA5 | BEAM_POSITION_42_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x1AA6 | BEAM_POSITION_42_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x1AA7 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1AA8 | BEAM_POSITION_42_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x1AA9 | BEAM_POSITION_42_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x1AAA | BEAM_POSITION_42_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x1AAB | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1AAC | BEAM_POSITION_42_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x1AAD | BEAM_POSITION_42_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x1AAE | BEAM_POSITION_42_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x1AAF | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1AB0 | BEAM_POSITION_43_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x1AB1 | BEAM_POSITION_43_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x1AB2 | BEAM_POSITION_43_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x1AB3 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1AB4 | BEAM_POSITION_43_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x1AB5 | BEAM_POSITION_43_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x1AB6 | BEAM_POSITION_43_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x1AB7 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1AB8 | BEAM_POSITION_43_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x1AB9 | BEAM_POSITION_43_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x1ABA | BEAM_POSITION_43_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x1ABB | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1ABC | BEAM_POSITION_43_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x1ABD | BEAM_POSITION_43_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x1ABE | BEAM_POSITION_43_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x1ABF | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1AC0 | BEAM_POSITION_44_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x1AC1 | BEAM_POSITION_44_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x1AC2 | BEAM_POSITION_44_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x1AC3 | RESERVED | [7:0] | RESERVED | | | | | | | |

| Addr | Name | Bits | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
|------|------|------|-------|-------|-------|-------|-------|-------|-------|-------|
| 0x1AC4 | BEAM_POSITION_44_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x1AC5 | BEAM_POSITION_44_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x1AC6 | BEAM_POSITION_44_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x1AC7 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1AC8 | BEAM_POSITION_44_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x1AC9 | BEAM_POSITION_44_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x1ACA | BEAM_POSITION_44_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x1ACB | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1ACC | BEAM_POSITION_44_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x1ACD | BEAM_POSITION_44_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x1ACE | BEAM_POSITION_44_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x1ACF | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1AD0 | BEAM_POSITION_45_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x1AD1 | BEAM_POSITION_45_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x1AD2 | BEAM_POSITION_45_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x1AD3 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1AD4 | BEAM_POSITION_45_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x1AD5 | BEAM_POSITION_45_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x1AD6 | BEAM_POSITION_45_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x1AD7 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1AD8 | BEAM_POSITION_45_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x1AD9 | BEAM_POSITION_45_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x1ADA | BEAM_POSITION_45_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x1ADB | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1ADC | BEAM_POSITION_45_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x1ADD | BEAM_POSITION_45_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x1ADE | BEAM_POSITION_45_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x1ADF | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1AE0 | BEAM_POSITION_46_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x1AE1 | BEAM_POSITION_46_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x1AE2 | BEAM_POSITION_46_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x1AE3 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1AE4 | BEAM_POSITION_46_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x1AE5 | BEAM_POSITION_46_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x1AE6 | BEAM_POSITION_46_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x1AE7 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1AE8 | BEAM_POSITION_46_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x1AE9 | BEAM_POSITION_46_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x1AEA | BEAM_POSITION_46_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |

| Addr | Name | Bits | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
|------|------|------|-------|-------|-------|-------|-------|-------|-------|-------|
| 0x1AEB | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1AEC | BEAM_POSITION_46_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x1AED | BEAM_POSITION_46_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x1AEE | BEAM_POSITION_46_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x1AEF | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1AF0 | BEAM_POSITION_47_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x1AF1 | BEAM_POSITION_47_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x1AF2 | BEAM_POSITION_47_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x1AF3 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1AF4 | BEAM_POSITION_47_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x1AF5 | BEAM_POSITION_47_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x1AF6 | BEAM_POSITION_47_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x1AF7 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1AF8 | BEAM_POSITION_47_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x1AF9 | BEAM_POSITION_47_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x1AFA | BEAM_POSITION_47_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x1AFB | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1AFC | BEAM_POSITION_47_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x1AFD | BEAM_POSITION_47_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x1AFE | BEAM_POSITION_47_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x1AFF | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1B00 | BEAM_POSITION_48_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x1B01 | BEAM_POSITION_48_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x1B02 | BEAM_POSITION_48_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x1B03 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1B04 | BEAM_POSITION_48_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x1B05 | BEAM_POSITION_48_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x1B06 | BEAM_POSITION_48_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x1B07 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1B08 | BEAM_POSITION_48_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x1B09 | BEAM_POSITION_48_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x1B0A | BEAM_POSITION_48_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x1B0B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1B0C | BEAM_POSITION_48_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x1B0D | BEAM_POSITION_48_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x1B0E | BEAM_POSITION_48_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x1B0F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1B10 | BEAM_POSITION_49_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x1B11 | BEAM_POSITION_49_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |

| Addr | Name | Bits | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
|---|---|---|---|---|---|---|---|---|---|---|
| 0x1B12 | BEAM_POSITION_49_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x1B13 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1B14 | BEAM_POSITION_49_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x1B15 | BEAM_POSITION_49_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x1B16 | BEAM_POSITION_49_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x1B17 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1B18 | BEAM_POSITION_49_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x1B19 | BEAM_POSITION_49_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x1B1A | BEAM_POSITION_49_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x1B1B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1B1C | BEAM_POSITION_49_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x1B1D | BEAM_POSITION_49_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x1B1E | BEAM_POSITION_49_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x1B1F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1B20 | BEAM_POSITION_50_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x1B21 | BEAM_POSITION_50_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x1B22 | BEAM_POSITION_50_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x1B23 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1B24 | BEAM_POSITION_50_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x1B25 | BEAM_POSITION_50_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x1B26 | BEAM_POSITION_50_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x1B27 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1B28 | BEAM_POSITION_50_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x1B29 | BEAM_POSITION_50_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x1B2A | BEAM_POSITION_50_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x1B2B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1B2C | BEAM_POSITION_50_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x1B2D | BEAM_POSITION_50_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x1B2E | BEAM_POSITION_50_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x1B2F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1B30 | BEAM_POSITION_51_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x1B31 | BEAM_POSITION_51_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x1B32 | BEAM_POSITION_51_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x1B33 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1B34 | BEAM_POSITION_51_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x1B35 | BEAM_POSITION_51_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x1B36 | BEAM_POSITION_51_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x1B37 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1B38 | BEAM_POSITION_51_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |

| Addr | Name | Bits | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
|---|---|---|---|---|---|---|---|---|---|---|
| 0x1B39 | BEAM_POSITION_51_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x1B3A | BEAM_POSITION_51_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x1B3B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1B3C | BEAM_POSITION_51_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x1B3D | BEAM_POSITION_51_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x1B3E | BEAM_POSITION_51_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x1B3F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1B40 | BEAM_POSITION_52_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x1B41 | BEAM_POSITION_52_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x1B42 | BEAM_POSITION_52_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x1B43 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1B44 | BEAM_POSITION_52_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x1B45 | BEAM_POSITION_52_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x1B46 | BEAM_POSITION_52_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x1B47 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1B48 | BEAM_POSITION_52_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x1B49 | BEAM_POSITION_52_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x1B4A | BEAM_POSITION_52_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x1B4B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1B4C | BEAM_POSITION_52_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x1B4D | BEAM_POSITION_52_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x1B4E | BEAM_POSITION_52_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x1B4F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1B50 | BEAM_POSITION_53_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x1B51 | BEAM_POSITION_53_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x1B52 | BEAM_POSITION_53_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x1B53 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1B54 | BEAM_POSITION_53_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x1B55 | BEAM_POSITION_53_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x1B56 | BEAM_POSITION_53_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x1B57 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1B58 | BEAM_POSITION_53_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x1B59 | BEAM_POSITION_53_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x1B5A | BEAM_POSITION_53_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x1B5B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1B5C | BEAM_POSITION_53_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x1B5D | BEAM_POSITION_53_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x1B5E | BEAM_POSITION_53_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x1B5F | RESERVED | [7:0] | RESERVED | | | | | | | |

| Addr | Name | Bits | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
|---|---|---|---|---|---|---|---|---|---|---|
| 0x1B60 | BEAM_POSITION_54_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x1B61 | BEAM_POSITION_54_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x1B62 | BEAM_POSITION_54_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x1B63 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1B64 | BEAM_POSITION_54_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x1B65 | BEAM_POSITION_54_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x1B66 | BEAM_POSITION_54_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x1B67 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1B68 | BEAM_POSITION_54_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x1B69 | BEAM_POSITION_54_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x1B6A | BEAM_POSITION_54_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x1B6B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1B6C | BEAM_POSITION_54_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x1B6D | BEAM_POSITION_54_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x1B6E | BEAM_POSITION_54_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x1B6F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1B70 | BEAM_POSITION_55_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x1B71 | BEAM_POSITION_55_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x1B72 | BEAM_POSITION_55_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x1B73 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1B74 | BEAM_POSITION_55_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x1B75 | BEAM_POSITION_55_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x1B76 | BEAM_POSITION_55_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x1B77 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1B78 | BEAM_POSITION_55_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x1B79 | BEAM_POSITION_55_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x1B7A | BEAM_POSITION_55_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x1B7B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1B7C | BEAM_POSITION_55_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x1B7D | BEAM_POSITION_55_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x1B7E | BEAM_POSITION_55_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x1B7F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1B80 | BEAM_POSITION_56_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x1B81 | BEAM_POSITION_56_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x1B82 | BEAM_POSITION_56_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x1B83 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1B84 | BEAM_POSITION_56_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x1B85 | BEAM_POSITION_56_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x1B86 | BEAM_POSITION_56_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |

| Addr | Name | Bits | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
|------|------|------|-------|-------|-------|-------|-------|-------|-------|-------|
| 0x1B87 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1B88 | BEAM_POSITION_56_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x1B89 | BEAM_POSITION_56_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x1B8A | BEAM_POSITION_56_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x1B8B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1B8C | BEAM_POSITION_56_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x1B8D | BEAM_POSITION_56_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x1B8E | BEAM_POSITION_56_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x1B8F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1B90 | BEAM_POSITION_57_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x1B91 | BEAM_POSITION_57_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x1B92 | BEAM_POSITION_57_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x1B93 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1B94 | BEAM_POSITION_57_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x1B95 | BEAM_POSITION_57_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x1B96 | BEAM_POSITION_57_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x1B97 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1B98 | BEAM_POSITION_57_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x1B99 | BEAM_POSITION_57_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x1B9A | BEAM_POSITION_57_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x1B9B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1B9C | BEAM_POSITION_57_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x1B9D | BEAM_POSITION_57_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x1B9E | BEAM_POSITION_57_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x1B9F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1BA0 | BEAM_POSITION_58_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x1BA1 | BEAM_POSITION_58_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x1BA2 | BEAM_POSITION_58_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x1BA3 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1BA4 | BEAM_POSITION_58_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x1BA5 | BEAM_POSITION_58_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x1BA6 | BEAM_POSITION_58_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x1BA7 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1BA8 | BEAM_POSITION_58_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x1BA9 | BEAM_POSITION_58_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x1BAA | BEAM_POSITION_58_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x1BAB | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1BAC | BEAM_POSITION_58_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x1BAD | BEAM_POSITION_58_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |

| Addr | Name | Bits | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
|---|---|---|---|---|---|---|---|---|---|---|
| 0x1BAE | BEAM_POSITION_58_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x1BAF | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1BB0 | BEAM_POSITION_59_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x1BB1 | BEAM_POSITION_59_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x1BB2 | BEAM_POSITION_59_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x1BB3 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1BB4 | BEAM_POSITION_59_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x1BB5 | BEAM_POSITION_59_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x1BB6 | BEAM_POSITION_59_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x1BB7 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1BB8 | BEAM_POSITION_59_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x1BB9 | BEAM_POSITION_59_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x1BBA | BEAM_POSITION_59_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x1BBB | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1BBC | BEAM_POSITION_59_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x1BBD | BEAM_POSITION_59_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x1BBE | BEAM_POSITION_59_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x1BBF | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1BC0 | BEAM_POSITION_60_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x1BC1 | BEAM_POSITION_60_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x1BC2 | BEAM_POSITION_60_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x1BC3 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1BC4 | BEAM_POSITION_60_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x1BC5 | BEAM_POSITION_60_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x1BC6 | BEAM_POSITION_60_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x1BC7 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1BC8 | BEAM_POSITION_60_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x1BC9 | BEAM_POSITION_60_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x1BCA | BEAM_POSITION_60_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x1BCB | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1BCC | BEAM_POSITION_60_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x1BCD | BEAM_POSITION_60_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x1BCE | BEAM_POSITION_60_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x1BCF | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1BD0 | BEAM_POSITION_61_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x1BD1 | BEAM_POSITION_61_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x1BD2 | BEAM_POSITION_61_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x1BD3 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1BD4 | BEAM_POSITION_61_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |

| Addr | Name | Bits | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
|------|------|------|-------|-------|-------|-------|-------|-------|-------|-------|
| 0x1BD5 | BEAM_POSITION_61_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x1BD6 | BEAM_POSITION_61_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x1BD7 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1BD8 | BEAM_POSITION_61_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x1BD9 | BEAM_POSITION_61_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x1BDA | BEAM_POSITION_61_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x1BDB | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1BDC | BEAM_POSITION_61_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x1BDD | BEAM_POSITION_61_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x1BDE | BEAM_POSITION_61_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x1BDF | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1BE0 | BEAM_POSITION_62_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x1BE1 | BEAM_POSITION_62_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x1BE2 | BEAM_POSITION_62_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x1BE3 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1BE4 | BEAM_POSITION_62_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x1BE5 | BEAM_POSITION_62_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x1BE6 | BEAM_POSITION_62_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x1BE7 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1BE8 | BEAM_POSITION_62_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x1BE9 | BEAM_POSITION_62_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x1BEA | BEAM_POSITION_62_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x1BEB | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1BEC | BEAM_POSITION_62_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x1BED | BEAM_POSITION_62_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x1BEE | BEAM_POSITION_62_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x1BEF | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1BF0 | BEAM_POSITION_63_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x1BF1 | BEAM_POSITION_63_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x1BF2 | BEAM_POSITION_63_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x1BF3 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1BF4 | BEAM_POSITION_63_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x1BF5 | BEAM_POSITION_63_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x1BF6 | BEAM_POSITION_63_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x1BF7 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1BF8 | BEAM_POSITION_63_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x1BF9 | BEAM_POSITION_63_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x1BFA | BEAM_POSITION_63_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x1BFB | RESERVED | [7:0] | RESERVED | | | | | | | |

| Addr | Name | Bits | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
|------|------|------|-------|-------|-------|-------|-------|-------|-------|-------|
| 0x1BFC | BEAM_POSITION_63_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x1BFD | BEAM_POSITION_63_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x1BFE | BEAM_POSITION_63_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x1BFF | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1C00 | BEAM_POSITION_64_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x1C01 | BEAM_POSITION_64_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x1C02 | BEAM_POSITION_64_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x1C03 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1C04 | BEAM_POSITION_64_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x1C05 | BEAM_POSITION_64_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x1C06 | BEAM_POSITION_64_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x1C07 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1C08 | BEAM_POSITION_64_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x1C09 | BEAM_POSITION_64_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x1C0A | BEAM_POSITION_64_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x1C0B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1C0C | BEAM_POSITION_64_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x1C0D | BEAM_POSITION_64_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x1C0E | BEAM_POSITION_64_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x1C0F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1C10 | BEAM_POSITION_65_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x1C11 | BEAM_POSITION_65_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x1C12 | BEAM_POSITION_65_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x1C13 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1C14 | BEAM_POSITION_65_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x1C15 | BEAM_POSITION_65_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x1C16 | BEAM_POSITION_65_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x1C17 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1C18 | BEAM_POSITION_65_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x1C19 | BEAM_POSITION_65_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x1C1A | BEAM_POSITION_65_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x1C1B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1C1C | BEAM_POSITION_65_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x1C1D | BEAM_POSITION_65_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x1C1E | BEAM_POSITION_65_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x1C1F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1C20 | BEAM_POSITION_66_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x1C21 | BEAM_POSITION_66_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x1C22 | BEAM_POSITION_66_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |

| Addr | Name | Bits | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
|------|------|------|-------|-------|-------|-------|-------|-------|-------|-------|
| 0x1C23 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1C24 | BEAM_POSITION_66_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x1C25 | BEAM_POSITION_66_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x1C26 | BEAM_POSITION_66_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x1C27 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1C28 | BEAM_POSITION_66_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x1C29 | BEAM_POSITION_66_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x1C2A | BEAM_POSITION_66_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x1C2B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1C2C | BEAM_POSITION_66_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x1C2D | BEAM_POSITION_66_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x1C2E | BEAM_POSITION_66_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x1C2F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1C30 | BEAM_POSITION_67_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x1C31 | BEAM_POSITION_67_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x1C32 | BEAM_POSITION_67_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x1C33 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1C34 | BEAM_POSITION_67_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x1C35 | BEAM_POSITION_67_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x1C36 | BEAM_POSITION_67_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x1C37 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1C38 | BEAM_POSITION_67_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x1C39 | BEAM_POSITION_67_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x1C3A | BEAM_POSITION_67_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x1C3B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1C3C | BEAM_POSITION_67_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x1C3D | BEAM_POSITION_67_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x1C3E | BEAM_POSITION_67_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x1C3F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1C40 | BEAM_POSITION_68_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x1C41 | BEAM_POSITION_68_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x1C42 | BEAM_POSITION_68_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x1C43 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1C44 | BEAM_POSITION_68_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x1C45 | BEAM_POSITION_68_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x1C46 | BEAM_POSITION_68_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x1C47 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1C48 | BEAM_POSITION_68_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x1C49 | BEAM_POSITION_68_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |

| Addr | Name | Bits | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
|------|------|------|-------|-------|-------|-------|-------|-------|-------|-------|
| 0x1C4A | BEAM_POSITION_68_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x1C4B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1C4C | BEAM_POSITION_68_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x1C4D | BEAM_POSITION_68_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x1C4E | BEAM_POSITION_68_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x1C4F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1C50 | BEAM_POSITION_69_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x1C51 | BEAM_POSITION_69_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x1C52 | BEAM_POSITION_69_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x1C53 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1C54 | BEAM_POSITION_69_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x1C55 | BEAM_POSITION_69_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x1C56 | BEAM_POSITION_69_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x1C57 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1C58 | BEAM_POSITION_69_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x1C59 | BEAM_POSITION_69_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x1C5A | BEAM_POSITION_69_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x1C5B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1C5C | BEAM_POSITION_69_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x1C5D | BEAM_POSITION_69_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x1C5E | BEAM_POSITION_69_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x1C5F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1C60 | BEAM_POSITION_70_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x1C61 | BEAM_POSITION_70_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x1C62 | BEAM_POSITION_70_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x1C63 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1C64 | BEAM_POSITION_70_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x1C65 | BEAM_POSITION_70_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x1C66 | BEAM_POSITION_70_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x1C67 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1C68 | BEAM_POSITION_70_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x1C69 | BEAM_POSITION_70_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x1C6A | BEAM_POSITION_70_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x1C6B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1C6C | BEAM_POSITION_70_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x1C6D | BEAM_POSITION_70_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x1C6E | BEAM_POSITION_70_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x1C6F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1C70 | BEAM_POSITION_71_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |

| Addr | Name | Bits | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
|------|------|------|-------|-------|-------|-------|-------|-------|-------|-------|
| 0x1C71 | BEAM_POSITION_71_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x1C72 | BEAM_POSITION_71_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x1C73 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1C74 | BEAM_POSITION_71_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x1C75 | BEAM_POSITION_71_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x1C76 | BEAM_POSITION_71_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x1C77 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1C78 | BEAM_POSITION_71_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x1C79 | BEAM_POSITION_71_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x1C7A | BEAM_POSITION_71_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x1C7B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1C7C | BEAM_POSITION_71_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x1C7D | BEAM_POSITION_71_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x1C7E | BEAM_POSITION_71_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x1C7F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1C80 | BEAM_POSITION_72_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x1C81 | BEAM_POSITION_72_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x1C82 | BEAM_POSITION_72_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x1C83 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1C84 | BEAM_POSITION_72_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x1C85 | BEAM_POSITION_72_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x1C86 | BEAM_POSITION_72_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x1C87 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1C88 | BEAM_POSITION_72_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x1C89 | BEAM_POSITION_72_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x1C8A | BEAM_POSITION_72_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x1C8B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1C8C | BEAM_POSITION_72_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x1C8D | BEAM_POSITION_72_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x1C8E | BEAM_POSITION_72_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x1C8F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1C90 | BEAM_POSITION_73_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x1C91 | BEAM_POSITION_73_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x1C92 | BEAM_POSITION_73_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x1C93 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1C94 | BEAM_POSITION_73_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x1C95 | BEAM_POSITION_73_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x1C96 | BEAM_POSITION_73_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x1C97 | RESERVED | [7:0] | RESERVED | | | | | | | |

| Addr | Name | Bits | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
|------|------|------|-------|-------|-------|-------|-------|-------|-------|-------|
| 0x1C98 | BEAM_POSITION_73_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x1C99 | BEAM_POSITION_73_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x1C9A | BEAM_POSITION_73_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x1C9B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1C9C | BEAM_POSITION_73_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x1C9D | BEAM_POSITION_73_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x1C9E | BEAM_POSITION_73_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x1C9F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1CA0 | BEAM_POSITION_74_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x1CA1 | BEAM_POSITION_74_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x1CA2 | BEAM_POSITION_74_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x1CA3 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1CA4 | BEAM_POSITION_74_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x1CA5 | BEAM_POSITION_74_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x1CA6 | BEAM_POSITION_74_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x1CA7 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1CA8 | BEAM_POSITION_74_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x1CA9 | BEAM_POSITION_74_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x1CAA | BEAM_POSITION_74_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x1CAB | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1CAC | BEAM_POSITION_74_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x1CAD | BEAM_POSITION_74_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x1CAE | BEAM_POSITION_74_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x1CAF | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1CB0 | BEAM_POSITION_75_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x1CB1 | BEAM_POSITION_75_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x1CB2 | BEAM_POSITION_75_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x1CB3 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1CB4 | BEAM_POSITION_75_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x1CB5 | BEAM_POSITION_75_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x1CB6 | BEAM_POSITION_75_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x1CB7 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1CB8 | BEAM_POSITION_75_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x1CB9 | BEAM_POSITION_75_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x1CBA | BEAM_POSITION_75_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x1CBB | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1CBC | BEAM_POSITION_75_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x1CBD | BEAM_POSITION_75_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x1CBE | BEAM_POSITION_75_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |

| Addr | Name | Bits | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
|---|---|---|---|---|---|---|---|---|---|---|
| 0x1CBF | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1CC0 | BEAM_POSITION_76_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x1CC1 | BEAM_POSITION_76_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x1CC2 | BEAM_POSITION_76_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x1CC3 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1CC4 | BEAM_POSITION_76_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x1CC5 | BEAM_POSITION_76_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x1CC6 | BEAM_POSITION_76_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x1CC7 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1CC8 | BEAM_POSITION_76_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x1CC9 | BEAM_POSITION_76_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x1CCA | BEAM_POSITION_76_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x1CCB | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1CCC | BEAM_POSITION_76_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x1CCD | BEAM_POSITION_76_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x1CCE | BEAM_POSITION_76_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x1CCF | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1CD0 | BEAM_POSITION_77_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x1CD1 | BEAM_POSITION_77_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x1CD2 | BEAM_POSITION_77_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x1CD3 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1CD4 | BEAM_POSITION_77_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x1CD5 | BEAM_POSITION_77_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x1CD6 | BEAM_POSITION_77_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x1CD7 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1CD8 | BEAM_POSITION_77_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x1CD9 | BEAM_POSITION_77_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x1CDA | BEAM_POSITION_77_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x1CDB | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1CDC | BEAM_POSITION_77_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x1CDD | BEAM_POSITION_77_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x1CDE | BEAM_POSITION_77_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x1CDF | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1CE0 | BEAM_POSITION_78_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x1CE1 | BEAM_POSITION_78_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x1CE2 | BEAM_POSITION_78_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x1CE3 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1CE4 | BEAM_POSITION_78_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x1CE5 | BEAM_POSITION_78_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |

| Addr | Name | Bits | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
|------|------|------|-------|-------|-------|-------|-------|-------|-------|-------|
| 0x1CE6 | BEAM_POSITION_78_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x1CE7 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1CE8 | BEAM_POSITION_78_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x1CE9 | BEAM_POSITION_78_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x1CEA | BEAM_POSITION_78_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x1CEB | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1CEC | BEAM_POSITION_78_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x1CED | BEAM_POSITION_78_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x1CEE | BEAM_POSITION_78_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x1CEF | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1CF0 | BEAM_POSITION_79_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x1CF1 | BEAM_POSITION_79_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x1CF2 | BEAM_POSITION_79_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x1CF3 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1CF4 | BEAM_POSITION_79_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x1CF5 | BEAM_POSITION_79_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x1CF6 | BEAM_POSITION_79_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x1CF7 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1CF8 | BEAM_POSITION_79_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x1CF9 | BEAM_POSITION_79_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x1CFA | BEAM_POSITION_79_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x1CFB | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1CFC | BEAM_POSITION_79_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x1CFD | BEAM_POSITION_79_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x1CFE | BEAM_POSITION_79_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x1CFF | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1D00 | BEAM_POSITION_80_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x1D01 | BEAM_POSITION_80_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x1D02 | BEAM_POSITION_80_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x1D03 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1D04 | BEAM_POSITION_80_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x1D05 | BEAM_POSITION_80_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x1D06 | BEAM_POSITION_80_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x1D07 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1D08 | BEAM_POSITION_80_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x1D09 | BEAM_POSITION_80_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x1D0A | BEAM_POSITION_80_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x1D0B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1D0C | BEAM_POSITION_80_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |

| Addr | Name | Bits | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
|------|------|------|-------|-------|-------|-------|-------|-------|-------|-------|
| 0x1D0D | BEAM_POSITION_80_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x1D0E | BEAM_POSITION_80_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x1D0F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1D10 | BEAM_POSITION_81_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x1D11 | BEAM_POSITION_81_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x1D12 | BEAM_POSITION_81_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x1D13 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1D14 | BEAM_POSITION_81_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x1D15 | BEAM_POSITION_81_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x1D16 | BEAM_POSITION_81_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x1D17 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1D18 | BEAM_POSITION_81_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x1D19 | BEAM_POSITION_81_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x1D1A | BEAM_POSITION_81_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x1D1B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1D1C | BEAM_POSITION_81_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x1D1D | BEAM_POSITION_81_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x1D1E | BEAM_POSITION_81_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x1D1F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1D20 | BEAM_POSITION_82_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x1D21 | BEAM_POSITION_82_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x1D22 | BEAM_POSITION_82_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x1D23 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1D24 | BEAM_POSITION_82_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x1D25 | BEAM_POSITION_82_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x1D26 | BEAM_POSITION_82_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x1D27 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1D28 | BEAM_POSITION_82_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x1D29 | BEAM_POSITION_82_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x1D2A | BEAM_POSITION_82_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x1D2B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1D2C | BEAM_POSITION_82_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x1D2D | BEAM_POSITION_82_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x1D2E | BEAM_POSITION_82_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x1D2F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1D30 | BEAM_POSITION_83_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x1D31 | BEAM_POSITION_83_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x1D32 | BEAM_POSITION_83_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x1D33 | RESERVED | [7:0] | RESERVED | | | | | | | |

| Addr | Name | Bits | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
|---|---|---|---|---|---|---|---|---|---|---|
| 0x1D34 | BEAM_POSITION_83_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x1D35 | BEAM_POSITION_83_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x1D36 | BEAM_POSITION_83_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x1D37 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1D38 | BEAM_POSITION_83_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x1D39 | BEAM_POSITION_83_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x1D3A | BEAM_POSITION_83_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x1D3B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1D3C | BEAM_POSITION_83_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x1D3D | BEAM_POSITION_83_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x1D3E | BEAM_POSITION_83_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x1D3F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1D40 | BEAM_POSITION_84_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x1D41 | BEAM_POSITION_84_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x1D42 | BEAM_POSITION_84_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x1D43 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1D44 | BEAM_POSITION_84_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x1D45 | BEAM_POSITION_84_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x1D46 | BEAM_POSITION_84_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x1D47 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1D48 | BEAM_POSITION_84_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x1D49 | BEAM_POSITION_84_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x1D4A | BEAM_POSITION_84_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x1D4B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1D4C | BEAM_POSITION_84_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x1D4D | BEAM_POSITION_84_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x1D4E | BEAM_POSITION_84_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x1D4F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1D50 | BEAM_POSITION_85_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x1D51 | BEAM_POSITION_85_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x1D52 | BEAM_POSITION_85_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x1D53 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1D54 | BEAM_POSITION_85_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x1D55 | BEAM_POSITION_85_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x1D56 | BEAM_POSITION_85_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x1D57 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1D58 | BEAM_POSITION_85_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x1D59 | BEAM_POSITION_85_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x1D5A | BEAM_POSITION_85_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |

| Addr | Name | Bits | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
|------|------|------|-------|-------|-------|-------|-------|-------|-------|-------|
| 0x1D5B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1D5C | BEAM_POSITION_85_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x1D5D | BEAM_POSITION_85_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x1D5E | BEAM_POSITION_85_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x1D5F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1D60 | BEAM_POSITION_86_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x1D61 | BEAM_POSITION_86_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x1D62 | BEAM_POSITION_86_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x1D63 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1D64 | BEAM_POSITION_86_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x1D65 | BEAM_POSITION_86_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x1D66 | BEAM_POSITION_86_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x1D67 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1D68 | BEAM_POSITION_86_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x1D69 | BEAM_POSITION_86_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x1D6A | BEAM_POSITION_86_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x1D6B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1D6C | BEAM_POSITION_86_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x1D6D | BEAM_POSITION_86_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x1D6E | BEAM_POSITION_86_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x1D6F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1D70 | BEAM_POSITION_87_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x1D71 | BEAM_POSITION_87_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x1D72 | BEAM_POSITION_87_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x1D73 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1D74 | BEAM_POSITION_87_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x1D75 | BEAM_POSITION_87_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x1D76 | BEAM_POSITION_87_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x1D77 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1D78 | BEAM_POSITION_87_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x1D79 | BEAM_POSITION_87_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x1D7A | BEAM_POSITION_87_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x1D7B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1D7C | BEAM_POSITION_87_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x1D7D | BEAM_POSITION_87_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x1D7E | BEAM_POSITION_87_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x1D7F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1D80 | BEAM_POSITION_88_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x1D81 | BEAM_POSITION_88_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |

| Addr | Name | Bits | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
|------|------|------|-------|-------|-------|-------|-------|-------|-------|-------|
| 0x1D82 | BEAM_POSITION_88_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x1D83 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1D84 | BEAM_POSITION_88_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x1D85 | BEAM_POSITION_88_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x1D86 | BEAM_POSITION_88_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x1D87 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1D88 | BEAM_POSITION_88_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x1D89 | BEAM_POSITION_88_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x1D8A | BEAM_POSITION_88_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x1D8B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1D8C | BEAM_POSITION_88_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x1D8D | BEAM_POSITION_88_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x1D8E | BEAM_POSITION_88_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x1D8F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1D90 | BEAM_POSITION_89_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x1D91 | BEAM_POSITION_89_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x1D92 | BEAM_POSITION_89_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x1D93 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1D94 | BEAM_POSITION_89_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x1D95 | BEAM_POSITION_89_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x1D96 | BEAM_POSITION_89_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x1D97 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1D98 | BEAM_POSITION_89_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x1D99 | BEAM_POSITION_89_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x1D9A | BEAM_POSITION_89_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x1D9B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1D9C | BEAM_POSITION_89_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x1D9D | BEAM_POSITION_89_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x1D9E | BEAM_POSITION_89_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x1D9F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1DA0 | BEAM_POSITION_90_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x1DA1 | BEAM_POSITION_90_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x1DA2 | BEAM_POSITION_90_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x1DA3 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1DA4 | BEAM_POSITION_90_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x1DA5 | BEAM_POSITION_90_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x1DA6 | BEAM_POSITION_90_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x1DA7 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1DA8 | BEAM_POSITION_90_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |

| Addr | Name | Bits | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
|---|---|---|---|---|---|---|---|---|---|---|
| 0x1DA9 | BEAM_POSITION_90_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x1DAA | BEAM_POSITION_90_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x1DAB | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1DAC | BEAM_POSITION_90_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x1DAD | BEAM_POSITION_90_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x1DAE | BEAM_POSITION_90_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x1DAF | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1DB0 | BEAM_POSITION_91_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x1DB1 | BEAM_POSITION_91_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x1DB2 | BEAM_POSITION_91_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x1DB3 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1DB4 | BEAM_POSITION_91_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x1DB5 | BEAM_POSITION_91_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x1DB6 | BEAM_POSITION_91_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x1DB7 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1DB8 | BEAM_POSITION_91_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x1DB9 | BEAM_POSITION_91_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x1DBA | BEAM_POSITION_91_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x1DBB | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1DBC | BEAM_POSITION_91_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x1DBD | BEAM_POSITION_91_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x1DBE | BEAM_POSITION_91_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x1DBF | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1DC0 | BEAM_POSITION_92_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x1DC1 | BEAM_POSITION_92_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x1DC2 | BEAM_POSITION_92_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x1DC3 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1DC4 | BEAM_POSITION_92_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x1DC5 | BEAM_POSITION_92_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x1DC6 | BEAM_POSITION_92_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x1DC7 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1DC8 | BEAM_POSITION_92_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x1DC9 | BEAM_POSITION_92_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x1DCA | BEAM_POSITION_92_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x1DCB | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1DCC | BEAM_POSITION_92_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x1DCD | BEAM_POSITION_92_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x1DCE | BEAM_POSITION_92_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x1DCF | RESERVED | [7:0] | RESERVED | | | | | | | |

| Addr | Name | Bits | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
|------|------|------|-------|-------|-------|-------|-------|-------|-------|-------|
| 0x1DD0 | BEAM_POSITION_93_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x1DD1 | BEAM_POSITION_93_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x1DD2 | BEAM_POSITION_93_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x1DD3 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1DD4 | BEAM_POSITION_93_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x1DD5 | BEAM_POSITION_93_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x1DD6 | BEAM_POSITION_93_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x1DD7 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1DD8 | BEAM_POSITION_93_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x1DD9 | BEAM_POSITION_93_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x1DDA | BEAM_POSITION_93_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x1DDB | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1DDC | BEAM_POSITION_93_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x1DDD | BEAM_POSITION_93_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x1DDE | BEAM_POSITION_93_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x1DDF | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1DE0 | BEAM_POSITION_94_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x1DE1 | BEAM_POSITION_94_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x1DE2 | BEAM_POSITION_94_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x1DE3 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1DE4 | BEAM_POSITION_94_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x1DE5 | BEAM_POSITION_94_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x1DE6 | BEAM_POSITION_94_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x1DE7 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1DE8 | BEAM_POSITION_94_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x1DE9 | BEAM_POSITION_94_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x1DEA | BEAM_POSITION_94_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x1DEB | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1DEC | BEAM_POSITION_94_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x1DED | BEAM_POSITION_94_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x1DEE | BEAM_POSITION_94_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x1DEF | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1DF0 | BEAM_POSITION_95_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x1DF1 | BEAM_POSITION_95_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x1DF2 | BEAM_POSITION_95_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x1DF3 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1DF4 | BEAM_POSITION_95_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x1DF5 | BEAM_POSITION_95_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x1DF6 | BEAM_POSITION_95_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |

| Addr | Name | Bits | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
|---|---|---|---|---|---|---|---|---|---|---|
| 0x1DF7 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1DF8 | BEAM_POSITION_95_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x1DF9 | BEAM_POSITION_95_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x1DFA | BEAM_POSITION_95_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x1DFB | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1DFC | BEAM_POSITION_95_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x1DFD | BEAM_POSITION_95_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x1DFE | BEAM_POSITION_95_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x1DFF | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1E00 | BEAM_POSITION_96_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x1E01 | BEAM_POSITION_96_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x1E02 | BEAM_POSITION_96_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x1E03 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1E04 | BEAM_POSITION_96_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x1E05 | BEAM_POSITION_96_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x1E06 | BEAM_POSITION_96_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x1E07 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1E08 | BEAM_POSITION_96_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x1E09 | BEAM_POSITION_96_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x1E0A | BEAM_POSITION_96_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x1E0B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1E0C | BEAM_POSITION_96_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x1E0D | BEAM_POSITION_96_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x1E0E | BEAM_POSITION_96_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x1E0F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1E10 | BEAM_POSITION_97_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x1E11 | BEAM_POSITION_97_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x1E12 | BEAM_POSITION_97_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x1E13 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1E14 | BEAM_POSITION_97_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x1E15 | BEAM_POSITION_97_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x1E16 | BEAM_POSITION_97_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x1E17 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1E18 | BEAM_POSITION_97_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x1E19 | BEAM_POSITION_97_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x1E1A | BEAM_POSITION_97_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x1E1B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1E1C | BEAM_POSITION_97_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x1E1D | BEAM_POSITION_97_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |

| Addr | Name | Bits | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
|------|------|------|-------|-------|-------|-------|-------|-------|-------|-------|
| 0x1E1E | BEAM_POSITION_97_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x1E1F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1E20 | BEAM_POSITION_98_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x1E21 | BEAM_POSITION_98_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x1E22 | BEAM_POSITION_98_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x1E23 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1E24 | BEAM_POSITION_98_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x1E25 | BEAM_POSITION_98_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x1E26 | BEAM_POSITION_98_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x1E27 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1E28 | BEAM_POSITION_98_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x1E29 | BEAM_POSITION_98_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x1E2A | BEAM_POSITION_98_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x1E2B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1E2C | BEAM_POSITION_98_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x1E2D | BEAM_POSITION_98_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x1E2E | BEAM_POSITION_98_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x1E2F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1E30 | BEAM_POSITION_99_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x1E31 | BEAM_POSITION_99_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x1E32 | BEAM_POSITION_99_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x1E33 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1E34 | BEAM_POSITION_99_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x1E35 | BEAM_POSITION_99_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x1E36 | BEAM_POSITION_99_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x1E37 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1E38 | BEAM_POSITION_99_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x1E39 | BEAM_POSITION_99_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x1E3A | BEAM_POSITION_99_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x1E3B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1E3C | BEAM_POSITION_99_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x1E3D | BEAM_POSITION_99_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x1E3E | BEAM_POSITION_99_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x1E3F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1E40 | BEAM_POSITION_100_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x1E41 | BEAM_POSITION_100_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x1E42 | BEAM_POSITION_100_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x1E43 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1E44 | BEAM_POSITION_100_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |

| Addr | Name | Bits | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
|------|------|------|-------|-------|-------|-------|-------|-------|-------|-------|
| 0x1E45 | BEAM_POSITION_100_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x1E46 | BEAM_POSITION_100_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x1E47 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1E48 | BEAM_POSITION_100_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x1E49 | BEAM_POSITION_100_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x1E4A | BEAM_POSITION_100_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x1E4B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1E4C | BEAM_POSITION_100_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x1E4D | BEAM_POSITION_100_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x1E4E | BEAM_POSITION_100_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x1E4F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1E50 | BEAM_POSITION_101_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x1E51 | BEAM_POSITION_101_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x1E52 | BEAM_POSITION_101_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x1E53 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1E54 | BEAM_POSITION_101_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x1E55 | BEAM_POSITION_101_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x1E56 | BEAM_POSITION_101_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x1E57 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1E58 | BEAM_POSITION_101_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x1E59 | BEAM_POSITION_101_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x1E5A | BEAM_POSITION_101_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x1E5B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1E5C | BEAM_POSITION_101_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x1E5D | BEAM_POSITION_101_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x1E5E | BEAM_POSITION_101_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x1E5F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1E60 | BEAM_POSITION_102_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x1E61 | BEAM_POSITION_102_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x1E62 | BEAM_POSITION_102_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x1E63 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1E64 | BEAM_POSITION_102_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x1E65 | BEAM_POSITION_102_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x1E66 | BEAM_POSITION_102_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x1E67 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1E68 | BEAM_POSITION_102_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x1E69 | BEAM_POSITION_102_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x1E6A | BEAM_POSITION_102_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x1E6B | RESERVED | [7:0] | RESERVED | | | | | | | |

| Addr | Name | Bits | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
|------|------|------|-------|-------|-------|-------|-------|-------|-------|-------|
| 0x1E6C | BEAM_POSITION_102_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x1E6D | BEAM_POSITION_102_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x1E6E | BEAM_POSITION_102_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x1E6F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1E70 | BEAM_POSITION_103_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x1E71 | BEAM_POSITION_103_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x1E72 | BEAM_POSITION_103_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x1E73 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1E74 | BEAM_POSITION_103_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x1E75 | BEAM_POSITION_103_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x1E76 | BEAM_POSITION_103_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x1E77 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1E78 | BEAM_POSITION_103_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x1E79 | BEAM_POSITION_103_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x1E7A | BEAM_POSITION_103_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x1E7B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1E7C | BEAM_POSITION_103_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x1E7D | BEAM_POSITION_103_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x1E7E | BEAM_POSITION_103_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x1E7F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1E80 | BEAM_POSITION_104_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x1E81 | BEAM_POSITION_104_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x1E82 | BEAM_POSITION_104_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x1E83 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1E84 | BEAM_POSITION_104_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x1E85 | BEAM_POSITION_104_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x1E86 | BEAM_POSITION_104_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x1E87 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1E88 | BEAM_POSITION_104_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x1E89 | BEAM_POSITION_104_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x1E8A | BEAM_POSITION_104_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x1E8B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1E8C | BEAM_POSITION_104_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x1E8D | BEAM_POSITION_104_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x1E8E | BEAM_POSITION_104_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x1E8F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1E90 | BEAM_POSITION_105_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x1E91 | BEAM_POSITION_105_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x1E92 | BEAM_POSITION_105_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |

| Addr | Name | Bits | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
|---|---|---|---|---|---|---|---|---|---|---|
| 0x1E93 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1E94 | BEAM_POSITION_105_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x1E95 | BEAM_POSITION_105_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x1E96 | BEAM_POSITION_105_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x1E97 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1E98 | BEAM_POSITION_105_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x1E99 | BEAM_POSITION_105_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x1E9A | BEAM_POSITION_105_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x1E9B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1E9C | BEAM_POSITION_105_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x1E9D | BEAM_POSITION_105_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x1E9E | BEAM_POSITION_105_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x1E9F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1EA0 | BEAM_POSITION_106_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x1EA1 | BEAM_POSITION_106_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x1EA2 | BEAM_POSITION_106_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x1EA3 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1EA4 | BEAM_POSITION_106_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x1EA5 | BEAM_POSITION_106_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x1EA6 | BEAM_POSITION_106_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x1EA7 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1EA8 | BEAM_POSITION_106_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x1EA9 | BEAM_POSITION_106_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x1EAA | BEAM_POSITION_106_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x1EAB | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1EAC | BEAM_POSITION_106_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x1EAD | BEAM_POSITION_106_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x1EAE | BEAM_POSITION_106_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x1EAF | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1EB0 | BEAM_POSITION_107_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x1EB1 | BEAM_POSITION_107_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x1EB2 | BEAM_POSITION_107_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x1EB3 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1EB4 | BEAM_POSITION_107_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x1EB5 | BEAM_POSITION_107_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x1EB6 | BEAM_POSITION_107_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x1EB7 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1EB8 | BEAM_POSITION_107_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x1EB9 | BEAM_POSITION_107_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |

| Addr | Name | Bits | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
|---|---|---|---|---|---|---|---|---|---|---|
| 0x1EBA | BEAM_POSITION_107_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x1EBB | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1EBC | BEAM_POSITION_107_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x1EBD | BEAM_POSITION_107_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x1EBE | BEAM_POSITION_107_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x1EBF | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1EC0 | BEAM_POSITION_108_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x1EC1 | BEAM_POSITION_108_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x1EC2 | BEAM_POSITION_108_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x1EC3 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1EC4 | BEAM_POSITION_108_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x1EC5 | BEAM_POSITION_108_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x1EC6 | BEAM_POSITION_108_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x1EC7 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1EC8 | BEAM_POSITION_108_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x1EC9 | BEAM_POSITION_108_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x1ECA | BEAM_POSITION_108_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x1ECB | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1ECC | BEAM_POSITION_108_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x1ECD | BEAM_POSITION_108_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x1ECE | BEAM_POSITION_108_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x1ECF | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1ED0 | BEAM_POSITION_109_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x1ED1 | BEAM_POSITION_109_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x1ED2 | BEAM_POSITION_109_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x1ED3 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1ED4 | BEAM_POSITION_109_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x1ED5 | BEAM_POSITION_109_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x1ED6 | BEAM_POSITION_109_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x1ED7 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1ED8 | BEAM_POSITION_109_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x1ED9 | BEAM_POSITION_109_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x1EDA | BEAM_POSITION_109_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x1EDB | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1EDC | BEAM_POSITION_109_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x1EDD | BEAM_POSITION_109_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x1EDE | BEAM_POSITION_109_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x1EDF | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1EE0 | BEAM_POSITION_110_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |

| Addr | Name | Bits | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
|---|---|---|---|---|---|---|---|---|---|---|
| 0x1EE1 | BEAM_POSITION_110_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x1EE2 | BEAM_POSITION_110_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x1EE3 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1EE4 | BEAM_POSITION_110_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x1EE5 | BEAM_POSITION_110_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x1EE6 | BEAM_POSITION_110_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x1EE7 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1EE8 | BEAM_POSITION_110_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x1EE9 | BEAM_POSITION_110_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x1EEA | BEAM_POSITION_110_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x1EEB | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1EEC | BEAM_POSITION_110_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x1EED | BEAM_POSITION_110_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x1EEE | BEAM_POSITION_110_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x1EEF | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1EF0 | BEAM_POSITION_111_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x1EF1 | BEAM_POSITION_111_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x1EF2 | BEAM_POSITION_111_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x1EF3 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1EF4 | BEAM_POSITION_111_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x1EF5 | BEAM_POSITION_111_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x1EF6 | BEAM_POSITION_111_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x1EF7 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1EF8 | BEAM_POSITION_111_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x1EF9 | BEAM_POSITION_111_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x1EFA | BEAM_POSITION_111_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x1EFB | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1EFC | BEAM_POSITION_111_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x1EFD | BEAM_POSITION_111_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x1EFE | BEAM_POSITION_111_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x1EFF | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1F00 | BEAM_POSITION_112_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x1F01 | BEAM_POSITION_112_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x1F02 | BEAM_POSITION_112_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x1F03 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1F04 | BEAM_POSITION_112_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x1F05 | BEAM_POSITION_112_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x1F06 | BEAM_POSITION_112_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x1F07 | RESERVED | [7:0] | RESERVED | | | | | | | |

| Addr | Name | Bits | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
|---|---|---|---|---|---|---|---|---|---|---|
| 0x1F08 | BEAM_POSITION_112_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x1F09 | BEAM_POSITION_112_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x1F0A | BEAM_POSITION_112_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x1F0B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1F0C | BEAM_POSITION_112_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x1F0D | BEAM_POSITION_112_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x1F0E | BEAM_POSITION_112_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x1F0F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1F10 | BEAM_POSITION_113_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x1F11 | BEAM_POSITION_113_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x1F12 | BEAM_POSITION_113_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x1F13 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1F14 | BEAM_POSITION_113_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x1F15 | BEAM_POSITION_113_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x1F16 | BEAM_POSITION_113_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x1F17 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1F18 | BEAM_POSITION_113_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x1F19 | BEAM_POSITION_113_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x1F1A | BEAM_POSITION_113_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x1F1B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1F1C | BEAM_POSITION_113_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x1F1D | BEAM_POSITION_113_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x1F1E | BEAM_POSITION_113_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x1F1F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1F20 | BEAM_POSITION_114_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x1F21 | BEAM_POSITION_114_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x1F22 | BEAM_POSITION_114_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x1F23 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1F24 | BEAM_POSITION_114_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x1F25 | BEAM_POSITION_114_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x1F26 | BEAM_POSITION_114_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x1F27 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1F28 | BEAM_POSITION_114_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x1F29 | BEAM_POSITION_114_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x1F2A | BEAM_POSITION_114_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x1F2B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1F2C | BEAM_POSITION_114_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x1F2D | BEAM_POSITION_114_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x1F2E | BEAM_POSITION_114_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |

| Addr | Name | Bits | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
|------|------|------|-------|-------|-------|-------|-------|-------|-------|-------|
| 0x1F2F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1F30 | BEAM_POSITION_115_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x1F31 | BEAM_POSITION_115_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x1F32 | BEAM_POSITION_115_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x1F33 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1F34 | BEAM_POSITION_115_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x1F35 | BEAM_POSITION_115_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x1F36 | BEAM_POSITION_115_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x1F37 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1F38 | BEAM_POSITION_115_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x1F39 | BEAM_POSITION_115_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x1F3A | BEAM_POSITION_115_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x1F3B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1F3C | BEAM_POSITION_115_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x1F3D | BEAM_POSITION_115_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x1F3E | BEAM_POSITION_115_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x1F3F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1F40 | BEAM_POSITION_116_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x1F41 | BEAM_POSITION_116_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x1F42 | BEAM_POSITION_116_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x1F43 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1F44 | BEAM_POSITION_116_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x1F45 | BEAM_POSITION_116_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x1F46 | BEAM_POSITION_116_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x1F47 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1F48 | BEAM_POSITION_116_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x1F49 | BEAM_POSITION_116_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x1F4A | BEAM_POSITION_116_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x1F4B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1F4C | BEAM_POSITION_116_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x1F4D | BEAM_POSITION_116_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x1F4E | BEAM_POSITION_116_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x1F4F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1F50 | BEAM_POSITION_117_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x1F51 | BEAM_POSITION_117_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x1F52 | BEAM_POSITION_117_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x1F53 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1F54 | BEAM_POSITION_117_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x1F55 | BEAM_POSITION_117_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |

| Addr | Name | Bits | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
|---|---|---|---|---|---|---|---|---|---|---|
| 0x1F56 | BEAM_POSITION_117_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x1F57 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1F58 | BEAM_POSITION_117_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x1F59 | BEAM_POSITION_117_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x1F5A | BEAM_POSITION_117_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x1F5B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1F5C | BEAM_POSITION_117_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x1F5D | BEAM_POSITION_117_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x1F5E | BEAM_POSITION_117_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x1F5F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1F60 | BEAM_POSITION_118_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x1F61 | BEAM_POSITION_118_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x1F62 | BEAM_POSITION_118_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x1F63 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1F64 | BEAM_POSITION_118_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x1F65 | BEAM_POSITION_118_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x1F66 | BEAM_POSITION_118_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x1F67 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1F68 | BEAM_POSITION_118_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x1F69 | BEAM_POSITION_118_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x1F6A | BEAM_POSITION_118_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x1F6B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1F6C | BEAM_POSITION_118_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x1F6D | BEAM_POSITION_118_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x1F6E | BEAM_POSITION_118_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x1F6F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1F70 | BEAM_POSITION_119_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x1F71 | BEAM_POSITION_119_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x1F72 | BEAM_POSITION_119_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x1F73 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1F74 | BEAM_POSITION_119_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x1F75 | BEAM_POSITION_119_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x1F76 | BEAM_POSITION_119_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x1F77 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1F78 | BEAM_POSITION_119_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x1F79 | BEAM_POSITION_119_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x1F7A | BEAM_POSITION_119_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x1F7B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1F7C | BEAM_POSITION_119_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |

| Addr | Name | Bits | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
|---|---|---|---|---|---|---|---|---|---|---|
| 0x1F7D | BEAM_POSITION_119_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x1F7E | BEAM_POSITION_119_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x1F7F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1F80 | BEAM_POSITION_120_CH1_TX_GAIN | [7:0] | CH1_ATTN_TX | TX_VGA_CH1 | | | | | | |
| 0x1F81 | BEAM_POSITION_120_CH1_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH1_POL_I | TX_VM_CH1_GAIN_I | | | | |
| 0x1F82 | BEAM_POSITION_120_CH1_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH1_POL_Q | TX_VM_CH1_GAIN_Q | | | | |
| 0x1F83 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1F84 | BEAM_POSITION_120_CH2_TX_GAIN | [7:0] | CH2_ATTN_TX | TX_VGA_CH2 | | | | | | |
| 0x1F85 | BEAM_POSITION_120_CH2_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH2_POL_I | TX_VM_CH2_GAIN_I | | | | |
| 0x1F86 | BEAM_POSITION_120_CH2_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH2_POL_Q | TX_VM_CH2_GAIN_Q | | | | |
| 0x1F87 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1F88 | BEAM_POSITION_120_CH3_TX_GAIN | [7:0] | CH3_ATTN_TX | TX_VGA_CH3 | | | | | | |
| 0x1F89 | BEAM_POSITION_120_CH3_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH3_POL_I | TX_VM_CH3_GAIN_I | | | | |
| 0x1F8A | BEAM_POSITION_120_CH3_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH3_POL_Q | TX_VM_CH3_GAIN_Q | | | | |
| 0x1F8B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1F8C | BEAM_POSITION_120_CH4_TX_GAIN | [7:0] | CH4_ATTN_TX | TX_VGA_CH4 | | | | | | |
| 0x1F8D | BEAM_POSITION_120_CH4_TX_PHASE_I | [7:0] | RESERVED | | TX_VM_CH4_POL_I | TX_VM_CH4_GAIN_I | | | | |
| 0x1F8E | BEAM_POSITION_120_CH4_TX_PHASE_Q | [7:0] | RESERVED | | TX_VM_CH4_POL_Q | TX_VM_CH4_GAIN_Q | | | | |
| 0x1F8F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1F90 | TX_BIASSETTING_1_CH1_PA_BIAS_OFF | [7:0] | EXT_PA1_BIAS_OFF | | | | | | | |
| 0x1F91 | TX_BIASSETTING_1_CH2_PA_BIAS_OFF | [7:0] | EXT_PA2_BIAS_OFF | | | | | | | |
| 0x1F92 | TX_BIASSETTING_1_CH3_PA_BIAS_OFF | [7:0] | EXT_PA3_BIAS_OFF | | | | | | | |
| 0x1F93 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1F94 | TX_BIASSETTING_1_CH1_PA_BIAS_ON | [7:0] | EXT_PA1_BIAS_ON | | | | | | | |
| 0x1F95 | TX_BIASSETTING_1_CH2_PA_BIAS_ON | [7:0] | EXT_PA2_BIAS_ON | | | | | | | |
| 0x1F96 | TX_BIASSETTING_1_CH3_PA_BIAS_ON | [7:0] | EXT_PA3_BIAS_ON | | | | | | | |
| 0x1F97 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1F98 | TX_BIASSETTING_1_CH4_PA_BIAS_OFF | [7:0] | EXT_PA4_BIAS_OFF | | | | | | | |
| 0x1F99 | TX_BIASSETTING_1_CH4_PA_BIAS_ON | [7:0] | EXT_PA4_BIAS_ON | | | | | | | |
| 0x1F9A | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1F9B | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1F9C | TX_BIASSETTING_1_BIAS_CURRENT_TX | [7:0] | RESERVED | TX_VM_BIAS | | | TX_VGA_BIAS | | | |
| 0x1F9D | TX_BIASSETTING_1_BIAS_CURRENT_TX_DRV | [7:0] | RESERVED | | | | TX_DRV_BIAS | | | |
| 0x1F9E | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1F9F | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1FA0 | TX_BIASSETTING_2_CH1_PA_BIAS_OFF | [7:0] | EXT_PA1_BIAS_OFF | | | | | | | |
| 0x1FA1 | TX_BIASSETTING_2_CH2_PA_BIAS_OFF | [7:0] | EXT_PA2_BIAS_OFF | | | | | | | |
| 0x1FA2 | TX_BIASSETTING_2_CH3_PA_BIAS_OFF | [7:0] | EXT_PA3_BIAS_OFF | | | | | | | |
| 0x1FA3 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1FA4 | TX_BIASSETTING_2_CH1_PA_BIAS_ON | [7:0] | EXT_PA1_BIAS_ON | | | | | | | |

| Addr | Name | Bits | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
|---|---|---|---|---|---|---|---|---|---|---|
| 0x1FA5 | TX_BIASSETTING_2_CH2_PA_BIAS_ON | [7:0] | EXT_PA2_BIAS_ON | | | | | | | |
| 0x1FA6 | TX_BIASSETTING_2_CH3_PA_BIAS_ON | [7:0] | EXT_PA3_BIAS_ON | | | | | | | |
| 0x1FA7 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1FA8 | TX_BIASSETTING_2_CH4_PA_BIAS_OFF | [7:0] | EXT_PA4_BIAS_OFF | | | | | | | |
| 0x1FA9 | TX_BIASSETTING_2_CH4_PA_BIAS_ON | [7:0] | EXT_PA4_BIAS_ON | | | | | | | |
| 0x1FAA | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1FAB | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1FAC | TX_BIASSETTING_2_BIAS_CURRENT_TX | [7:0] | RESERVED | TX_VM_BIAS | | | TX_VGA_BIAS | | | |
| 0x1FAD | TX_BIASSETTING_2_BIAS_CURRENT_TX_DRV | [7:0] | RESERVED | | | | | TX_DRV_BIAS | | |
| 0x1FAE | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1FAF | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1FB0 | TX_BIASSETTING_3_CH1_PA_BIAS_OFF | [7:0] | EXT_PA1_BIAS_OFF | | | | | | | |
| 0x1FB1 | TX_BIASSETTING_3_CH2_PA_BIAS_OFF | [7:0] | EXT_PA2_BIAS_OFF | | | | | | | |
| 0x1FB2 | TX_BIASSETTING_3_CH3_PA_BIAS_OFF | [7:0] | EXT_PA3_BIAS_OFF | | | | | | | |
| 0x1FB3 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1FB4 | TX_BIASSETTING_3_CH1_PA_BIAS_ON | [7:0] | EXT_PA1_BIAS_ON | | | | | | | |
| 0x1FB5 | TX_BIASSETTING_3_CH2_PA_BIAS_ON | [7:0] | EXT_PA2_BIAS_ON | | | | | | | |
| 0x1FB6 | TX_BIASSETTING_3_CH3_PA_BIAS_ON | [7:0] | EXT_PA3_BIAS_ON | | | | | | | |
| 0x1FB7 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1FB8 | TX_BIASSETTING_3_CH4_PA_BIAS_OFF | [7:0] | EXT_PA4_BIAS_OFF | | | | | | | |
| 0x1FB9 | TX_BIASSETTING_3_CH4_PA_BIAS_ON | [7:0] | EXT_PA4_BIAS_ON | | | | | | | |
| 0x1FBA | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1FBB | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1FBC | TX_BIASSETTING_3_BIAS_CURRENT_TX | [7:0] | RESERVED | TX_VM_BIAS | | | TX_VGA_BIAS | | | |
| 0x1FBD | TX_BIASSETTING_3_BIAS_CURRENT_TX_DRV | [7:0] | RESERVED | | | | | TX_DRV_BIAS | | |
| 0x1FBE | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1FBF | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1FC0 | TX_BIASSETTING_4_CH1_PA_BIAS_OFF | [7:0] | EXT_PA1_BIAS_OFF | | | | | | | |
| 0x1FC1 | TX_BIASSETTING_4_CH2_PA_BIAS_OFF | [7:0] | EXT_PA2_BIAS_OFF | | | | | | | |
| 0x1FC2 | TX_BIASSETTING_4_CH3_PA_BIAS_OFF | [7:0] | EXT_PA3_BIAS_OFF | | | | | | | |
| 0x1FC3 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1FC4 | TX_BIASSETTING_4_CH1_PA_BIAS_ON | [7:0] | EXT_PA1_BIAS_ON | | | | | | | |
| 0x1FC5 | TX_BIASSETTING_4_CH2_PA_BIAS_ON | [7:0] | EXT_PA2_BIAS_ON | | | | | | | |
| 0x1FC6 | TX_BIASSETTING_4_CH3_PA_BIAS_ON | [7:0] | EXT_PA3_BIAS_ON | | | | | | | |
| 0x1FC7 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1FC8 | TX_BIASSETTING_4_CH4_PA_BIAS_OFF | [7:0] | EXT_PA4_BIAS_OFF | | | | | | | |
| 0x1FC9 | TX_BIASSETTING_4_CH4_PA_BIAS_ON | [7:0] | EXT_PA4_BIAS_ON | | | | | | | |
| 0x1FCA | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1FCB | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1FCC | TX_BIASSETTING_4_BIAS_CURRENT_TX | [7:0] | RESERVED | TX_VM_BIAS | | | TX_VGA_BIAS | | | |
| 0x1FCD | TX_BIASSETTING_4_BIAS_CURRENT_TX_DRV | [7:0] | RESERVED | | | | | TX_DRV_BIAS | | |
| 0x1FCE | RESERVED | [7:0] | RESERVED | | | | | | | |

| Addr | Name | Bits | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
|---|---|---|---|---|---|---|---|---|---|---|
| 0x1FCF | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1FD0 | TX_BIASSETTING_5_CH1_PA_BIAS_OFF | [7:0] | EXT_PA1_BIAS_OFF | | | | | | | |
| 0x1FD1 | TX_BIASSETTING_5_CH2_PA_BIAS_OFF | [7:0] | EXT_PA2_BIAS_OFF | | | | | | | |
| 0x1FD2 | TX_BIASSETTING_5_CH3_PA_BIAS_OFF | [7:0] | EXT_PA3_BIAS_OFF | | | | | | | |
| 0x1FD3 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1FD4 | TX_BIASSETTING_5_CH1_PA_BIAS_ON | [7:0] | EXT_PA1_BIAS_ON | | | | | | | |
| 0x1FD5 | TX_BIASSETTING_5_CH2_PA_BIAS_ON | [7:0] | EXT_PA2_BIAS_ON | | | | | | | |
| 0x1FD6 | TX_BIASSETTING_5_CH3_PA_BIAS_ON | [7:0] | EXT_PA3_BIAS_ON | | | | | | | |
| 0x1FD7 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1FD8 | TX_BIASSETTING_5_CH4_PA_BIAS_OFF | [7:0] | EXT_PA4_BIAS_OFF | | | | | | | |
| 0x1FD9 | TX_BIASSETTING_5_CH4_PA_BIAS_ON | [7:0] | EXT_PA4_BIAS_ON | | | | | | | |
| 0x1FDA | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1FDB | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1FDC | TX_BIASSETTING_5_BIAS_CURRENT_TX | [7:0] | RESERVED | TX_VM_BIAS | | | TX_VGA_BIAS | | | |
| 0x1FDD | TX_BIASSETTING_5_BIAS_CURRENT_TX_DRV | [7:0] | RESERVED | | | | | TX_DRV_BIAS | | |
| 0x1FDE | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1FDF | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1FE0 | TX_BIASSETTING_6_CH1_PA_BIAS_OFF | [7:0] | EXT_PA1_BIAS_OFF | | | | | | | |
| 0x1FE1 | TX_BIASSETTING_6_CH2_PA_BIAS_OFF | [7:0] | EXT_PA2_BIAS_OFF | | | | | | | |
| 0x1FE2 | TX_BIASSETTING_6_CH3_PA_BIAS_OFF | [7:0] | EXT_PA3_BIAS_OFF | | | | | | | |
| 0x1FE3 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1FE4 | TX_BIASSETTING_6_CH1_PA_BIAS_ON | [7:0] | EXT_PA1_BIAS_ON | | | | | | | |
| 0x1FE5 | TX_BIASSETTING_6_CH2_PA_BIAS_ON | [7:0] | EXT_PA2_BIAS_ON | | | | | | | |
| 0x1FE6 | TX_BIASSETTING_6_CH3_PA_BIAS_ON | [7:0] | EXT_PA3_BIAS_ON | | | | | | | |
| 0x1FE7 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1FE8 | TX_BIASSETTING_6_CH4_PA_BIAS_OFF | [7:0] | EXT_PA4_BIAS_OFF | | | | | | | |
| 0x1FE9 | TX_BIASSETTING_6_CH4_PA_BIAS_ON | [7:0] | EXT_PA4_BIAS_ON | | | | | | | |
| 0x1FEA | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1FEB | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1FEC | TX_BIASSETTING_6_BIAS_CURRENT_TX | [7:0] | RESERVED | TX_VM_BIAS | | | TX_VGA_BIAS | | | |
| 0x1FED | TX_BIASSETTING_6_BIAS_CURRENT_TX_DRV | [7:0] | RESERVED | | | | | TX_DRV_BIAS | | |
| 0x1FEE | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1FEF | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1FF0 | TX_BIASSETTING_7_CH1_PA_BIAS_OFF | [7:0] | EXT_PA1_BIAS_OFF | | | | | | | |
| 0x1FF1 | TX_BIASSETTING_7_CH2_PA_BIAS_OFF | [7:0] | EXT_PA2_BIAS_OFF | | | | | | | |
| 0x1FF2 | TX_BIASSETTING_7_CH3_PA_BIAS_OFF | [7:0] | EXT_PA3_BIAS_OFF | | | | | | | |
| 0x1FF3 | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1FF4 | TX_BIASSETTING_7_CH1_PA_BIAS_ON | [7:0] | EXT_PA1_BIAS_ON | | | | | | | |
| 0x1FF5 | TX_BIASSETTING_7_CH2_PA_BIAS_ON | [7:0] | EXT_PA2_BIAS_ON | | | | | | | |
| 0x1FF6 | TX_BIASSETTING_7_CH3_PA_BIAS_ON | [7:0] | EXT_PA3_BIAS_ON | | | | | | | |
| 0x1FF7 | RESERVED | [7:0] | RESERVED | | | | | | | |

| Addr | Name | Bits | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
|---|---|---|---|---|---|---|---|---|---|---|
| 0x1FF8 | TX_BIASSETTING_7_CH4_PA_BIAS_OFF | [7:0] | EXT_PA4_BIAS_OFF | | | | | | | |
| 0x1FF9 | TX_BIASSETTING_7_CH4_PA_BIAS_ON | [7:0] | EXT_PA4_BIAS_ON | | | | | | | |
| 0x1FFA | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1FFB | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1FFC | TX_BIASSETTING_7_BIAS_CURRENT_TX | [7:0] | RESERVED | TX_VM_BIAS | | | TX_VGA_BIAS | | | |
| 0x1FFD | TX_BIASSETTING_7_BIAS_CURRENT_TX_DRV | [7:0] | RESERVED | | | | | TX_DRV_BIAS | | |
| 0x1FFE | RESERVED | [7:0] | RESERVED | | | | | | | |
| 0x1FFF | RESERVED | [7:0] | RESERVED | | | | | | | |