

Figure 3. Evaluation Board Circuit Diagram (Page 1)

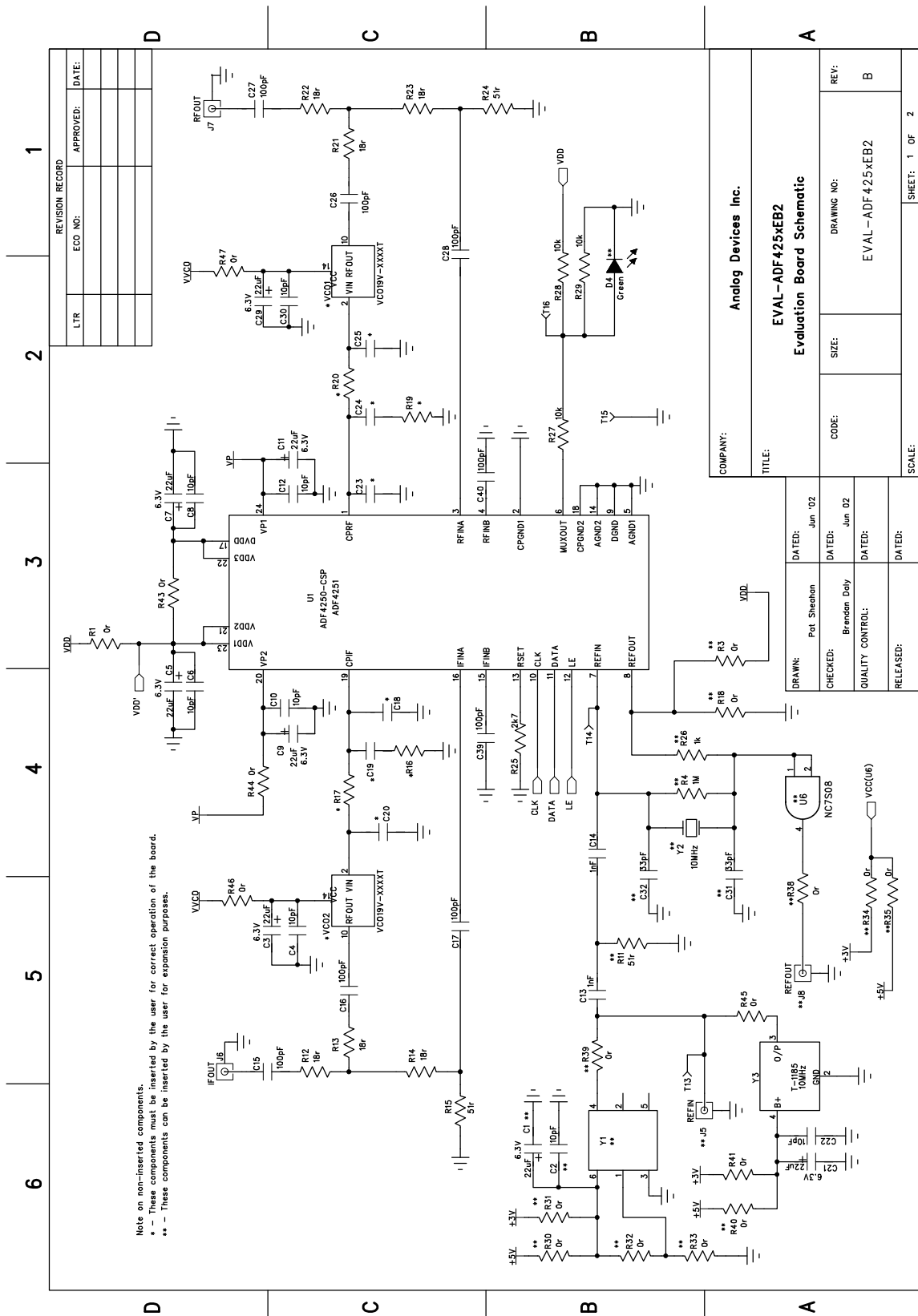


Figure 4. Evaluation Board Circuit Diagram (Page 2)