

PLATED: +/- .003
HOLE TOLERANCE
UNLESS SPECIFIED
NON PLATED: +/- .001

DRILL CHART: TOP to BOTTOM			
ALL UNITS ARE IN MILLIMETERS			
FIGURE	SIZE	PLATED	QTY
B	0.2032	PLATED	50
C	0.254	PLATED	47
D	0.254	PLATED	570
E	0.4064	PLATED	794
F	0.7874	PLATED	60
G	0.889	PLATED	4
H	0.9906	PLATED	10
J	1.016	PLATED	6
L	1.6002	PLATED	18
M	1.905	PLATED	4
N	0.7112	NON-PLATED	1
R	1.0922	NON-PLATED	1
S	1.1938	NON-PLATED	1
Ⓢ	2.1082	NON-PLATED	6
Ⓣ	3.175	NON-PLATED	1

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED
X	INITIAL RELEASE	05MAR14	CIARAN
B	Removed clamp holes, added SDP hole,	26NOV14	PSheahan

NOTES:

ACCEPTABILITY; PER ANALOG DEVICES, INC. SPECIFICATION TST00115 LATEST REVISION.
MATERIALS; ROGERS 4003(8 MILS) AND RF4 COMPOSITE FOR RF BUILD. SEE LAYER STACK-UP FOR MORE DETAIL.
BONDING AGENT; PREIMPREGNATED B STAGE EPOXY GLASS CLOTH IN ACCORDANCE WITH IPC-L-109 (LATEST REV.).
CLADDING; EXTERNAL LAYERS 1/2 OZ. AFTER PLATING 53UM TOTAL.
INTERNAL SIGNAL LAYERS 1 OZ COPPER.
INTERNAL PLANE LAYERS 1 OZ COPPER.
SOLDER MASK; SHALL BE LIQUID PHOTOIMAGABLE (LPI) APPLIED ON BOTH SIDES OVER BARE COPPER AND SHALL MEET IPC-SM-840 (LATEST REV.) CLASS 3.
SILK SCREEN; SHALL BE PERMANENT NON-CONDUCTIVE EPOXY INK, COLOR WHITE.
U.L. RATING; 94VO MINIMUM.

FABRICATION:

- REFER TO IPC-6010 SERIES (LATEST REV.), CLASS 2 FOR FABRICATION UNLESS OTHERWISE SPECIFIED.
- UNDIMENSIONED HOLES TO BE LOCATED WITHIN +/- .005 OF THEIR TRUE POSITION WITH RESPECT TO ARTWORK.
- PLATED HOLE WALL THICKNESS SHALL NOT BE LESS THAN .001 INCH MINIMUM AVERAGE, WITH NO READING LESS THAN .0008 BY CROSS SECTION.
- HOLE DIAMETERS APPLY AFTER PLATING.
- FINISHED CONDUCTOR WIDTHS SHALL NOT BE REDUCED FROM THE NOMINAL, INDICATED ON THE MASTER PATTERN, BY MORE THAN THE CONDUCTOR THICKNESS.
- MINIMUM DESIGN LINE WIDTH IS .008 INCH.
- MINIMUM DESIGN SPACING IS .008 INCH.
- BOARD/PANEL MUST MEET IPC-A-600 (LATEST REV.) CLASS 2 FOR FLATNESS.
- MFGR. TO LEGIBLY ETCH OR STAMP/SCREEN WITH PERMANENT NON-CONDUCTIVE INK ON SECONDARY SIDE IN A CLEAR AREA UNLESS OTHERWISE INDICATED;
A. U.L. CODE
B. DATE CODE (STAMP)
C. FLAMMABILITY RATING
D. MFGR. LOGO - DESIGNATED AREA,
E. SUCCESSFUL ELECTRICAL BOARD TEST.
F. LOT NUMBER - DESIGNATED AREA.
- NON-FUNCTIONAL PADS MAY BE REMOVED FROM INNER SIGNAL LAYERS AT MFGR. DISCRETION.
- IF PAD SIZES PROVIDED ARE NOT LARGE ENOUGH TO MAINTAIN ANNULAR RING REQUIREMENT, MFGR. MAY TEAR DROP PADS TO MAINTAIN ANNULAR RING AT PAD TO CIRCUIT INTERFACE ONLY AND MUST INSURE ELECTRICAL INTEGRITY. REPAIRS PER IPC-R-700 ARE ALLOWED.
- MODIFICATIONS TO THE ARTWORK, OTHER THAN THOSE DESCRIBED ON THE FABRICATION DRAWING, ARE NOT ALLOWED WITHOUT WRITTEN AUTHORIZATION.
- SURFACE FINISH TOP AND BOTTOM IS ENIG.
- FINISHED BOARD MUST BE ROHS COMPLIANT.
- THE VIAS IN THE VIA FILL AREA MUST BE FILLED WITH NON-CONDUCTIVE EPOXY, SANDED FLUSH AND PLATED OVER. THE FINISHED PADS MUST BE FLAT AND CO-PLANAR.THIS INCLUDES THE BLIND AND THRU VIAS - REALLY IMPORTANT.
- THE BLIND VIAS GO FROM LAYER 1 TO LAYER 2.
- NOTE THE SIX HOLES AT THE EDGE OF THE BOARD THAT WILL BE A HALF MOON AND PLATED.

4 LAYER STACKUP

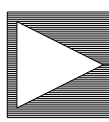
	PRIMARY SILKSCREEN
	PRIMARY SOLDER MASK
	LYR 1 - SIG 50ohm(TOP)- [Start Cu=17um, After Plating=53um or 1.5oz]
8mil ROGERS 4003	LYR 2 - GND Plane - [Cu = 17um]
CORE FR4	LYR 3 - PWR Plane - [Cu = 17um]
8mil ROGERS 4003	LYR 4 - SIG (BOT)- [Start Cu=17um, After Plating=53um]
	SECONDARY SOLDER MASK
	SECONDARY SILKSCREEN

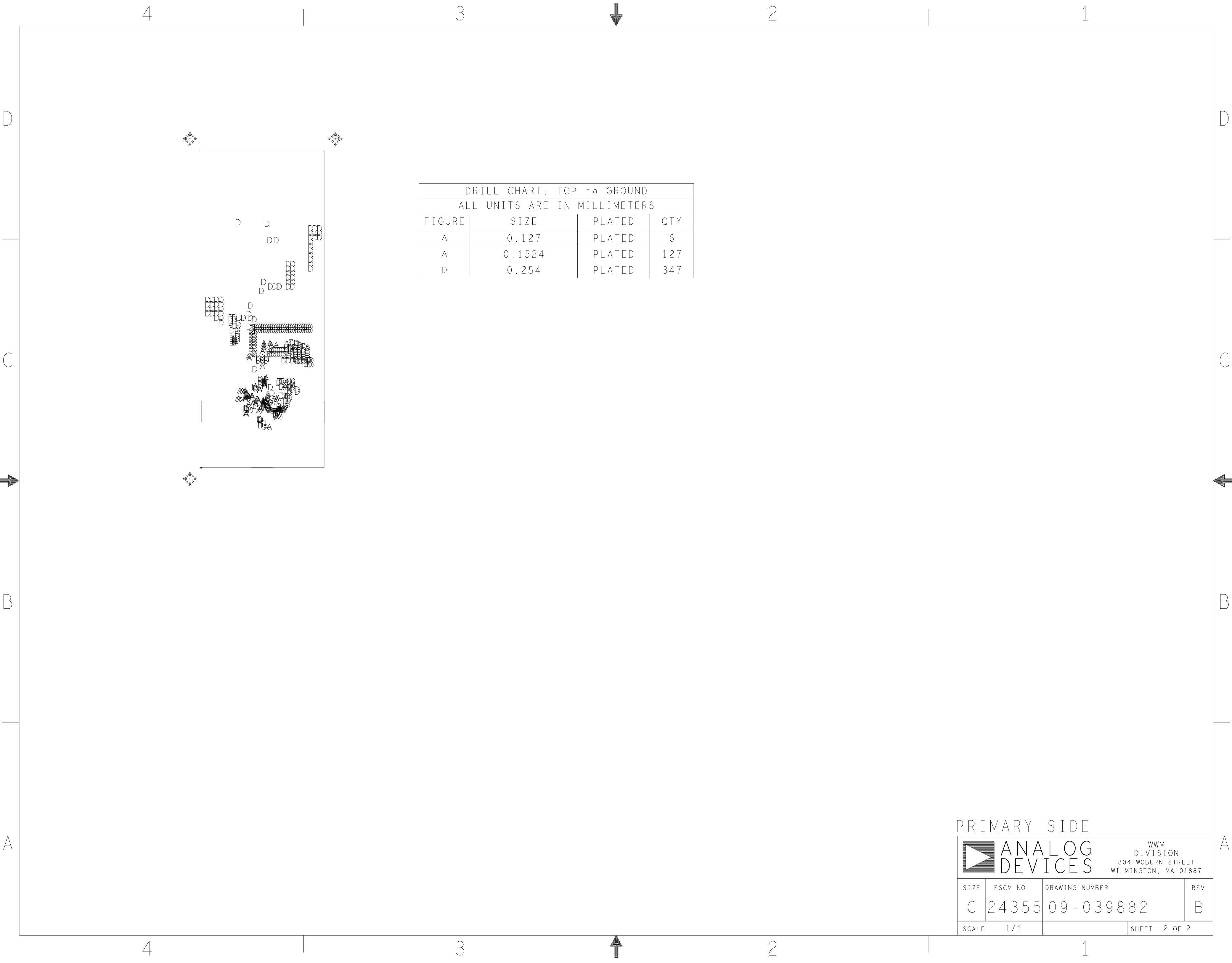
Cu-Cu
THICKNESS
0.058"
+/- .003"

CHARACTERISTIC IMPEDANCE ON LAYER 1 :
A.--> 50 OHM SINGLE
ENDED CO-PLANAR WAVEGUIDE (+/-5%).
LINE WIDTH = 0.380 mm
GAP TO GROUND PLANE ON LAYER 1 = 0.2mm EITHER SIDE
OF THE TRACE

GROUND REFERENCE PLANE ON LAYER 2.

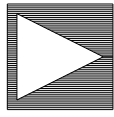
PRIMARY SIDE

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES			APPROVAL		DATE	 ANALOG DEVICES	RFG DIVISION RAHEEN BUSINESS PARK LIMERICK IRELAND		
TOLERANCES			EVAL ENGINEER Ian Collins		24Nov14				
DECIMALS	FRACTIONS	ANGLES	HARDWARE SERVICES -----		ddMMMyy				
.XX --.010	--.010	--.010	CHECKED Ian Collins		24Nov14				
			APPROVED XXXXXXXXXX XXXXX		ddMMMyy	TITLE	FABRICATION EV-ADF5355SD1Z		
.XXX --.005	--.005		PCB DESIGN ENGINEER Pat Sheahan		24Nov14				
.XXXX --.0000	--.0000				ddMMMyy				
MATERIAL						SIZE	FSCM NO 24355	DRAWING NUMBER 09-039882	REV B
FINISH						SCALE	1 / 1		SHEET 1 OF 2
DO NOT SCALE DWG									



DRILL CHART: TOP to GROUND			
ALL UNITS ARE IN MILLIMETERS			
FIGURE	SIZE	PLATED	QTY
A	0.127	PLATED	6
A	0.1524	PLATED	127
D	0.254	PLATED	347

PRIMARY SIDE

		ANALOG DEVICES		WWM DIVISION 804 WOBURN STREET WILMINGTON, MA 01887	
		SIZE C	FSCM NO 24355	DRAWING NUMBER 09-039882	REV B
SCALE 1 / 1				SHEET 2 OF 2	

VIA FILL
039882
REV A

