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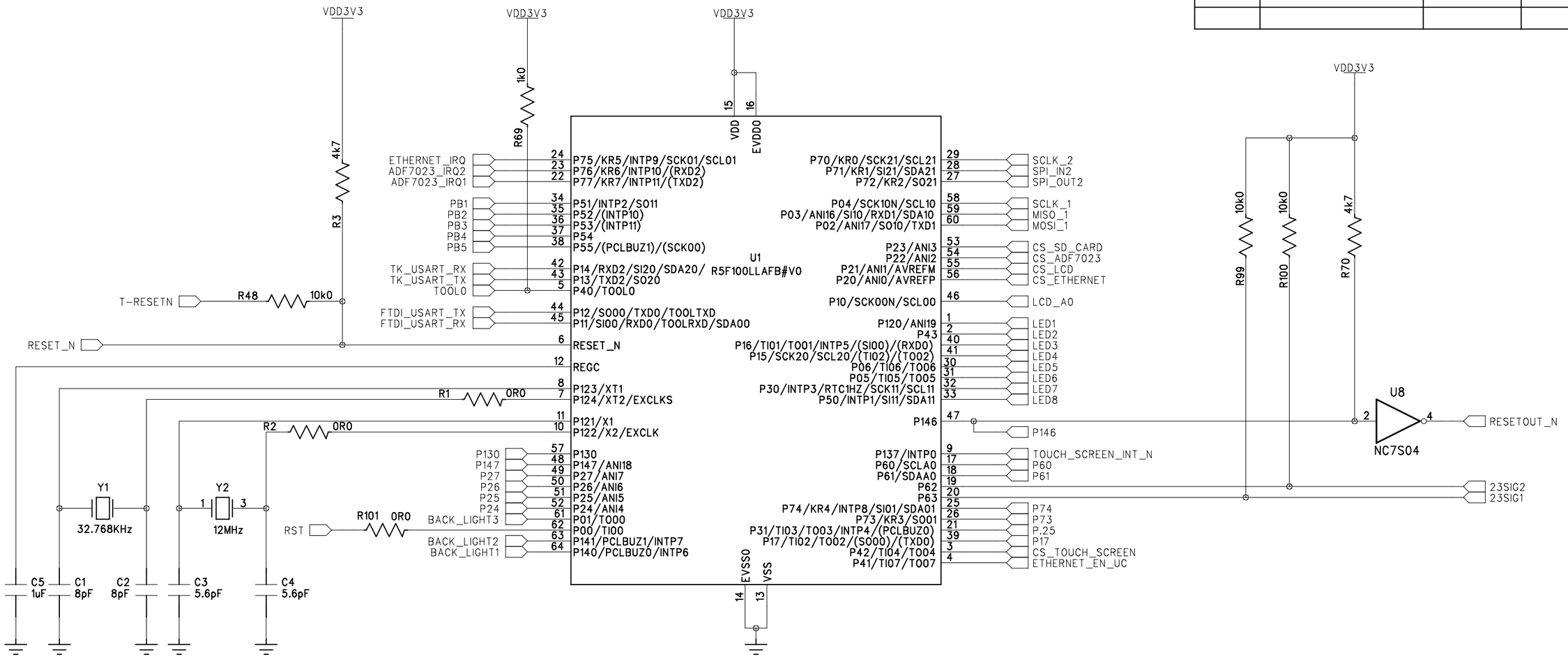
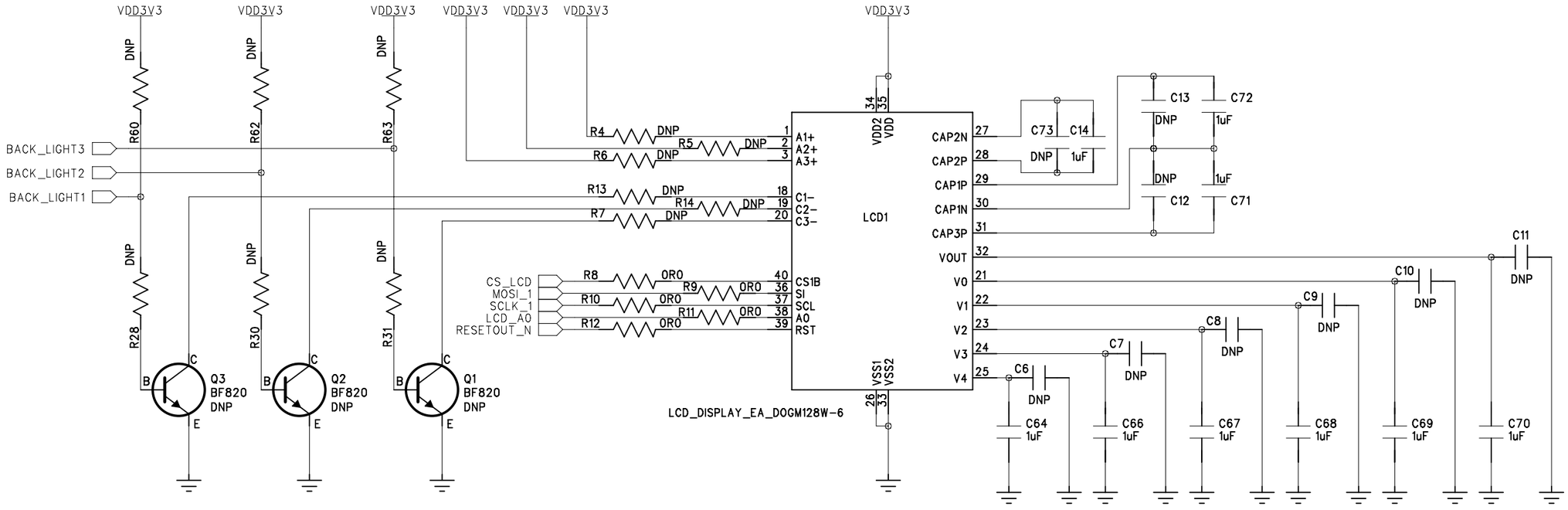


COMPANY:	Analog Devices Inc.
TITLE:	EVAL-ADF7xxxMB4 Schematic
DRAWN: Vincent Heffernan	DATED: 7/24/13
CHECKED: SRD Team	DATED:
QUALITY CONTROL:	DATED:
RELEASED:	DATED:
CODE:	SIZE:
DRAWING NO:	EVAL-ADF7xxxMB4Z
REV:	B
SCALE:	SHEET: 1 OF 12

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:



DRAWN: Vincent Heffernan	DATED: 7/24/13	COMPANY: Analog Devices Inc.	
CHECKED: SRD Team	DATED:	TITLE: EVAL−ADF7xxxMB4 Schematic	
QUALITY CONTROL:	DATED:	CODE:	SIZE: DRAWING NO: EVAL−ADF7xxxMB4Z REV: B
RELEASED:	DATED:	SCALE:	SHEET: 2 OF 12

REVISION RECORD			
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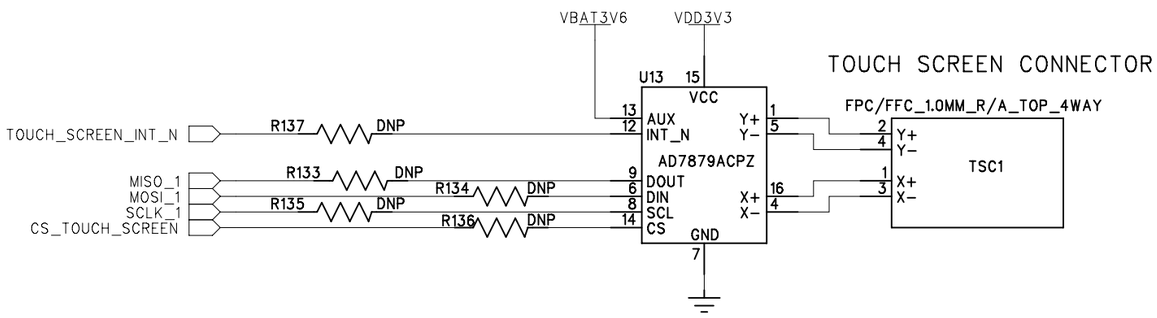

DRAWN: Vincent Heffernan	DATED: 7/24/13		
CHECKED: SRD Team	DATED:		
QUALITY CONTROL:	DATED:		
RELEASED:	DATED:		

COMPANY: Analog Devices Inc.			
TITLE: EVAL-ADF7xxxMB4 Schematic			
CODE:	SIZE:	DRAWING NO: EVAL-ADF7xxxMB4Z	REV: B
SCALE:		SHEET: 3 OF 12	

REVISION RECORD			
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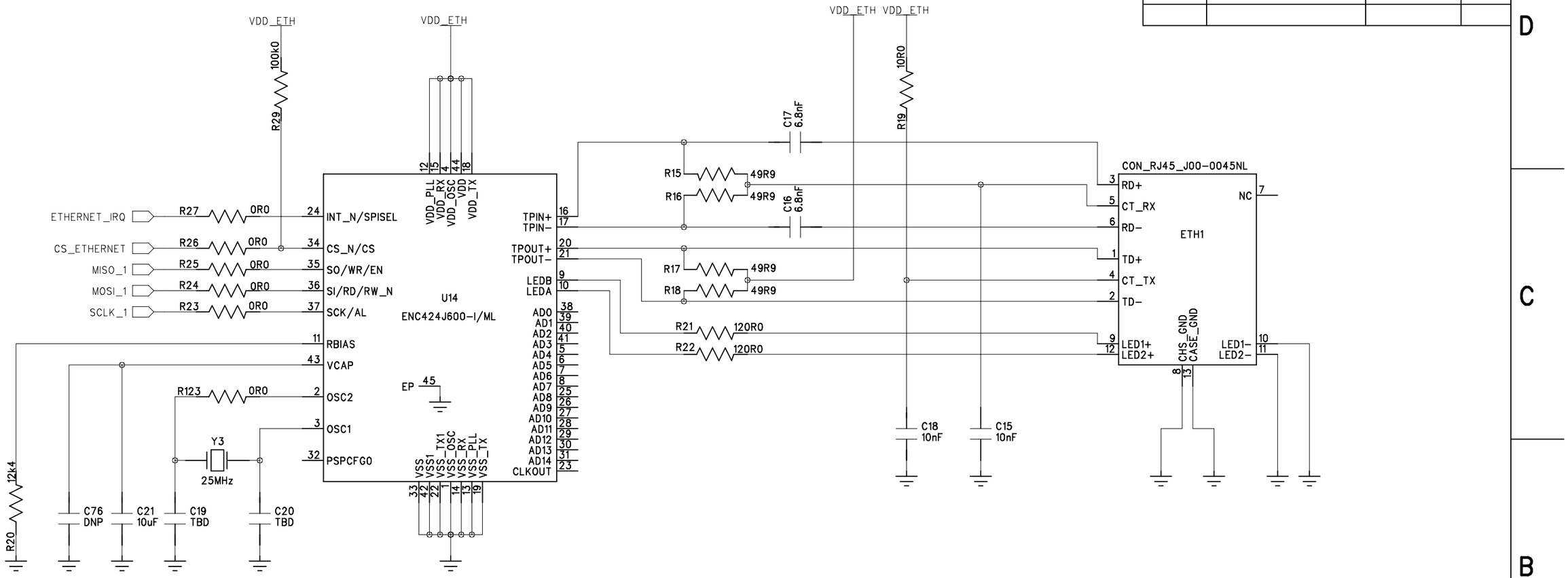

DRAWN: Vincent Heffernan	DATED: 7/24/13	COMPANY: Analog Devices Inc.	
CHECKED: SRD Team	DATED:	TITLE: EVAL-ADF7xxxMB4 Schematic	
QUALITY CONTROL:	DATED:	CODE:	SIZE:
RELEASED:	DATED:	DRAWING NO: EVAL-ADF7xxxMB4Z	REV: B
		SCALE:	SHEET: 4 OF 12

REVISION RECORD			
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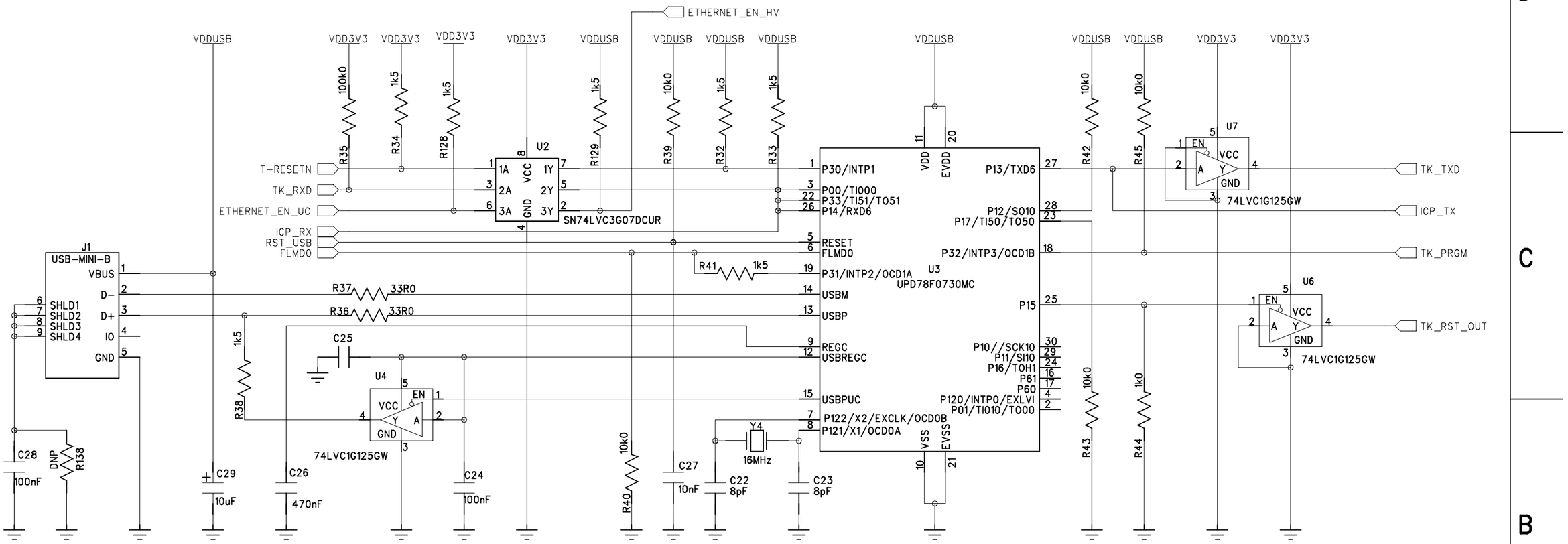


DRAWN: Vincent Heffernan	DATED: 7/24/13
CHECKED: SRD Team	DATED:
QUALITY CONTROL:	DATED:
RELEASED:	DATED:

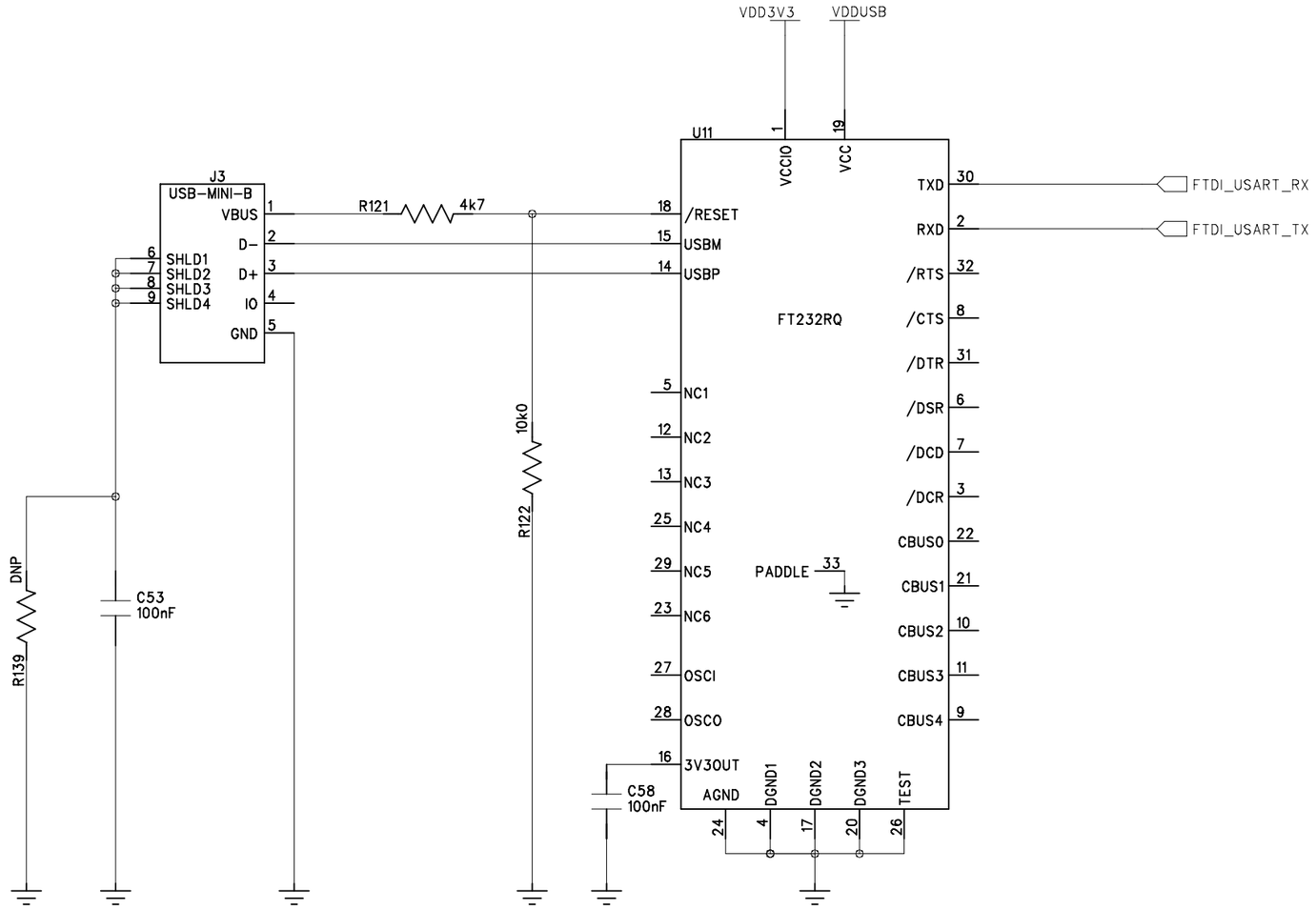
COMPANY: Analog Devices Inc.

TITLE: EVAL-ADF7xxxMB4 Schematic

CODE:	SIZE:	DRAWING NO:	REV:
		EVAL-ADF7xxxMB4Z	B

SCALE: | SHEET: 5 OF 12

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:



DRAWN: Vincent Heffernan	DATED: 7/24/13	COMPANY: Analog Devices Inc.	
CHECKED: SRD Team	DATED:	TITLE: EVAL-ADF7xxxMB4 Schematic	
QUALITY CONTROL:	DATED:	CODE:	SIZE:
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		SCALE:	SHEET: 6 OF 12

REVISION RECORD			
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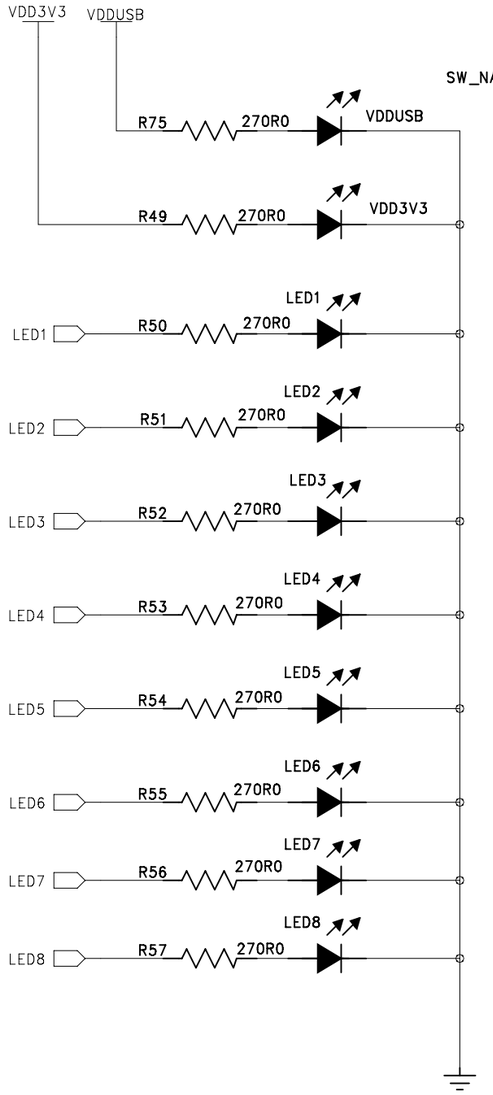

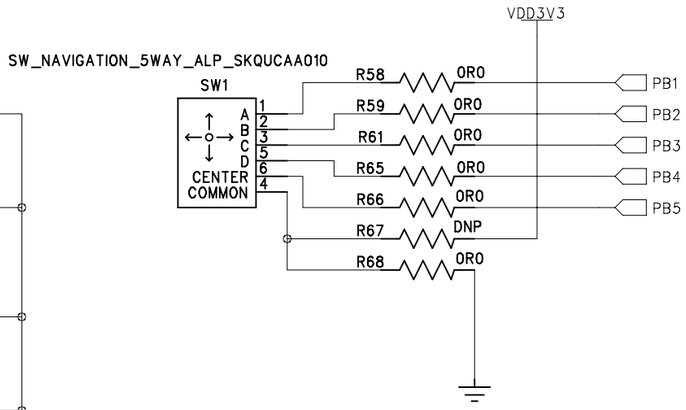

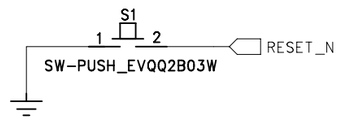

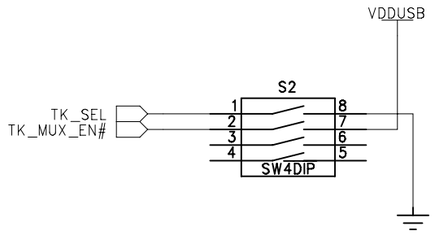

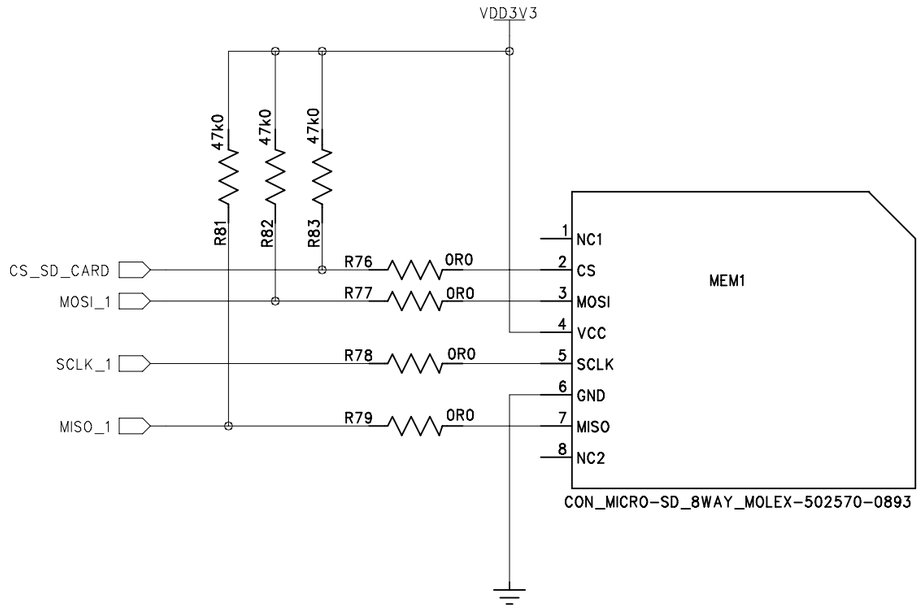

DRAWN: Vincent Heffernan	DATED: 7/24/13	COMPANY: Analog Devices Inc.	
CHECKED: SRD Team	DATED:	TITLE: EVAL-ADF7xxxMB4 Schematic	
QUALITY CONTROL:	DATED:	CODE:	SIZE: DRAWING NO: EVAL-ADF7xxxMB4Z REV: B
RELEASED:	DATED:	SCALE:	SHEET: 7 OF 12

REVISION RECORD			
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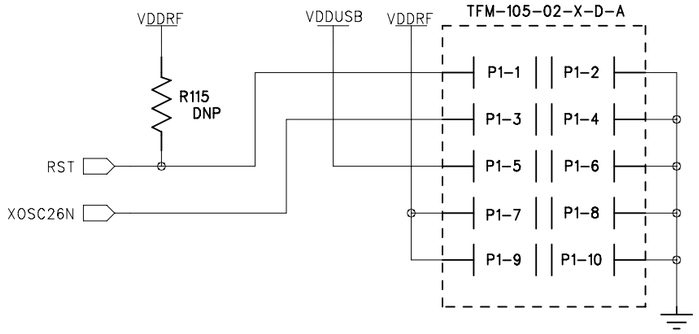
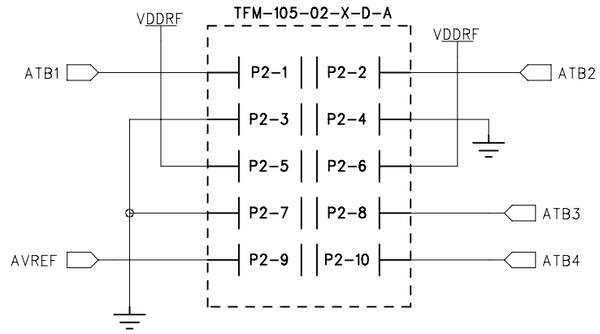
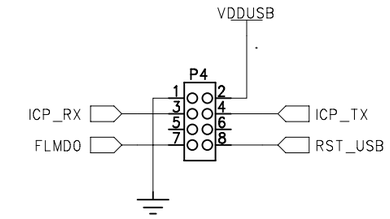
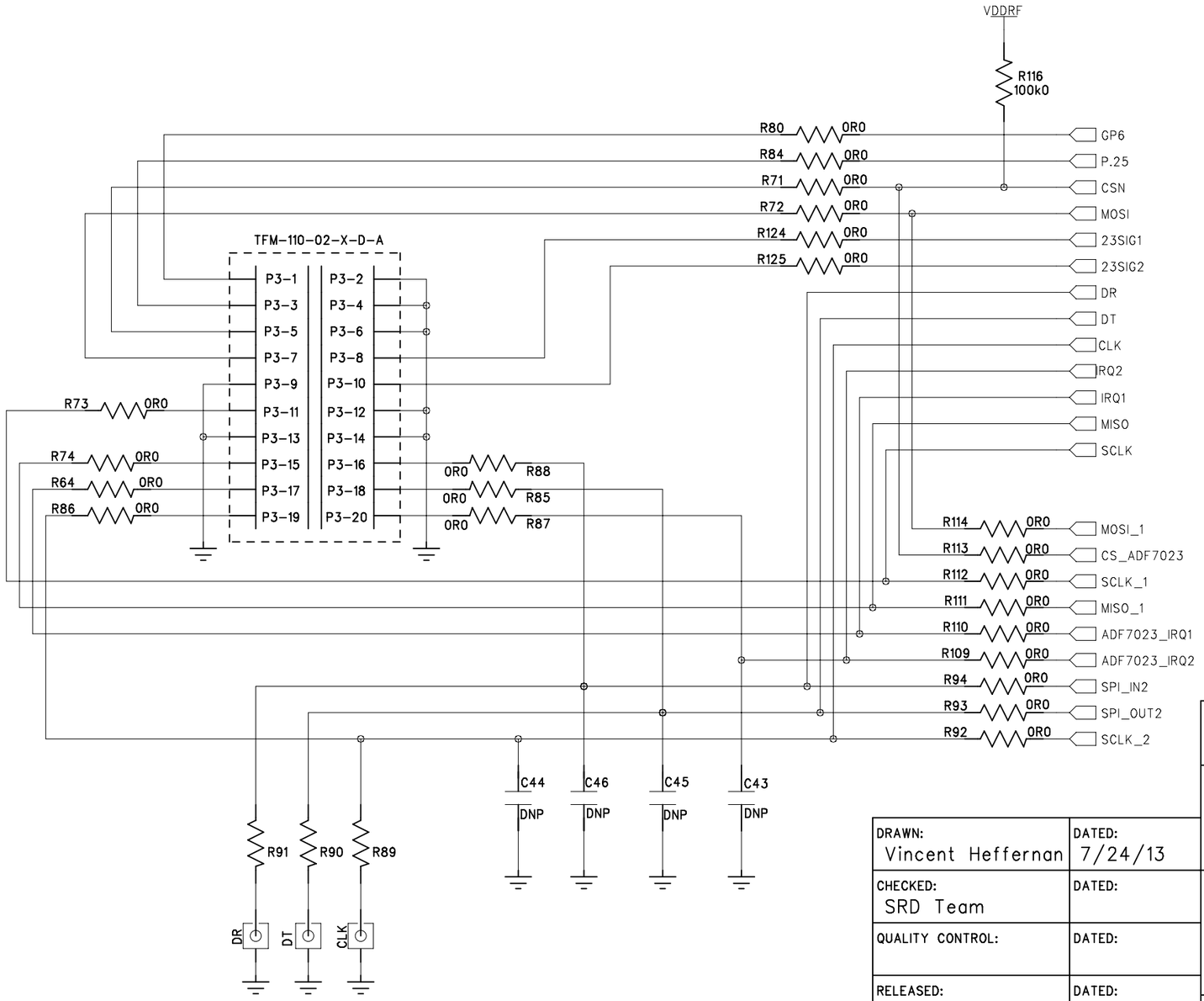
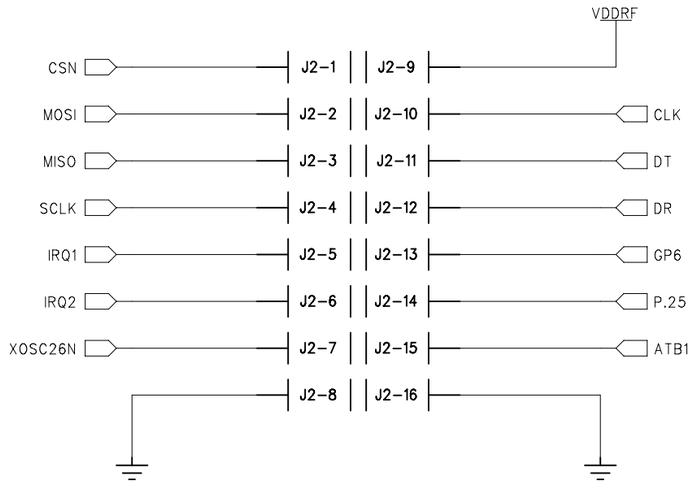










DRAWN: Vincent Heffernan	DATED: 7/24/13
CHECKED: SRD Team	DATED:
QUALITY CONTROL:	DATED:
RELEASED:	DATED:

COMPANY: Analog Devices Inc.

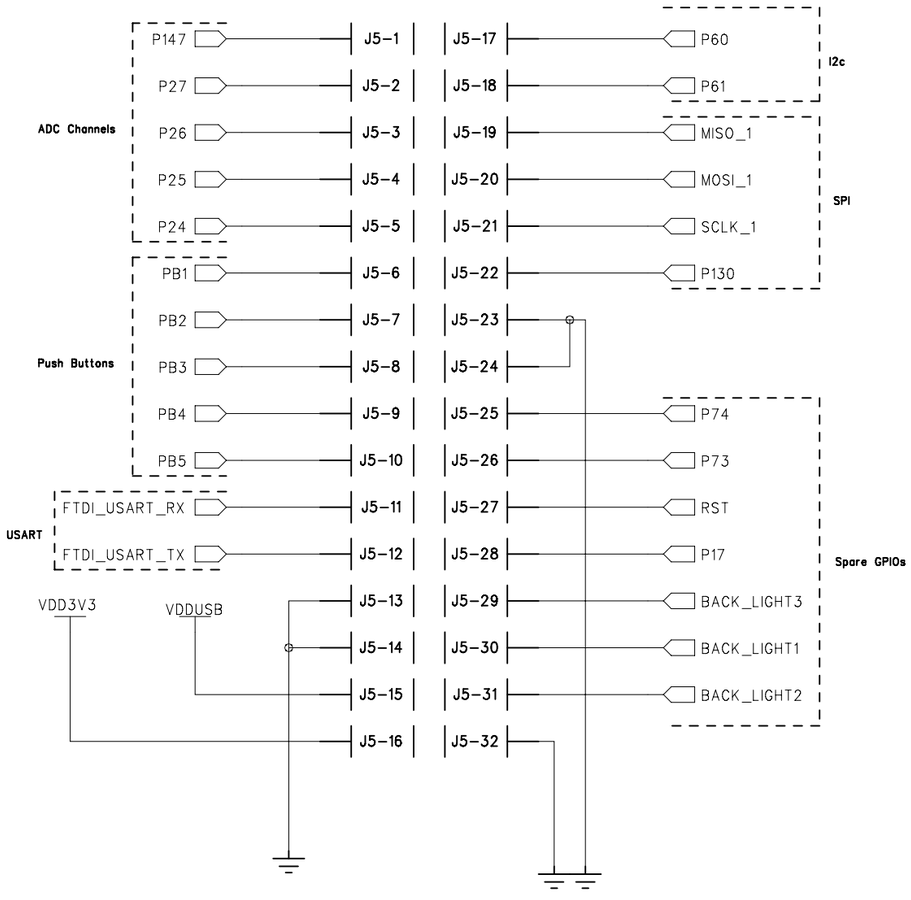
TITLE: EVAL-ADF7xxxMB4 Schematic

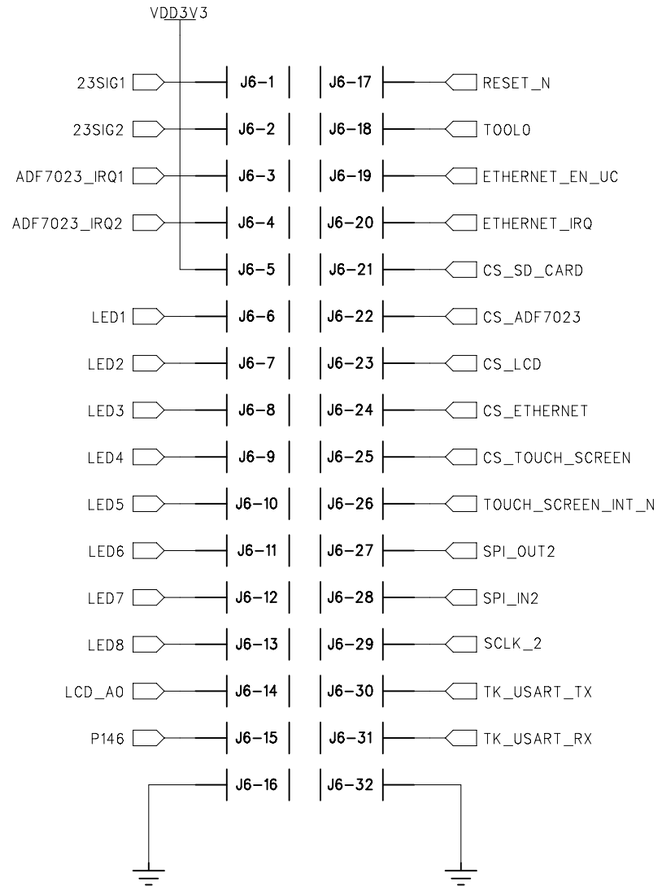
CODE:	SIZE:	DRAWING NO:	REV:
		EVAL-ADF7xxxMB4Z	B

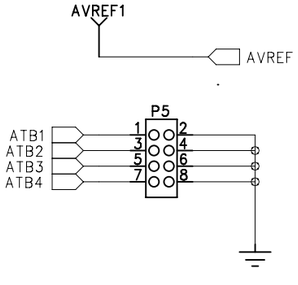
SCALE:

SHEET: 8 OF 12

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

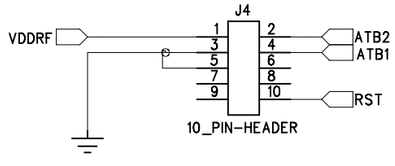






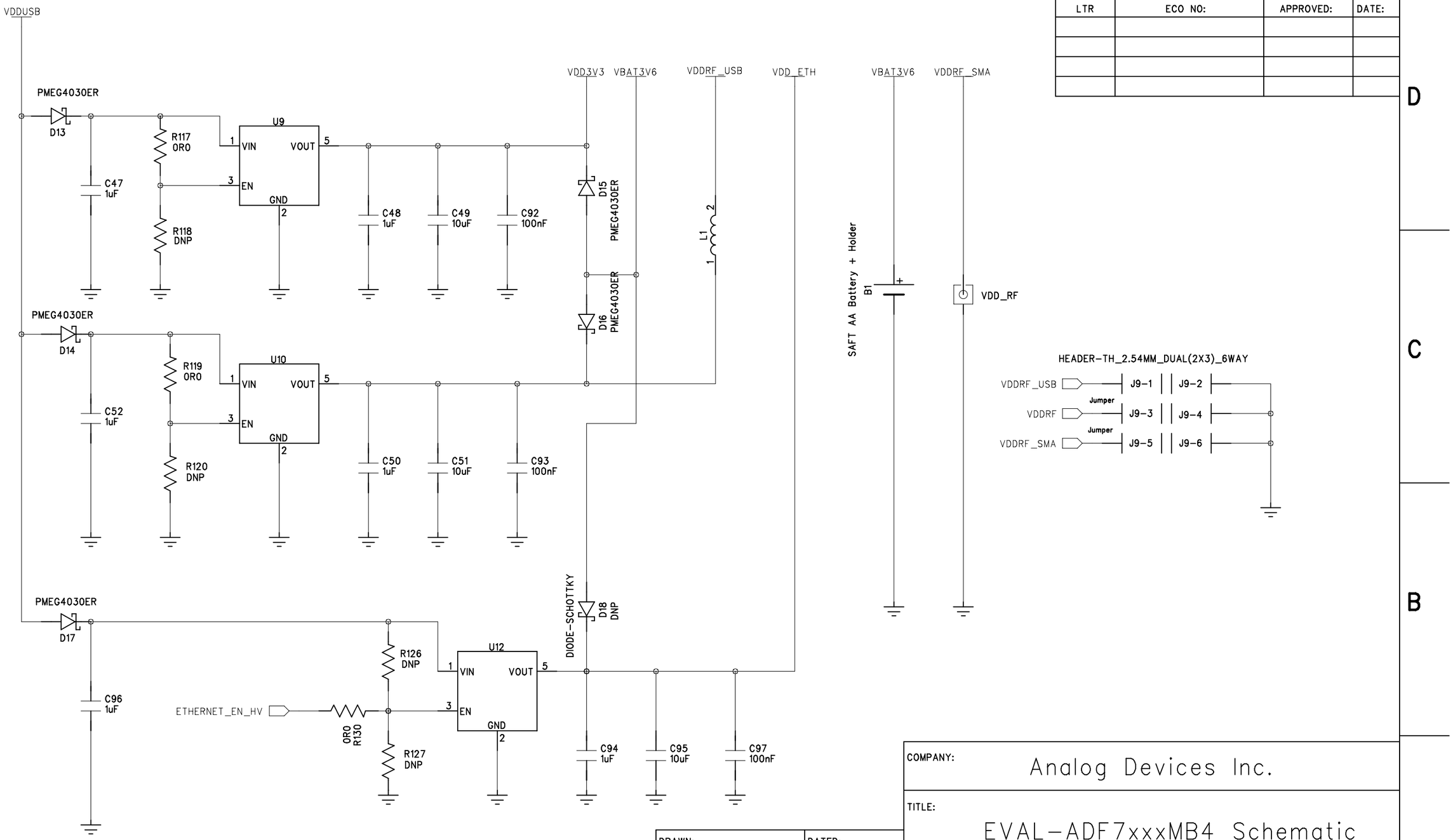
SWD Connector – J-LINK LITE

Place pin 1 on inside of PCB



DRAWN: Vincent Heffernan	DATED: 7/24/13	COMPANY: Analog Devices Inc.	
CHECKED: SRD Team	DATED:	TITLE: EVAL-ADF7xxxMB4 Schematic	
QUALITY CONTROL:	DATED:	CODE:	SIZE: DRAWING NO: EVAL-ADF7xxxMB4Z REV: B
RELEASED:	DATED:	SCALE:	SHEET: 9 OF 12

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:



DRAWN: Vincent Heffernan	DATED: 7/24/13
CHECKED: SRD Team	DATED:
QUALITY CONTROL:	DATED:
RELEASED:	DATED:

COMPANY: Analog Devices Inc.

TITLE: EVAL-ADF7xxxMB4 Schematic

CODE:	SIZE:	DRAWING NO:	REV:
		EVAL-ADF7xxxMB4Z	B

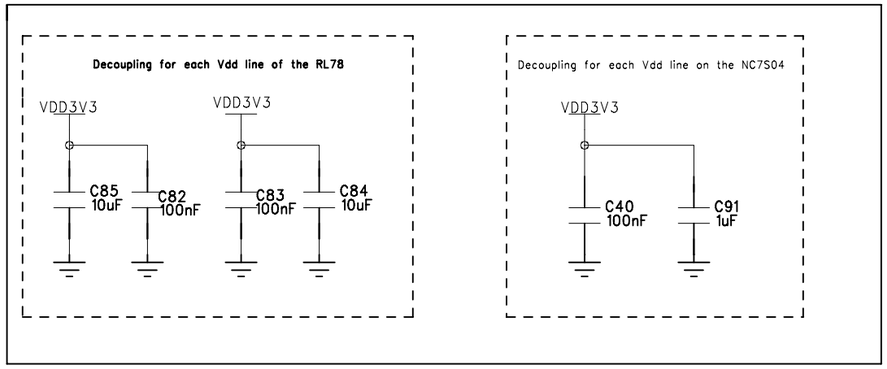
SCALE:

SHEET: 10 OF 12

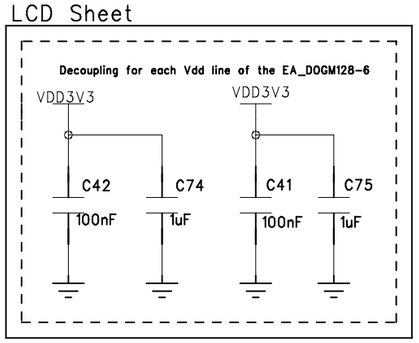
6 5 4 3 2 1

D C B A

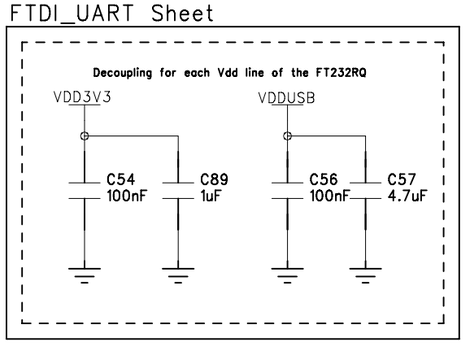
RL87 Sheet



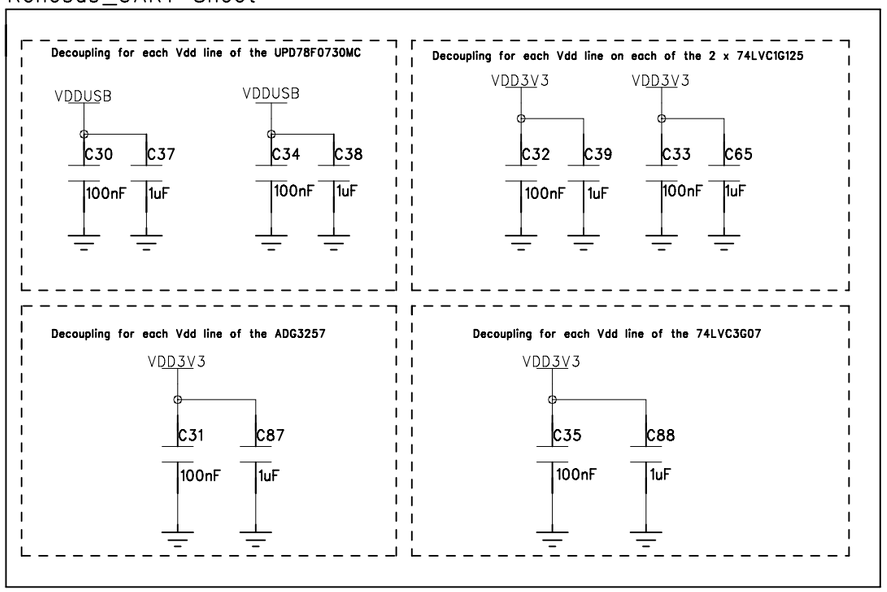
LCD Sheet



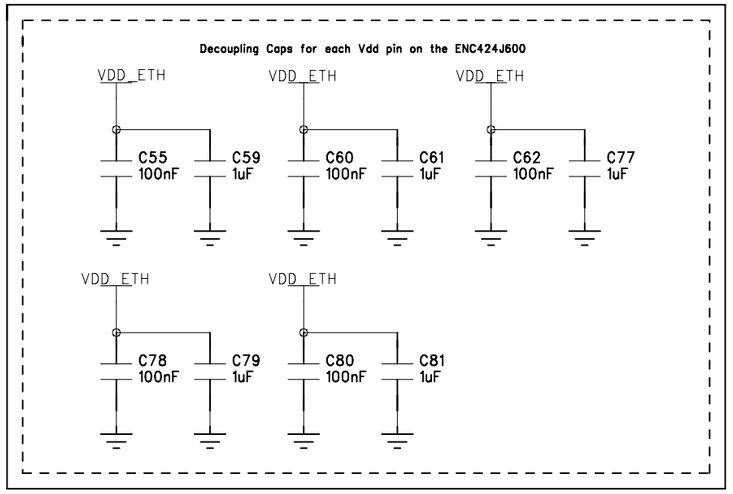
FTDI_UART Sheet



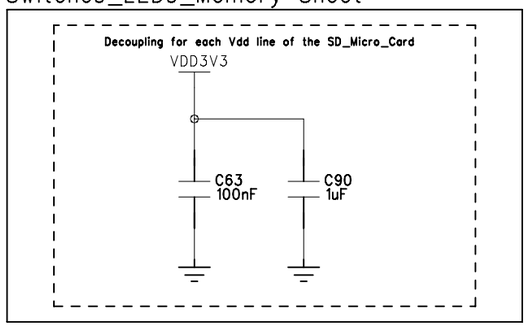
Renesas_UART Sheet



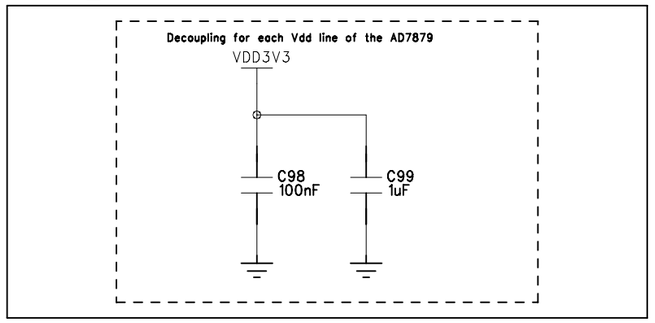
Ethernet Sheet



Switches_LEDs_Memory Sheet



Touch_Screen Sheet



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DRAWN: Vincent Heffernan	DATED: 7/24/13		
CHECKED: SRD Team	DATED:		
QUALITY CONTROL:	DATED:		
RELEASED:	DATED:		

COMPANY: Analog Devices Inc.

TITLE: EVAL-ADF7xxxMB4 Schematic

CODE:	SIZE:	DRAWING NO:	REV:
		EVAL-ADF7xxxMB4Z	B

SCALE:

SHEET: 11 OF 12

Changes from RevA

Added J19, Segger J-Link Lite 9 pin adaptor
SPI Lines Swapped and naming convention Changed
Pullup Resistors Added to P62 & P63 of RL78