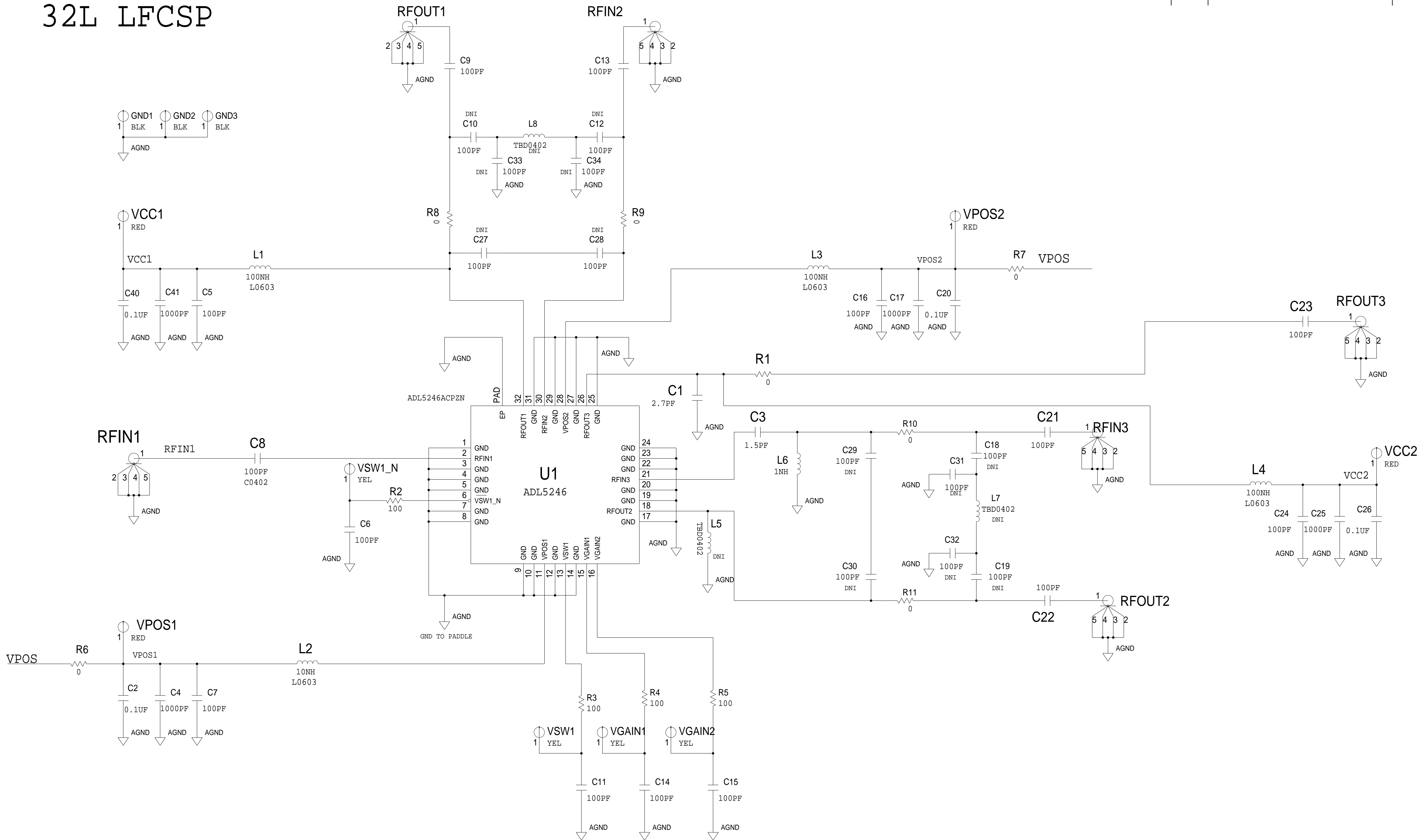


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D	<div>THIS DRAWING IS THE PROPERTY OF ANALOG DEVICES INC. IT IS NOT TO BE REPRODUCED OR COPIED, IN WHOLE OR IN PART, OR USED IN FURNISHING INFORMATION TO OTHERS, OR FOR ANY OTHER PURPOSE DETRIMENTAL TO THE INTERESTS OF ANALOG DEVICES. THE EQUIPMENT SHOWN HEREON MAY BE PROTECTED BY PATENTS OWNED OR CONTROLLED BY ANALOG DEVICES.</div>				<div>JUMPER TABLE</div> <table><tr><td>JP#</td><td>ON</td><td>OFF</td></tr><tr><td>1</td><td></td><td></td></tr><tr><td>2</td><td></td><td></td></tr><tr><td>3</td><td></td><td></td></tr><tr><td>4</td><td></td><td></td></tr><tr><td>5</td><td></td><td></td></tr></table>			JP#	ON	OFF	1			2			3			4			5			<div>REVISIONS</div> <table><tr><td>REV</td><td>DESCRIPTION</td><td>DATE</td><td>APPROVED</td></tr><tr><td>A</td><td>INITIAL DESIGN</td><td>28FEB14</td><td>J.BEDROSIAN</td></tr></table>				REV	DESCRIPTION	DATE	APPROVED	A	INITIAL DESIGN	28FEB14	J.BEDROSIAN
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				<div>TEMPLATE ENGINEER -</div> <div>HARDWARE SERVICES -</div> <div>HARDWARE SYSTEMS -</div> <div>TEST ENGINEER -</div> <div>COMPONENT ENGINEER -</div> <div>TEST PROCESS -</div> <div>HARDWARE RELEASE -</div> <div>DESIGNER -</div> <div>PTD ENGINEER &lt;PTD_ENGINEER&gt;</div> <div>CHECKER -</div>				<div>DATE</div> <div></div> <div></div> <div></div> <div></div> <div></div> <div></div> <div></div> <div></div> <div></div> <div>MASTER PROJECT TEMPLATE TBD</div> <div>TESTER TEMPLATE no_template</div> <div>DRAWING NO. 02_038024</div> <div>REV. A</div> <div>UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES</div> <div>TOLERANCES</div> <div>DECIMALS X.XX +0.010 X.XXX +0.005</div> <div>FRACTIONS +1/32</div> <div>ANGLES +2</div> <div>SIZE <b>D</b></div> <div>SCALE 1:1</div> <div>CODE ID NO. CodeID</div> <div>SHEET 1 OF 2</div>																													

ADL5246/7 32L LFCSP



REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

SCHEMATIC			
HW TYPE : Customer Evaluation Product(s) : ADL5246 : ADL5247			
DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. 02_038024		REV A
PTD ENGINEER <PTD_ENGINEER>	SIZE D	SCALE 1:1	SHEET 2 OF 2