RADIATION TEST REPORT

PRODUCT:	ADL5501ALQMLR
GAMMA:	0, 100k / TM1019 Condition A
GAMMA SOURCE:	Co60
DOSE RATE:	101 Rad(si)/s
FACILITIES:	University of Massachusetts @ Lowell
TESTED:	11/15/12

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ANALOG DEVICES

		IQ @ 3.3v No RFIN mA		IQ @ 2.97v No RFIN mA		IQ @ 5v No RFIN mA	
Wafer	**SN**	**PRE**	**100k**	**PRE**	**100k**	**PRE**	**100k**
1	**3**	**0.87022**	**0.87406**	**0.86181**	**0.86578**	**0.91453**	**0.91818**
1	**1**	0.8694	0.91276	0.86093	0.9047	0.91471	0.95513
1	**5**	0.87307	0.9195	0.86464	0.9116	0.91892	0.96338
1	**16**	0.86899	0.91762	0.86109	0.90997	0.91293	0.95883
1	**17**	0.87357	0.9243	0.86539	0.9169	0.92014	0.96812
4	**50**	0.89785	0.94974	0.88982	0.94218	0.94282	0.99161
4	**51**	0.93495	0.98646	0.92623	0.97803	0.98437	1.03172
4	**52**	0.88816	0.94365	0.87938	0.93528	0.93573	0.98913
4	**54**	0.84751	0.90479	0.83917	0.8962	0.89542	0.94933
	Min	0.84751	0.90479	0.83917	0.89620	0.89542	0.94933
	Max	0.93495	0.98646	0.92623	0.97803	0.98437	1.03172
	Mean	0.88169	0.93235	0.87333	0.92436	0.92813	0.97591
	Std. Dev	0.02608	0.02659	0.02596	0.02652	0.02690	0.02721
	Mean - 3 Sigma	0.80345	0.85259	0.79546	0.84480	0.84742	0.89427
	Mean + 3 Sigma	0.95992	1.01211	0.95120	1.00391	1.00884	1.05754

		IQ @ 5.25v No RFIN mA		IQ @ 3.3v RFIN @ 5dBm & 100MHz mA		IQ @ 2.97v RFIN @ 5dBm & 100MHz mA	
Wafer	**SN**	**PRE**	**100k**	**PRE**	**100k**	**PRE**	**100k**
1	**3**	**0.92215**	**0.92594**	**3.49983**	**3.48246**	**3.50121**	**3.47688**
1	**1**	0.92231	0.96292	3.52862	3.87839	3.52526	3.87889
1	**5**	0.92708	0.97063	3.46473	3.82479	3.46144	3.82257
1	**16**	0.92118	0.96627	3.45692	3.85619	3.45331	3.40574
1	**17**	0.92852	0.97618	3.48483	3.73293	3.48088	3.73346
4	**50**	0.95095	0.99946	3.67747	3.39994	3.67509	3.39797
4	**51**	0.99269	1.04048	3.77909	3.77863	3.7763	3.7764
4	**52**	0.9443	0.99682	3.66587	3.65976	3.66267	3.64235
4	**54**	0.90399	0.95762	3.69409	3.75288	3.69168	3.74833
	Min	0.90399	0.95762	3.45692	3.39994	3.45331	3.39797
	Max	0.99269	1.04048	3.77909	3.87839	3.77630	3.87889
	Mean	0.93638	0.98380	3.59395	3.73544	3.59083	3.67571
	Std. Dev	0.02692	0.02748	0.12427	0.15281	0.12471	0.18234
	Mean - 3 Sigma	0.85562	0.90135	3.22114	3.27700	3.21669	3.12870
	Mean + 3 Sigma	1.01713	1.06625	3.96677	4.19388	3.96497	4.22273

		IQ @ 5v RFIN @ 5dBm & 100MHz mA		IQ @ 5.25v RFIN @ 5dBm & 100MHz mA		IQZ @ 3.3v No RFIN uA	
Wafer	**SN**	**PRE**	**100k**	**PRE**	**100k**	**PRE**	**100k**
1	**3**	**3.54344**	**3.51586**	**3.55092**	**3.52857**	**0.07943**	**0.27067**
1	**1**	3.56781	3.90656	3.57525	3.91256	-0.23421	0.27067
1	**5**	3.5043	3.85236	3.51227	3.85761	-0.29694	0.3334
1	**16**	3.49495	3.44448	3.50233	3.45296	-0.14012	0.36476
1	**17**	3.52362	3.76602	3.53053	3.77468	0.1108	0.3334
4	**50**	3.71494	3.43404	3.72198	3.43912	0.04807	0.17658
4	**51**	3.8187	3.8119	3.82661	3.81834	-0.01466	0.36476
4	**52**	3.70516	3.67224	3.71263	3.6746	-0.04602	0.30203
4	**54**	3.73339	3.7807	3.74098	3.79152	-0.10875	0.3334
	Min	3.49495	3.43404	3.50233	3.43912	-0.29694	0.17658
	Max	3.81870	3.90656	3.82661	3.91256	0.11080	0.36476
	Mean	3.63286	3.70854	3.64032	3.71517	-0.08523	0.30988
	Std. Dev	0.12440	0.17948	0.12448	0.17963	0.13799	0.06216
	Mean - 3 Sigma	3.25967	3.17010	3.26690	3.17628	-0.49921	0.12338
	Mean + 3 Sigma	4.00604	4.24697	4.01375	4.25407	0.32875	0.49637

		IQZ @ 2.97v No RFIN uA		IQZ @ 5v No RFIN uA		IQZ @ 5.25v No RFIN uA	
Wafer	**SN**	**PRE**	**100k**	**PRE**	**100k**	**PRE**	**100k**
1	**3**	**-0.0452**	**0.14435**	**0.10182**	**0.48994**	**-0.04802**	**0.37275**
1	**1**	-0.32748	0.39525	-0.18046	0.5213	0.1088	0.37275
1	**5**	0.14298	0.42662	-0.14910	0.36448	-0.26757	0.46684
1	**16**	-0.1393	0.39525	-0.14910	0.20767	0.01471	0.34138
4	**17**	-0.0452	0.45798	0.03909	0.33312	0.04607	0.43547
4	**50**	-0.10793	0.2698	0.03909	0.39585	-0.20484	0.27866
4	**51**	-0.17066	0.39525	-0.1491	0.42721	-0.14211	0.40411
4	**52**	-0.26475	0.30116	-0.21182	0.39585	-0.14211	0.40411
4	**54**	-0.1393	0.36389	0.25864	0.42721	-0.04802	0.43547
	Min	-0.32748	0.26980	-0.21182	0.20767	-0.26757	0.27866
	Max	0.14298	0.45798	0.25864	0.52130	0.10880	0.46684
	Mean	-0.13146	0.37565	-0.06285	0.38409	-0.07938	0.39235
	Std. Dev	0.14201	0.06259	0.16146	0.09018	0.13094	0.06030
	Mean - 3 Sigma	-0.55748	0.18789	-0.54722	0.11354	-0.47219	0.21145
	Mean + 3 Sigma	0.29457	0.56341	0.42153	0.65463	0.31343	0.57324

		ENBL IIL Vs @ 2.97v and ENBL @ 0v nA		ENBL IIH Vs and ENBL @ 2.97v nA		ENBL IIL Vs @ 3.3v and ENBL @ 0v nA	
Wafer	**SN**	**PRE**	**100k**	**PRE**	**100k**	**PRE**	**100k**
1	**3**	**-14.99391**	**-15.03976**	**41.73696**	**41.4266**	**-13.86722**	**-14.13847**
1	**1**	-13.86722	-14.25113	38.58225	41.65192	-14.76857	-14.58911
1	**5**	-14.54323	-14.58911	39.14559	40.86329	-14.43057	-14.13847
1	**16**	-14.76857	-14.25113	41.96230	42.55321	-14.09256	-12.8992
1	**17**	-14.88124	-13.57516	39.37093	40.86329	-14.20523	-14.25113
4	**50**	-13.52922	-14.25113	51.98978	53.59401	-14.20523	-13.57516
4	**51**	-13.97989	-13.57516	50.29975	51.45345	-13.75455	-13.91315
4	**52**	-14.54323	-14.9271	49.96175	50.66482	-13.86722	-14.13847
4	**54**	-13.75455	-13.68783	50.07442	51.22813	-13.97989	-14.02581
	Min	-14.88124	-14.92710	38.58225	40.86329	-14.76857	-14.58911
	Max	-13.52922	-13.57516	51.98978	53.59401	-13.75455	-12.89920
	Mean	-14.23339	-14.13847	45.17335	46.60902	-14.16298	-13.94131
	Std. Dev	0.51013	0.49292	5.89732	5.56896	0.32397	0.51009
	Mean - 3 Sigma	-15.76378	-15.61723	27.48140	29.90212	-15.13488	-15.47159
	Mean + 3 Sigma	-12.70301	-12.65970	62.86529	63.31591	-13.19108	-12.41103

		ENBL IIH Vs and ENBL @ 3.3v nA		ENBL IIL Vs @ 5v and ENBL @ 0v nA		ENBL IIH Vs and ENBL @ 5v nA	
Wafer	**SN**	**PRE**	**100k**	**PRE**	**100k**	**PRE**	**100k**
1	**3**	**55.73908**	**55.12652**	**-14.76857**	**-14.25113**	**119.38721**	**119.61679**
1	**1**	51.68302	53.77458	-14.54323	-14.81444	112.73978	117.70155
1	**5**	50.331	53.32394	-13.30388	-13.57516	113.07779	116.80025
1	**16**	52.8097	55.01386	-13.86722	-13.80049	118.14786	120.06743
1	**17**	51.79569	53.21128	-14.99391	-13.57516	114.0918	115.7863
4	**50**	68.13259	68.75853	-13.52922	-13.80049	142.37155	145.64154
4	**51**	66.55524	67.51926	-14.6559	-13.80049	141.58287	142.71234
4	**52**	65.54122	66.84329	-14.43057	-13.34984	139.78018	141.69839
4	**54**	64.41454	66.95595	-13.86722	-14.81444	139.89285	142.03637
	Min	50.33100	53.21128	-14.99391	-14.81444	112.73978	115.78630
	Max	68.13259	68.75853	-13.30388	-13.34984	142.37155	145.64154
	Mean	58.90788	60.67509	-14.14889	-13.94131	127.71059	130.30552
	Std. Dev	7.85070	7.35911	0.59314	0.56089	14.22587	13.69782
	Mean - 3 Sigma	35.35578	38.59777	-15.92830	-15.62398	85.03298	89.21207
	Mean + 3 Sigma	82.45997	82.75240	-12.36948	-12.25865	170.38819	171.39898

		ENBL IIL Vs @ 5.25v and ENBL @ 0v nA		ENBL IIH Vs and ENBL @ 5.25v nA		LINEARITY ERROR Ain 50MHz Vs @ 5v dB	
Wafer	**SN**	**PRE**	**100k**	**PRE**	**100k**	**PRE**	**100k**
1	**3**	**-12.74054**	**-15.60307**	**128.79454**	**246.55478**	**-0.33234**	**-0.24637**
1	**1**	-13.97989	-13.68783	121.69644	127.02118	-0.25068	-0.20239
1	**5**	-13.52922	-13.91315	122.25978	126.6832	-0.27199	-0.20592
1	**16**	-13.86722	-13.91315	128.23121	130.73901	-0.25232	-0.1914
1	**17**	-13.52922	-13.80049	122.03445	125.66925	-0.24914	-0.16692
4	**50**	-14.3179	-14.36379	154.5956	156.42577	-0.29949	-0.16095
4	**51**	-13.64189	-13.80049	152.56757	154.84852	-0.39571	-0.24304
4	**52**	-14.76857	-14.13847	149.63818	152.48263	-0.23384	-0.13378
4	**54**	-14.3179	-13.4625	149.30018	153.04593	-0.25566	-0.19091
	Min	-14.76857	-14.36379	121.69644	125.66925	-0.39571	-0.24304
	Max	-13.52922	-13.46250	154.59560	156.42577	-0.23384	-0.13378
	Mean	-13.99398	-13.88498	137.54043	140.86444	-0.27610	-0.18691
	Std. Dev	0.44434	0.27431	15.17972	14.37876	0.05212	0.03313
	Mean - 3 Sigma	-15.32700	-14.70793	92.00125	97.72814	-0.43245	-0.28631
	Mean + 3 Sigma	-12.66095	-13.06204	183.07960	184.00073	-0.11975	-0.08752

		GAIN Ain 50MHz Vs @ 5v V/Vrms		Output Intercept Ain 50MHz Vs @ 5v V		Output Voltage Hi Power In 50MHz Vs @ 5v V	
Wafer	**SN**	**PRE**	**100k**	**PRE**	**100k**	**PRE**	**100k**
1	**3**	**4.81268**	**4.87996**	**0.01833**	**0.01063**	**1.73652**	**1.69059**
1	**1**	4.77247	4.97625	0.02808	0.02812	1.74343	1.75018
1	**5**	4.69138	5.01443	0.01839	0.01284	1.70136	1.74566
1	**16**	4.69415	4.98387	0.02156	0.01848	1.70827	1.74422
1	**17**	4.69362	4.90434	0.02874	0.02483	1.71838	1.72657
4	**50**	4.94234	4.54568	0.02796	0.01744	1.79235	1.57869
4	**51**	5.06817	5.00471	0.03282	0.0314	1.82443	1.74221
4	**52**	4.93995	4.79529	0.04204	0.04144	1.81426	1.69486
4	**54**	4.95906	4.78512	0.03596	0.03732	1.82217	1.68444
	Min	4.69138	4.54568	0.01839	0.01284	1.70136	1.57869
	Max	5.06817	5.01443	0.04204	0.04144	1.82443	1.75018
	Mean	4.84514	4.87621	0.02944	0.02648	1.76558	1.70835
	Std. Dev	0.14921	0.16129	0.00757	0.01002	0.05329	0.05791
	Mean - 3 Sigma	4.39753	4.39234	0.00673	-0.00357	1.60571	1.53463
	Mean + 3 Sigma	5.29276	5.36008	0.05216	0.05654	1.92545	1.88207

		Output Voltage Lo Power In 50MHz Vs @ 5v V		LINEARITY ERROR Ain 100MHz Vs @ 2.97v dB		GAIN Ain 100MHz Vs @ 2.97v V/Vrms	
Wafer	**SN**	**PRE**	**100k**	**PRE**	**100k**	**PRE**	**100k**
1	**3**	**0.10179**	**0.09375**	**-0.30026**	**-0.33613**	**6.34403**	**6.50037**
1	**1**	0.11654	0.11573	-0.23380	-0.27076	6.33156	6.66068
1	**5**	0.10543	0.10179	-0.26624	-0.27971	6.19611	6.70217
1	**16**	0.10945	0.1075	-0.24723	-0.27327	6.2059	6.63808
1	**17**	0.1178	0.11497	-0.20757	-0.25327	6.22225	6.51894
4	**50**	0.11905	0.10204	-0.26311	-0.34518	6.57616	5.87229
4	**51**	0.11811	0.11654	-0.32523	-0.38534	6.72807	6.56068
4	**52**	0.13795	0.1335	-0.20643	-0.27081	6.59315	6.30086
4	**54**	0.1274	0.12238	-0.16737	-0.24857	6.65137	6.52868
	Min	0.10543	0.10204	-0.32523	-0.38534	6.19611	5.87229
	Max	0.13795	0.13350	-0.16737	-0.24857	6.72807	6.70217
	Mean	0.11897	0.11431	-0.23962	-0.29086	6.43807	6.47280
	Std. Dev	0.01009	0.01065	0.04785	0.04828	0.22139	0.27213
	Mean - 3 Sigma	0.08868	0.08235	-0.38318	-0.43571	5.77391	5.65641
	Mean + 3 Sigma	0.14925	0.14627	-0.09606	-0.14602	7.10223	7.28918

		Output Intercept Ain 100MHz Vs @ 2.97v V		LINEARITY ERROR Ain 100MHz Vs @ 5v dB		GAIN Ain 100MHz Vs @ 5v V/Vrms	
Wafer	**SN**	**PRE**	**100k**	**PRE**	**100k**	**PRE**	**100k**
1	**3**	**0.02447**	**0.03672**	**-0.30785**	**-0.34131**	**6.36556**	**6.57888**
1	**1**	0.03056	0.0478	-0.23358	-0.27357	6.354	6.68449
1	**5**	0.02263	0.03437	-0.26971	-0.27722	6.22064	6.72267
1	**16**	0.02439	0.03909	-0.25891	-0.28006	6.22919	6.66306
1	**17**	0.03053	0.04459	-0.22090	-0.27426	6.24314	6.54153
4	**50**	0.03159	0.041	-0.26676	-0.34751	6.5987	5.90901
4	**51**	0.03981	0.05992	-0.33749	-0.39454	6.7607	6.58889
4	**52**	0.04229	0.05958	-0.21361	-0.27003	6.6165	6.33173
4	**54**	0.03497	0.05368	-0.17999	-0.25853	6.6758	6.54769
	Min	0.02263	0.03437	-0.33749	-0.39454	6.22064	5.90901
	Max	0.04229	0.05992	-0.17999	-0.25853	6.76070	6.72267
	Mean	0.03210	0.04750	-0.24762	-0.29697	6.46233	6.49863
	Std. Dev	0.00682	0.00950	0.04736	0.04783	0.22345	0.26717
	Mean - 3 Sigma	0.01163	0.01899	-0.38970	-0.44047	5.79199	5.69711
	Mean + 3 Sigma	0.05256	0.07602	-0.10554	-0.15346	7.13268	7.30016

		Output Intercept Ain 100MHz Vs @ 5v V		Output Voltage Hi Power In 100MHz Vs @ 5v V		Output Voltage Lo Power In 100MHz Vs @ 5v V	
Wafer	**SN**	**PRE**	**100k**	**PRE**	**100k**	**PRE**	**100k**
1	**3**	**0.02555**	**0.03903**	**2.63778**	**2.65969**	**0.17525**	**0.16194**
1	**1**	0.03183	0.04957	2.66102	2.73158	0.1848	0.17846
1	**5**	0.02355	0.03574	2.58121	2.72556	0.17268	0.1654
1	**16**	0.02618	0.04036	2.59308	2.70992	0.17569	0.16941
1	**17**	0.0324	0.04637	2.61185	2.6691	0.18323	0.17438
4	**50**	0.03284	0.04164	2.75627	2.38126	0.1907	0.15491
4	**51**	0.04127	0.06172	2.81084	2.65893	0.19842	0.18266
4	**52**	0.0443	0.06121	2.78679	2.59262	0.20583	0.18681
4	**54**	0.03658	0.05555	2.82302	2.69014	0.19886	0.18367
	Min	0.02355	0.03574	2.58121	2.38126	0.17268	0.15491
	Max	0.04430	0.06172	2.82302	2.73158	0.20583	0.18681
	Mean	0.03362	0.04902	2.70301	2.64489	0.18878	0.17446
	Std. Dev	0.00700	0.00975	0.10204	0.11546	0.01175	0.01076
	Mean - 3 Sigma	0.01261	0.01976	2.39690	2.29851	0.15353	0.14218
	Mean + 3 Sigma	0.05463	0.07828	3.00912	2.99127	0.22402	0.20674

		LINEARITY ERROR Ain 4GHz Vs @ 5v dB		GAIN Ain 4GHz Vs @ 5v V/Vrms		Output Intercept Ain 4GHz Vs @ 5v V	
Wafer	**SN**	**PRE**	**100k**	**PRE**	**100k**	**PRE**	**100k**
1	**3**	**-0.10678**	**-0.09551**	**3.66716**	**3.6826**	**0.00246**	**-0.00052079**
1	**1**	0.02247	-0.03145	3.51887	3.57059	0.01418	0.01642
1	**5**	-0.02125	-0.02966	3.53460	3.48141	0.0059	0.00207
1	**16**	0.04314	0.04101	3.51888	3.54723	0.00856	0.00839
1	**17**	0.0756	0.08754	3.38657	3.41875	0.01646	0.01703
4	**50**	-0.04457	0.04228	3.58524	3.54101	0.01336	0.01192
4	**51**	-0.13671	-0.13321	3.72813	3.74364	0.0137	0.02521
4	**52**	0.17047	0.16501	3.45648	3.46427	0.02766	0.03528
4	**54**	0.06188	0.07726	3.40132	3.3592	0.01921	0.0254
	Min	-0.13671	-0.13321	3.38657	3.35920	0.00590	0.00207
	Max	0.17047	0.16501	3.72813	3.74364	0.02766	0.03528
	Mean	0.02138	0.02735	3.51626	3.51576	0.01488	0.01772
	Std. Dev	0.09137	0.09100	0.10928	0.11608	0.00665	0.01063
	Mean - 3 Sigma	-0.25274	-0.24564	3.18841	3.16753	-0.00506	-0.01418
	Mean + 3 Sigma	0.29549	0.30033	3.84411	3.86399	0.03482	0.04961

		Output Voltage Hi Power In 4GHz Vs @ 5v V		Output Voltage Lo Power In 4GHz Vs @ 5v V		LINEARITY ERROR Ain 50MHz Vs @ 3.3v dB	
Wafer	**SN**	**PRE**	**100k**	**PRE**	**100k**	**PRE**	**100k**
1	**3**	**1.21007**	**1.21474**	**0.08646**	**0.07196**	**-0.36111**	**-0.24946**
1	**1**	1.18558	1.20557	0.09996	0.09268	-0.27938	-0.19464
1	**5**	1.1761	1.15892	0.09124	0.07723	-0.28148	-0.21759
1	**16**	1.17968	1.18925	0.09488	0.08402	-0.27278	-0.22147
1	**17**	1.14251	1.15647	0.10103	0.09274	-0.25676	-0.18024
4	**50**	1.19714	1.18422	0.10147	0.08898	-0.3138	-0.17899
4	**51**	1.22772	1.25003	0.09726	0.09896	-0.41367	-0.26186
4	**52**	1.1734	1.19427	0.11748	0.11566	-0.26663	-0.15335
4	**54**	1.15645	1.15672	0.10342	0.09984	-0.28658	-0.18311
	Min	1.14251	1.15647	0.09124	0.07723	-0.41367	-0.26186
	Max	1.22772	1.25003	0.11748	0.11566	-0.25676	-0.15335
	Mean	1.17982	1.18693	0.10084	0.09376	-0.29639	-0.19891
	Std. Dev	0.02569	0.03167	0.00779	0.01157	0.05026	0.03361
	Mean - 3 Sigma	1.10274	1.09192	0.07746	0.05906	-0.44715	-0.29975
	Mean + 3 Sigma	1.25690	1.28194	0.12423	0.12847	-0.14562	-0.09806

		GAIN Ain 50MHz Vs @ 3.3v V/Vrms		Output Intercept Ain 50MHz Vs @ 3.3v V		Output Voltage Hi Power In 50MHz Vs @ 3.3v V	
Wafer	**SN**	**PRE**	**100k**	**PRE**	**100k**	**PRE**	**100k**
1	**3**	**4.75724**	**4.89634**	**0.01891**	**0.01278**	**1.70067**	**1.68098**
1	**1**	4.71542	4.91464	0.02848	0.02771	1.70645	1.71106
1	**5**	4.6366	4.96214	0.01896	0.01287	1.66689	1.71144
1	**16**	4.64039	4.93488	0.02192	0.01875	1.67229	1.70943
1	**17**	4.64198	4.8554	0.02866	0.02465	1.68183	1.69166
4	**50**	4.88655	4.50037	0.02879	0.01758	1.75568	1.54993
4	**51**	5.00857	4.94842	0.03308	0.0315	1.78657	1.70742
4	**52**	4.88572	4.74054	0.04188	0.04101	1.77608	1.65913
4	**54**	4.90266	4.74133	0.03621	0.03695	1.78443	1.65103
	Min	4.63660	4.50037	0.01896	0.01287	1.66689	1.54993
	Max	5.00857	4.96214	0.04188	0.04101	1.78657	1.71144
	Mean	4.78974	4.82472	0.02975	0.02638	1.72878	1.67389
	Std. Dev	0.14752	0.15793	0.00738	0.00982	0.05227	0.05555
	Mean - 3 Sigma	4.34718	4.35093	0.00760	-0.00308	1.57198	1.50724
	Mean + 3 Sigma	5.23230	5.29850	0.05189	0.05584	1.88557	1.84054

		Output Voltage Lo Power In 50MHz Vs @ 3.3v V		LINEARITY ERROR Ain 100MHz Vs @ 3.3v dB		GAIN Ain 100MHz Vs @ 3.3v V/Vrms	
Wafer	**SN**	**PRE**	**100k**	**PRE**	**100k**	**PRE**	**100k**
1	**3**	**0.10084**	**0.09463**	**-0.30496**	**-0.3286**	**6.34917**	**6.56666**
1	**1**	0.1151	0.11428	-0.23226	-0.27145	6.33735	6.64888
1	**5**	0.1043	0.10103	-0.26135	-0.27188	6.20198	6.7061
1	**16**	0.10725	0.10612	-0.25015	-0.27227	6.20779	6.64421
1	**17**	0.11585	0.11321	-0.22921	-0.25703	6.22895	6.52268
4	**50**	0.11817	0.10059	-0.25567	-0.31643	6.58035	5.89715
4	**51**	0.11723	0.11541	-0.33242	-0.38774	6.73895	6.56905
4	**52**	0.13607	0.13155	-0.2062	-0.25124	6.59883	6.32303
4	**54**	0.12627	0.12088	-0.16763	-0.21813	6.65825	6.54788
	Min	0.10430	0.10059	-0.33242	-0.38774	6.20198	5.89715
	Max	0.13607	0.13155	-0.16763	-0.21813	6.73895	6.70610
	Mean	0.11753	0.11288	-0.24186	-0.28077	6.44406	6.48237
	Std. Dev	0.01007	0.01041	0.04762	0.05115	0.22279	0.26330
	Mean - 3 Sigma	0.08732	0.08166	-0.38472	-0.43421	5.77568	5.69248
	Mean + 3 Sigma	0.14774	0.14411	-0.09900	-0.12733	7.11243	7.27226

		Output Intercept Ain 100MHz Vs @ 3.3v V		Output Voltage Hi Power In 100MHz Vs @ 3.3v V		Output Voltage Lo Power In 100MHz Vs @ 3.3v V	
Wafer	**SN**	**PRE**	**100k**	**PRE**	**100k**	**PRE**	**100k**
1	**3**	**0.02485**	**0.03717**	**2.63282**	**2.65887**	**0.1745**	**0.16062**
1	**1**	0.03063	0.04759	2.65606	2.7189	0.18373	0.17664
1	**5**	0.02268	0.03435	2.57625	2.72141	0.17167	0.1637
1	**16**	0.02533	0.0398	2.58742	2.70565	0.17481	0.16885
1	**17**	0.03097	0.045	2.60601	2.66414	0.18216	0.1723
4	**50**	0.03162	0.04007	2.75043	2.37944	0.189	0.1534
4	**51**	0.0395	0.05983	2.80431	2.65491	0.19648	0.18116
4	**52**	0.04309	0.05928	2.78076	2.59262	0.2042	0.18411
4	**54**	0.03499	0.05393	2.81674	2.6897	0.19736	0.18279
	Min	0.02268	0.03435	2.57625	2.37944	0.17167	0.15340
	Max	0.04309	0.05983	2.81674	2.72141	0.20420	0.18411
	Mean	0.03235	0.04748	2.69725	2.64085	0.18743	0.17287
	Std. Dev	0.00678	0.00946	0.10161	0.11375	0.01142	0.01061
	Mean - 3 Sigma	0.01202	0.01912	2.39243	2.29961	0.15316	0.14105
	Mean + 3 Sigma	0.05269	0.07585	3.00207	2.98209	0.22169	0.20469

		LINEARITY ERROR Ain 4GHz Vs @ 3.3v dB		GAIN Ain 4GHz Vs @ 3.3v V/Vrms		Output Intercept Ain 4GHz Vs @ 3.3v V	
Wafer	**SN**	**PRE**	**100k**	**PRE**	**100k**	**PRE**	**100k**
1	**3**	**-0.07516**	**-0.06986**	**3.6385**	**3.65543**	**0.00236**	**-0.0005919**
1	**1**	0.0442	-0.02765	3.48828	3.542	0.01343	0.01595
1	**5**	-0.01937	-0.00947	3.50273	3.45536	0.00507	0.00112
1	**16**	0.03146	0.0314	3.49247	3.52404	0.0075	0.00726
1	**17**	0.09089	0.08558	3.34893	3.38843	0.01536	0.01567
4	**50**	0.02003	-0.038	3.5558	3.60014	0.01276	0.01224
4	**51**	-0.13408	-0.08633	3.69209	3.70213	0.01331	0.0238
4	**52**	-0.08768	0.14659	3.35624	3.43765	0.02975	0.03358
4	**54**	0.0877	0.08145	3.37324	3.32847	0.01845	0.02377
	Min	-0.13408	-0.08633	3.34893	3.32847	0.00507	0.00112
	Max	0.09089	0.14659	3.69209	3.70213	0.02975	0.03358
	Mean	0.00414	0.02295	3.47622	3.49728	0.01445	0.01667
	Std. Dev	0.08036	0.07743	0.11665	0.12007	0.00749	0.01027
	Mean - 3 Sigma	-0.23693	-0.20935	3.12628	3.13707	-0.00801	-0.01414
	Mean + 3 Sigma	0.24521	0.25525	3.82617	3.85749	0.03692	0.04749

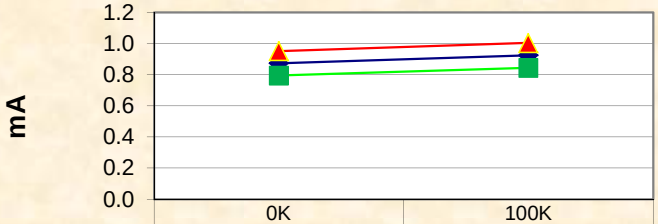
		Output Voltage Hi Power In 4GHz Vs @ 3.3v V		Output Voltage Lo Power In 4GHz Vs @ 3.3v V		OFFSET @ 3.3V V	
Wafer	**SN**	**PRE**	**100k**	**PRE**	**100k**	**PRE**	**100k**
1	**3**	**1.20185**	**1.20683**	**0.08546**	**0.07146**	**0.0237**	**0.03015**
1	**1**	1.17579	1.19741	0.09865	0.09193	0.05397	0.05948
1	**5**	1.16662	1.14944	0.09023	0.07535	0.04374	0.04604
1	**16**	1.17108	1.18077	0.09318	0.08276	0.04801	0.05238
1	**17**	1.13051	1.14674	0.09909	0.09055	0.05874	0.06369
4	**50**	1.18784	1.20614	0.09996	0.09055	0.05623	0.05753
4	**51**	1.21748	1.23697	0.09689	0.09676	0.03626	0.0532
4	**52**	1.1518	1.18573	0.11642	0.11422	0.08116	0.08975
4	**54**	1.14747	1.1456	0.10204	0.09739	0.06056	0.0689
	Min	1.13051	1.14560	0.09023	0.07535	0.03626	0.04604
	Max	1.21748	1.23697	0.11642	0.11422	0.08116	0.08975
	Mean	1.16857	1.18110	0.09956	0.09244	0.05483	0.06137
	Std. Dev	0.02673	0.03269	0.00781	0.01141	0.01341	0.01346
	Mean - 3 Sigma	1.08839	1.08304	0.07614	0.05822	0.01461	0.02099
	Mean + 3 Sigma	1.24876	1.27916	0.12297	0.12666	0.09506	0.10175

		OFFSET @ 5v V	
Wafer	**SN**	**PRE**	**100k**
1	**3**	**0.00788**	**0.01238**
1	**1**	0.04286	0.04824
1	**5**	0.03168	0.03336
1	**16**	0.03871	0.04265
1	**17**	0.05121	0.05559
4	**50**	0.04675	0.04893
4	**51**	0.01768	0.03574
4	**52**	0.07469	0.08403
4	**54**	0.05146	0.06048
	Min	0.01768	0.03336
	Max	0.07469	0.08403
	Mean	0.04438	0.05113
	Std. Dev	0.01660	0.01616
	Mean - 3 Sigma	-0.00543	0.00266
	Mean + 3 Sigma	0.09419	0.09960

+Supply Current Vs=+3.3V, No RFIN

	0K	100K
Average	0.88169	0.93235
-3 Sigma	0.80345	0.85259
+3 Sigma	0.95992	1.01211

+Supply Current Vs=+2.97V, No RFIN



	0K	100K
Average	0.87333	0.92436
-3 Sigma	0.79546	0.84480
+3 Sigma	0.95120	1.00391

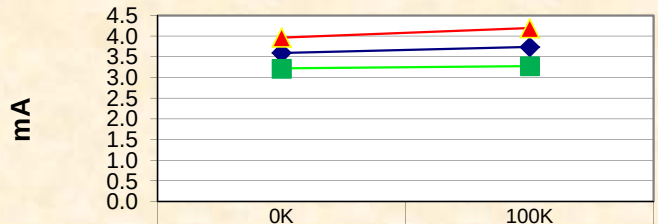
+Supply Current Vs=+5V, No RFIN

	0K	100K
Average	0.92813	0.97591
-3 Sigma	0.84742	0.89427
+3 Sigma	1.00884	1.05754

+Supply Current Vs=+5.25V, No RFIN

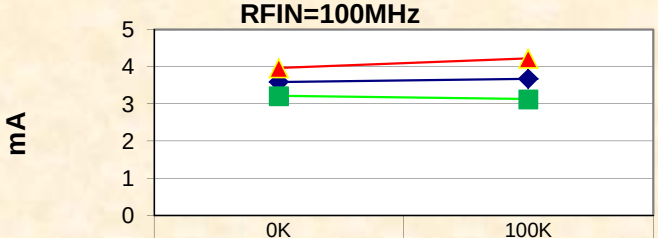
	0K	100K
Average	0.93638	0.98380
-3 Sigma	0.85562	0.90135
+3 Sigma	1.01713	1.06625

+Supply Current Vs=+3.3V, RFIN=100MHz



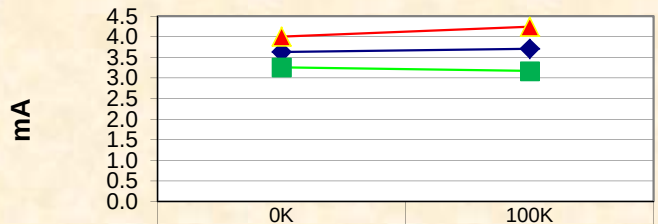
	0K	100K
Average	3.59395	3.73544
-3 Sigma	3.22114	3.27700
+3 Sigma	3.96677	4.19388

+Supply Current Vs=+2.97V, RFIN=100MHz



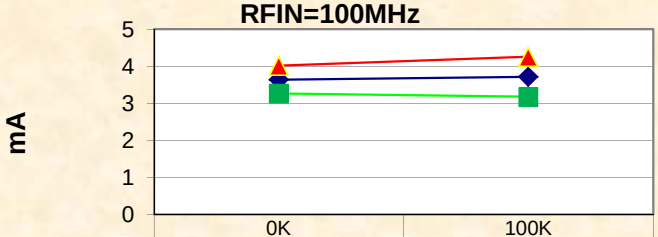
	0K	100K
Average	3.59083	3.67571
-3 Sigma	3.21669	3.12870
+3 Sigma	3.96497	4.22273

+Supply Current Vs=+5V, RFIN=100MHz



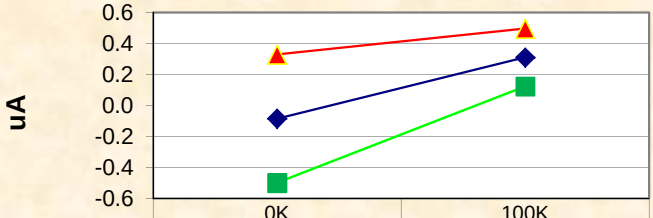
	0K	100K
Average	3.63286	3.70854
-3 Sigma	3.25967	3.17010
+3 Sigma	4.00604	4.24697

+Supply Current Vs=+5.25V, RFIN=100MHz



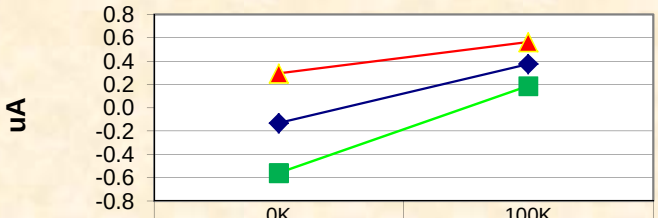
	0K	100K
Average	3.64032	3.71517
-3 Sigma	3.26690	3.17628
+3 Sigma	4.01375	4.25407

Disable Current @ 3.3v No RFIN



	0K	100K
Average	-0.08523	0.30988
-3 Sigma	-0.49921	0.12338
+3 Sigma	0.32875	0.49637

Disable Current @ 2.97v No RFIN

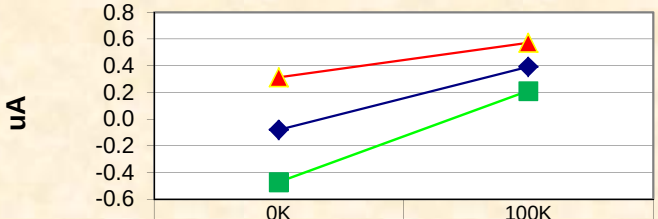


	0K	100K
Average	-0.13146	0.37565
-3 Sigma	-0.55748	0.18789
+3 Sigma	0.29457	0.56341

Disable Current @ 5v No RFIN

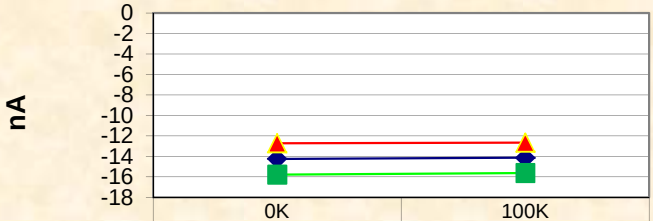
	0K	100K
Average	-0.06285	0.38409
-3 Sigma	-0.54722	0.11354
+3 Sigma	0.42153	0.65463

Disable Current @ 5.25v No RFIN



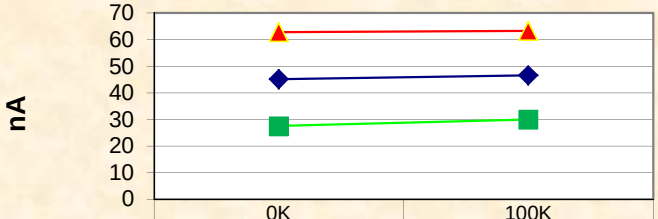
	0K	100K
Average	-0.07938	0.39235
-3 Sigma	-0.47219	0.21145
+3 Sigma	0.31343	0.57324

Input Current Low (Enable) Vs @ 2.97v



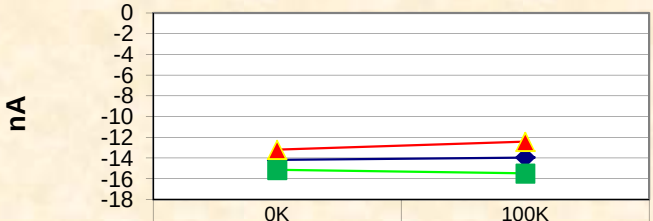
	0K	100K
Average	-14.23339	-14.13847
-3 Sigma	-15.76378	-15.61723
+3 Sigma	-12.70301	-12.65970

Input Current High (Enable) Vs = 2.97v



	0K	100K
Average	45.17335	46.60902
-3 Sigma	27.48140	29.90212
+3 Sigma	62.86529	63.31591

Input Current Low (Enable) Vs = 3.3v

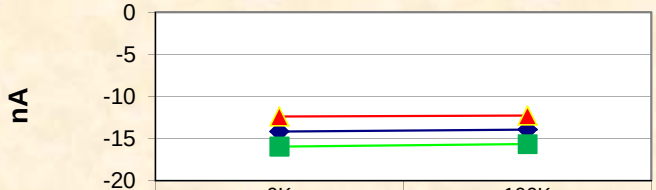


	0K	100K
Average	-14.16298	-13.94131
-3 Sigma	-15.13488	-15.47159
+3 Sigma	-13.19108	-12.41103

Input Current High (Enable) Vs = 3.3v

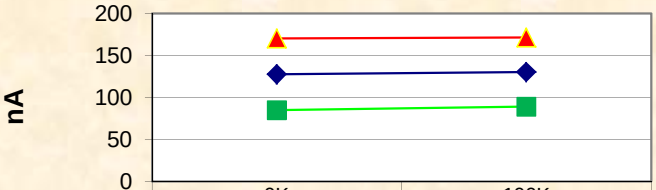
	0K	100K
Average	58.90788	60.67509
-3 Sigma	35.35578	38.59777
+3 Sigma	82.45997	82.75240

Input Current Low (Enable) Vs = 5v



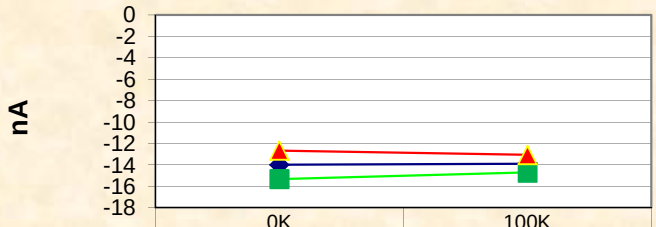
	0K	100K
Average	-14.14889	-13.94131
-3 Sigma	-15.92830	-15.62398
+3 Sigma	-12.36948	-12.25865

Input Current High (Enable) Vs = 5v



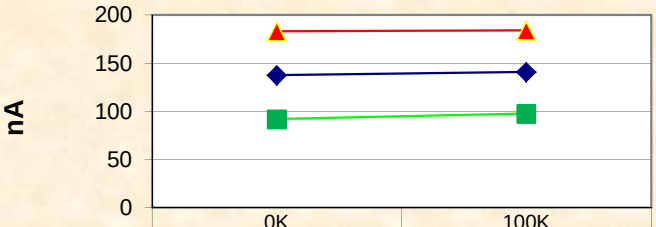
	0K	100K
Average	127.71059	130.30552
-3 Sigma	85.03298	89.21207
+3 Sigma	170.38819	171.39898

Input Current Low (Enable) Vs = 5.25v



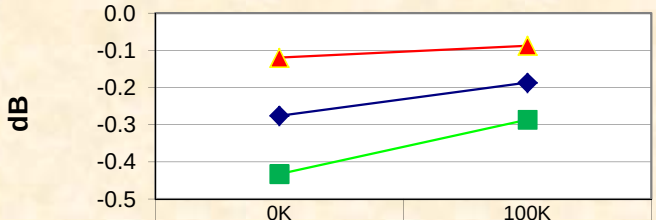
	0K	100K
Average	-13.99398	-13.88498
-3 Sigma	-15.32700	-14.70793
+3 Sigma	-12.66095	-13.06204

Input Current High (Enable) Vs = 5.25v



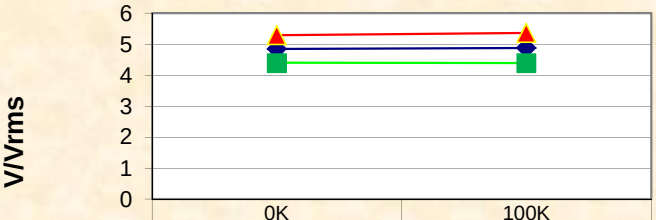
	0K	100K
Average	137.54043	140.86444
-3 Sigma	92.00125	97.72814
+3 Sigma	183.07960	184.00073

LINEARITY ERROR Ain 50MHz Vs = 5v



	0K	100K
Average	-0.27610	-0.18691
-3 Sigma	-0.43245	-0.28631
+3 Sigma	-0.11975	-0.08752

GAIN Ain 50MHz Vs = 5v

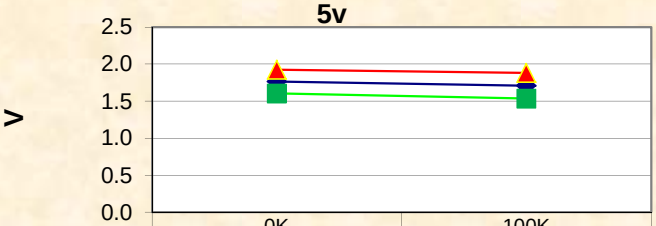


	0K	100K
Average	4.84514	4.87621
-3 Sigma	4.39753	4.39234
+3 Sigma	5.29276	5.36008

Output Intercept Ain 50MHz Vs = 5v

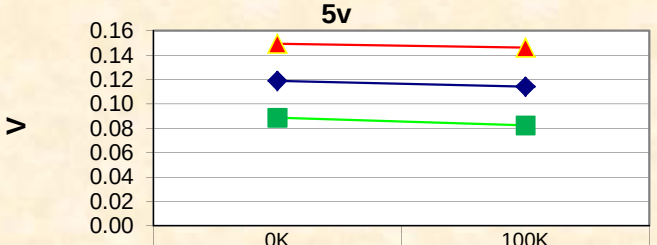
	0K	100K
Average	0.02944	0.02648
-3 Sigma	0.00673	-0.00357
+3 Sigma	0.05216	0.05654

Output Voltage Hi Power In 50MHz Vs = 5v



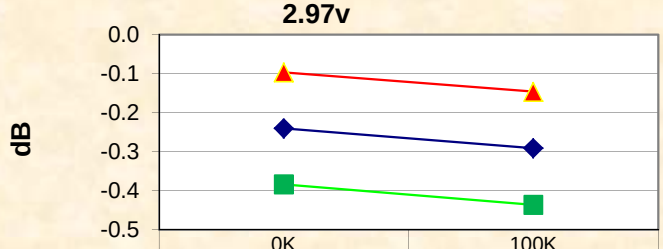
	0K	100K
Average	1.76558	1.70835
-3 Sigma	1.60571	1.53463
+3 Sigma	1.92545	1.88207

Output Voltage Lo Power In 50MHz Vs = 5v



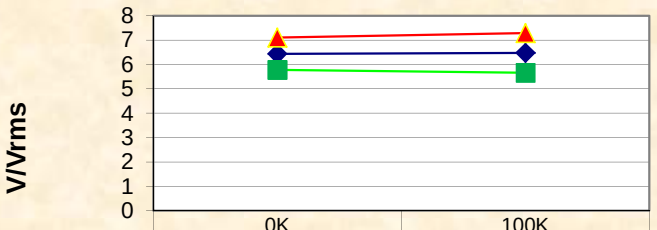
	0K	100K
Average	0.11897	0.11431
-3 Sigma	0.08868	0.08235
+3 Sigma	0.14925	0.14627

LINEARITY ERROR Ain 100MHz Vs = 2.97v



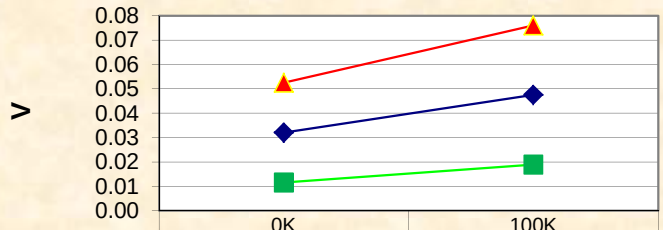
	0K	100K
Average	-0.23962	-0.29086
-3 Sigma	-0.38318	-0.43571
+3 Sigma	-0.09606	-0.14602

GAIN Ain 100MHz Vs = 2.97v



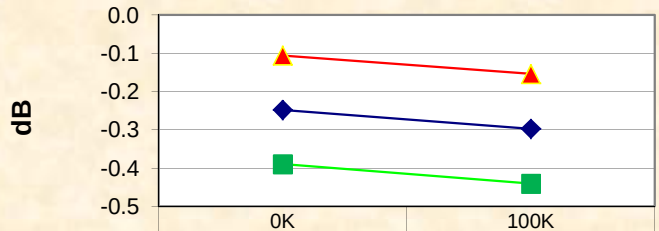
	0K	100K
Average	6.43807	6.47280
-3 Sigma	5.77391	5.65641
+3 Sigma	7.10223	7.28918

Output Intercept Ain 100MHz Vs = 2.97v



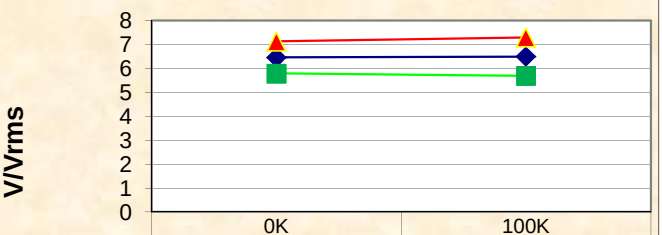
	0K	100K
Average	0.03210	0.04750
-3 Sigma	0.01163	0.01899
+3 Sigma	0.05256	0.07602

LINEARITY ERROR Ain 100MHz Vs=5v



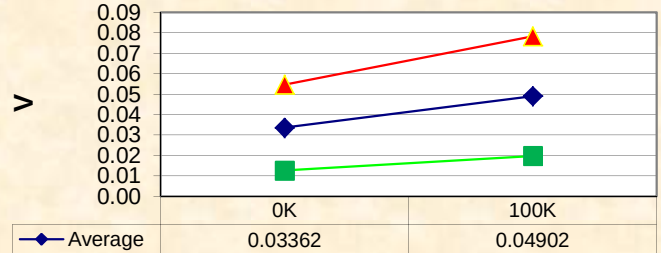
	0K	100K
Average	-0.24762	-0.29697
-3 Sigma	-0.38970	-0.44047
+3 Sigma	-0.10554	-0.15346

GAIN Ain 100MHz Vs = 5v



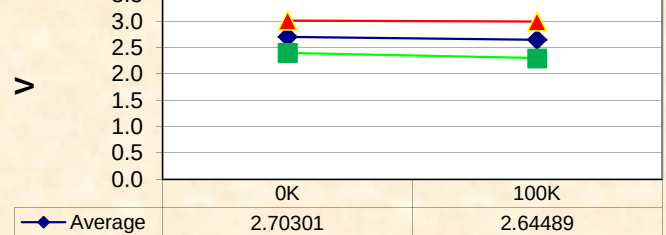
	0K	100K
Average	6.46233	6.49863
-3 Sigma	5.79199	5.69711
+3 Sigma	7.13268	7.30016

Output Intercept Ain 100MHz Vs = 5v



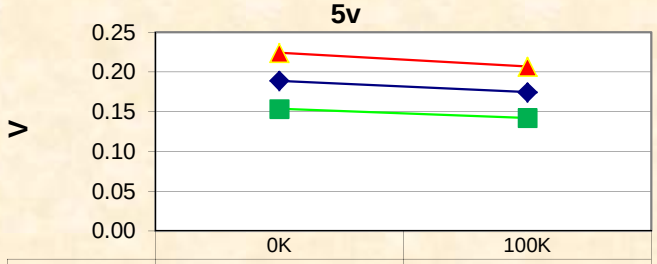
	0K	100K
Average	0.03362	0.04902
-3 Sigma	0.01261	0.01976
+3 Sigma	0.05463	0.07828

Output Voltage Hi Power In 100MHz Vs = 5v



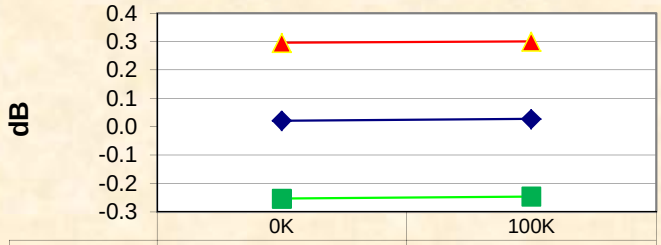
	0K	100K
Average	2.70301	2.64489
-3 Sigma	2.39690	2.29851
+3 Sigma	3.00912	2.99127

Output Voltage Lo Power In 100MHz Vs = 5v



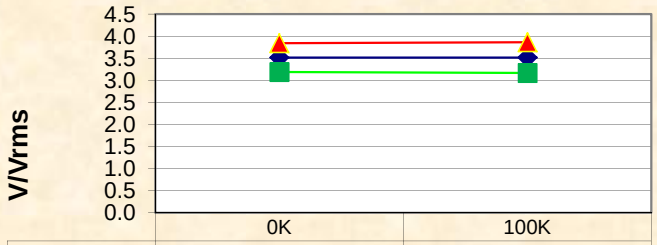
	0K	100K
Average	0.18878	0.17446
-3 Sigma	0.15353	0.14218
+3 Sigma	0.22402	0.20674

LINEARITY ERROR Ain 4GHz Vs = 5v



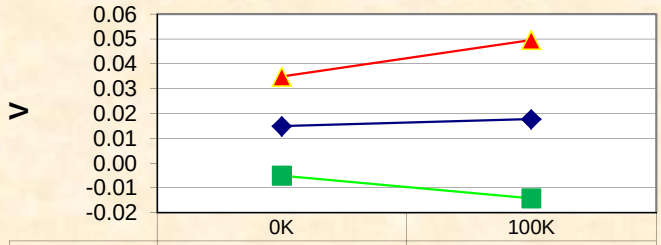
	0K	100K
Average	0.02138	0.02735
-3 Sigma	-0.25274	-0.24564
+3 Sigma	0.29549	0.30033

GAIN Ain 4GHz Vs = 5v



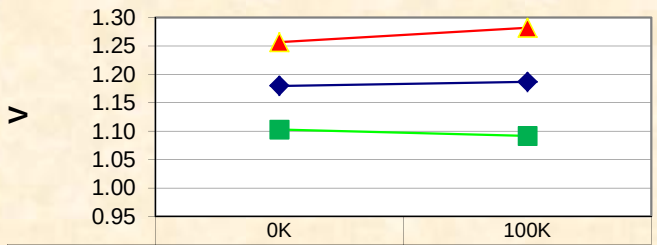
	0K	100K
Average	3.51626	3.51576
-3 Sigma	3.18841	3.16753
+3 Sigma	3.84411	3.86399

Output Intercept Ain 4GHz Vs = 5v



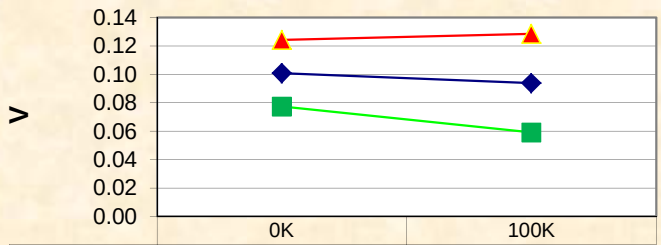
	0K	100K
Average	0.01488	0.01772
-3 Sigma	-0.00506	-0.01418
+3 Sigma	0.03482	0.04961

Output Voltage Hi Power In 4GHz Vs=5v



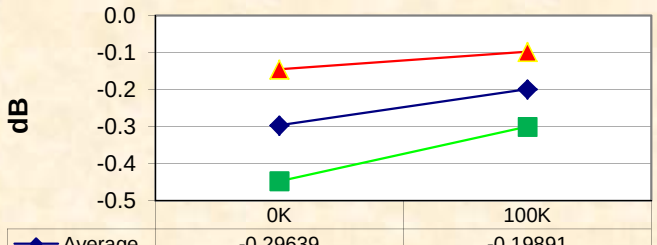
	0K	100K
Average	1.17982	1.18693
-3 Sigma	1.10274	1.09192
+3 Sigma	1.25690	1.28194

Output Voltage Lo Power In 4GHz Vs = 5v



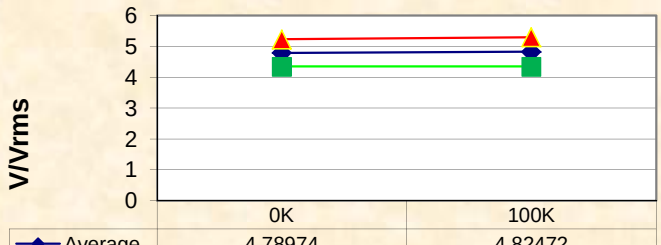
	0K	100K
Average	0.10084	0.09376
-3 Sigma	0.07746	0.05906
+3 Sigma	0.12423	0.12847

LINEARITY ERROR Ain 50MHz Vs = 3.3v



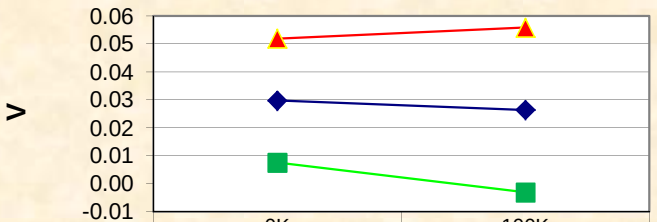
	0K	100K
Average	-0.29639	-0.19891
-3 Sigma	-0.44715	-0.29975
+3 Sigma	-0.14562	-0.09806

GAIN Ain 50MHz Vs = 3.3v



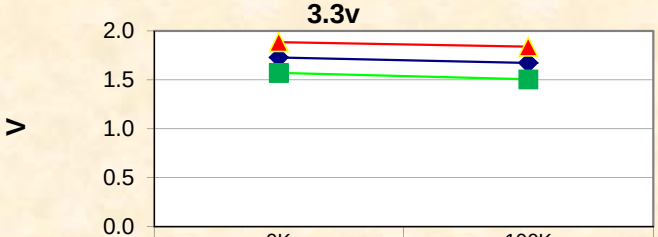
	0K	100K
Average	4.78974	4.82472
-3 Sigma	4.34718	4.35093
+3 Sigma	5.23230	5.29850

Output Intercept Ain 50MHz Vs = 3.3v



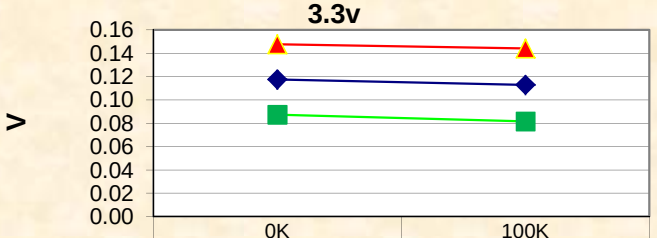
	0K	100K
Average	0.02975	0.02638
-3 Sigma	0.00760	-0.00308
+3 Sigma	0.05189	0.05584

Output Voltage Hi Power In 50MHz Vs = 3.3v



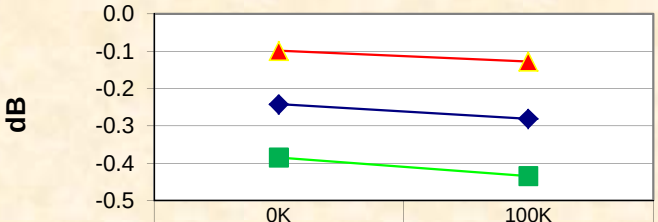
	0K	100K
Average	1.72878	1.67389
-3 Sigma	1.57198	1.50724
+3 Sigma	1.88557	1.84054

Output Voltage Lo Power In 50MHz Vs = 3.3v



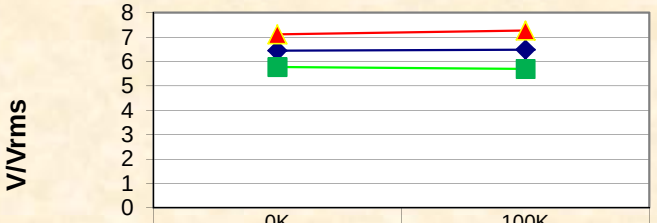
	0K	100K
Average	0.11753	0.11288
-3 Sigma	0.08732	0.08166
+3 Sigma	0.14774	0.14411

LINEARITY ERROR Ain 100MHz Vs = 3.3v



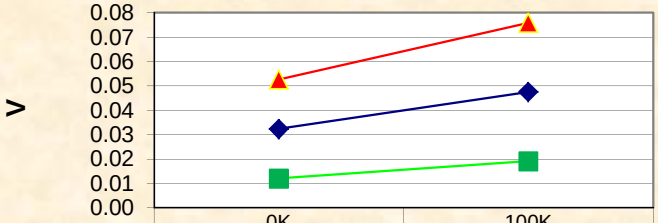
	0K	100K
Average	-0.24186	-0.28077
-3 Sigma	-0.38472	-0.43421
+3 Sigma	-0.09900	-0.12733

GAIN Ain 100MHz Vs = 3.3v



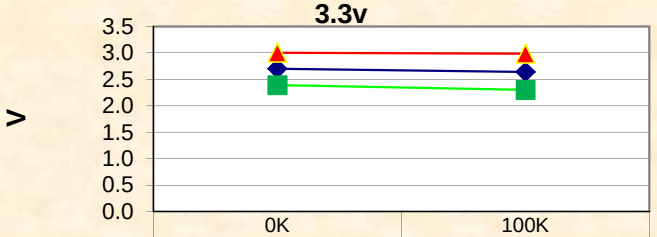
	0K	100K
Average	6.44406	6.48237
-3 Sigma	5.77568	5.69248
+3 Sigma	7.11243	7.27226

Output Intercept Ain 100MHz Vs = 3.3v



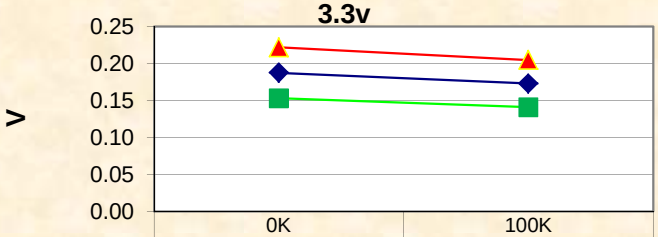
	0K	100K
Average	0.03235	0.04748
-3 Sigma	0.01202	0.01912
+3 Sigma	0.05269	0.07585

Output Voltage Hi Power In 100MHz Vs = 3.3v



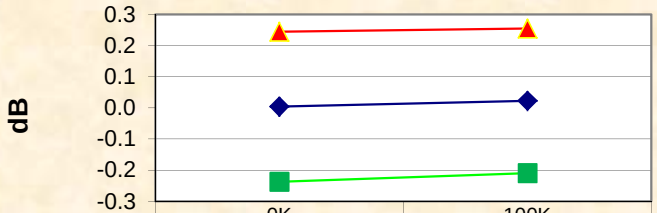
	0K	100K
Average	2.69725	2.64085
-3 Sigma	2.39243	2.29961
+3 Sigma	3.00207	2.98209

Output Voltage Lo Power In 100MHz Vs = 3.3v



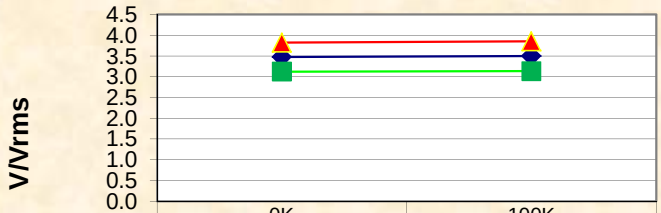
	0K	100K
Average	0.18743	0.17287
-3 Sigma	0.15316	0.14105
+3 Sigma	0.22169	0.20469

LINEARITY ERROR Ain 4GHz Vs = 3.3v



	0K	100K
Average	0.00414	0.02295
-3 Sigma	-0.23693	-0.20935
+3 Sigma	0.24521	0.25525

GAIN Ain 4GHz Vs = 3.3v

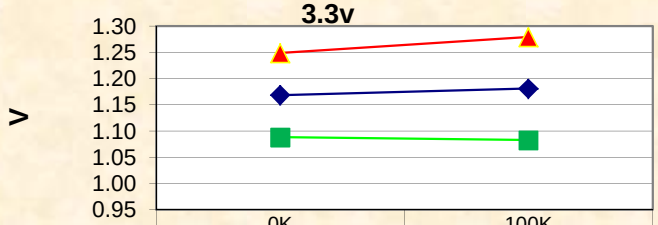


	0K	100K
Average	3.47622	3.49728
-3 Sigma	3.12628	3.13707
+3 Sigma	3.82617	3.85749

Output Intercept Ain 4GHz Vs = 3.3v

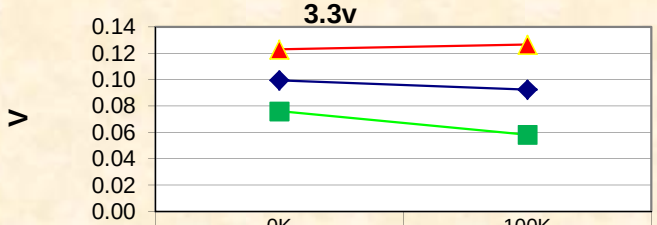
	0K	100K
Average	0.01445	0.01667
-3 Sigma	-0.00801	-0.01414
+3 Sigma	0.03692	0.04749

Output Voltage Hi Power In 4GHz Vs = 3.3v



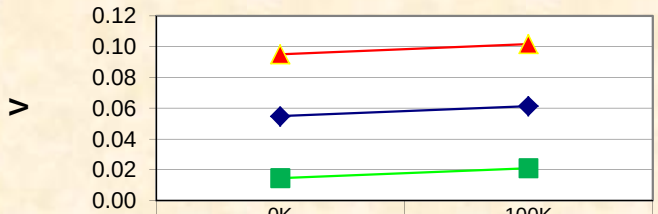
	0K	100K
Average	1.16857	1.18110
-3 Sigma	1.08839	1.08304
+3 Sigma	1.24876	1.27916

Output Voltage Lo Power In 4GHz Vs = 3.3v



	0K	100K
Average	0.09956	0.09244
-3 Sigma	0.07614	0.05822
+3 Sigma	0.12297	0.12666

OFFSET = 3.3V



	0K	100K
Average	0.05483	0.06137
-3 Sigma	0.01461	0.02099
+3 Sigma	0.09506	0.10175

OFFSET = 5v

	0K	100K
Average	0.04438	0.05113
-3 Sigma	-0.00543	0.00266
+3 Sigma	0.09419	0.09960