
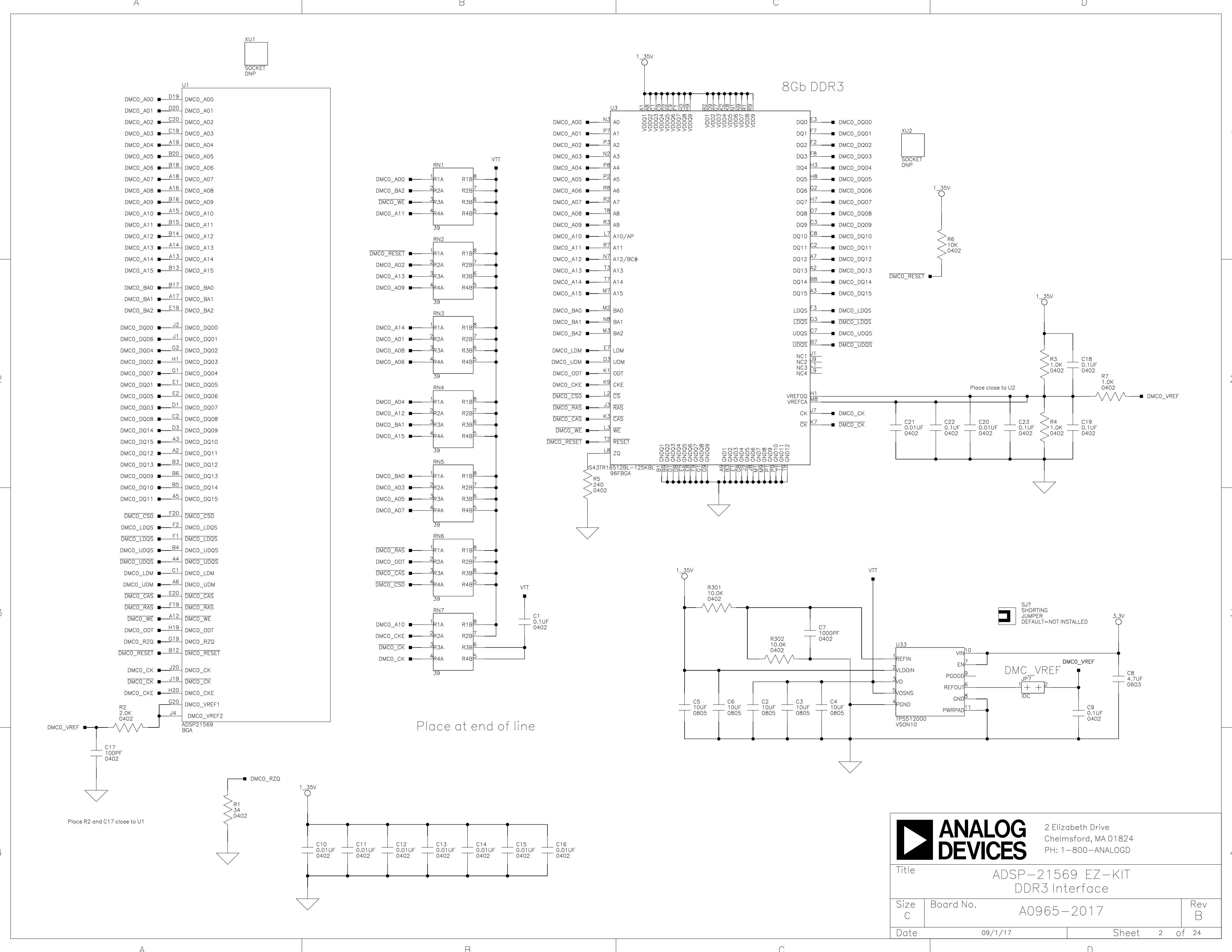


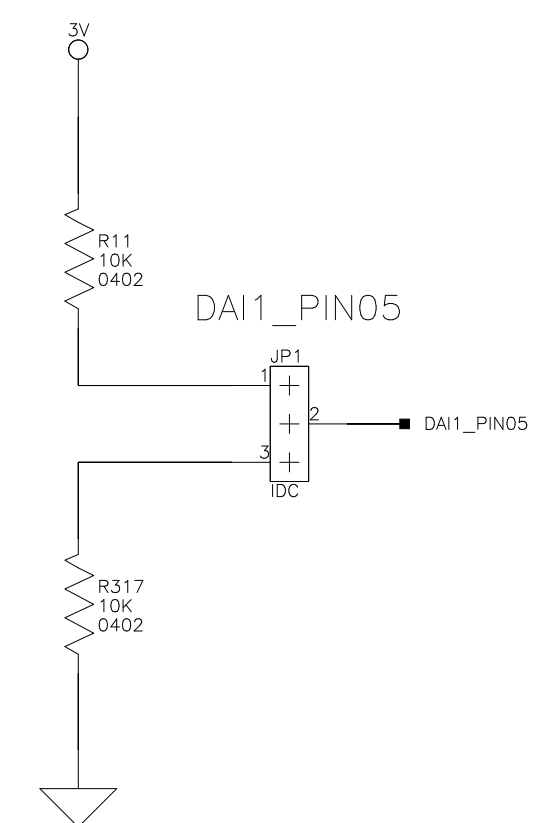
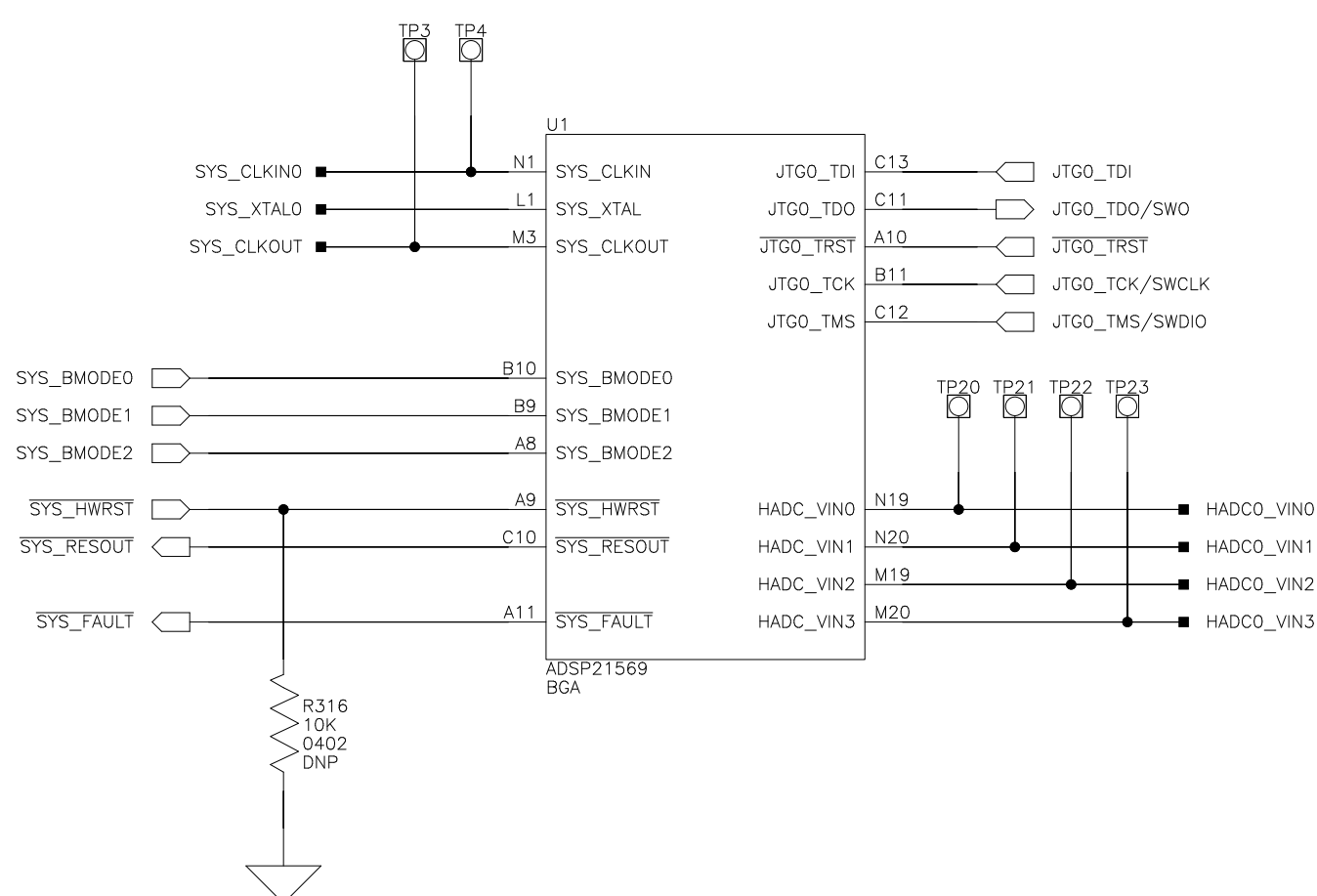
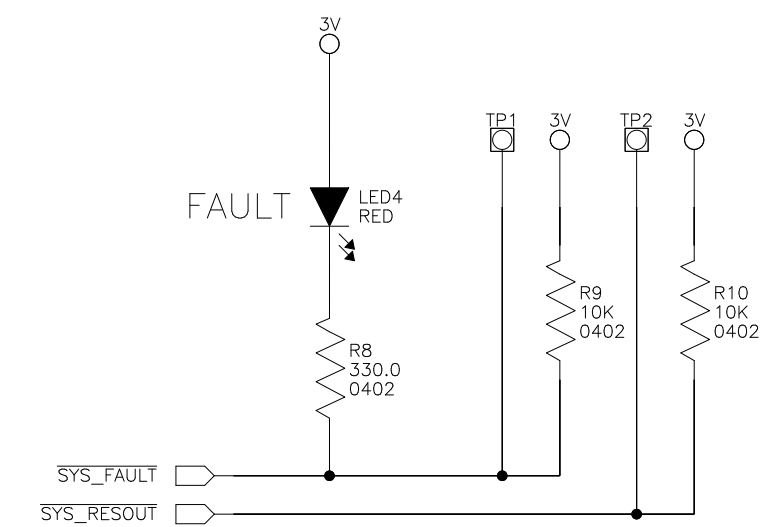
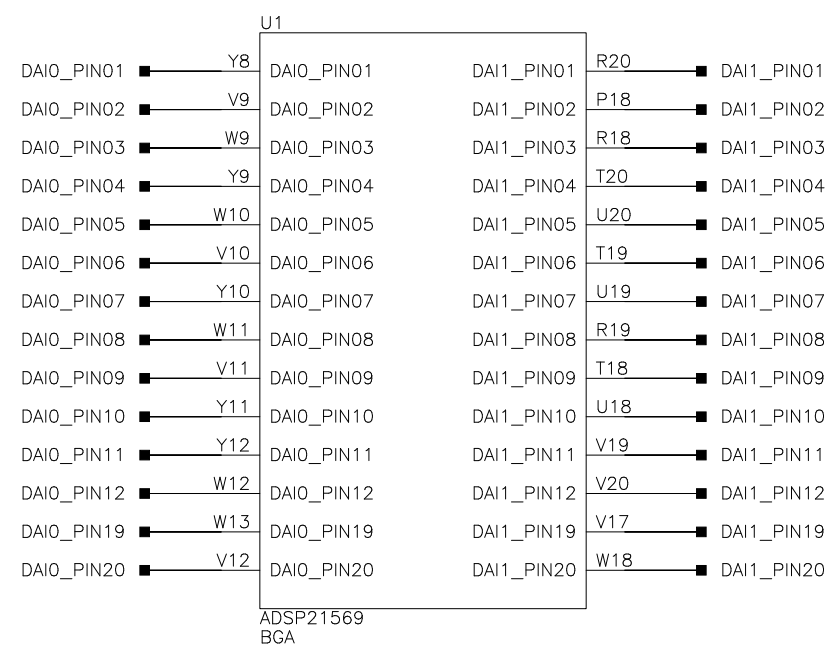
ADSP-21569 EZ-KIT

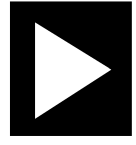
Schematic

		2 Elizabeth Drive Chelmsford, MA 01824 PH: 1-800-ANALOGD	
Title		ADSP-21569 EZ-KIT Title Block	
Size C	Board No.	A0965-2017	Rev B
Date	09/1/17	Sheet	1 of 24

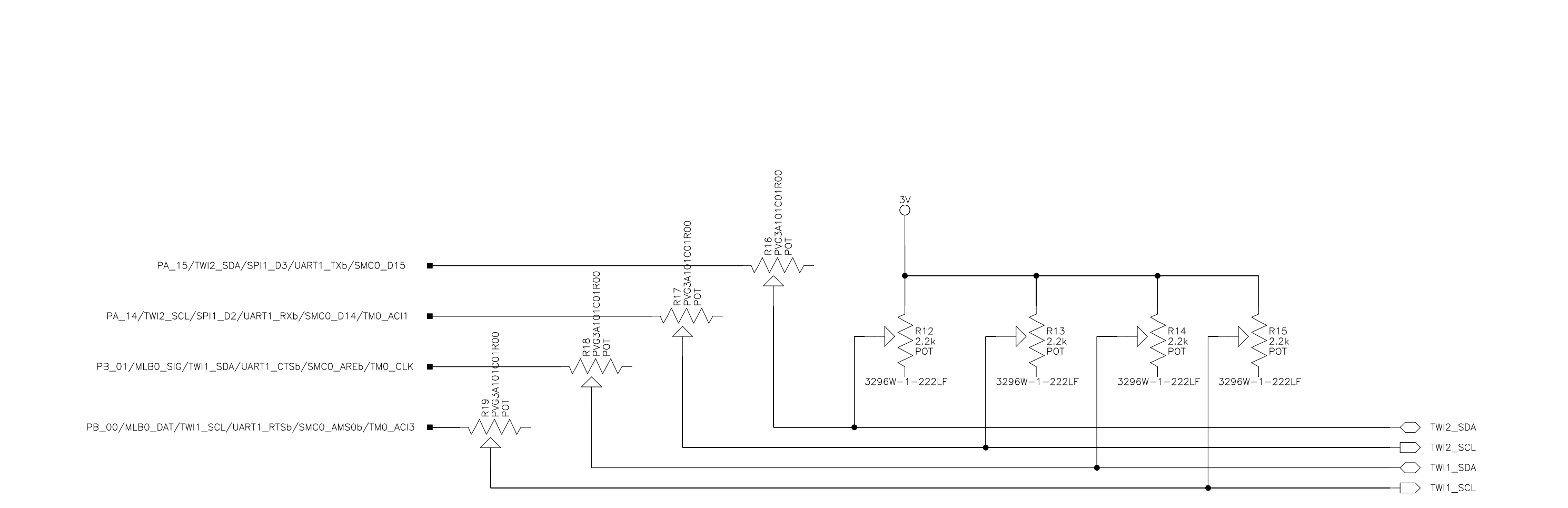
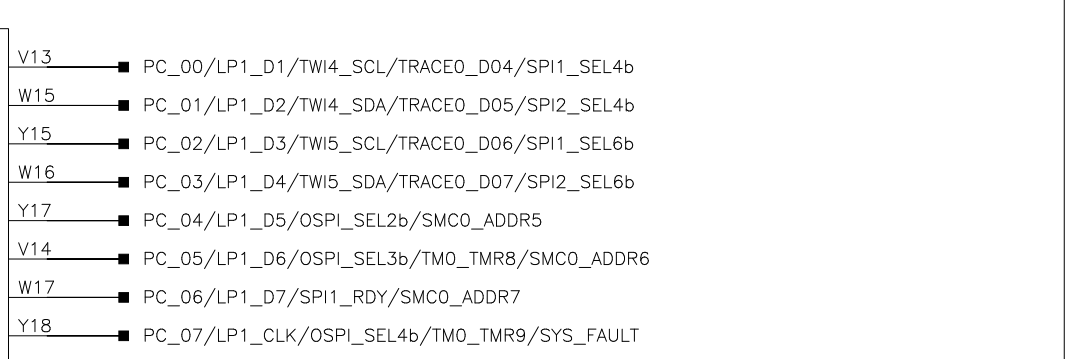
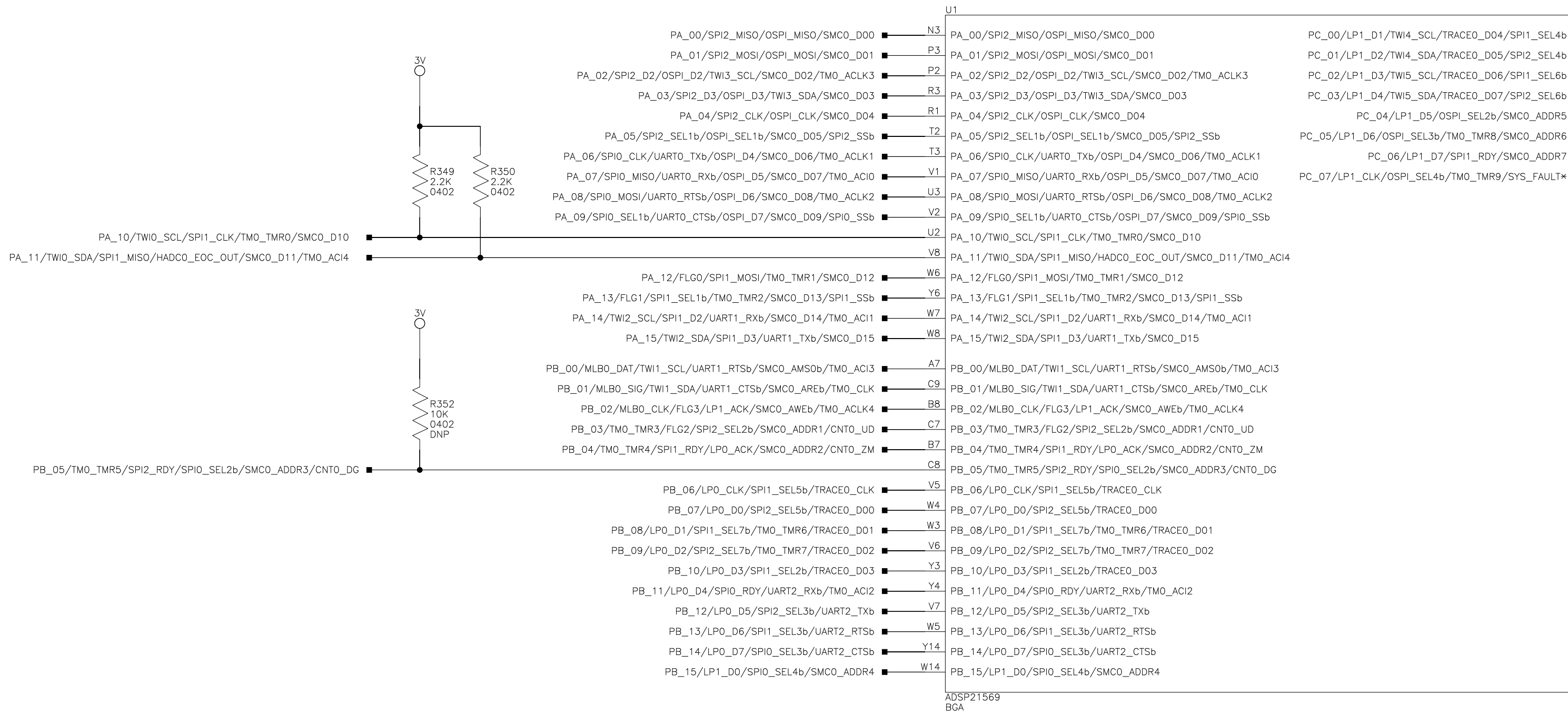


		2 Elizabeth Drive Chelmsford, MA 01824 PH: 1-800-ANALOGD	
		Title ADSP-21569 EZ-KIT DDR3 Interface	
Size C	Board No. A0965-2017	Rev B	
Date 09/1/17	Sheet 2 of 24		

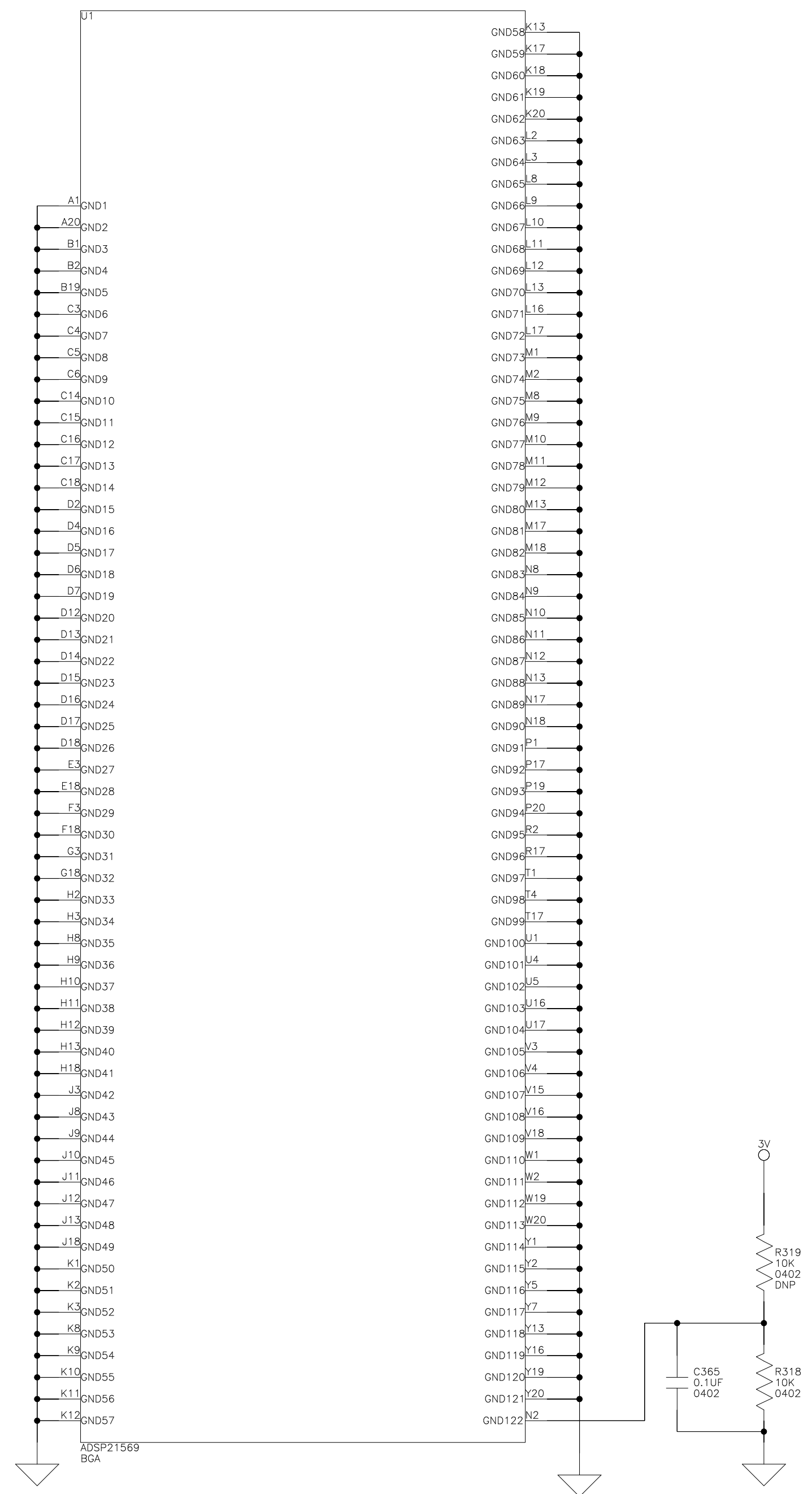
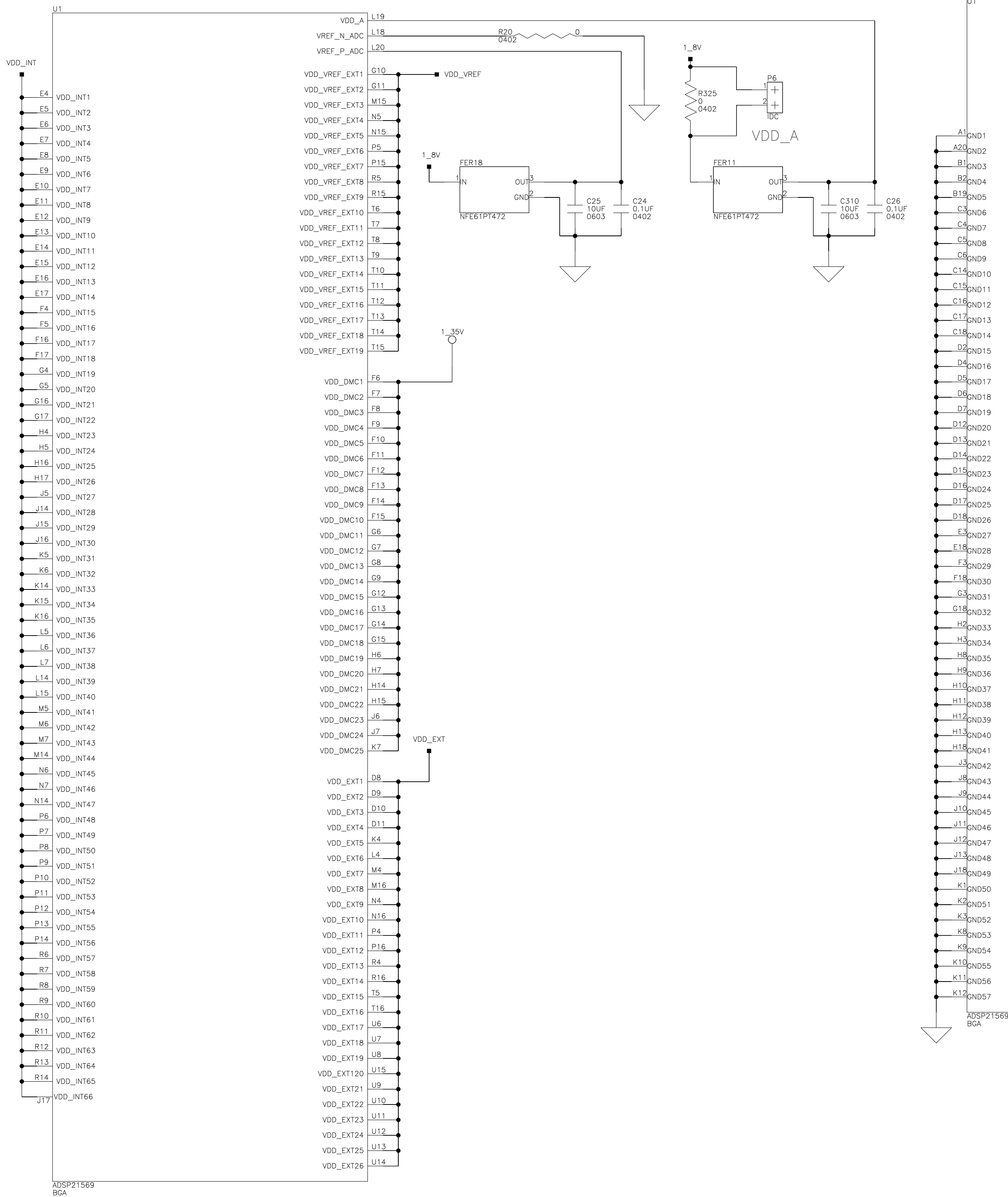




ANALOG DEVICES
 2 Elizabeth Drive
 Chelmsford, MA 01824
 PH: 1-800-ANALOGD

Title		ADSP-21569 EZ-KIT Non-Mux Ports	
Size C	Board No.	A0965-2017	Rev B
Date	09/1/17	Sheet	3 of 24



		2 Elizabeth Drive Chelmsford, MA 01824 PH: 1-800-ANALOGD	
Title		ADSP-21569 EZ-KIT Mux Ports	
Size C	Board No.	A0965-2017	Rev B
Date	09/1/17	Sheet	4 of 24



		2 Elizabeth Drive Chelmsford, MA 01824 PH: 1-800-ANALOGD	
Title ADSP-21569 EZ-KIT Processor Power/Ground			
Size C	Board No. A0965-2017	Rev B	
Date 09/1/17	Sheet 5 of 24		

A

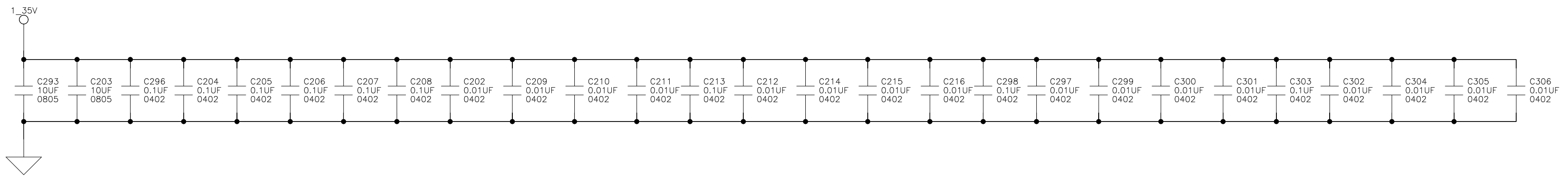
B

C

D

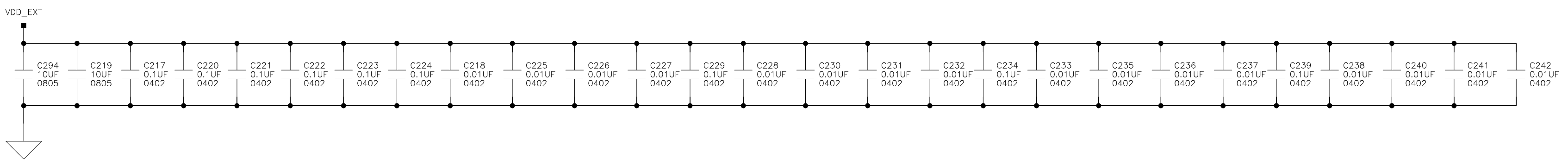
1

1



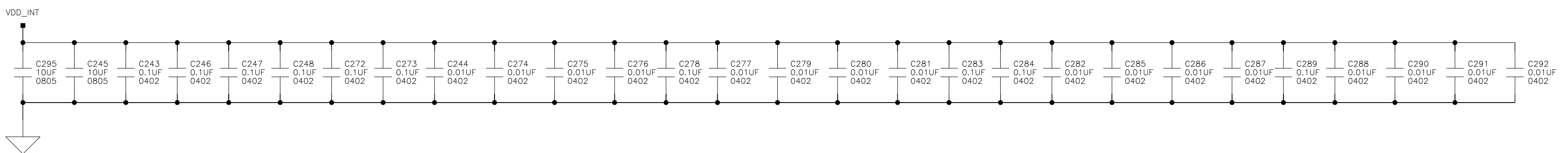
2

2



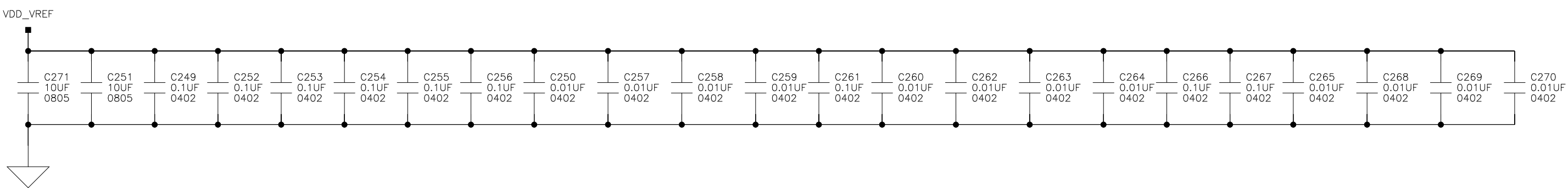
3

3



4

4




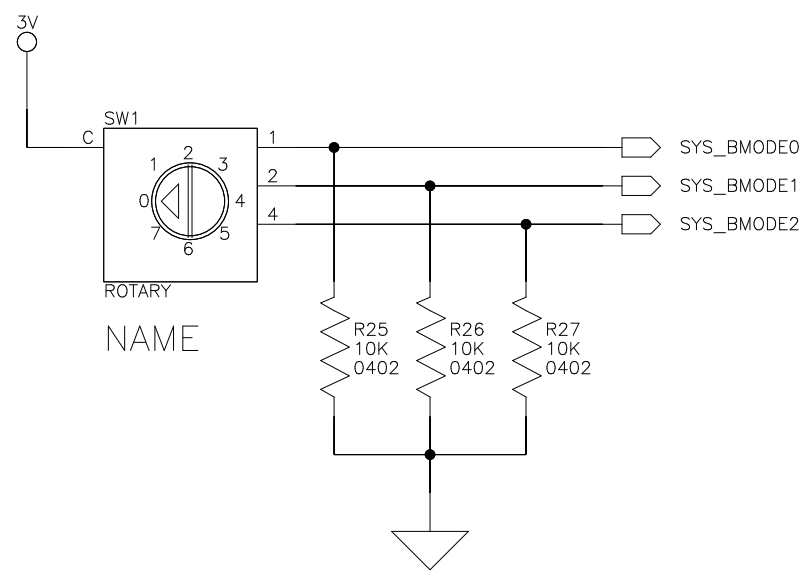
A

B

C

D

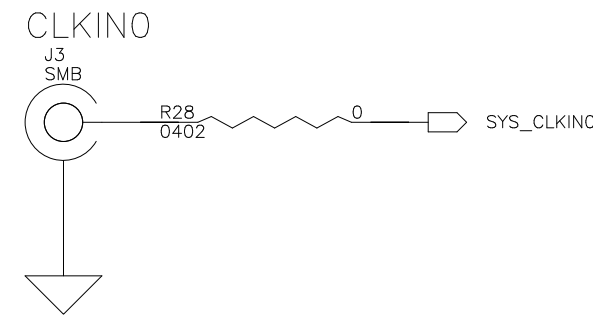
		2 Elizabeth Drive Chelmsford, MA 01824 PH: 1-800-ANALOGD
Title		ADSP-21569 EZ-KIT Proc Bypass Caps
Size C	Board No.	Rev B
	A0965-2017	
Date	09/1/17	Sheet 6 of 24



SW1: Boot mode selection switch

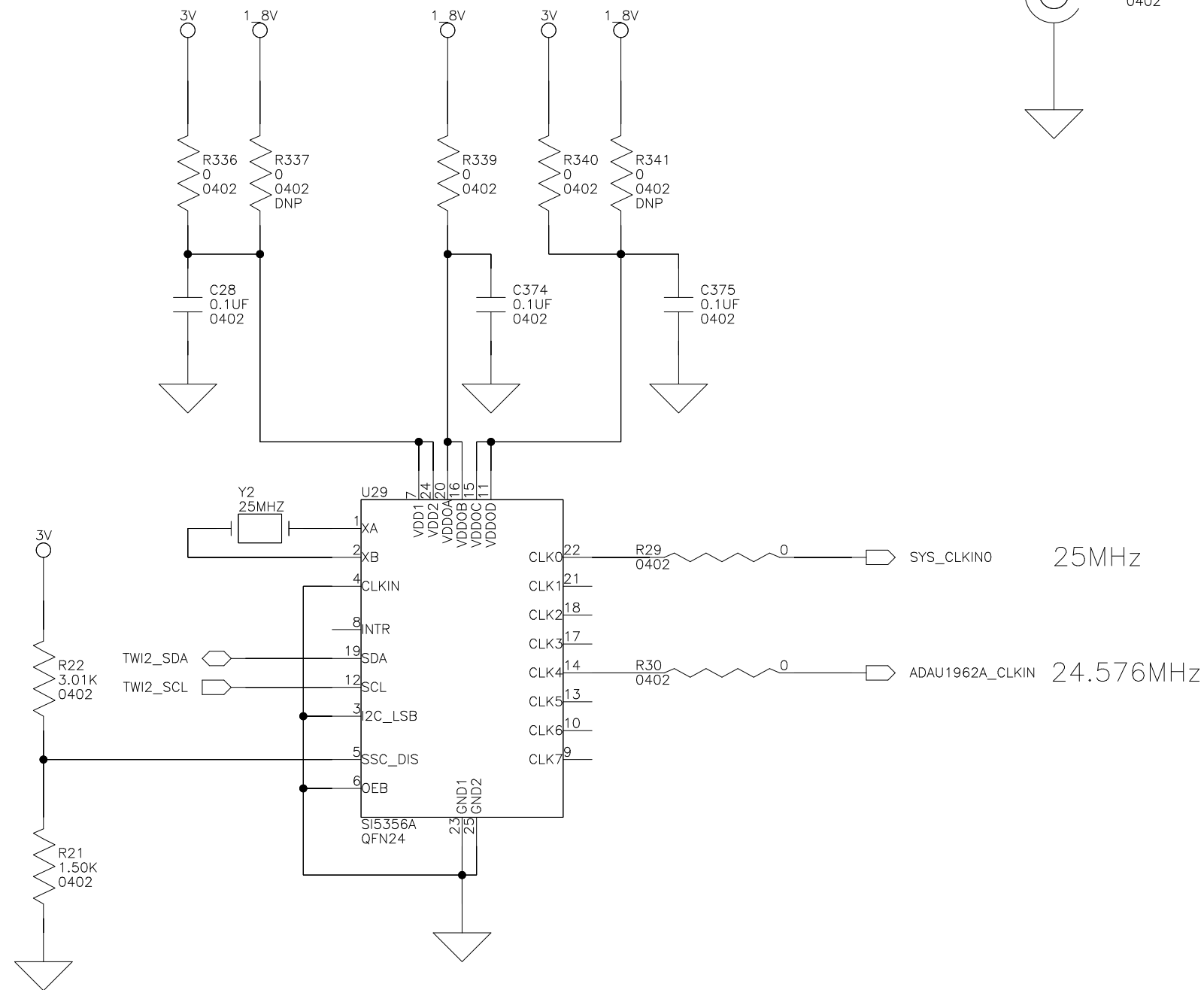
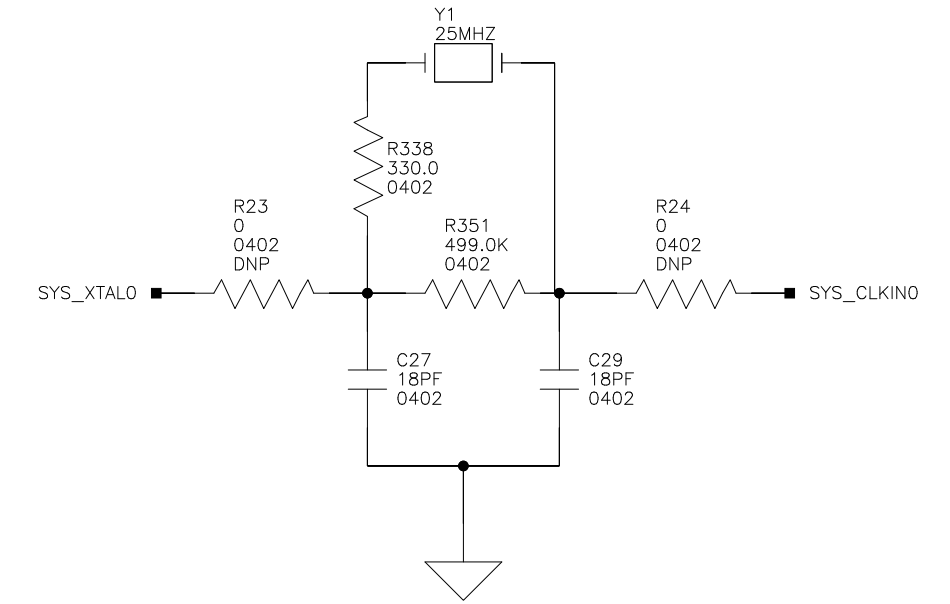
BMODE[2:0]	Boot Source
000	No boot/Custom ROM
001	SPI2 master boot
010	SPI2 slave boot
011	UART0 slave boot
100	LP0 slave boot
101	OSPI Boot
110	Reserved
111	Reserved

DEFAULT




OPTIONAL DSP CRYSTALS

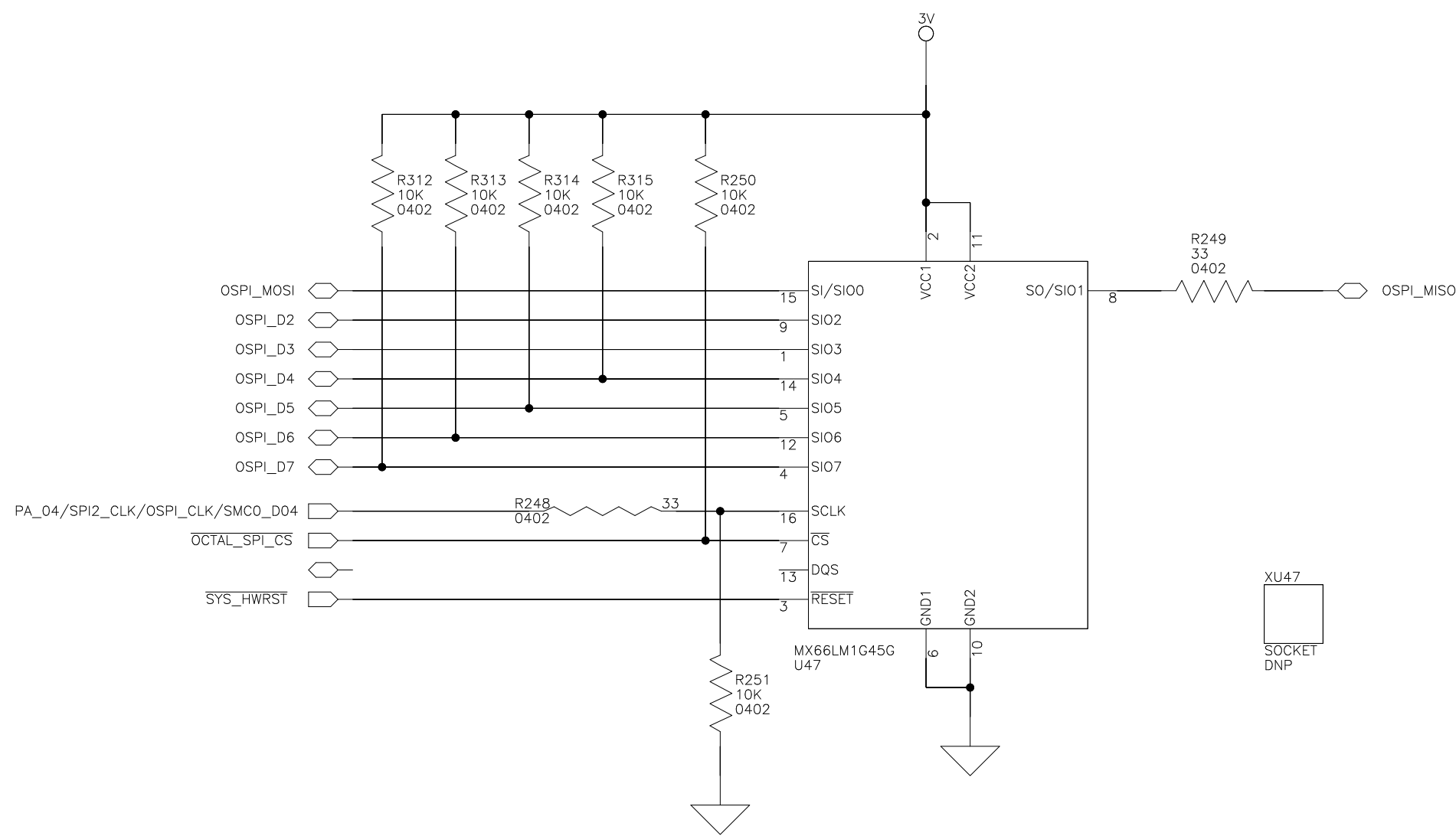
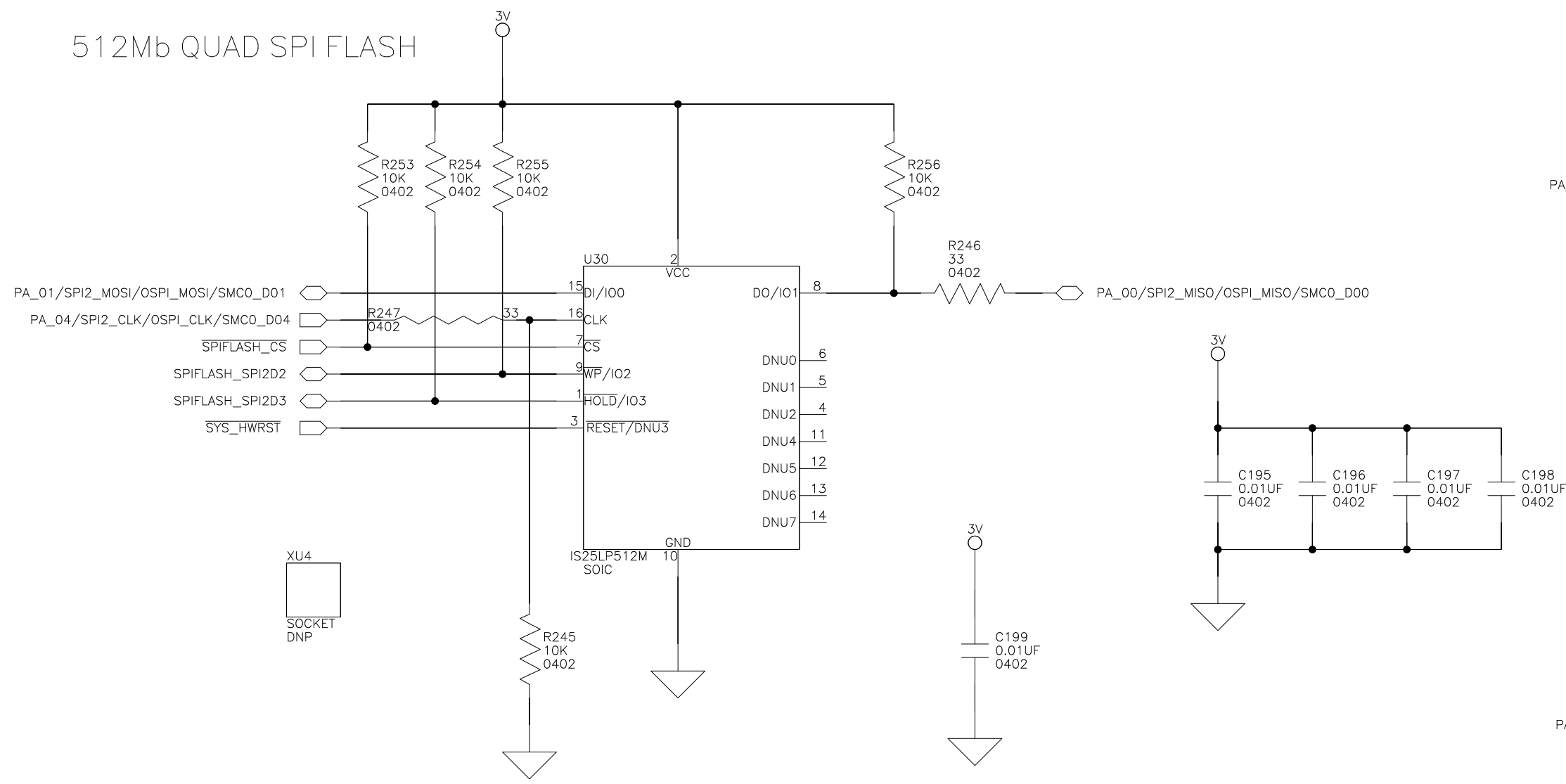
R29 should be depopulated when using the optional circuit



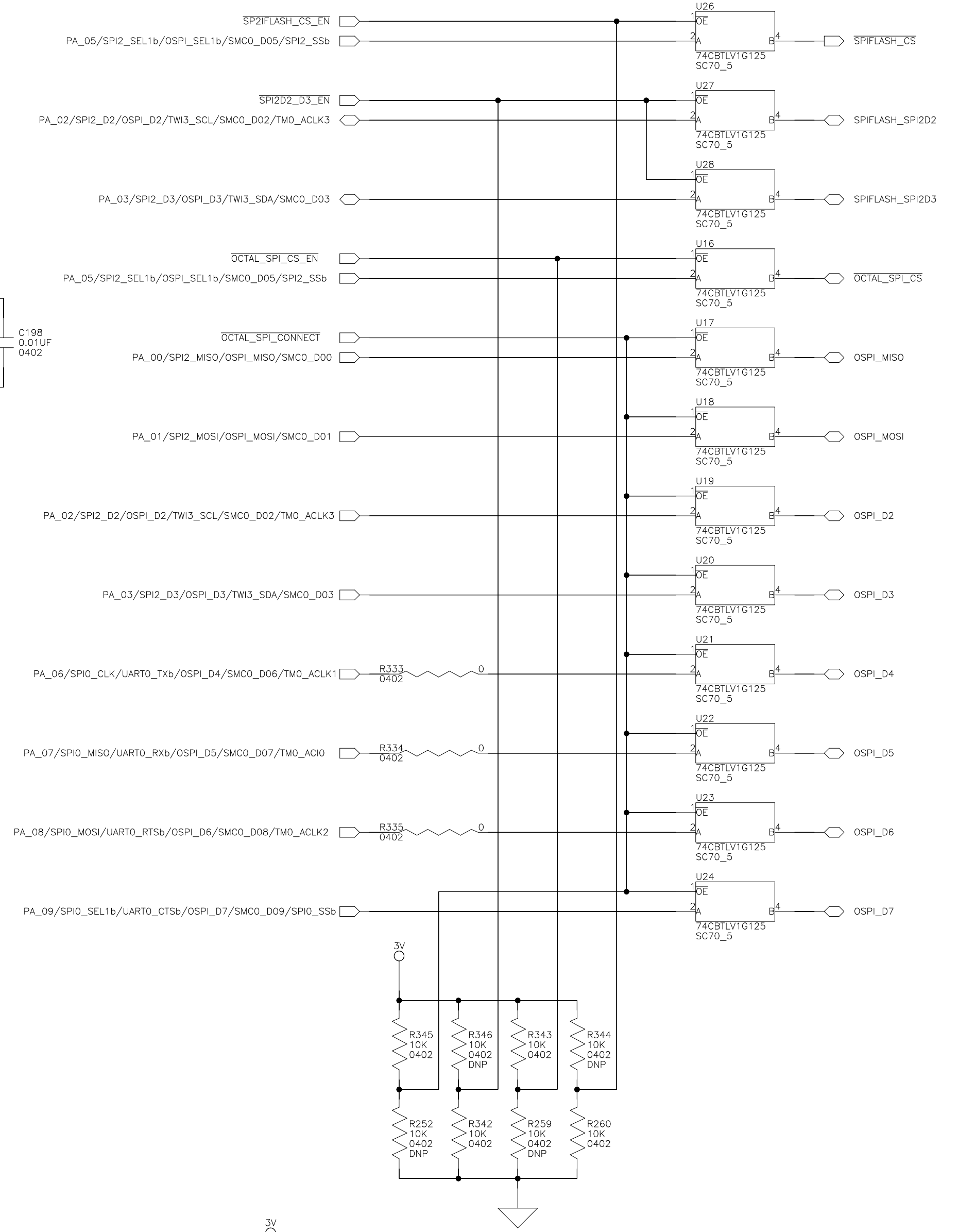
TWI Address 1110000x
x is the R/W bit. Read - 1, Write - 0

		2 Elizabeth Drive Chelmsford, MA 01824 PH: 1-800-ANALOGD
Title ADSP-21569 EZ-KIT Clocks/Boot Mode		
Size C	Board No. A0965-2017	Rev B
Date 09/1/17	Sheet 7 of 24	

512Mb QUAD SPI FLASH

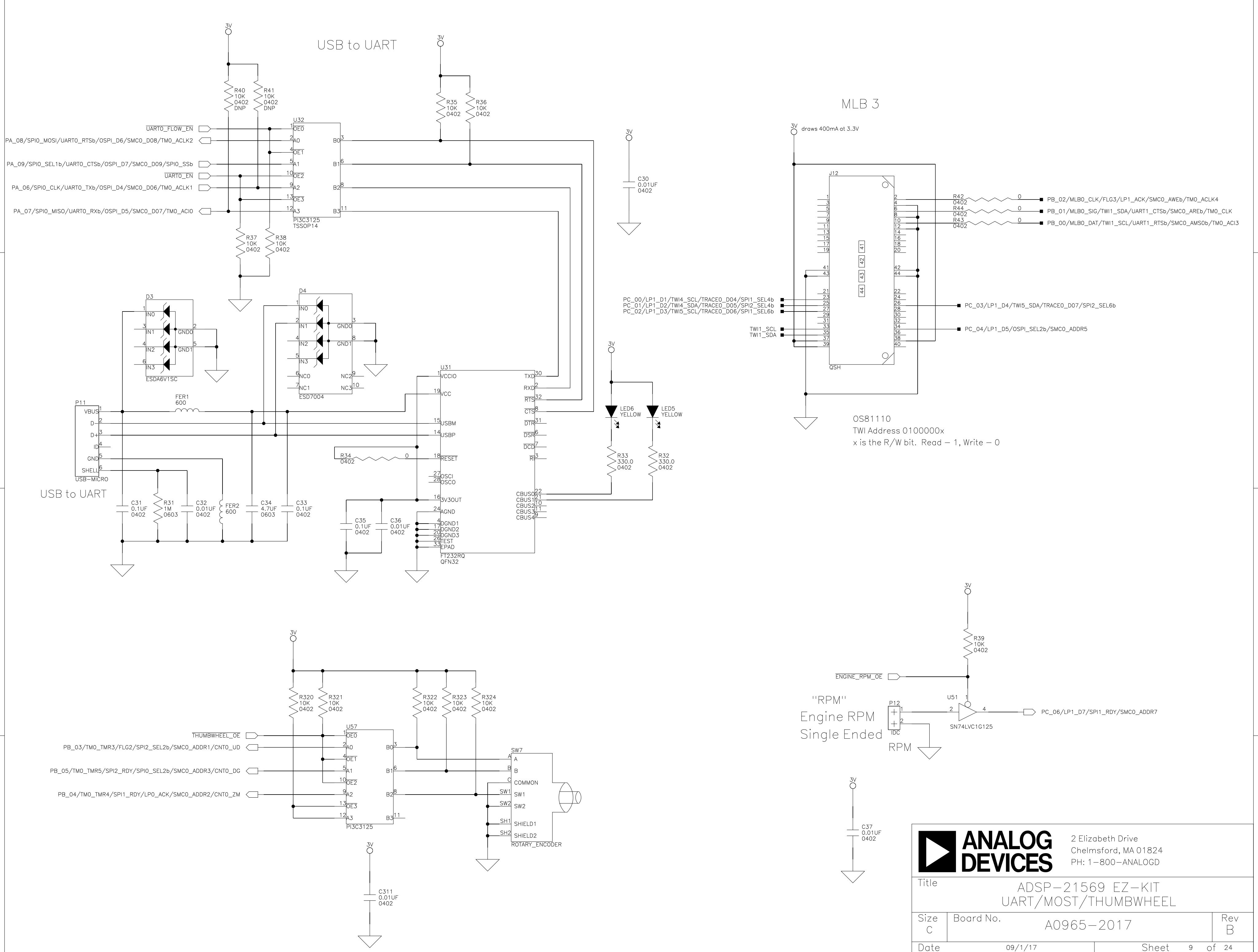


1Kb SPI EEPROM



2 Elizabeth Drive
Chelmsford, MA 01824
PH: 1-800-ANALOGD

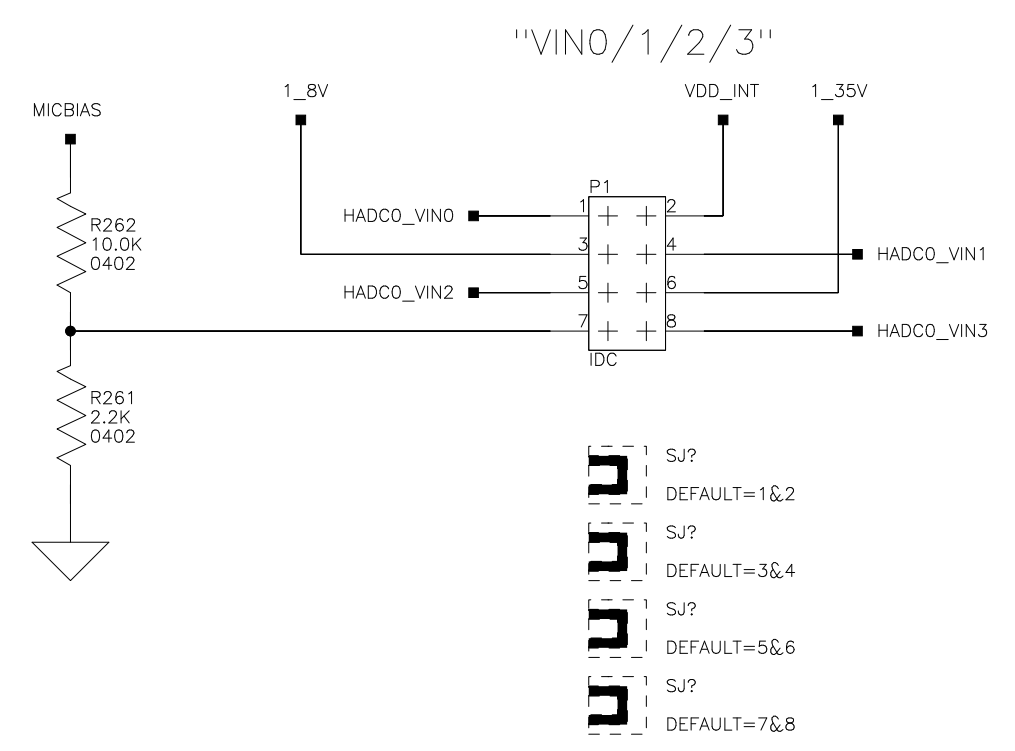
Title			ADSP-21569 EZ-KIT SPI Flash		
Size	Board No.				Rev
C	A0965-2017				B
Date	09/1/17	Sheet	8	of	24



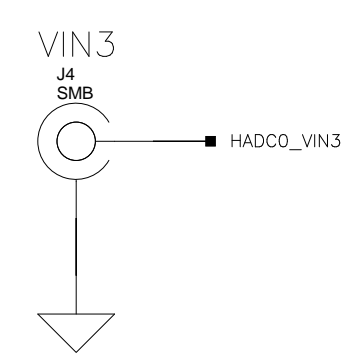
ANALOG DEVICES
 2 Elizabeth Drive
 Chelmsford, MA 01824
 PH: 1-800-ANALOGD


Title: ADSP-21569 EZ-KIT
 UART/MOST/THUMBWHEEL

Size C	Board No. A0965-2017	Rev B
Date	09/1/17	Sheet 9 of 24

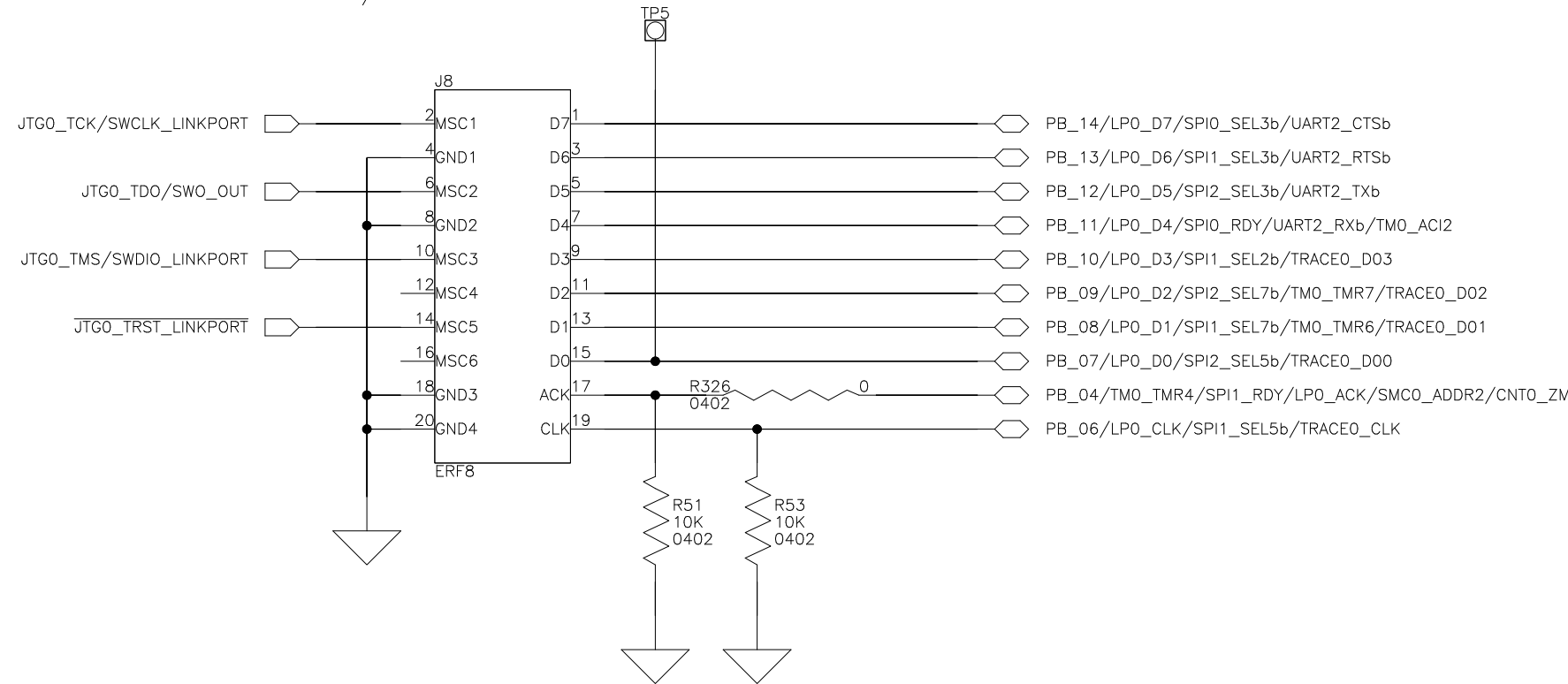


HADC Connectors

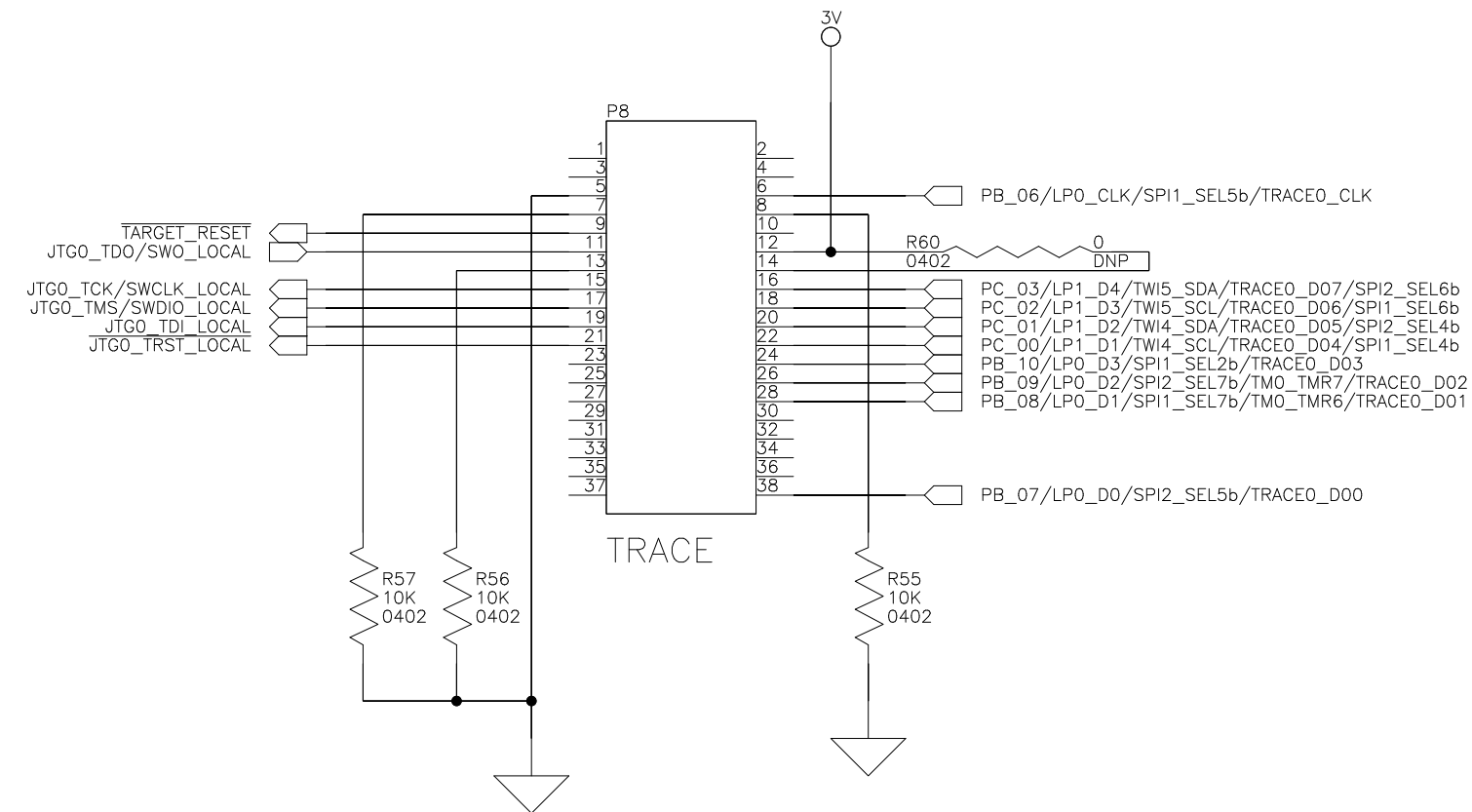
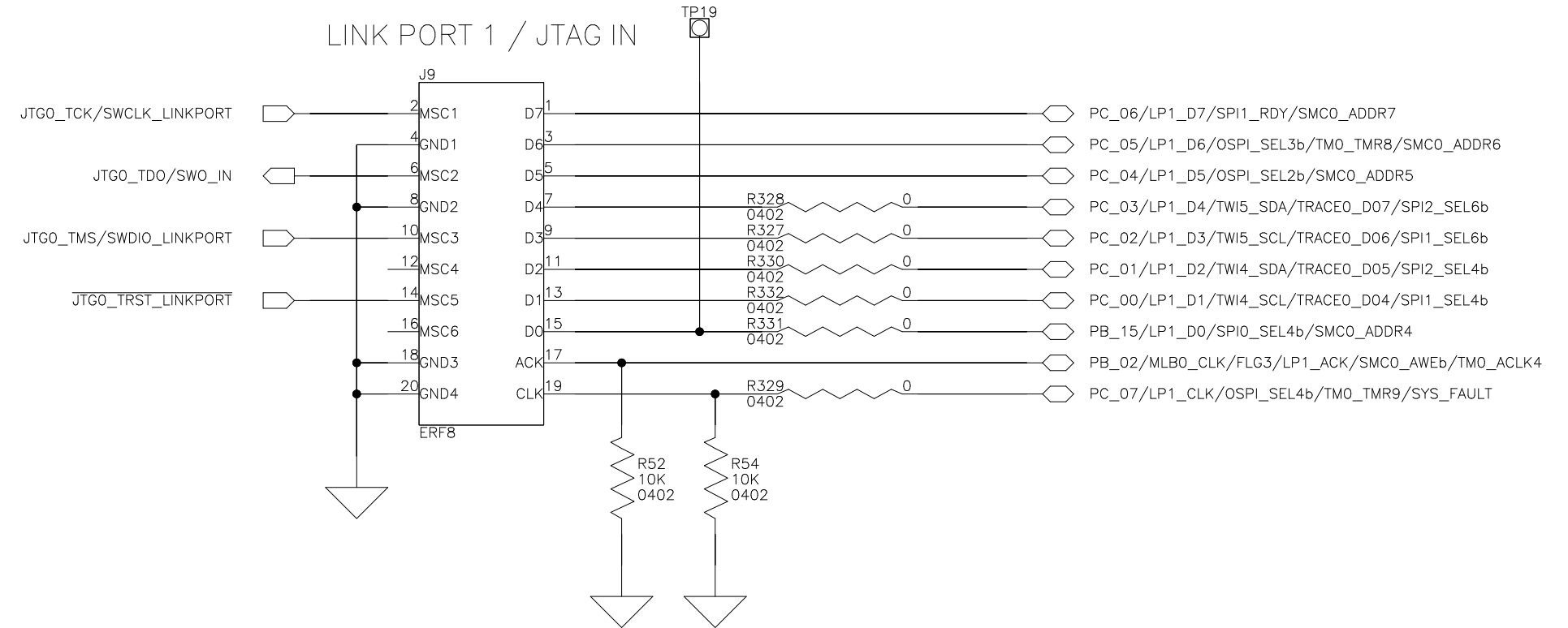


		2 Elizabeth Drive Chelmsford, MA 01824 PH: 1-800-ANALOGD	
Title ADSP-21569 EZ-KIT HADC			
Size C	Board No. A0965-2017	Rev B	
Date 09/1/17	Sheet 10 of 24		

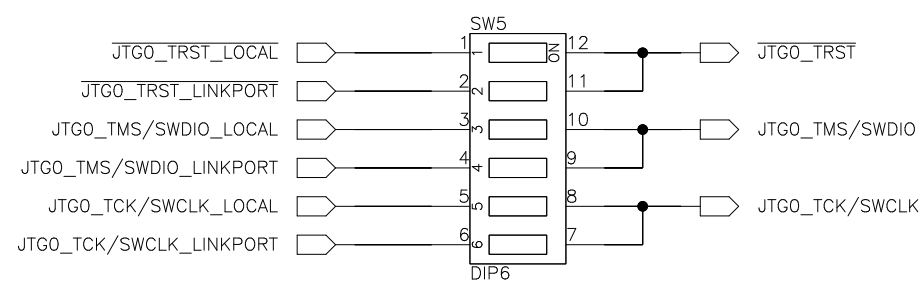
LINK PORT 0 / JTAG OUT



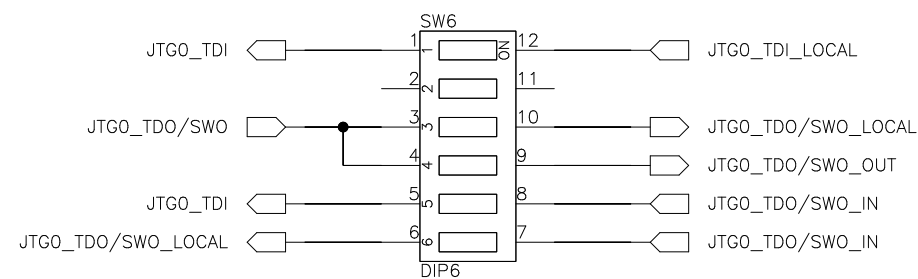
LINK PORT 1 / JTAG IN



JTAG SWITCHES



JTAG



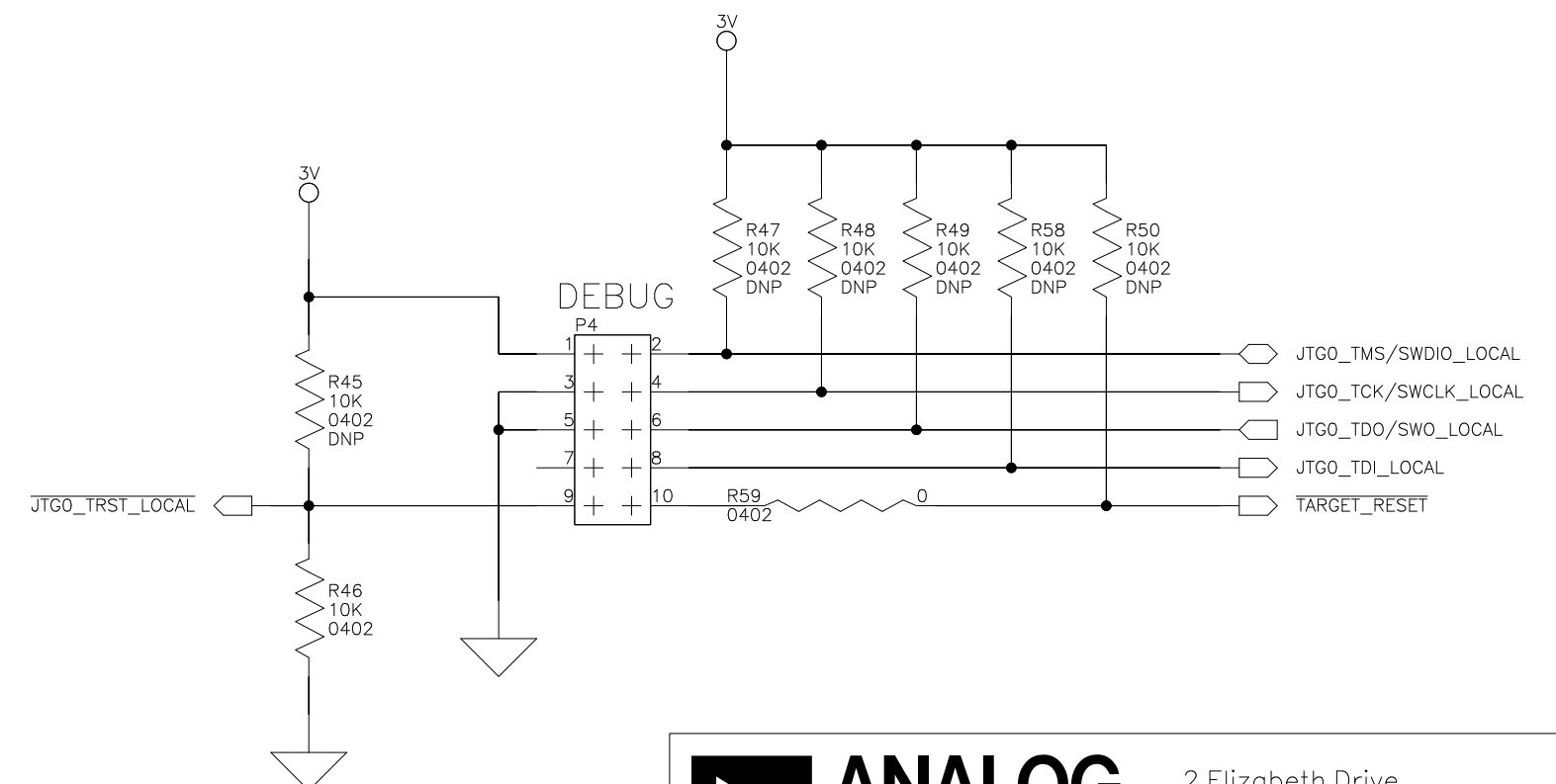
JTAG

SINGLE PROCESSOR JTAG SETTINGS
EMULATOR OR DEBUG AGENT (DEFAULT)

SWITCH	BOARD ATTACHED TO EMULATOR
SW5.1	ON
SW5.2	OFF
SW5.3	ON
SW5.4	OFF
SW5.5	ON
SW5.6	OFF
SW6.1	ON
SW6.2	OFF
SW6.3	ON
SW6.4	OFF
SW6.5	OFF
SW6.6	OFF

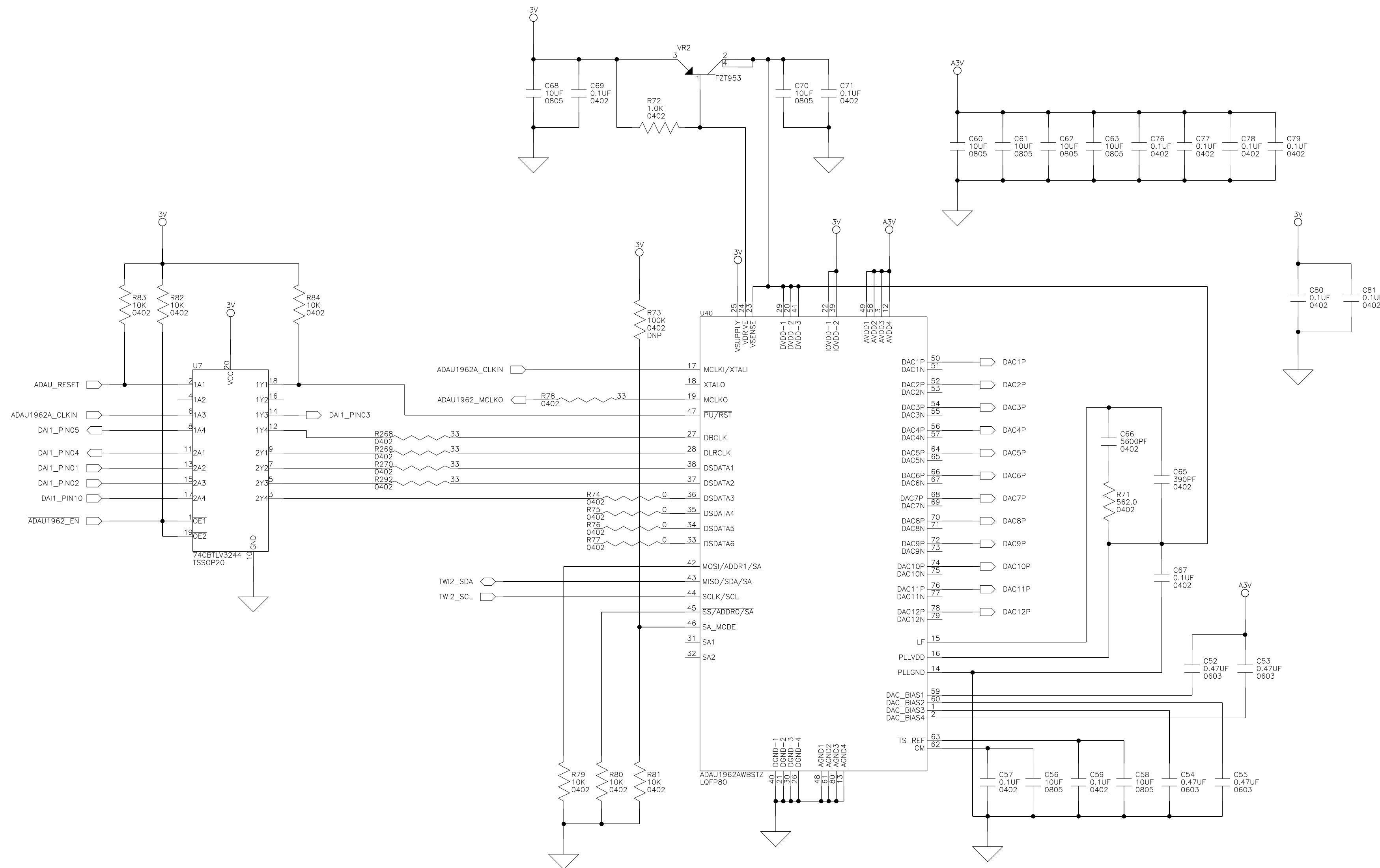
MULTI PROCESSOR JTAG SETTINGS
USING TWO OR MORE EZ-BOARDS (LINK PORT CABLES
REQUIRED FOR MORE THAN TWO BOARDS)

SWITCH	BOARD ATTACHED TO EMULATOR	BOARD(S) NOT ATTACHED TO EMULATOR
SW5.1	ON	OFF
SW5.2	ON	ON
SW5.3	ON	OFF
SW5.4	ON	ON
SW5.5	ON	OFF
SW5.6	ON	ON
SW6.1	ON	OFF
SW6.2	OFF	OFF
SW6.3	OFF	OFF
SW6.4	ON	ON
SW6.5	OFF	ON
SW6.6	ON	OFF

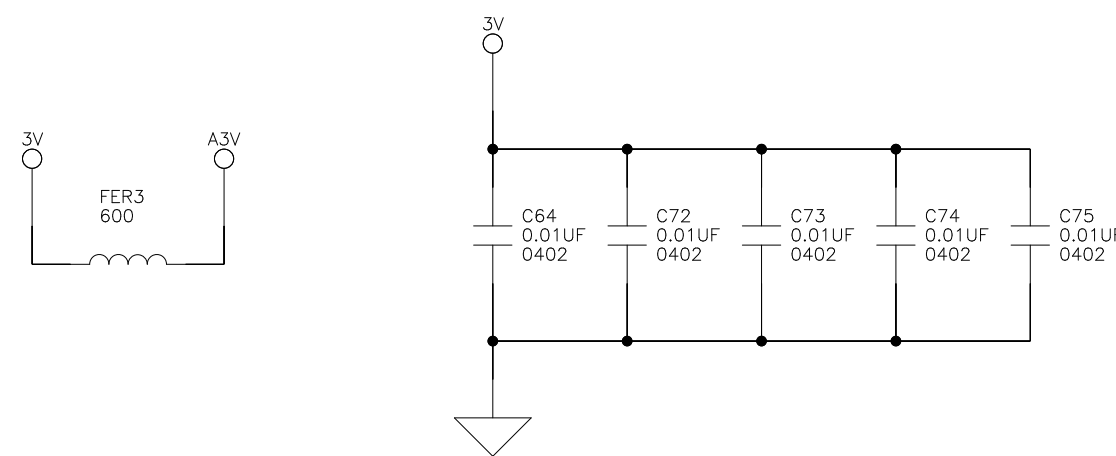



2 Elizabeth Drive
Chelmsford, MA 01824
PH: 1-800-ANALOGD

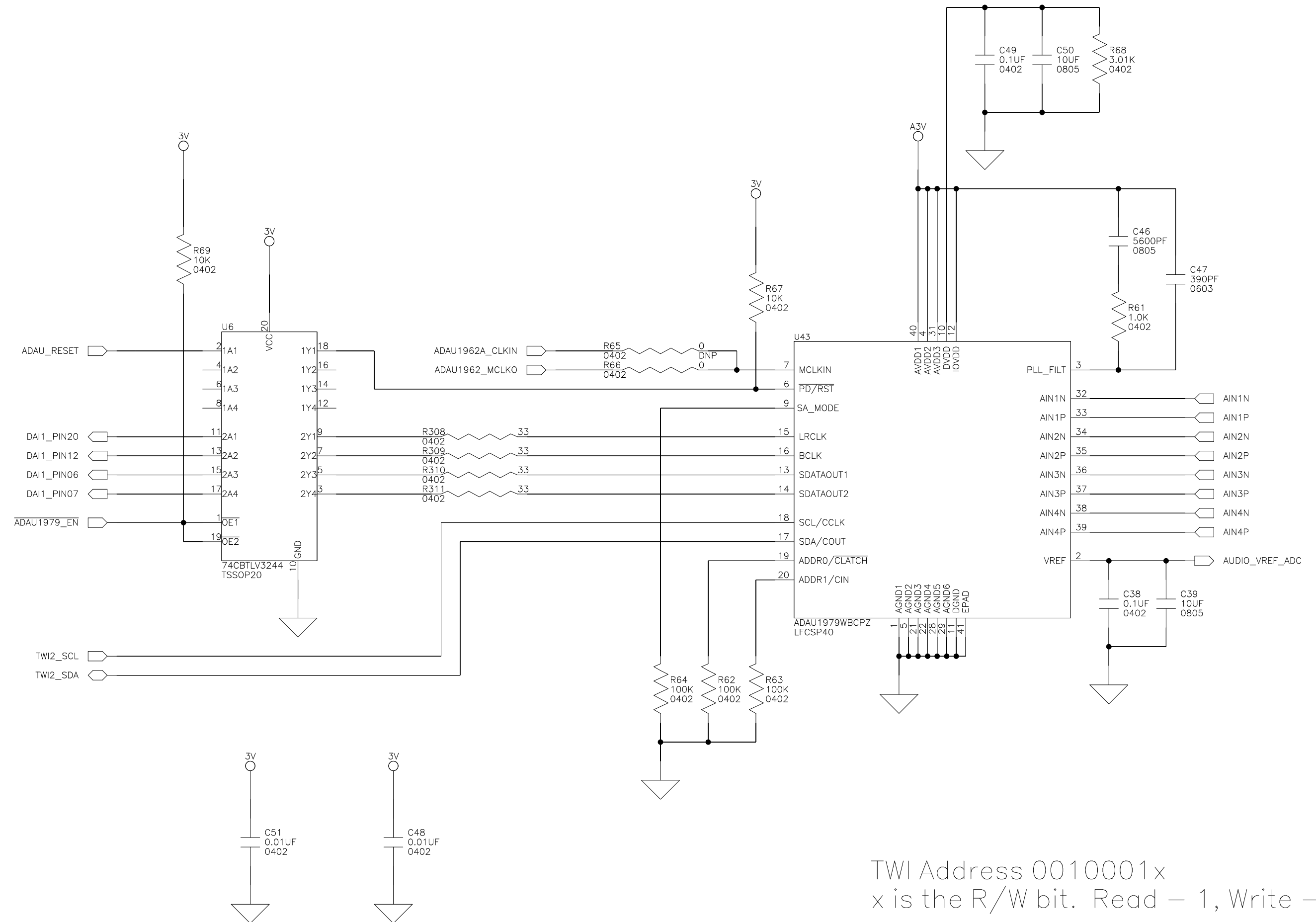
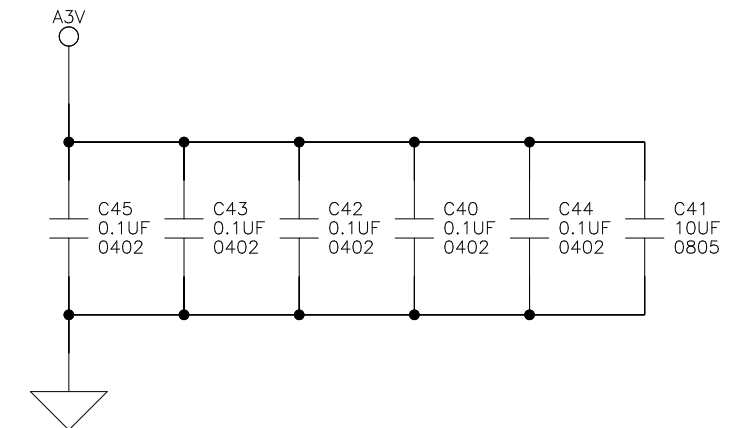
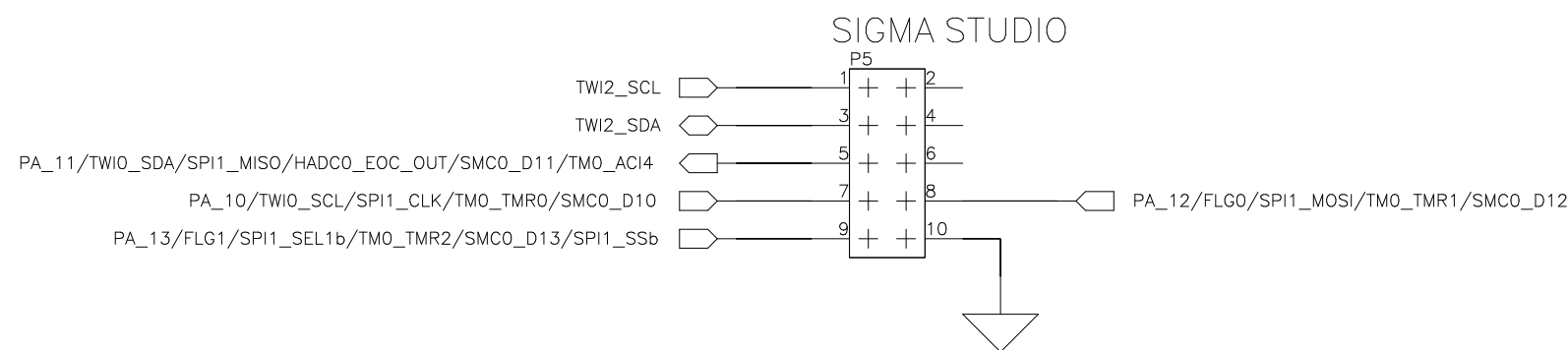
Title			ADSP-21569 EZ-KIT Link Port/Debug		
Size	Board No.				Rev
C	A0965-2017				B
Date	09/1/17	Sheet	11	of	24



Check for extender conflict
 TWI Address 0000100x
 x is the R/W bit. Read - 1, Write - 0



		2 Elizabeth Drive Chelmsford, MA 01824 PH: 1-800-ANALOGD	
		Title ADSP-21569 EZ-KIT ADAU1962A	
Size C	Board No. A0965-2017	Rev B	
Date 09/1/17	Sheet 12 of 24		



TWI Address 0010001x
 x is the R/W bit. Read - 1, Write - 0

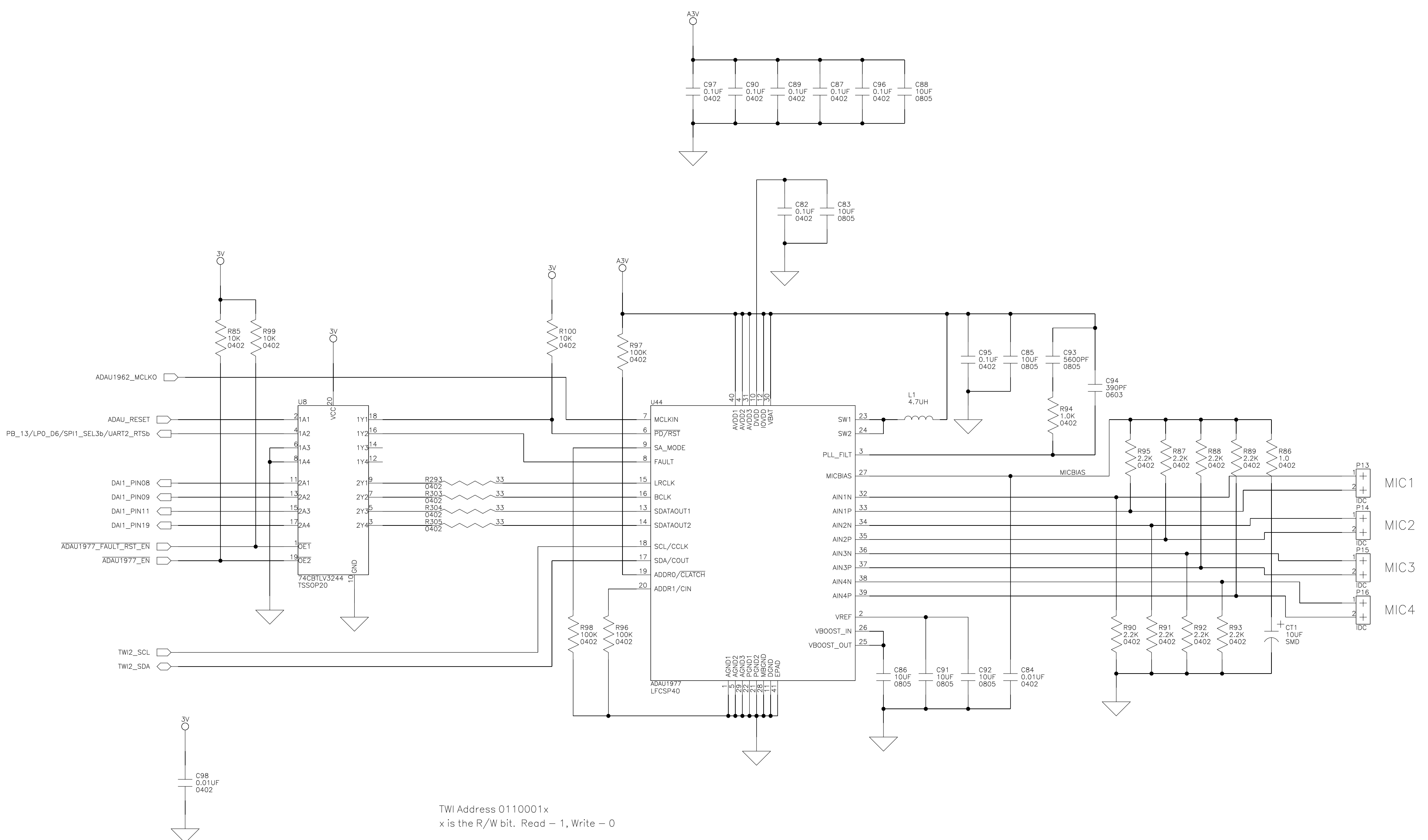
		2 Elizabeth Drive Chelmsford, MA 01824 PH: 1-800-ANALOGD	
		Title ADSP-21569 EZ-KIT ADAU1979	
Size C	Board No. A0965-2017	Rev B	
Date 09/1/17	Sheet 13 of 24		

A


B

C

D



TWI Address 0110001x
 x is the R/W bit. Read = 1, Write = 0

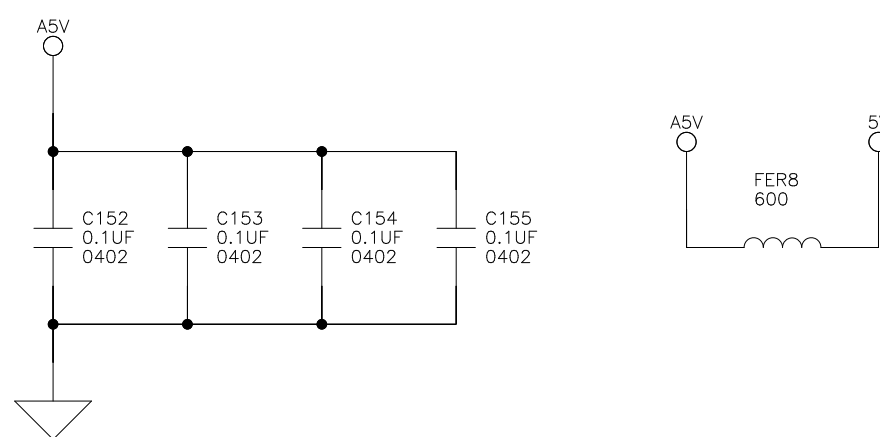
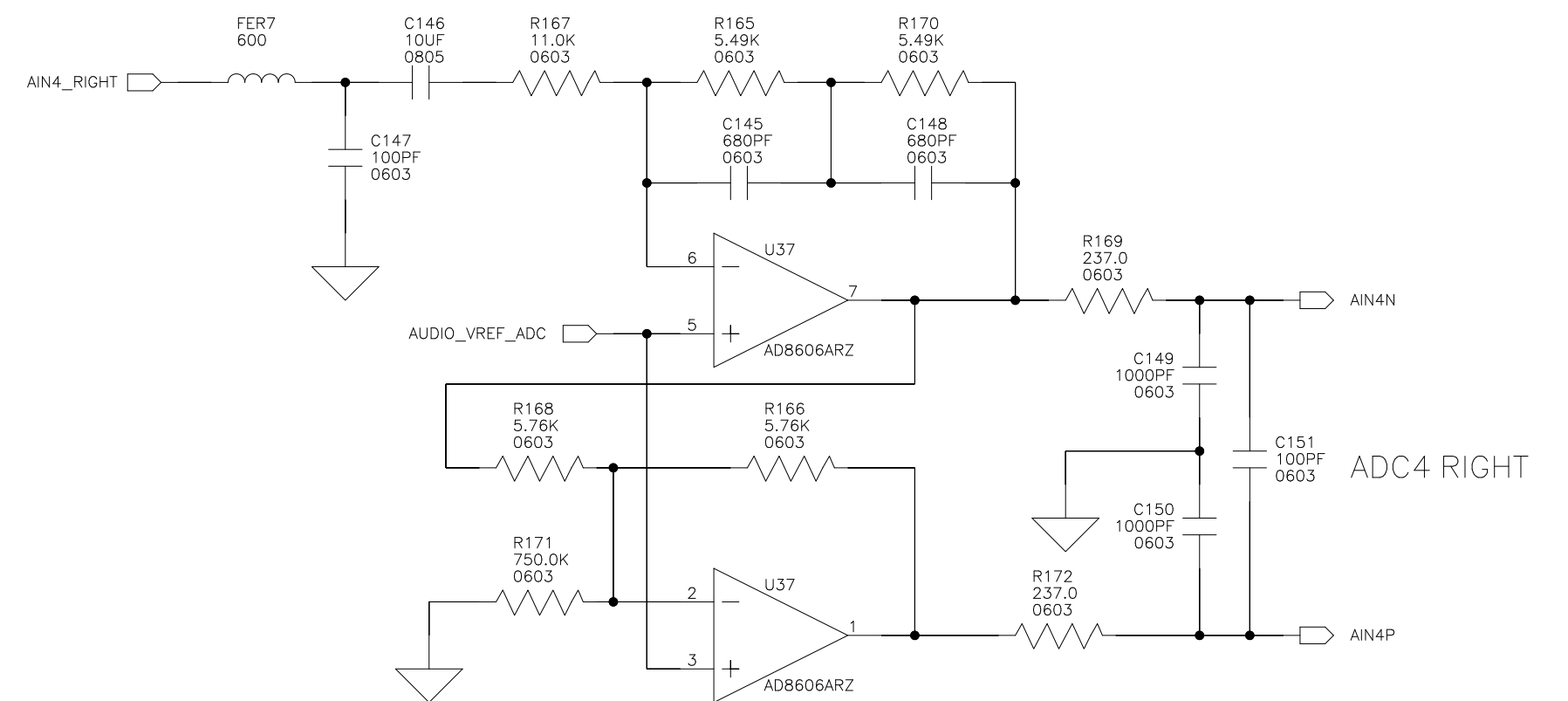
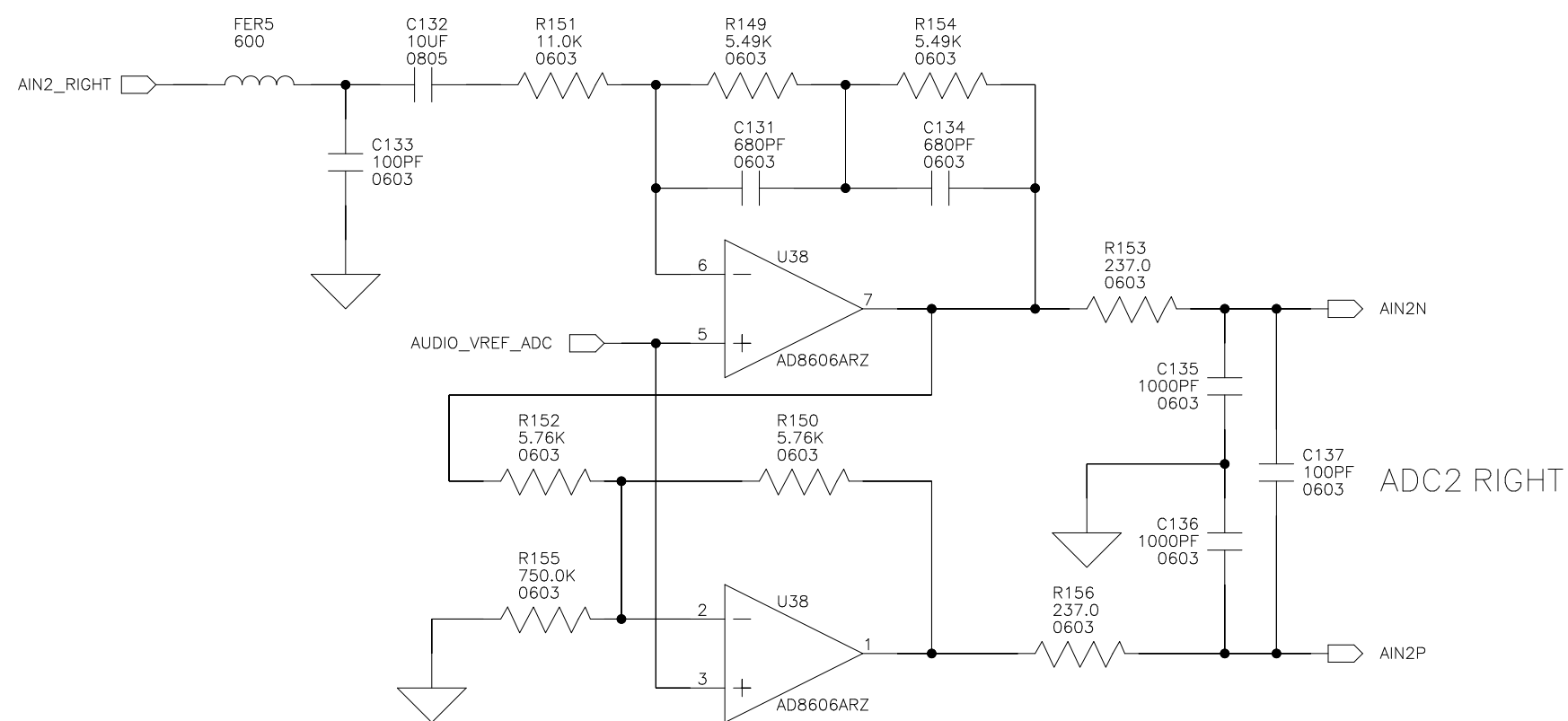
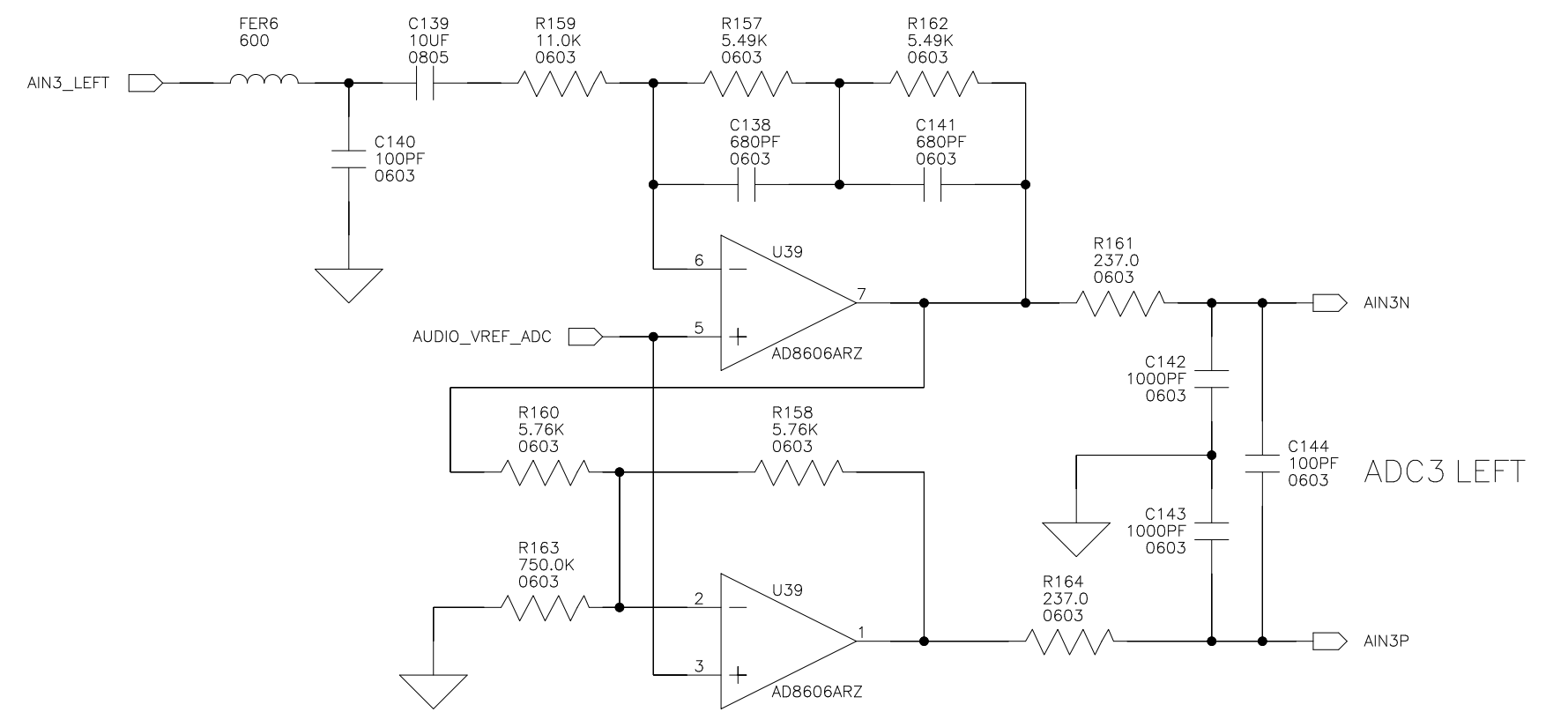
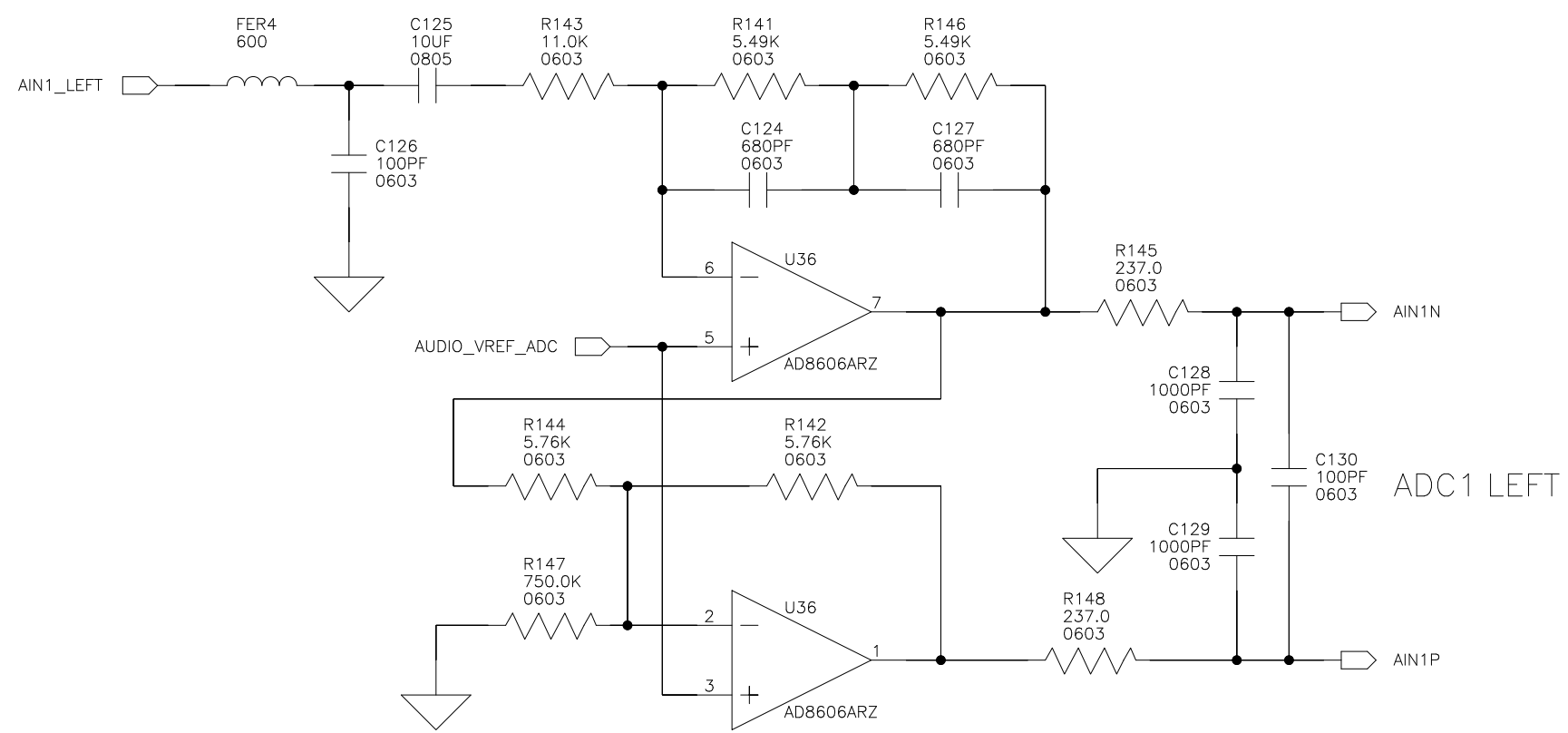
		2 Elizabeth Drive Chelmsford, MA 01824 PH: 1-800-ANALOGD	
		Title ADSP-21569 EZ-KIT ADAU1977	
Size C	Board No. A0965-2017	Rev B	
Date 09/1/17	Sheet 14 of 24		


A

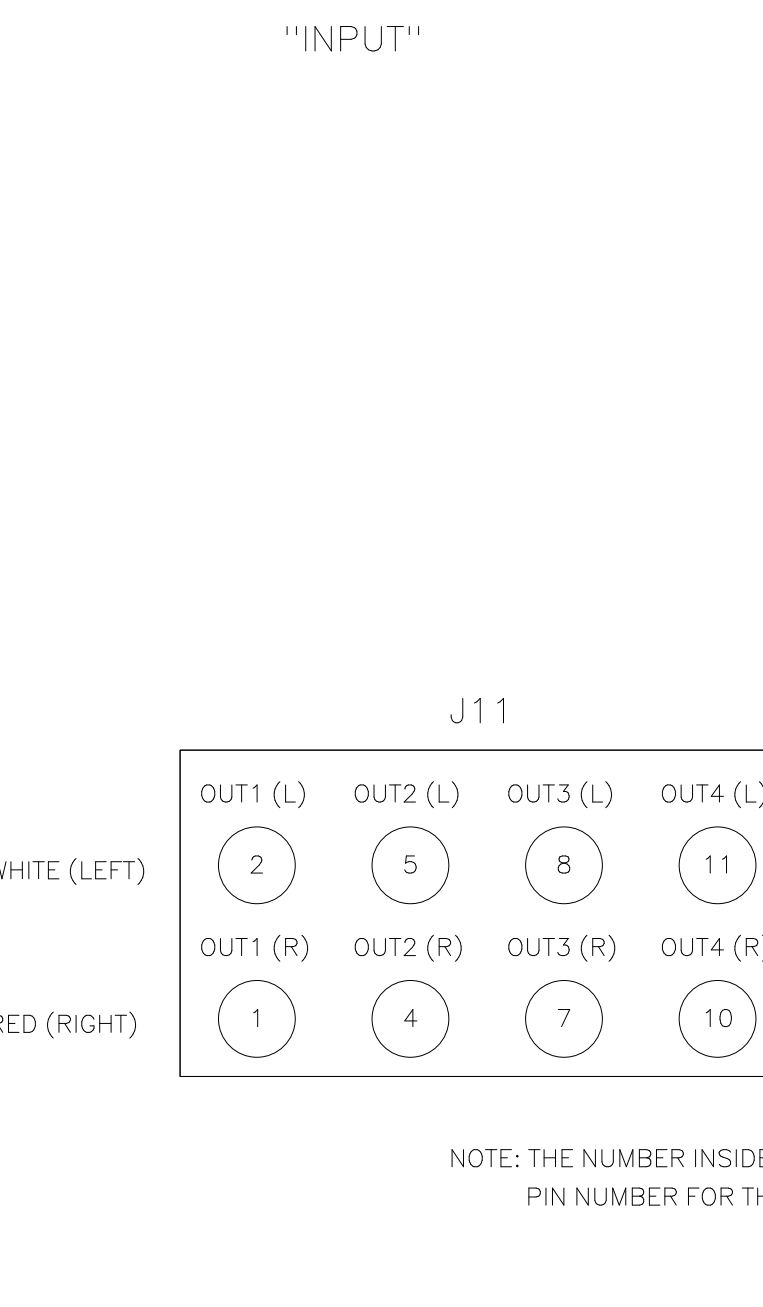
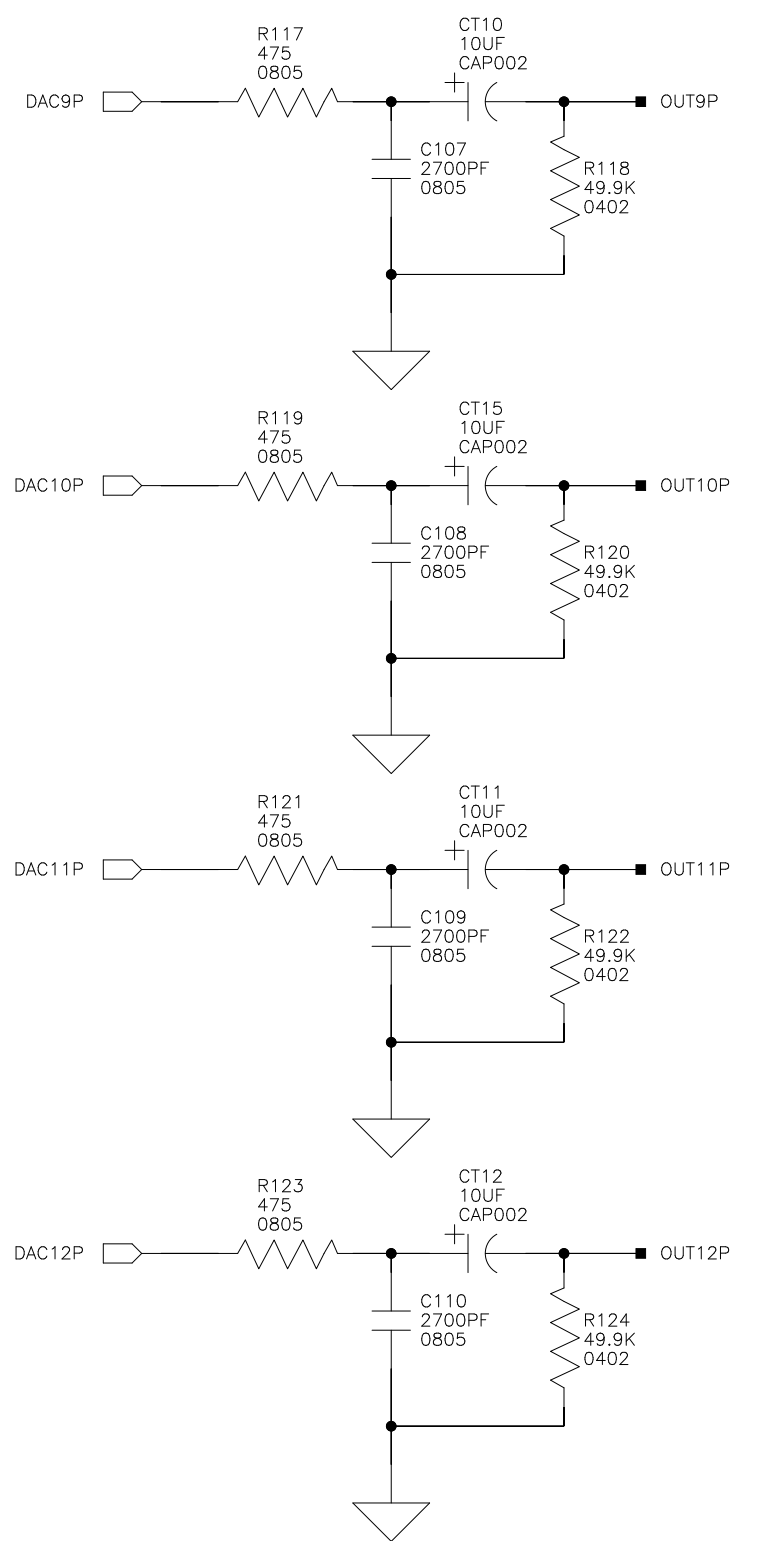
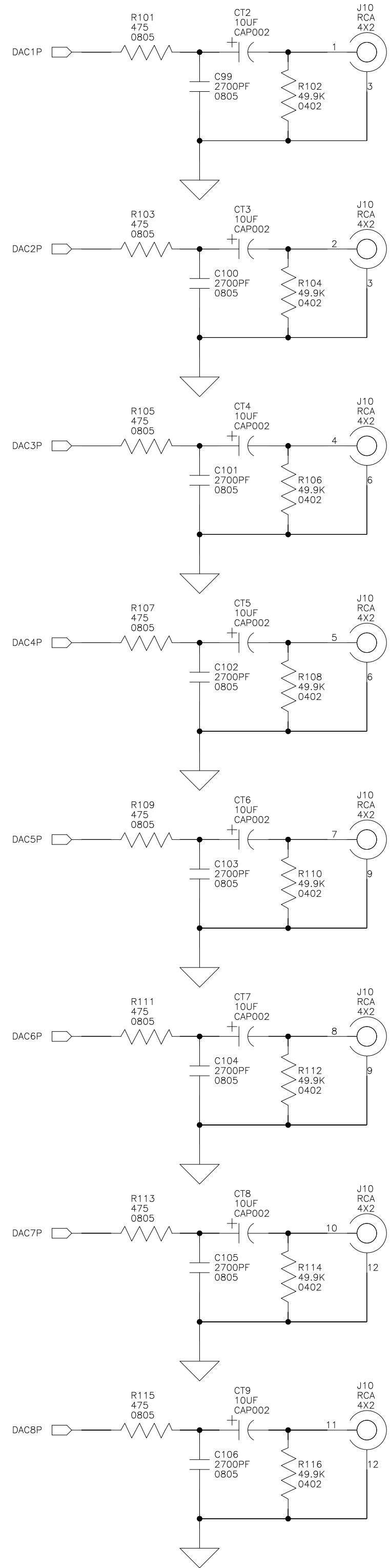
B

C

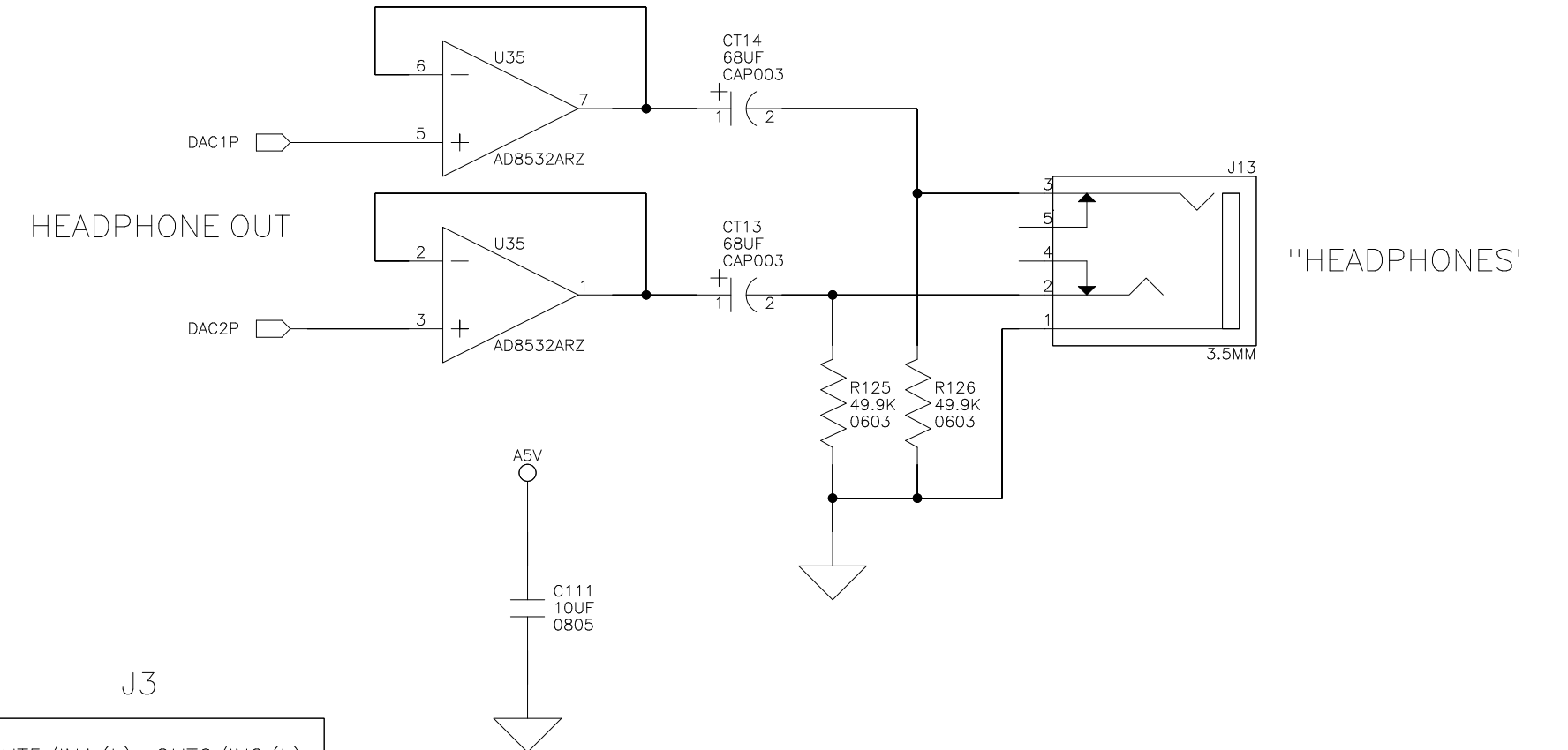
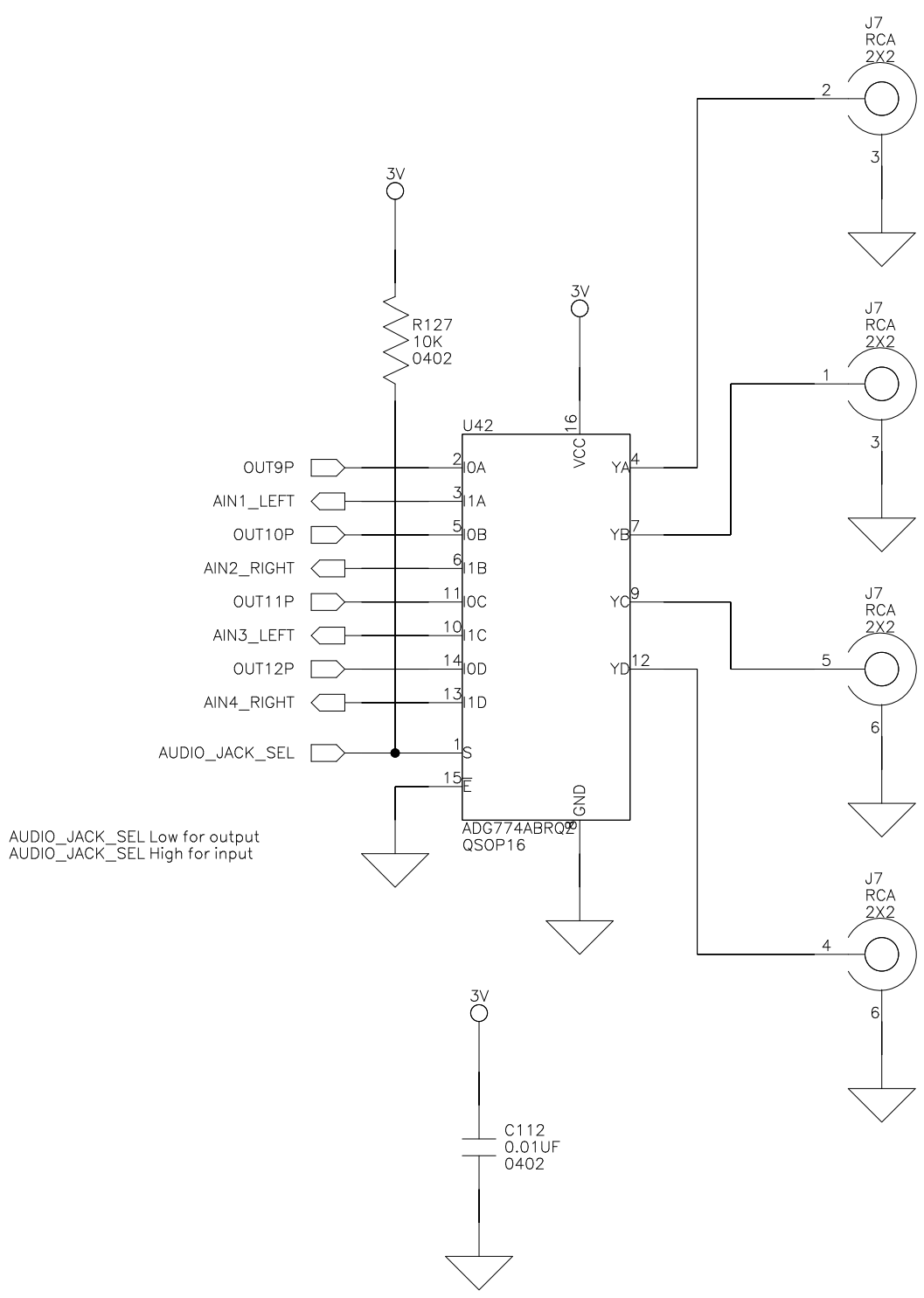
D




		2 Elizabeth Drive Chelmsford, MA 01824 PH: 1-800-ANALOGD	
		Title ADSP-21569 EZ-KIT Audio Input	
Size C	Board No. A0965-2017	Rev B	
Date 09/1/17	Sheet 15 of 24		



NOTE: THE NUMBER INSIDE EACH OF THE CIRCLES IS THE ACTUAL PIN NUMBER FOR THE RESPECTIVE CONNECTOR.



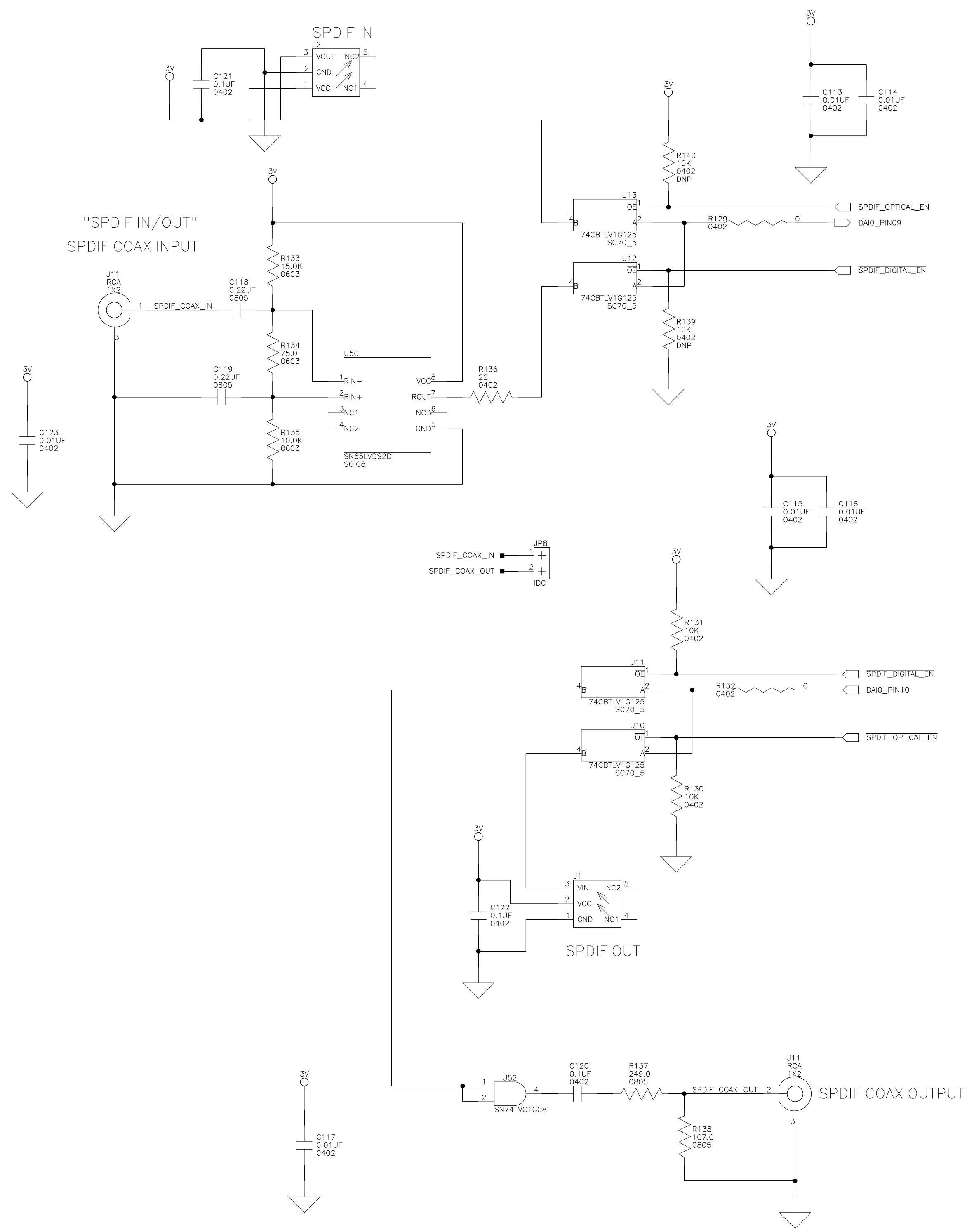
		2 Elizabeth Drive Chelmsford, MA 01824 PH: 1-800-ANALOGD	
		Title ADSP-21569 EZ-KIT Audio Connectors	
Size C	Board No.	A0965-2017	Rev B
Date	09/1/17	Sheet 16	of 24

A

B


C

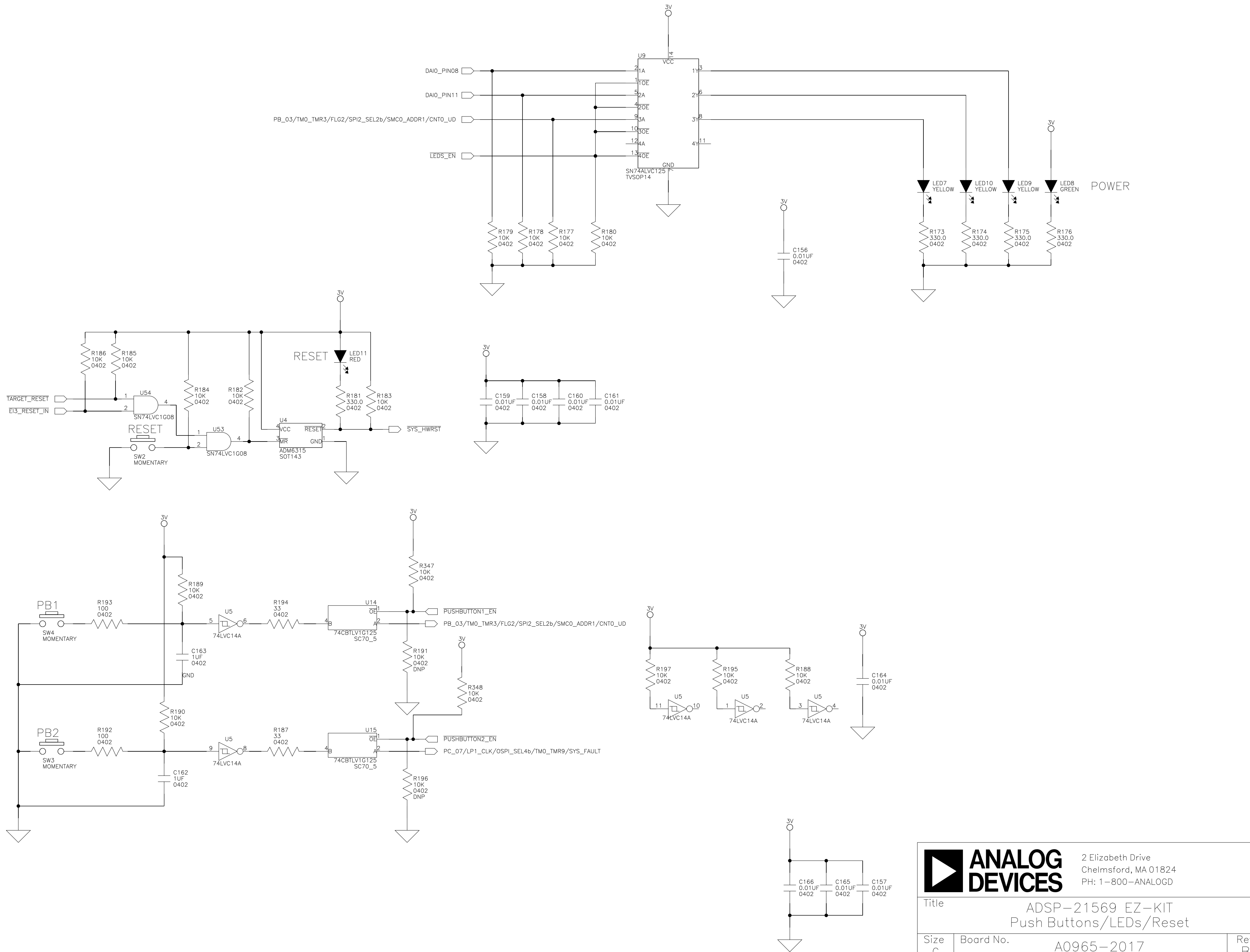
D



HU Data

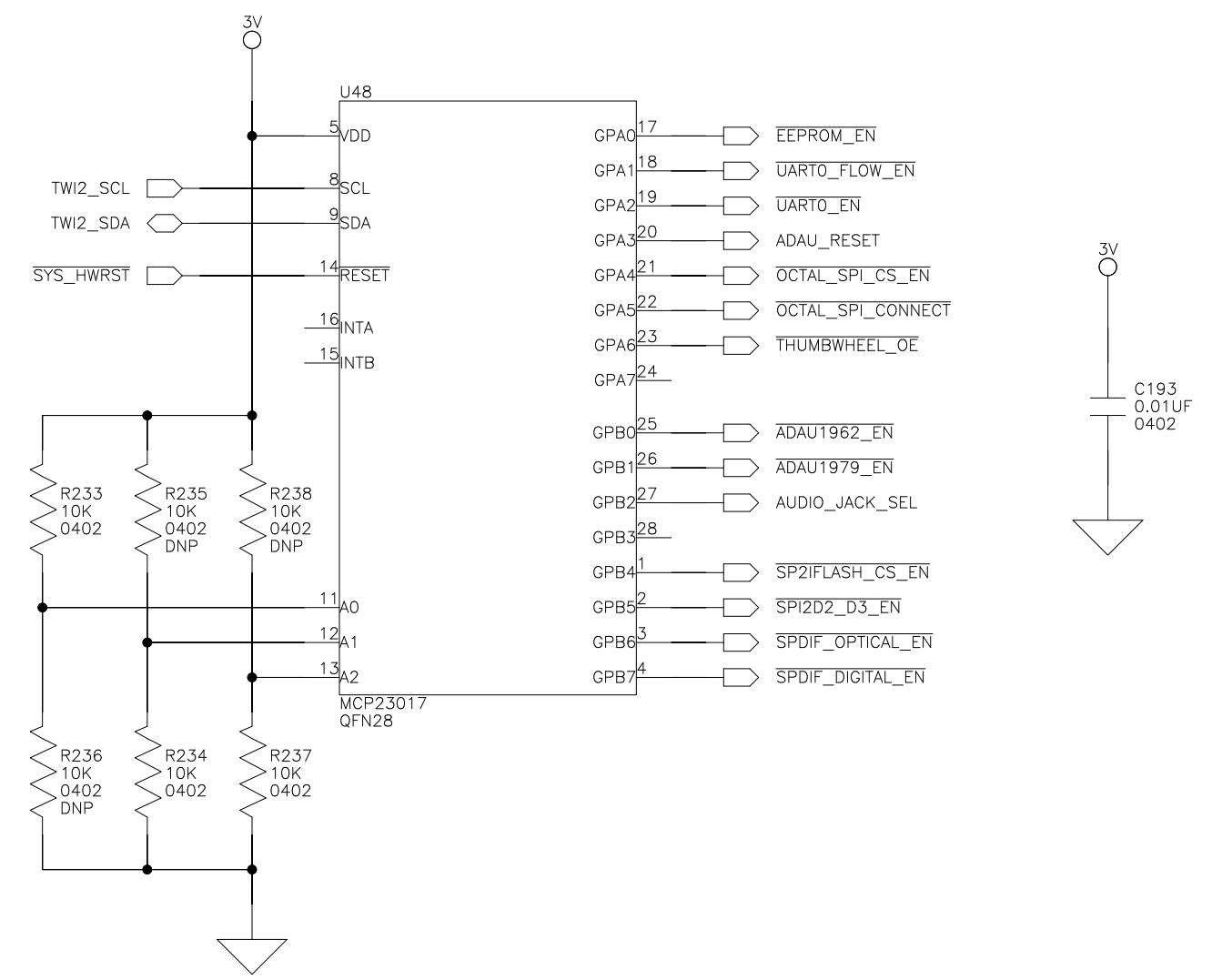
Debug

		2 Elizabeth Drive Chelmsford, MA 01824 PH: 1-800-ANALOGD	
		Title ADSP-21569 EZ-KIT SPDIF	
Size C	Board No. A0965-2017	Rev B	
Date 09/1/17	Sheet 17 of 24		

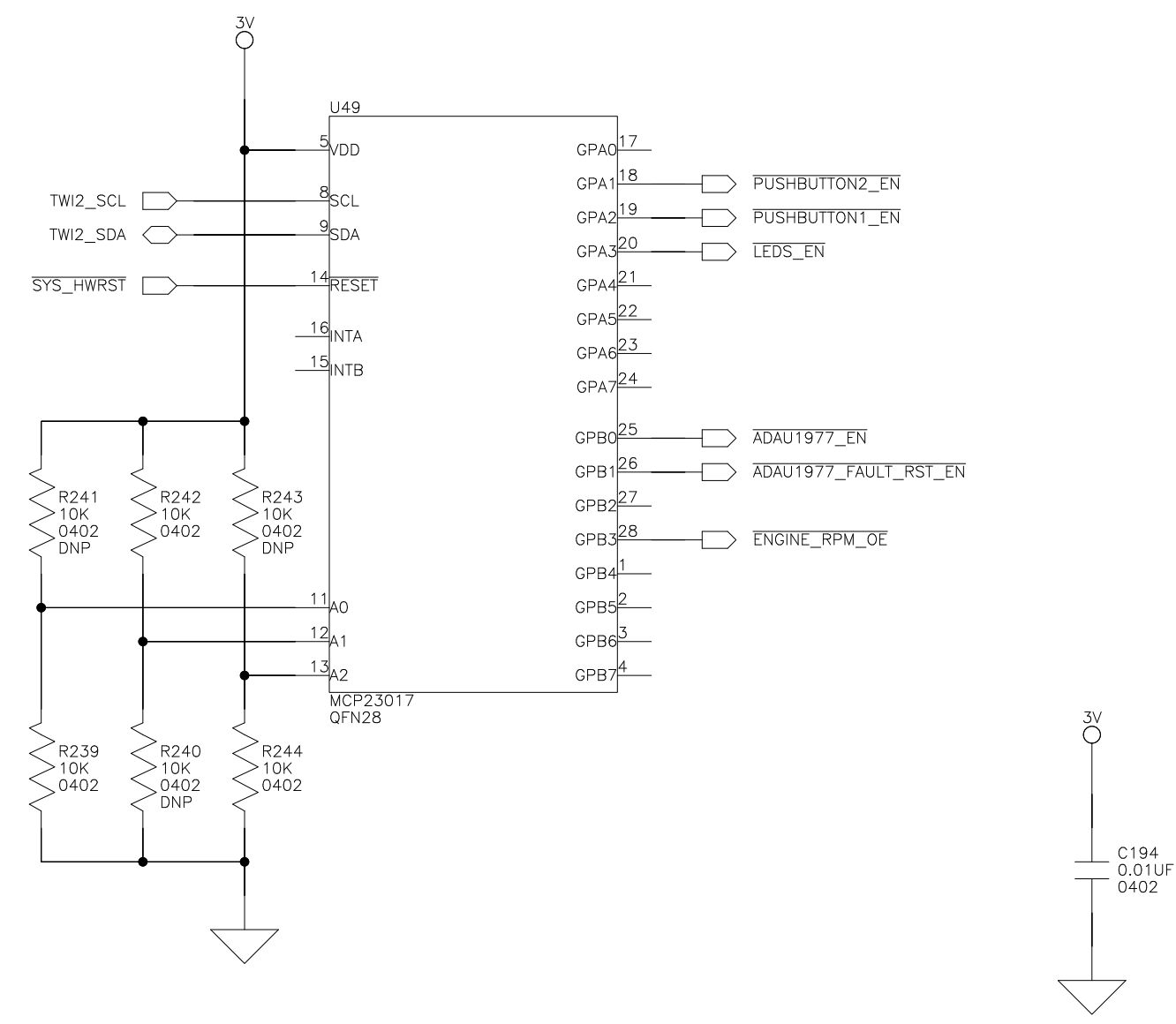


		2 Elizabeth Drive Chelmsford, MA 01824 PH: 1-800-ANALOGD	
Title ADSP-21569 EZ-KIT Push Buttons/LEDs/Reset			
Size C	Board No. A0965-2017	Rev B	
Date 09/1/17	Sheet 18 of 24		

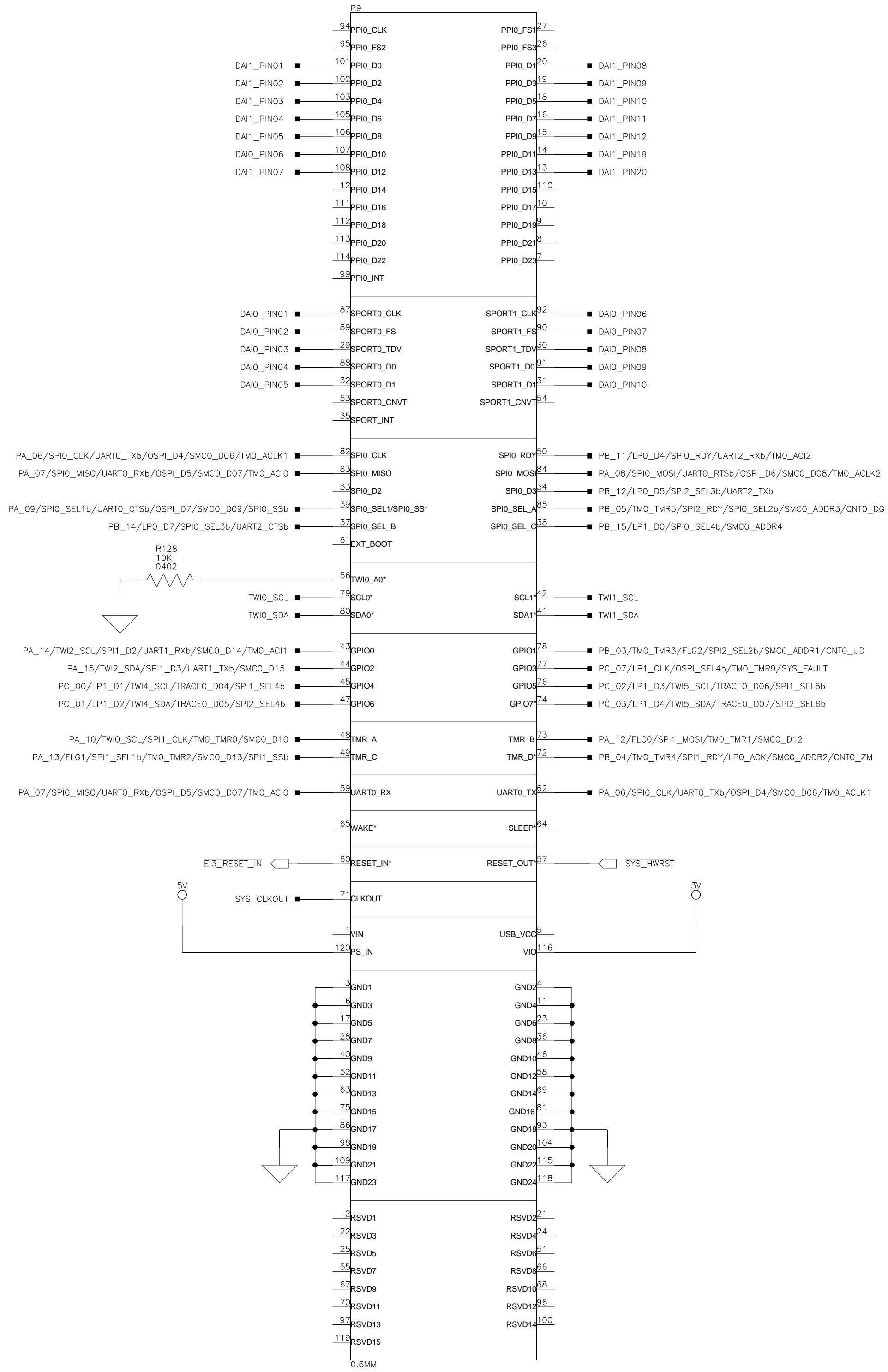
TWI Address 0100001x
 x is the R/W bit. Read - 1, Write - 0




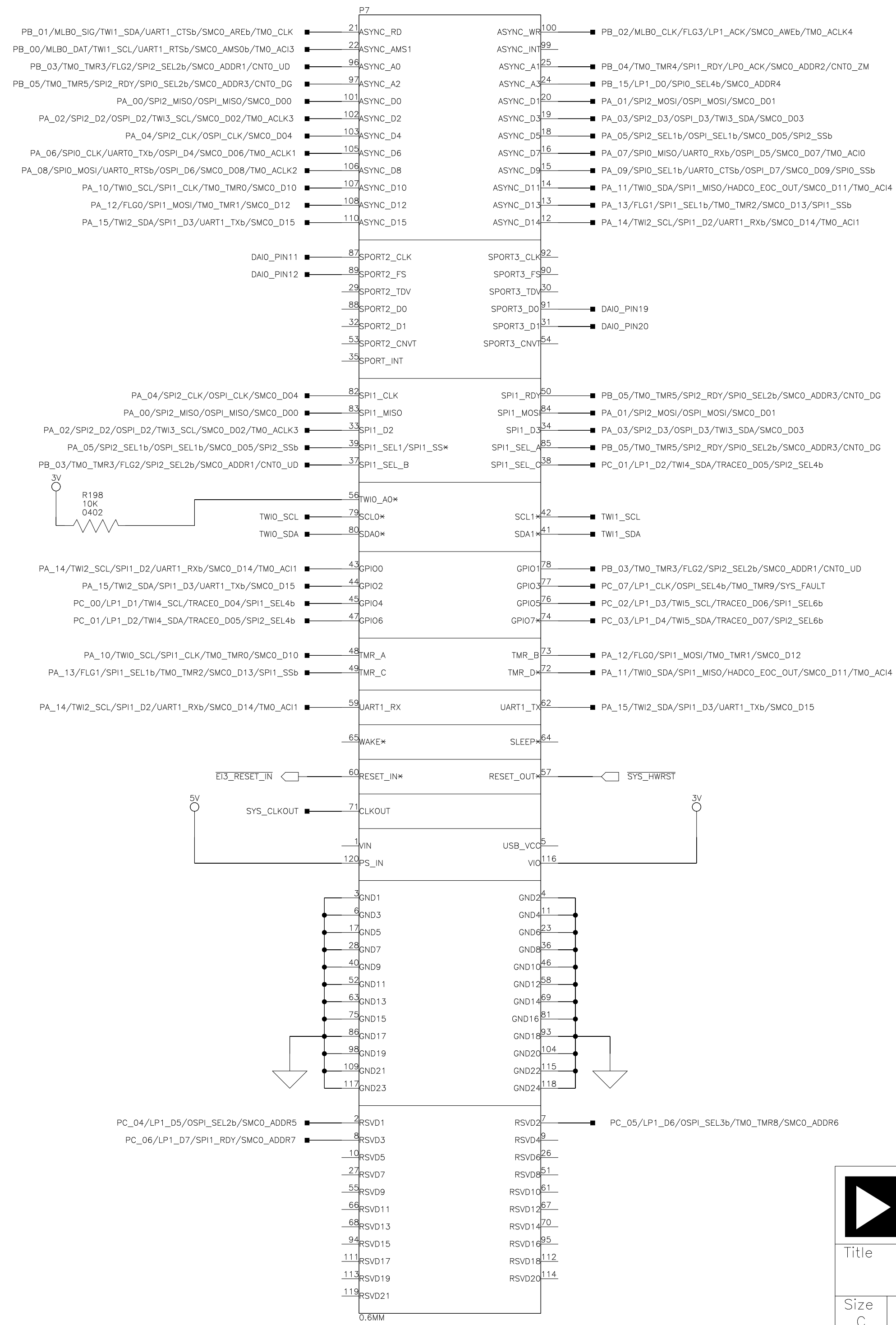
TWI Address 0100010x
 x is the R/W bit. Read - 1, Write - 0




		2 Elizabeth Drive Chelmsford, MA 01824 PH: 1-800-ANALOGD
Title ADSP-21569 EZ-KIT SoftConfig		
Size C	Board No. A0965-2017	Rev B
Date 09/1/17	Sheet 19 of 24	

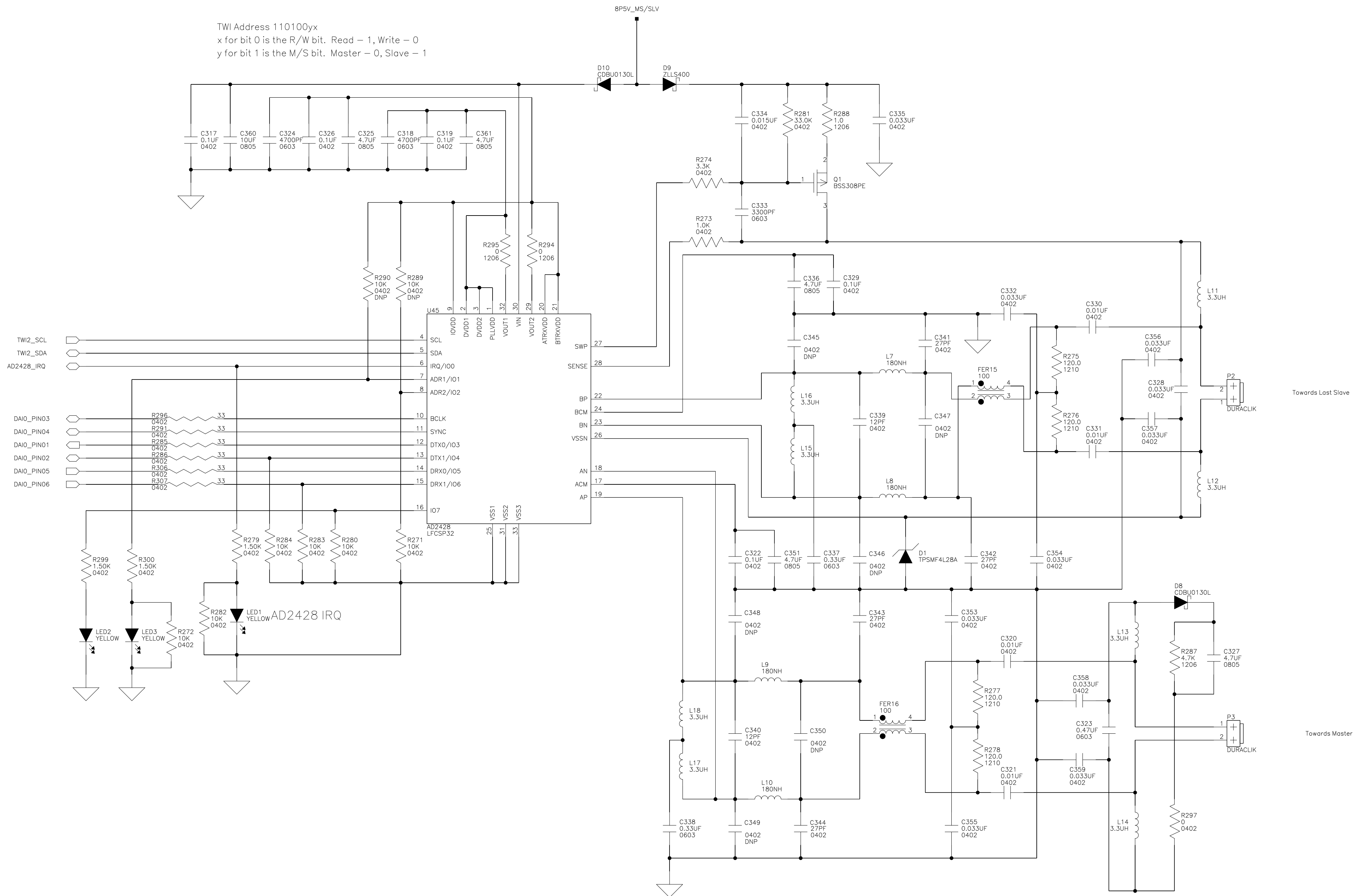


		2 Elizabeth Drive Chelmsford, MA 01824 PH: 1-800-ANALOGD	
		Title ADSP-21569 EZ-KIT E13-1A	
Size C	Board No. A0965-2017	Rev B	
Date 09/1/17	Sheet 20 of 24		



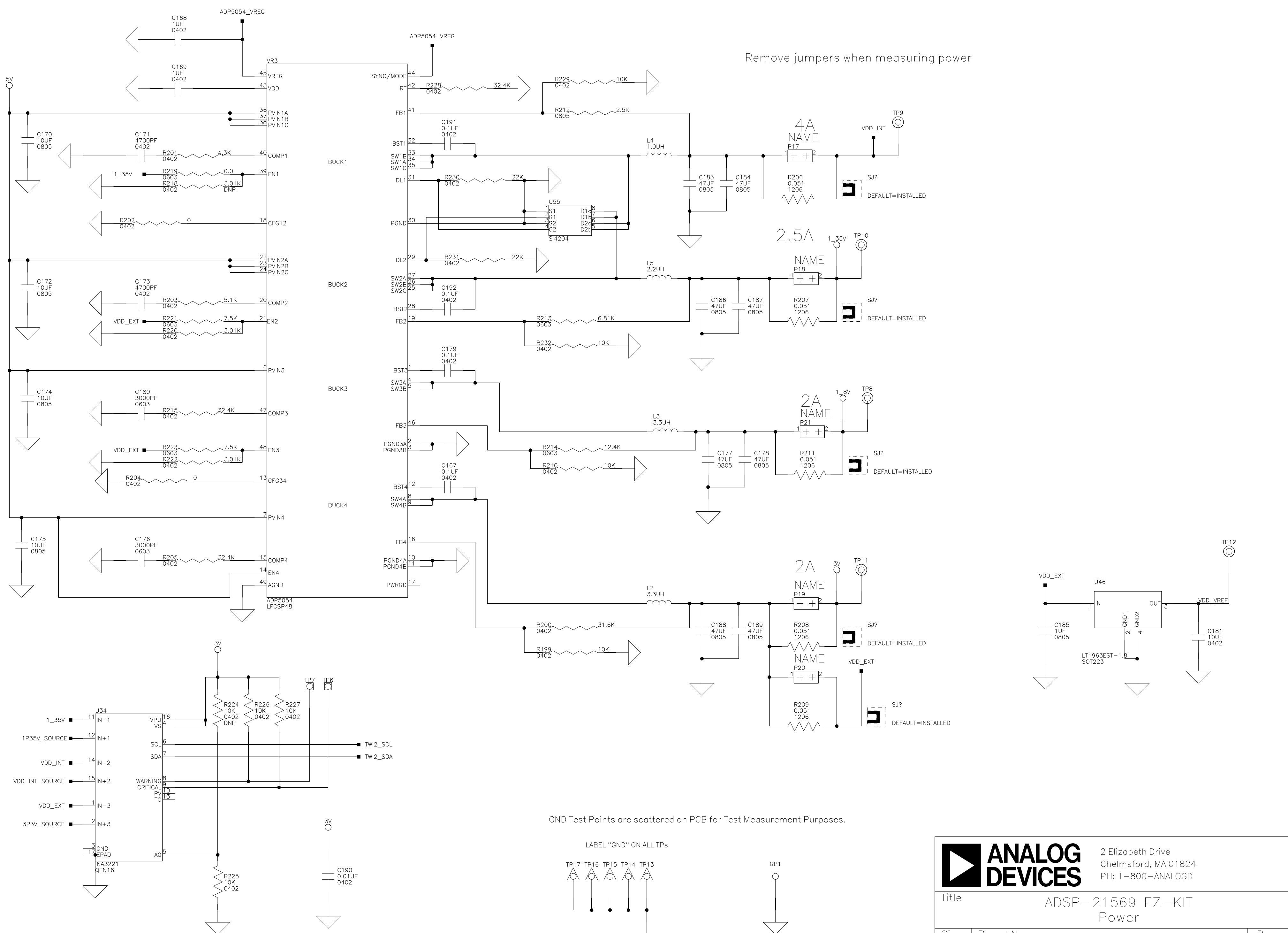
		2 Elizabeth Drive Chelmsford, MA 01824 PH: 1-800-ANALOGD	
		Title ADSP-21569 EZ-KIT E13-1B	
Size C	Board No. A0965-2017	Rev B	
Date 09/1/17	Sheet 21	of 24	

TWI Address 110100yx
 x for bit 0 is the R/W bit. Read - 1, Write - 0
 y for bit 1 is the M/S bit. Master - 0, Slave - 1



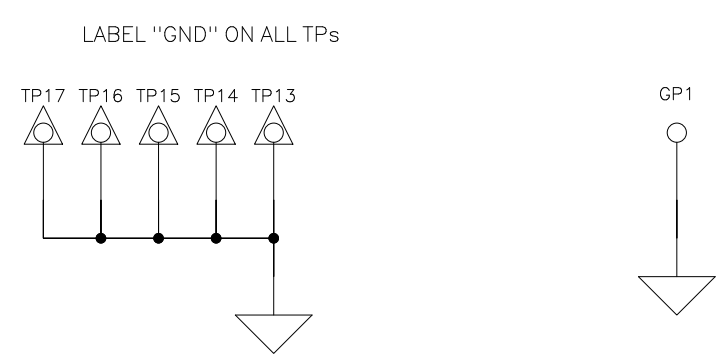
PB_05/TM0_TMR5/SPI2_RDY/SPI0_SEL2b/SMCO_ADDR3/CNT0_DG $\xrightarrow{R298}$ 0 $\xrightarrow{R298}$ AD2428_IRQ

		2 Elizabeth Drive Chelmsford, MA 01824 PH: 1-800-ANALOGD	
		Title ADSP-21569 EZ-KIT AD2428	
Size C	Board No. A0965-2017	Rev B	
Date 09/1/17	Sheet 22 of 24		

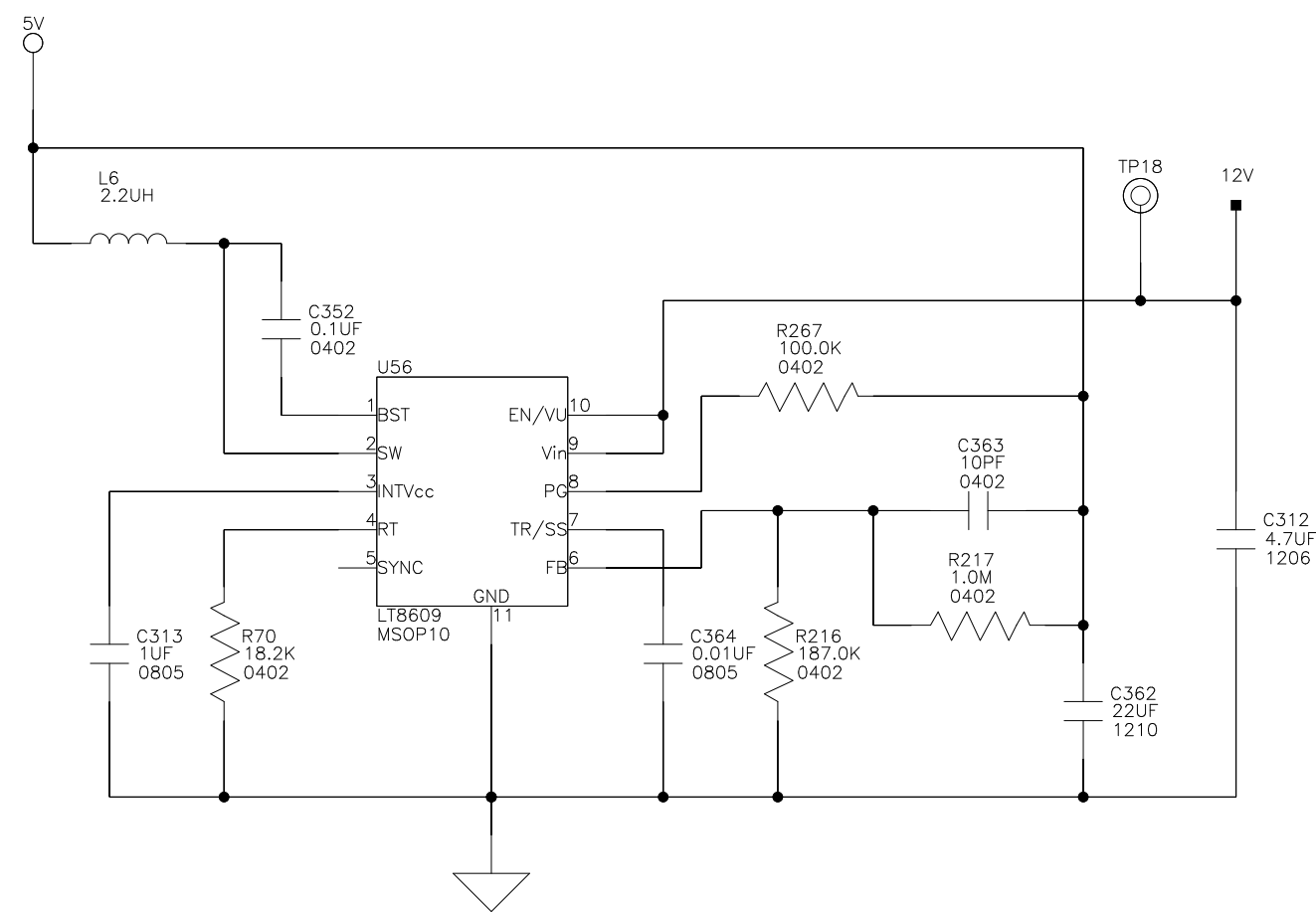
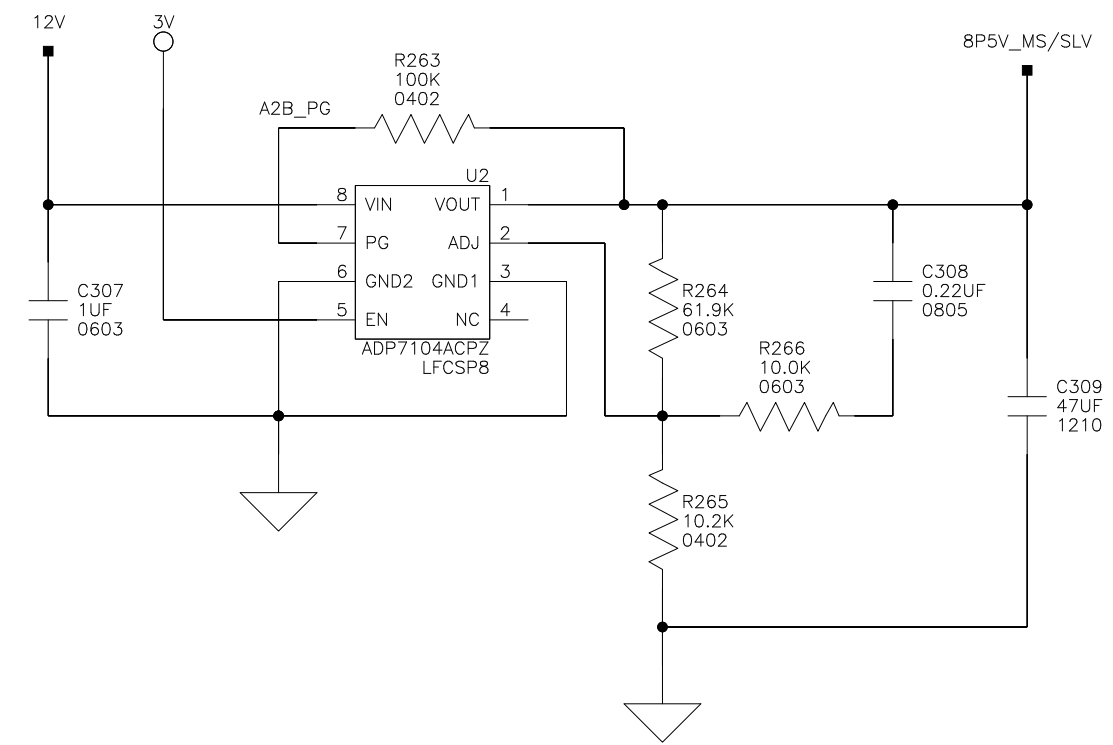
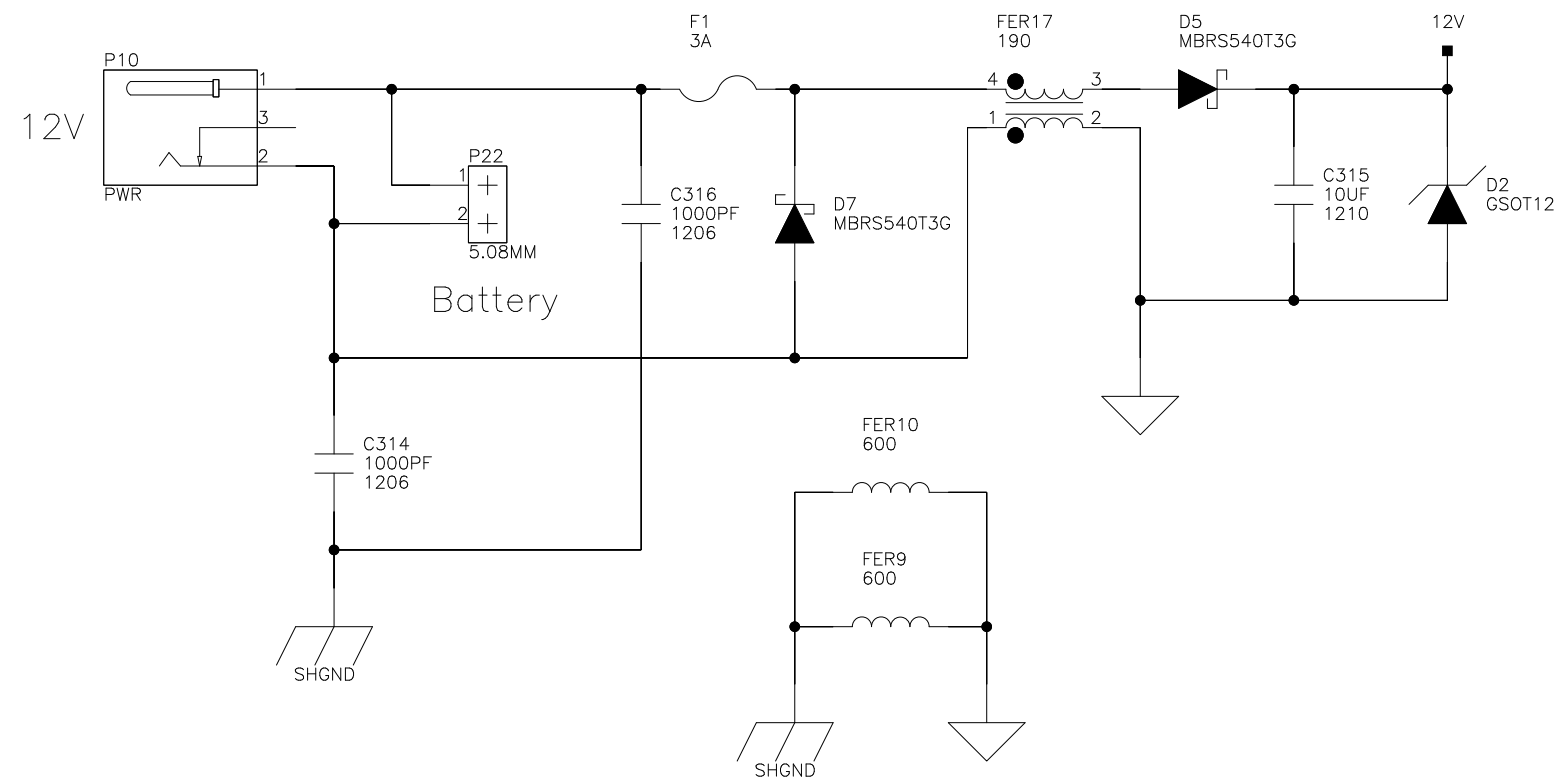



TWI address 1000 000x
 where x is the R/W bit. Read - 1, Write - 0

GND Test Points are scattered on PCB for Test Measurement Purposes.



		2 Elizabeth Drive Chelmsford, MA 01824 PH: 1-800-ANALOGD	
		Title ADSP-21569 EZ-KIT Power	
Size C	Board No. A0965-2017	Rev B	
Date 09/1/17	Sheet 23 of 24		



		2 Elizabeth Drive Chelmsford, MA 01824 PH: 1-800-ANALOGD	
Title		ADSP-21569 EZ-KIT Battery/A2B Power	
Size C	Board No.	A0965-2017	Rev B
Date	09/1/17	Sheet	24 of 24