
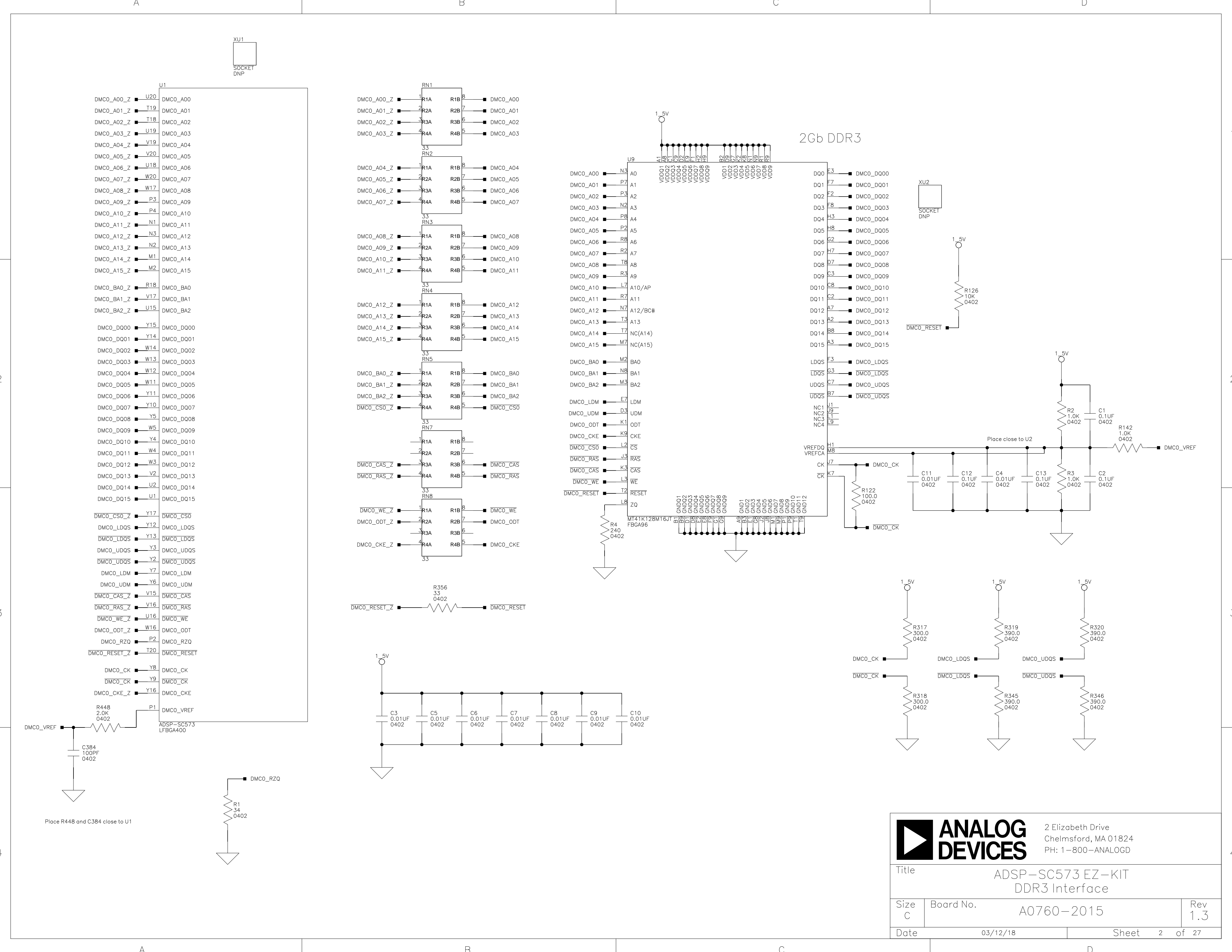



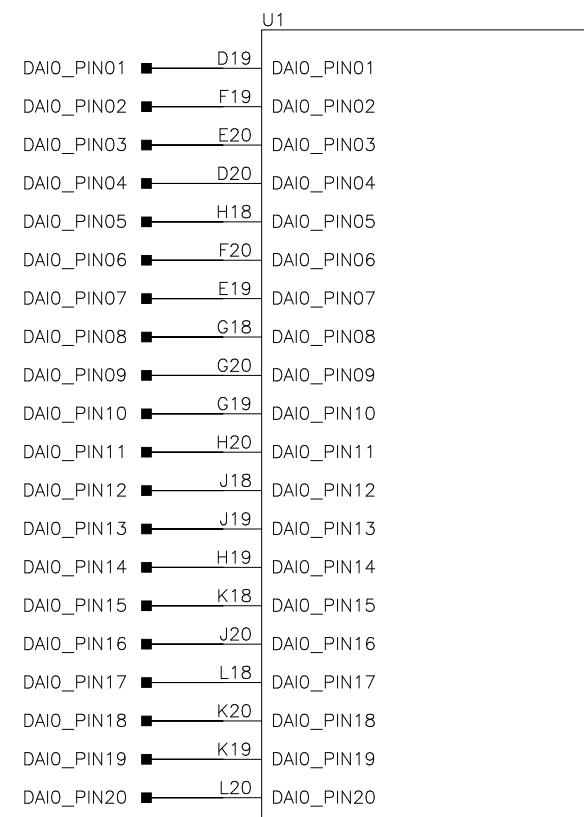
ADSP-SC573 EZ-KIT

Schematic

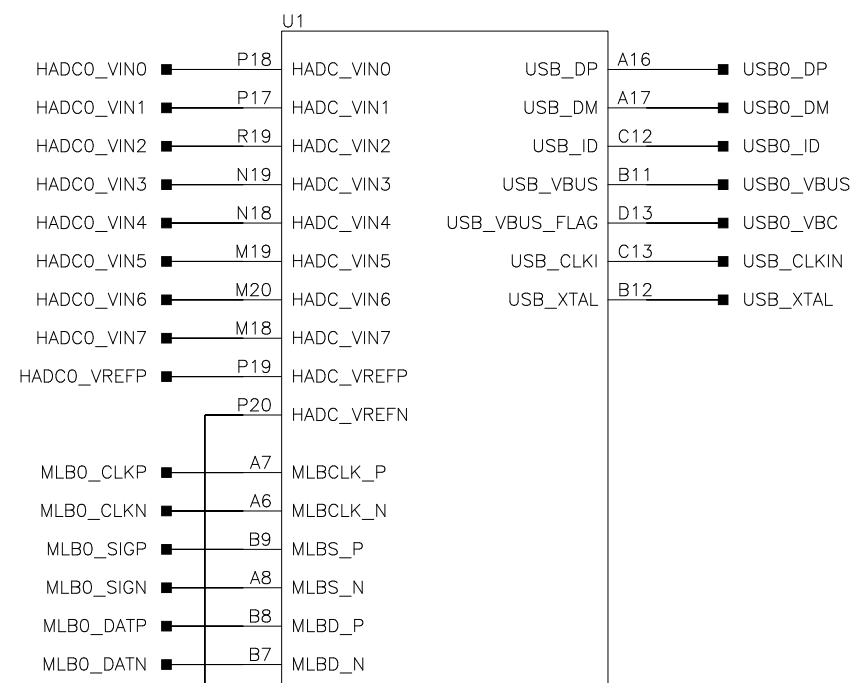
		2 Elizabeth Drive Chelmsford, MA 01824 PH: 1-800-ANALOGD	
Title		ADSP-SC573 EZ-KIT Title Block	
Size C	Board No.	A0760-2015	Rev 1.3
Date	03/12/18	Sheet	1 of 27



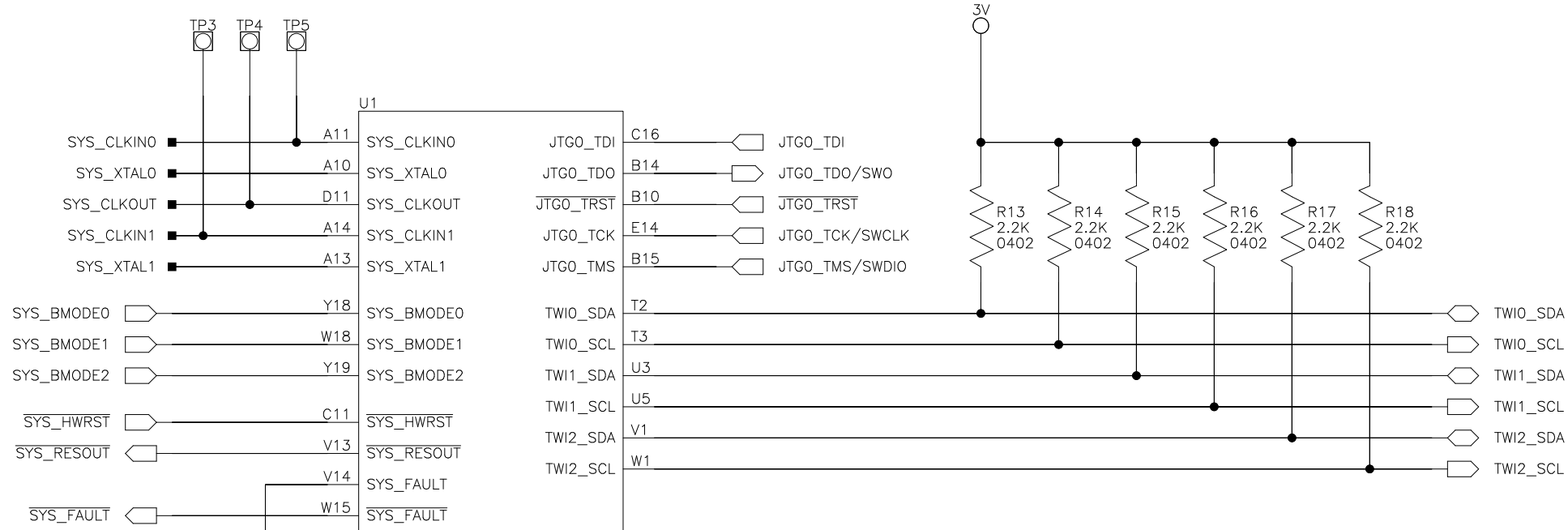
		2 Elizabeth Drive Chelmsford, MA 01824 PH: 1-800-ANALOGD	
		Title ADSP-SC573 EZ-KIT DDR3 Interface	
Size C	Board No. A0760-2015	Rev 1.3	
Date 03/12/18	Sheet 2 of 27		



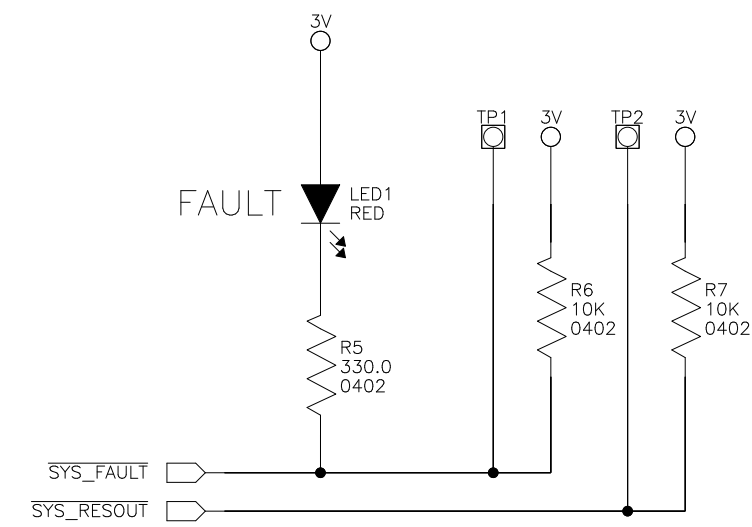
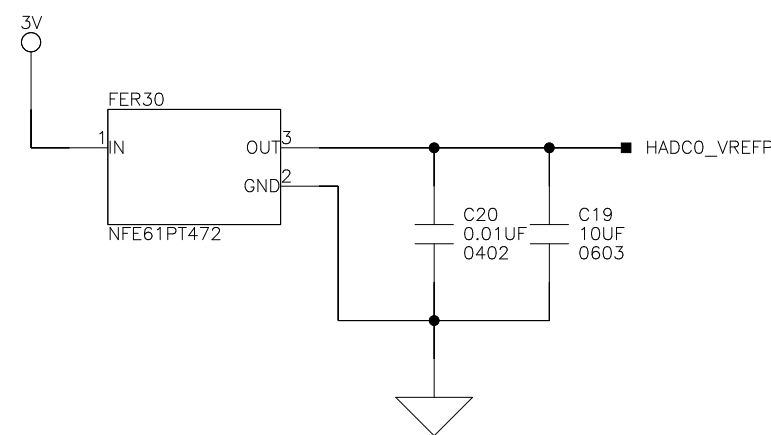
ADSP-SC573
LFBGA400



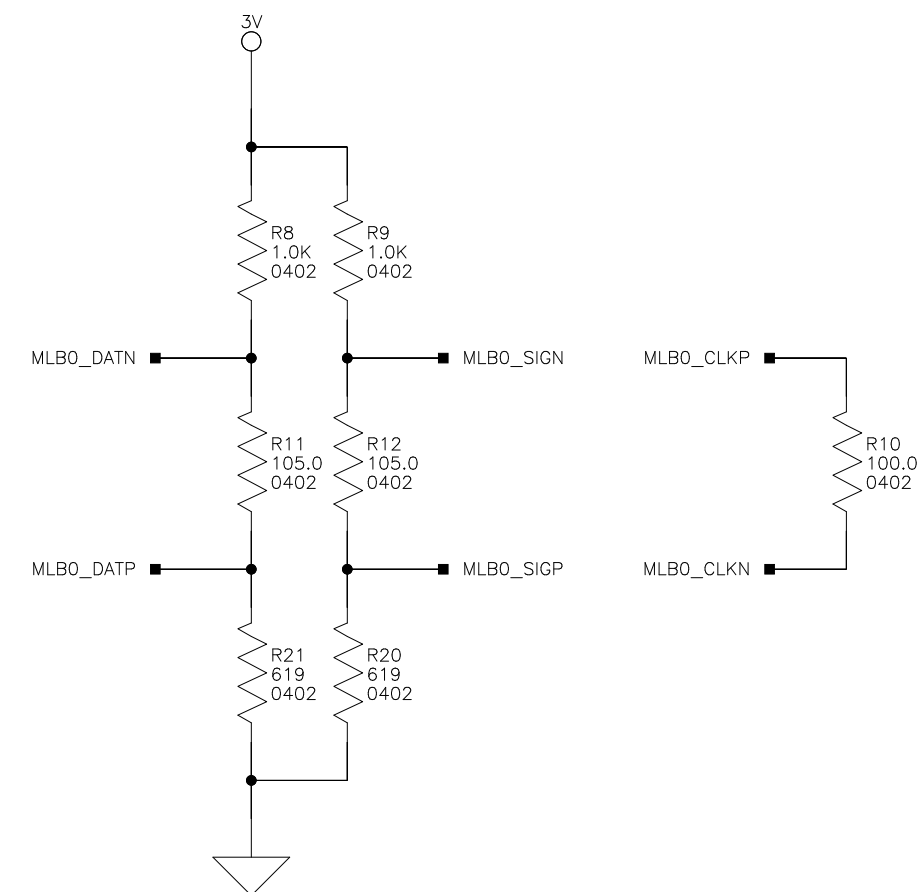
ADSP-SC573
LFBGA400



ADSP-SC573
LFBGA400



MLB signals need to be short and matched trace length.



		2 Elizabeth Drive Chelmsford, MA 01824 PH: 1-800-ANALOGD
Title ADSP-SC573 EZ-KIT Non-Mux Ports		
Size C	Board No. A0760-2015	Rev 1.3
Date 03/12/18	Sheet 3 of 27	

1

1

2

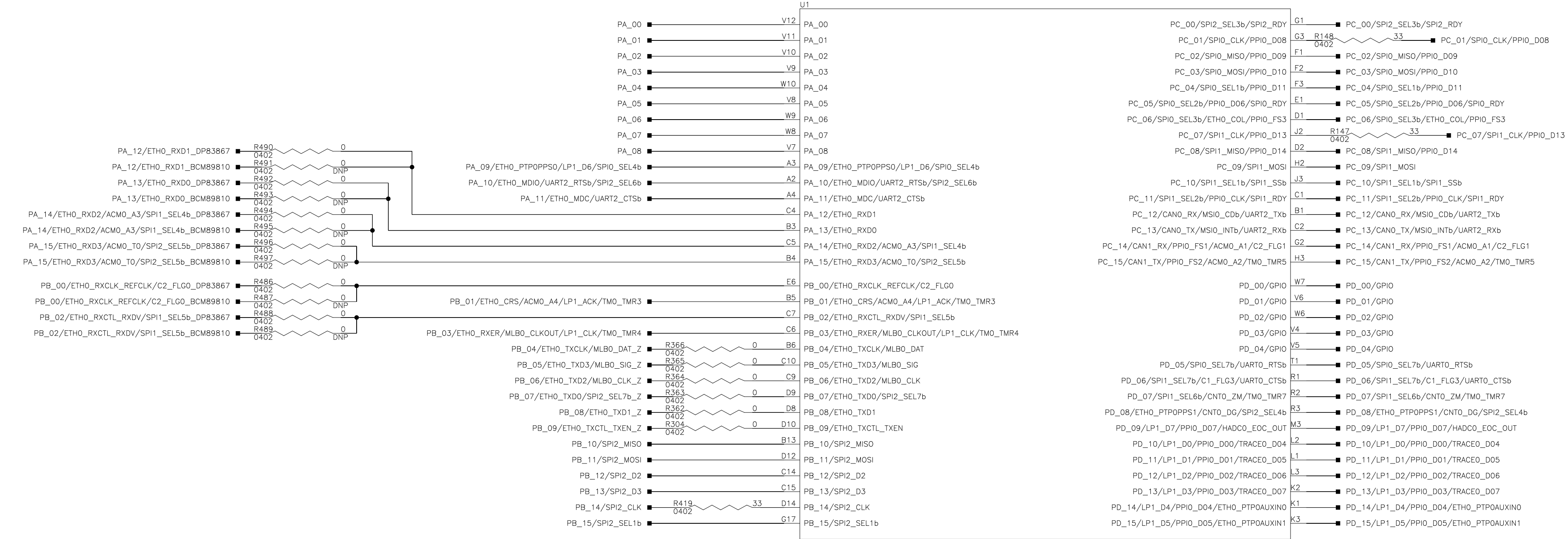
2

3

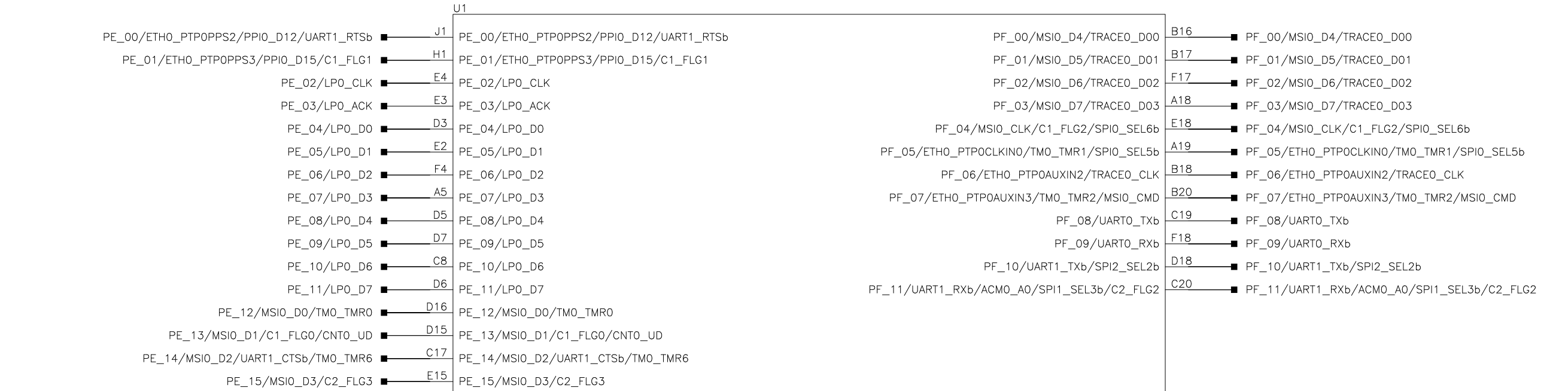
3

4

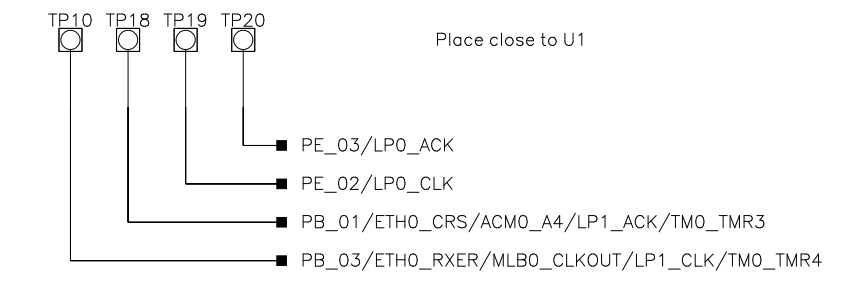
4




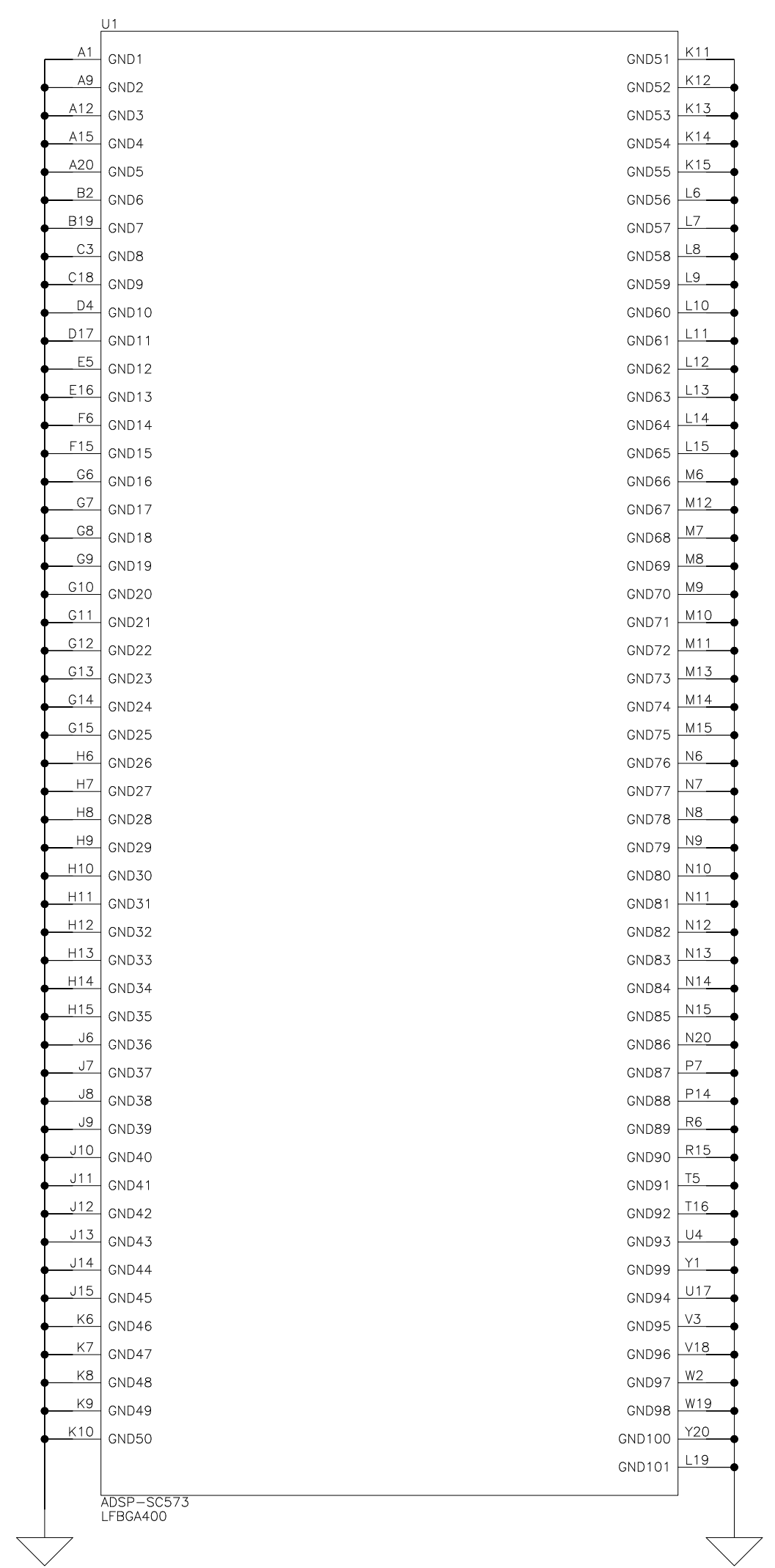
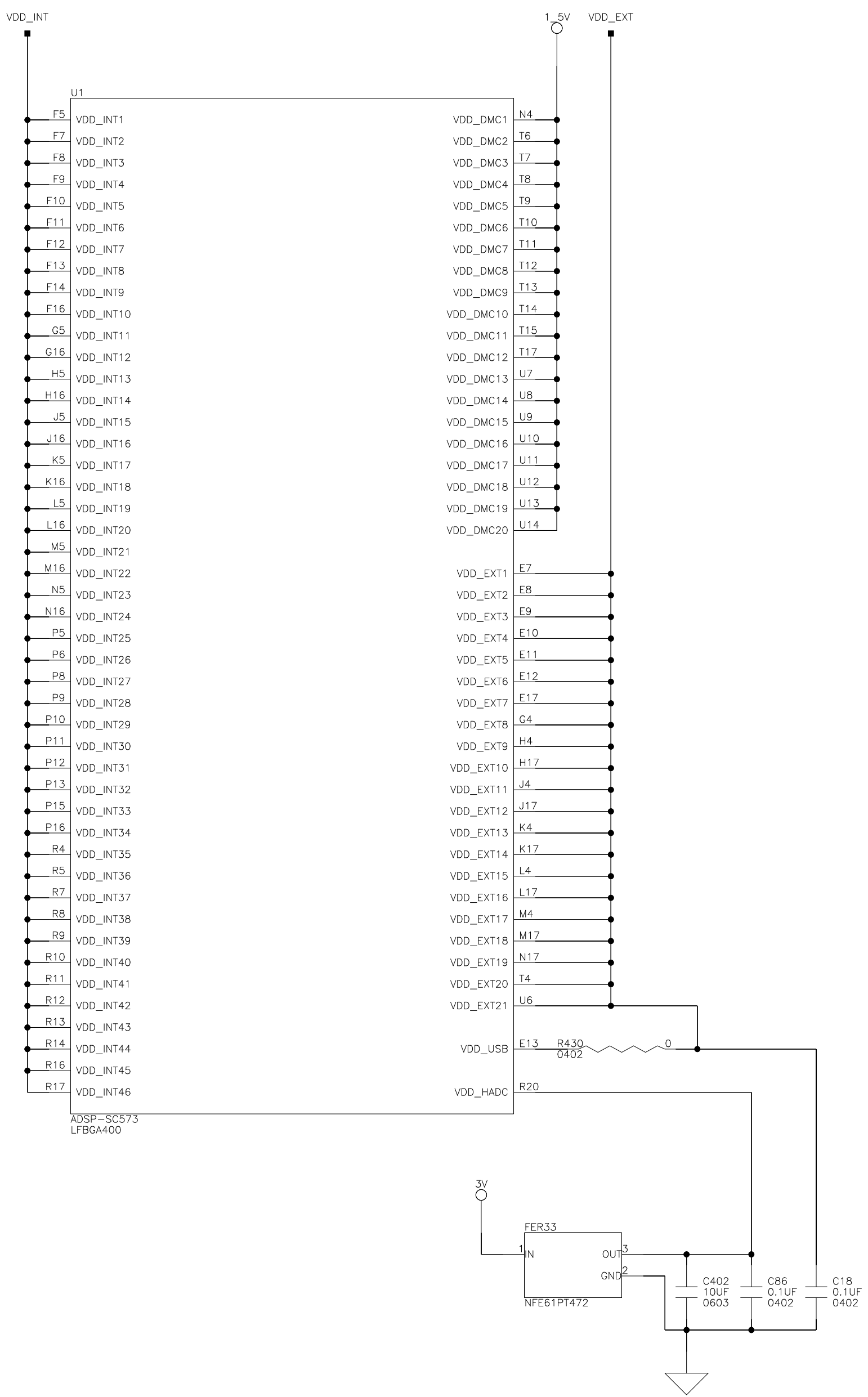
ADSP-SC573
LFBGA400




ADSP-SC573
LFBGA400



		2 Elizabeth Drive Chelmsford, MA 01824 PH: 1-800-ANALOGD	
		Title ADSP-SC573 EZ-KIT Mux Ports	
Size C	Board No. A0760-2015	Rev 1.3	
Date 03/12/18	Sheet 4 of 27		



Connect to GND on customers design

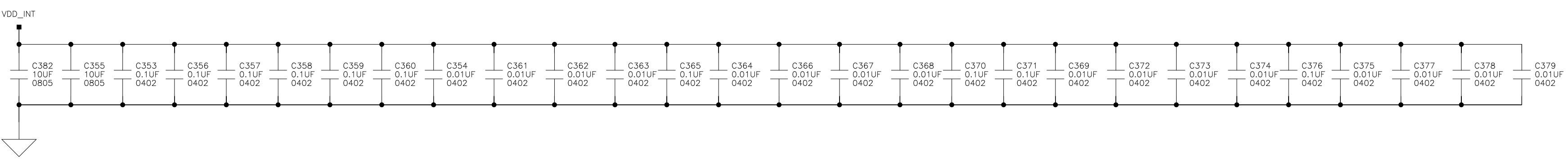
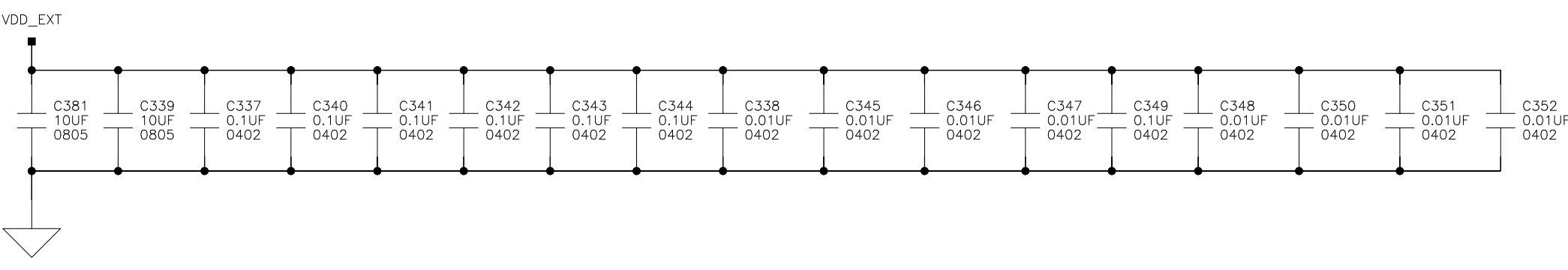
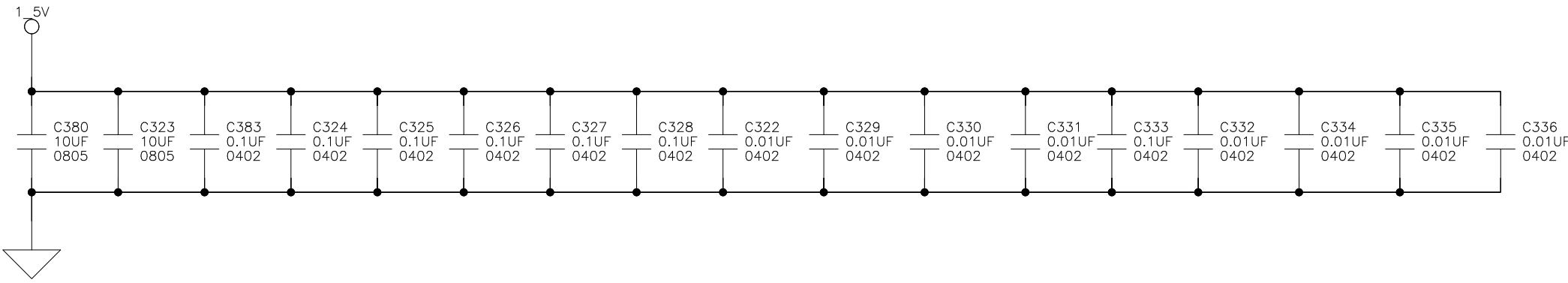
		2 Elizabeth Drive Chelmsford, MA 01824 PH: 1-800-ANALOGD	
		Title ADSP-SC573 EZ-KIT Processor Power/Ground	
Size C	Board No. A0760-2015	Rev 1.3	
Date 03/12/18	Sheet 5 of 27		


A

B

C

D



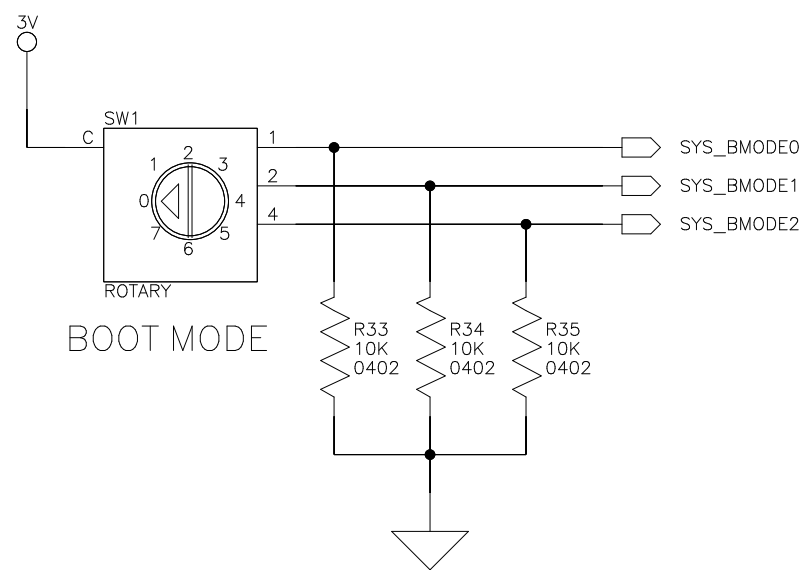
		2 Elizabeth Drive Chelmsford, MA 01824 PH: 1-800-ANALOGD	
Title		ADSP-SC573 EZ-KIT Proc Bypass Caps	
Size C	Board No.	A0760-2015	Rev 1.3
Date	03/12/18	Sheet	6 of 27

A

B

C

D

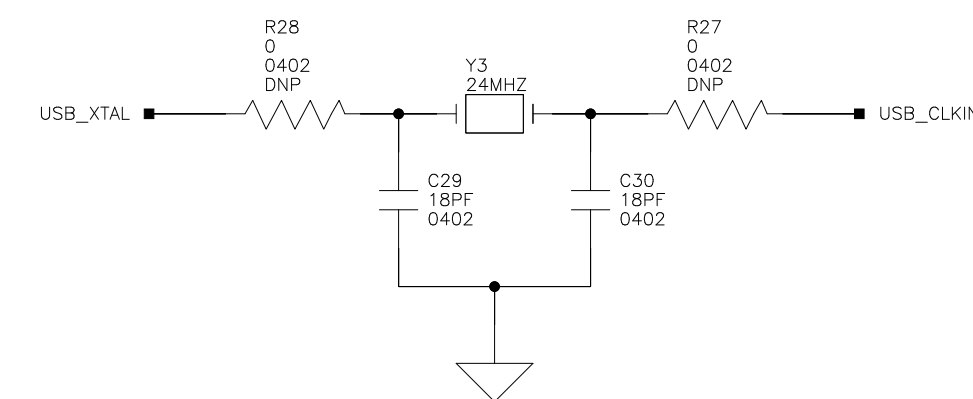


SW1: Boot mode selection switch

BMODE[2:0]	Boot Source	
000	No boot/Custom ROM	
001	SPI2 master boot	DEFAULT
010	SPI2 slave boot	
011	UART0 slave boot	
100	Reserved	
101	Reserved	
110	LP0 slave boot	
111	Reserved	

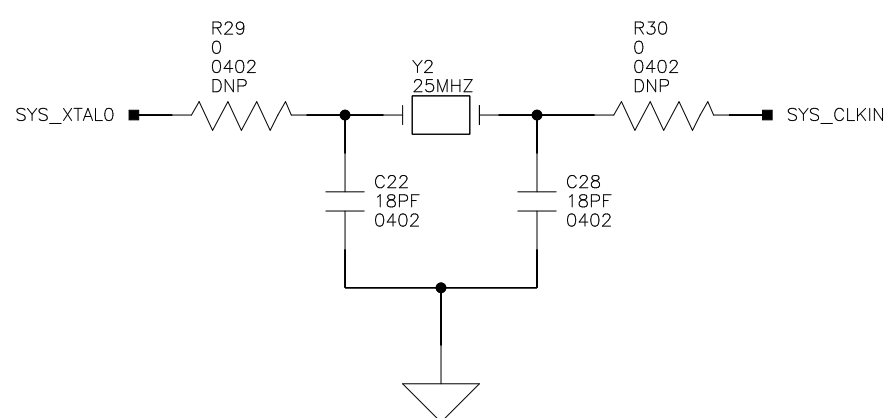
R36 should be depopulated when using the optional circuit

OPTIONAL USB CRYSTAL

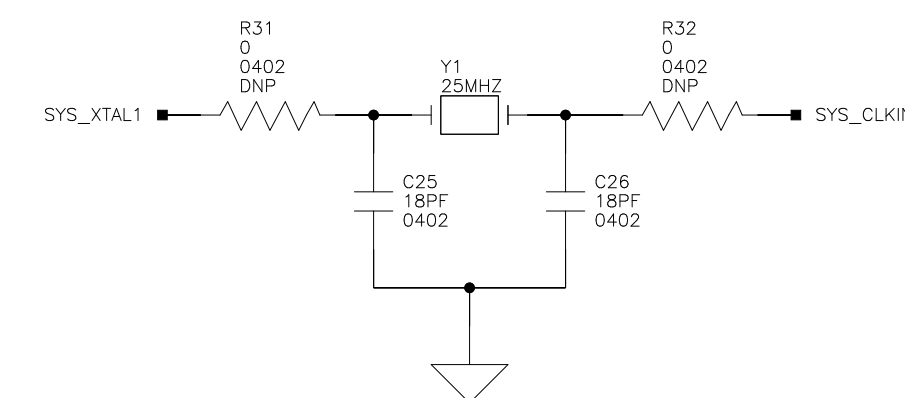


R43 should be depopulated when using the optional circuit

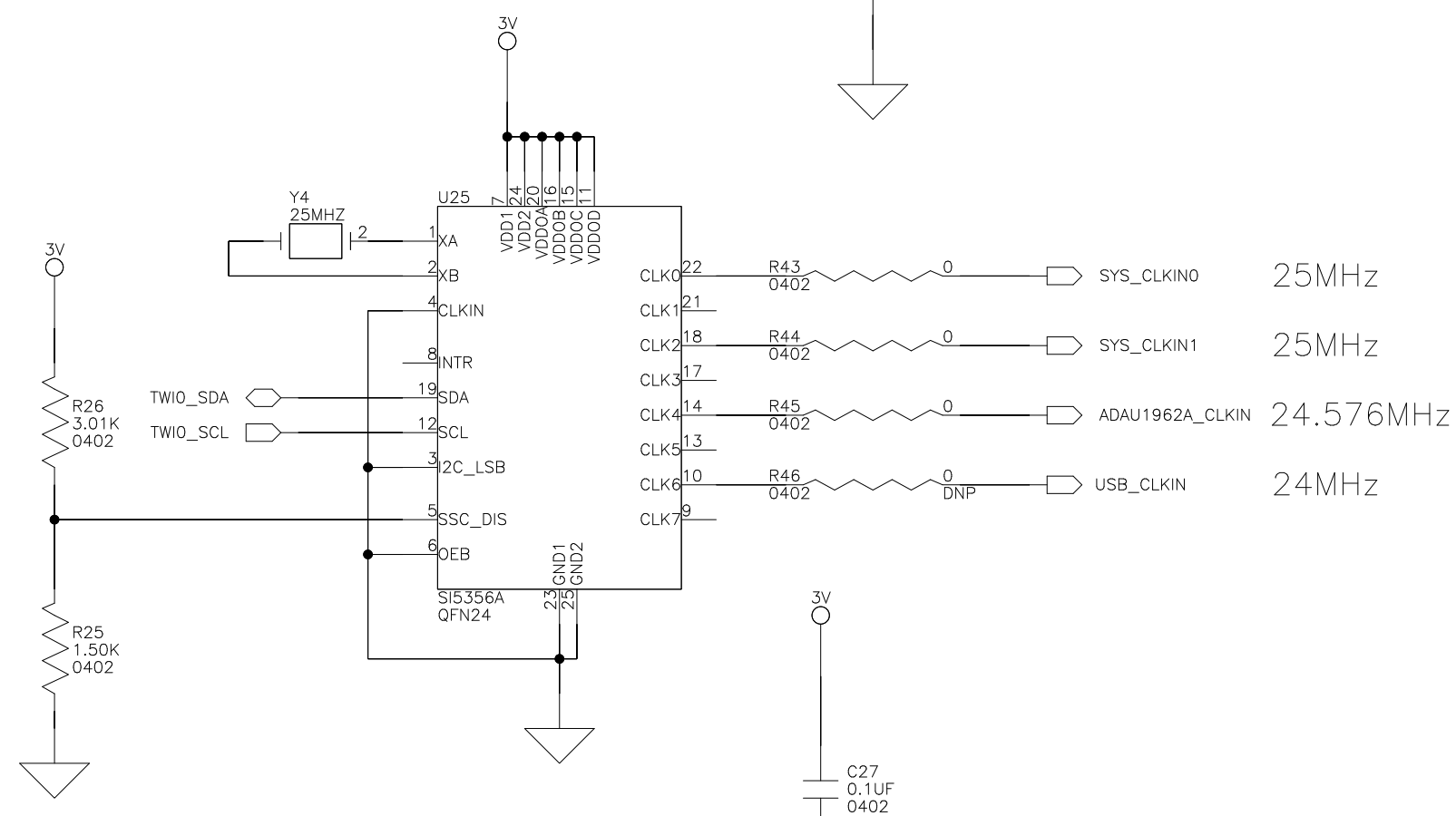
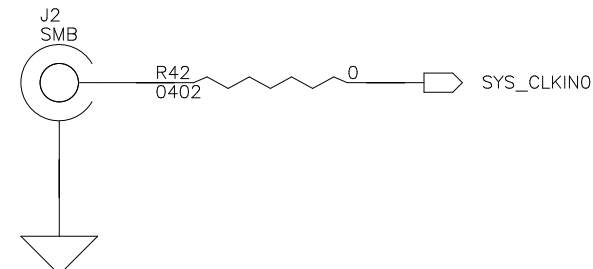
OPTIONAL DSP CRYSTALS



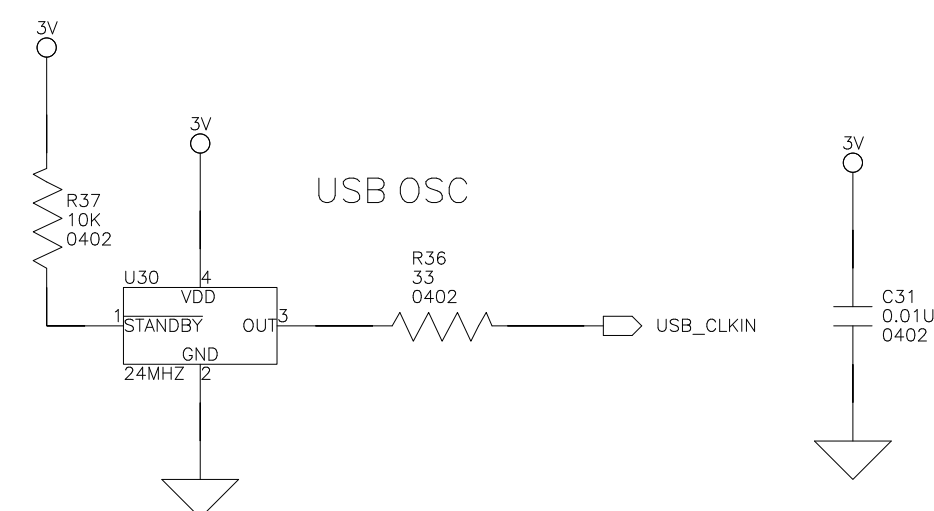
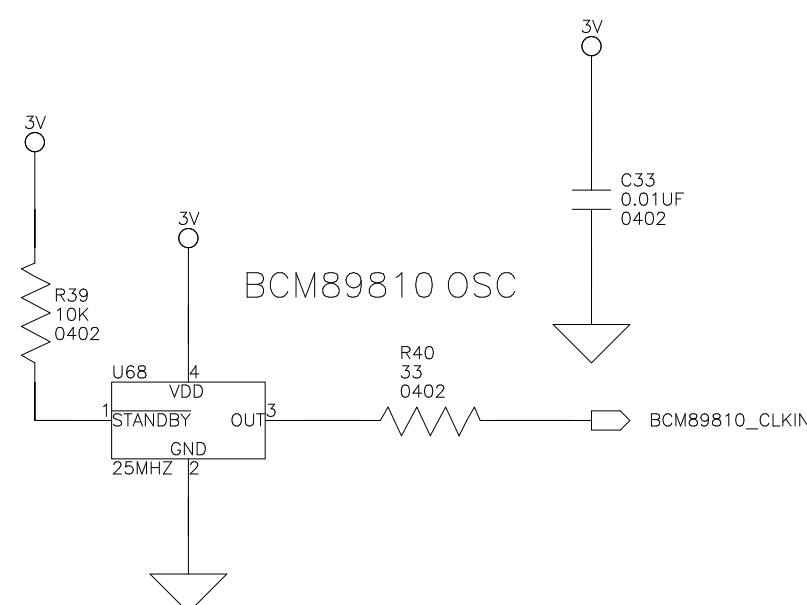
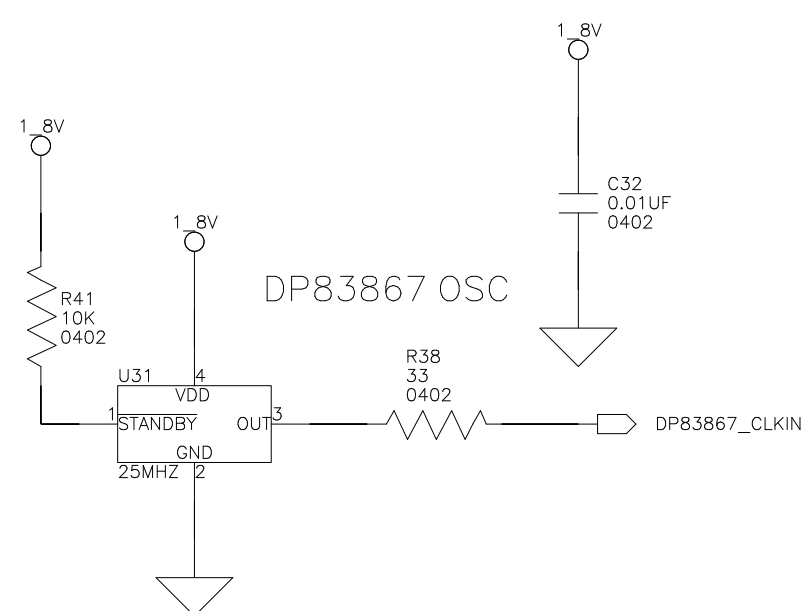
R44 should be depopulated when using the optional circuit



CLKIN0

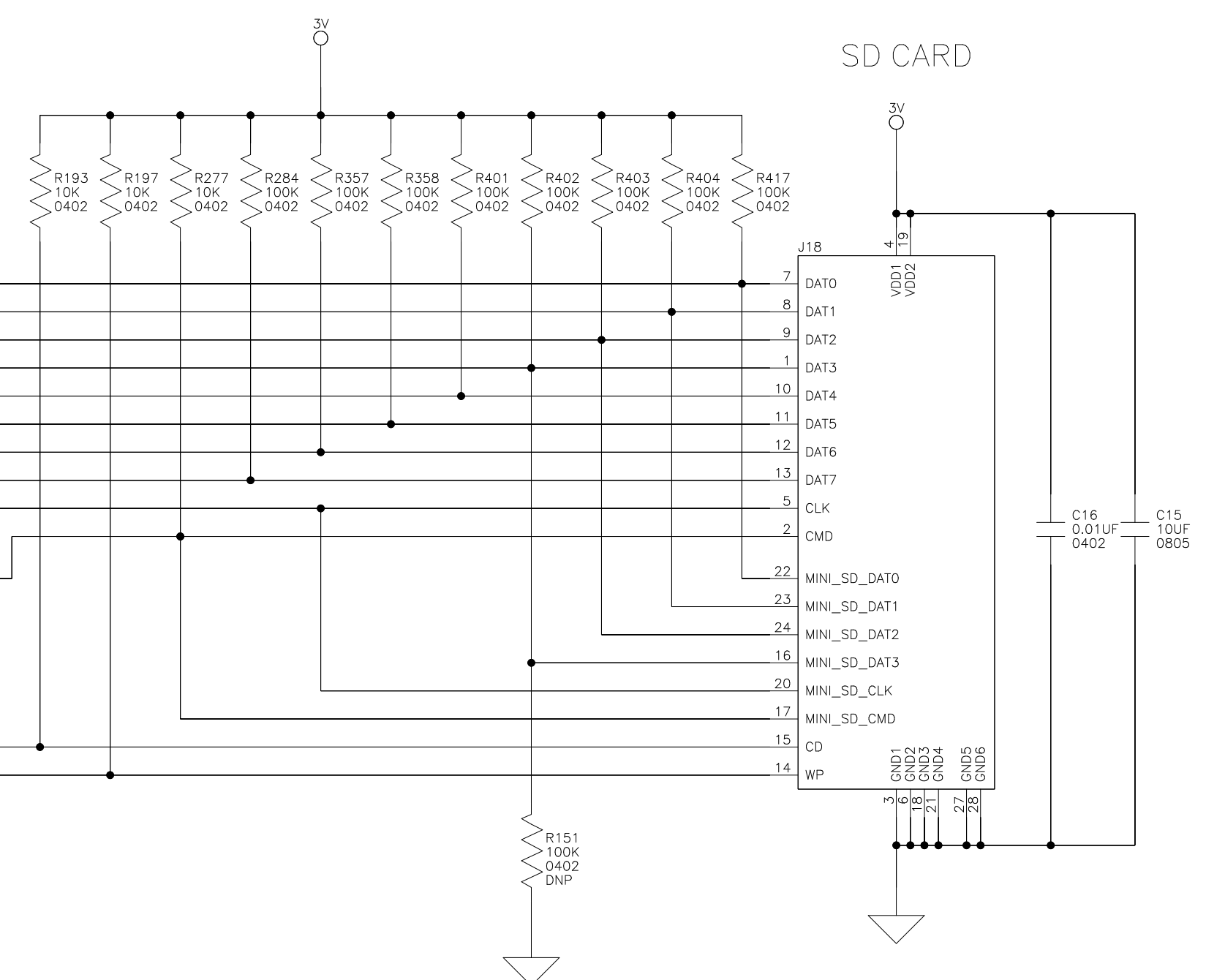
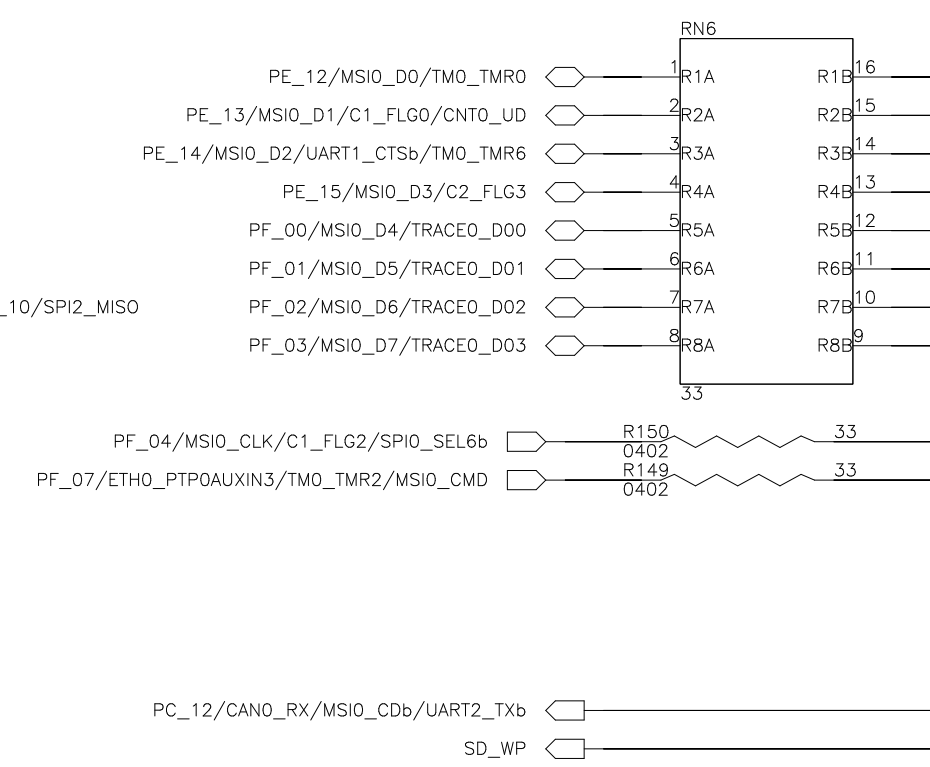
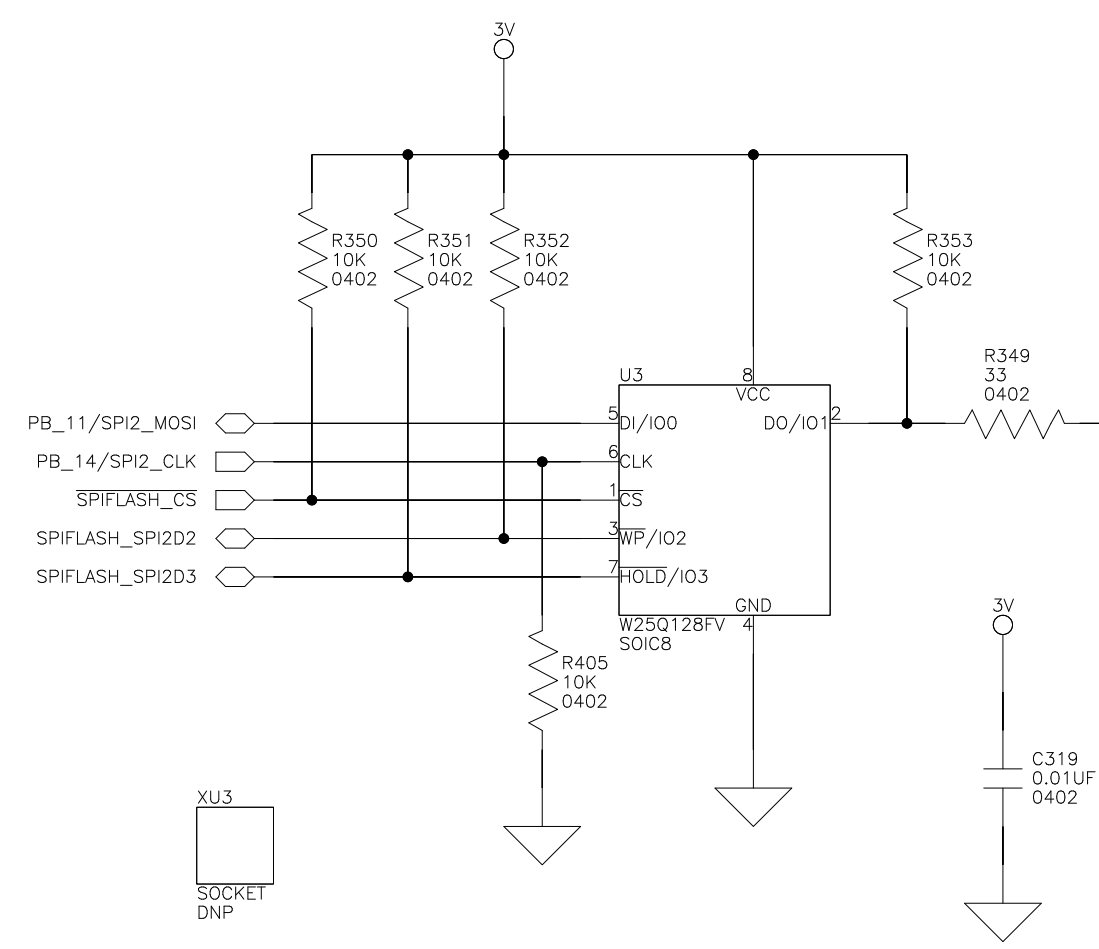


TWI Address 1110000x
x is the R/W bit. Read - 1, Write - 0

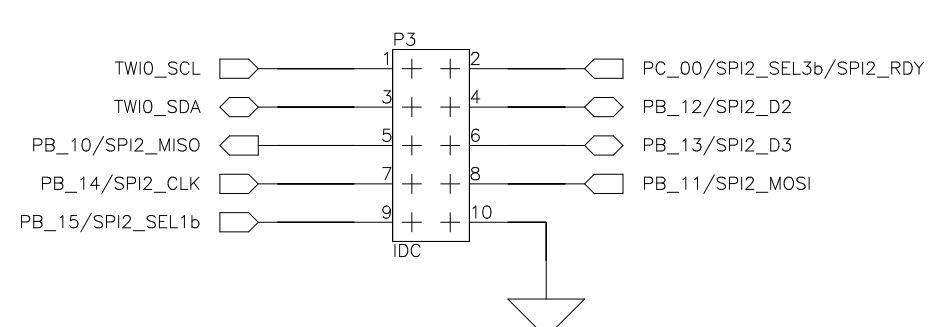
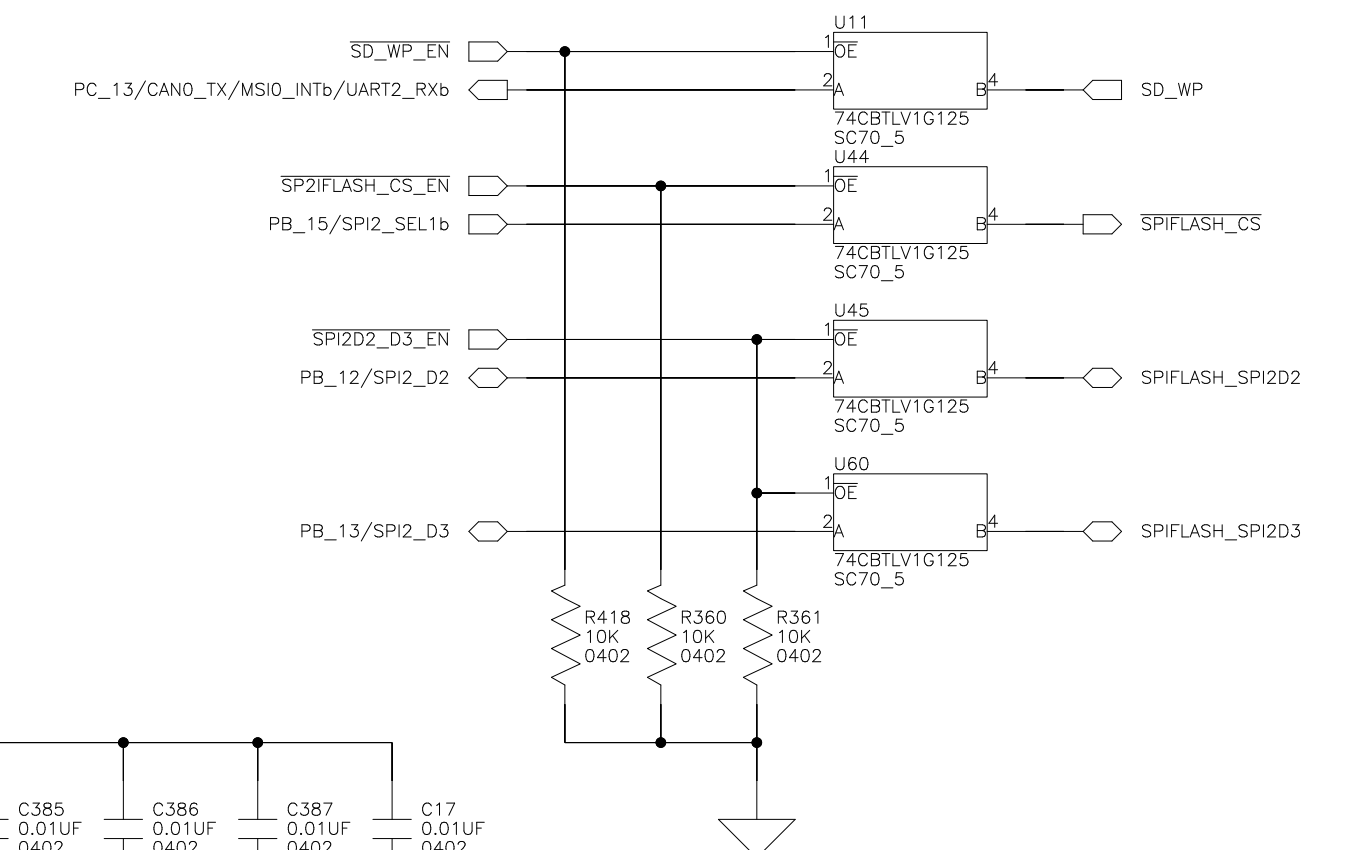
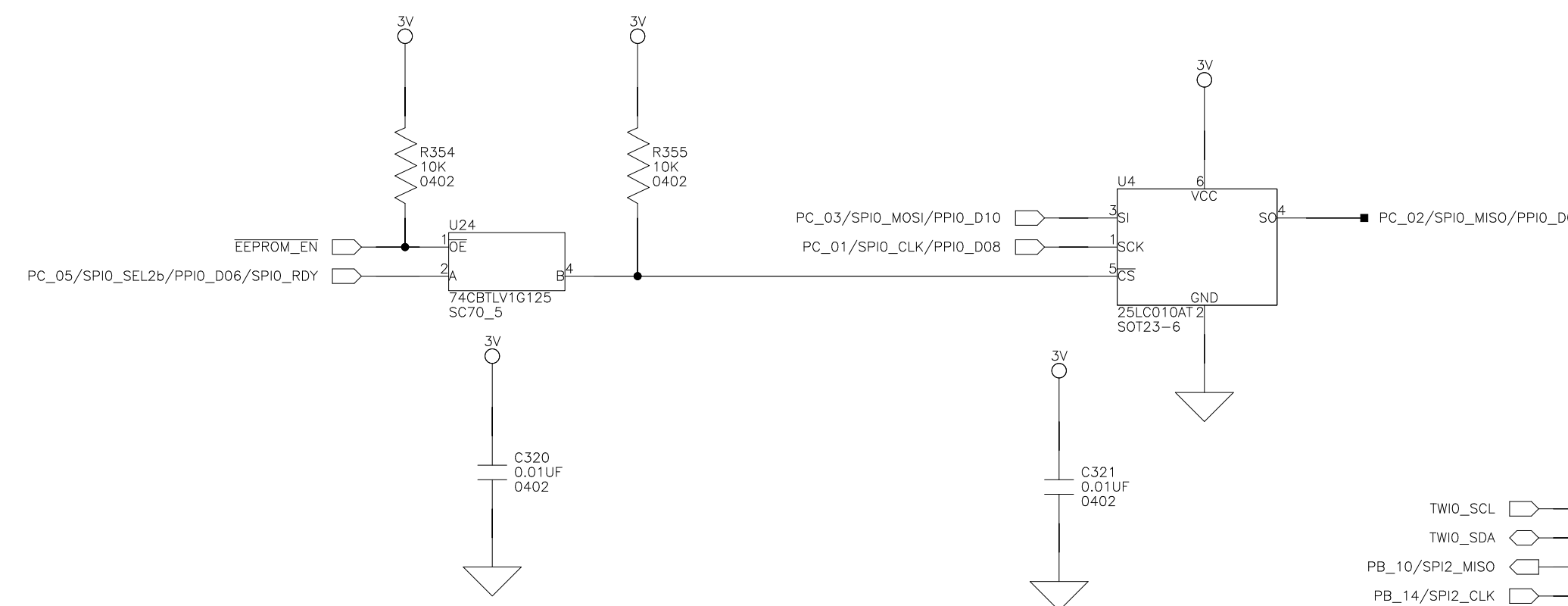


		2 Elizabeth Drive Chelmsford, MA 01824 PH: 1-800-ANALOGD	
		Title ADSP-SC573 EZ-KIT Clocks/Boot Mode	
Size C	Board No. A0760-2015	Rev 1.3	
Date 03/12/18	Sheet 7 of 27		

128Mb QUAD SPI FLASH



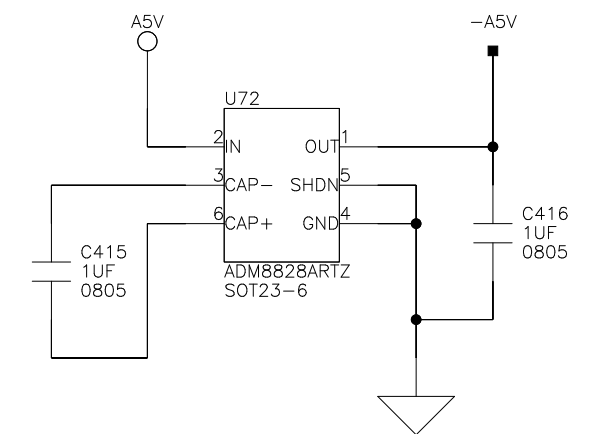
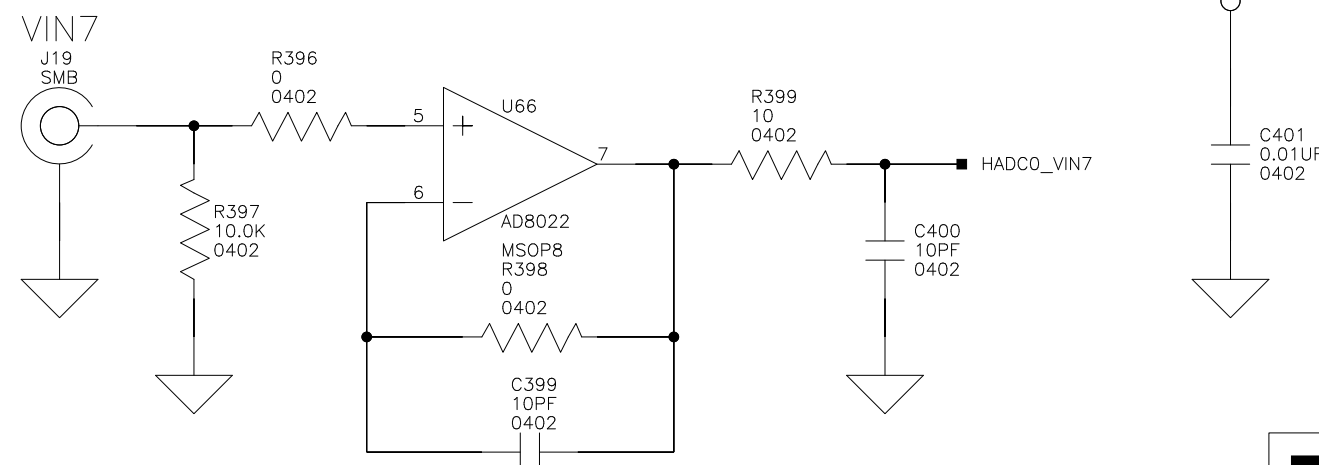
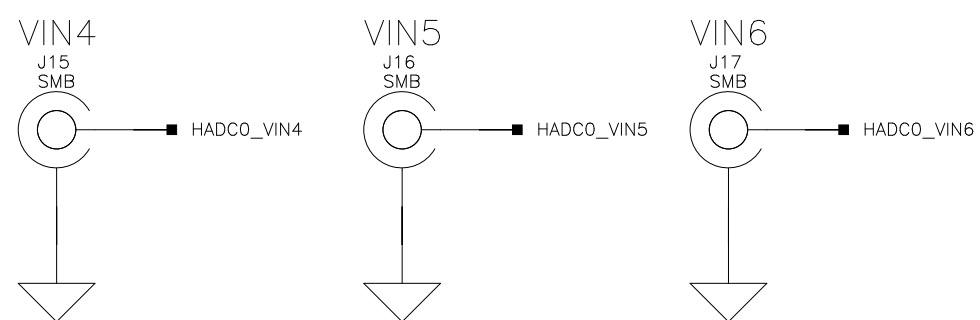
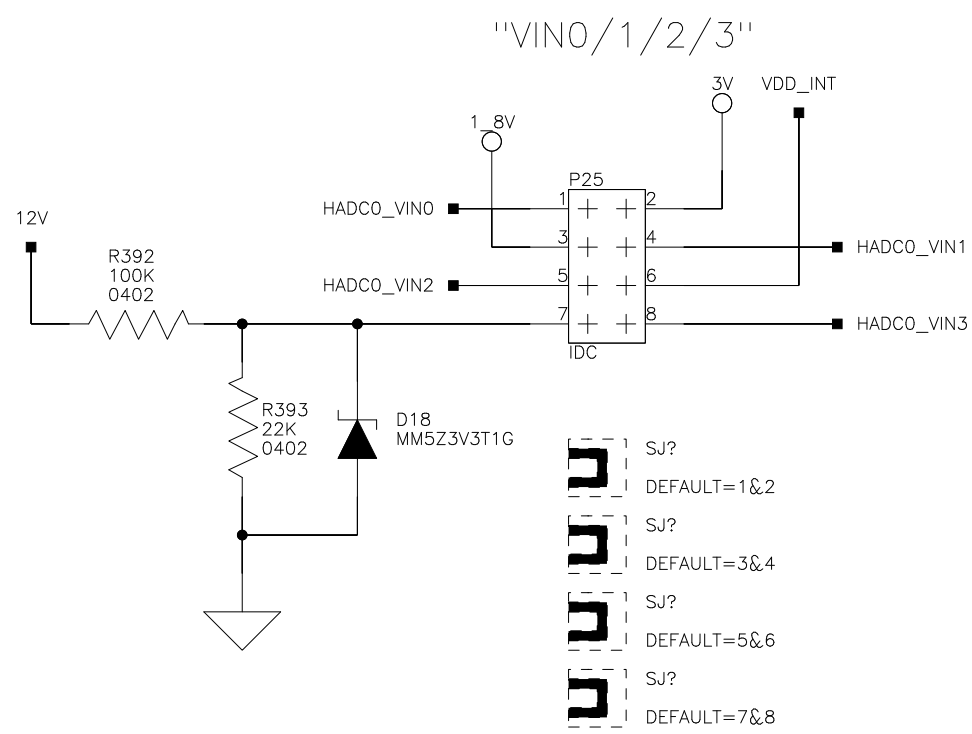
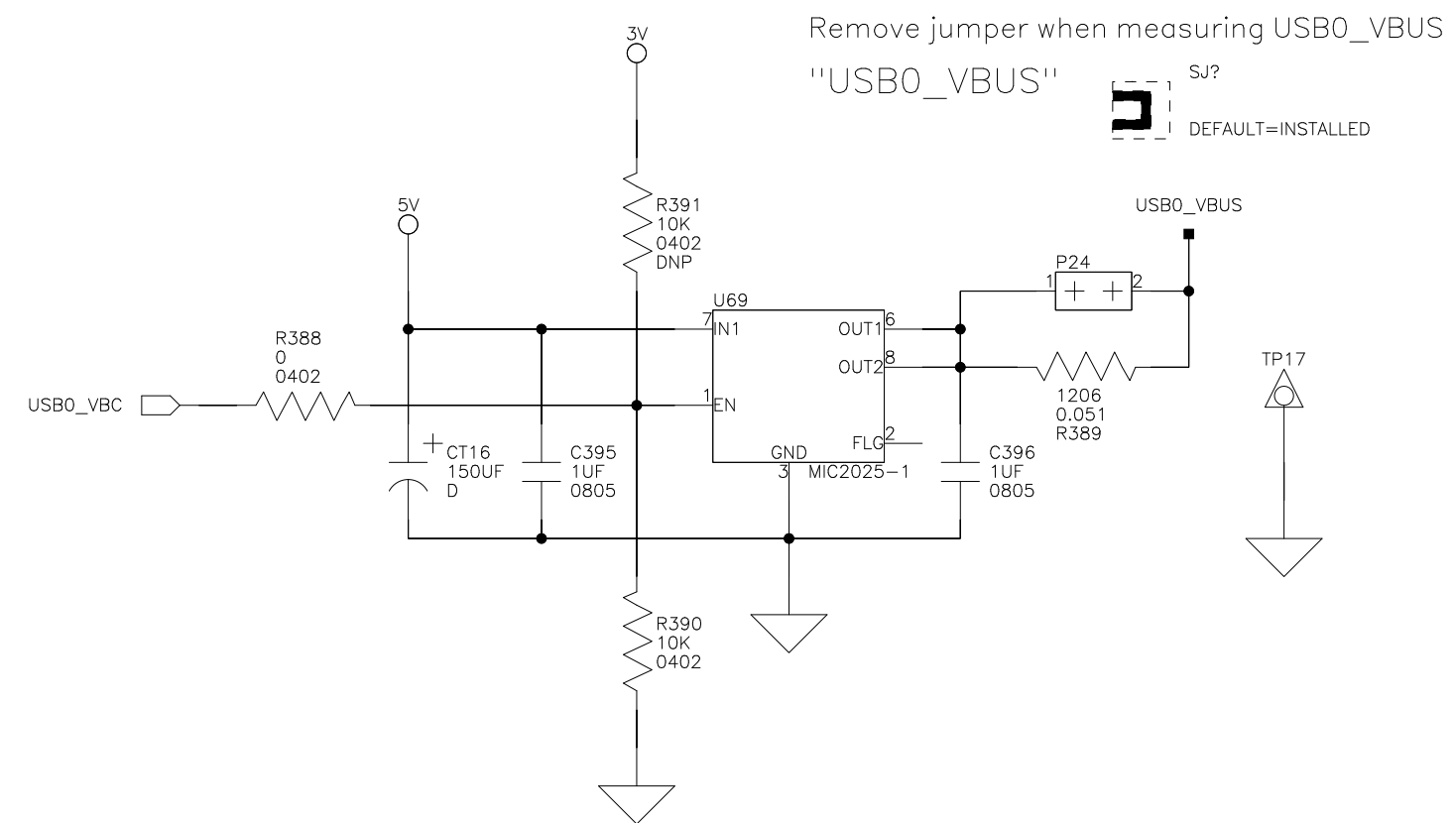
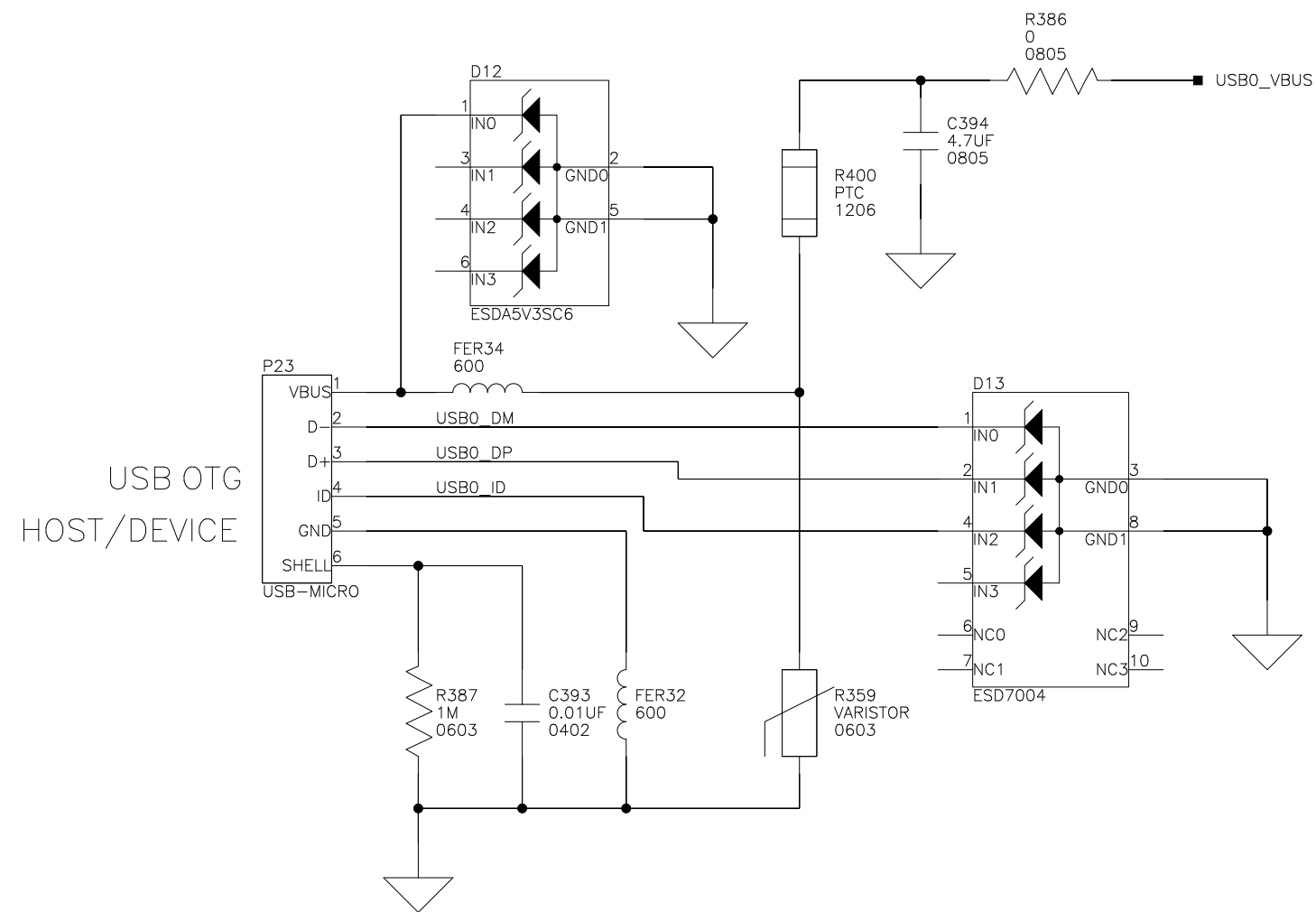
1Kb SPI EEPROM



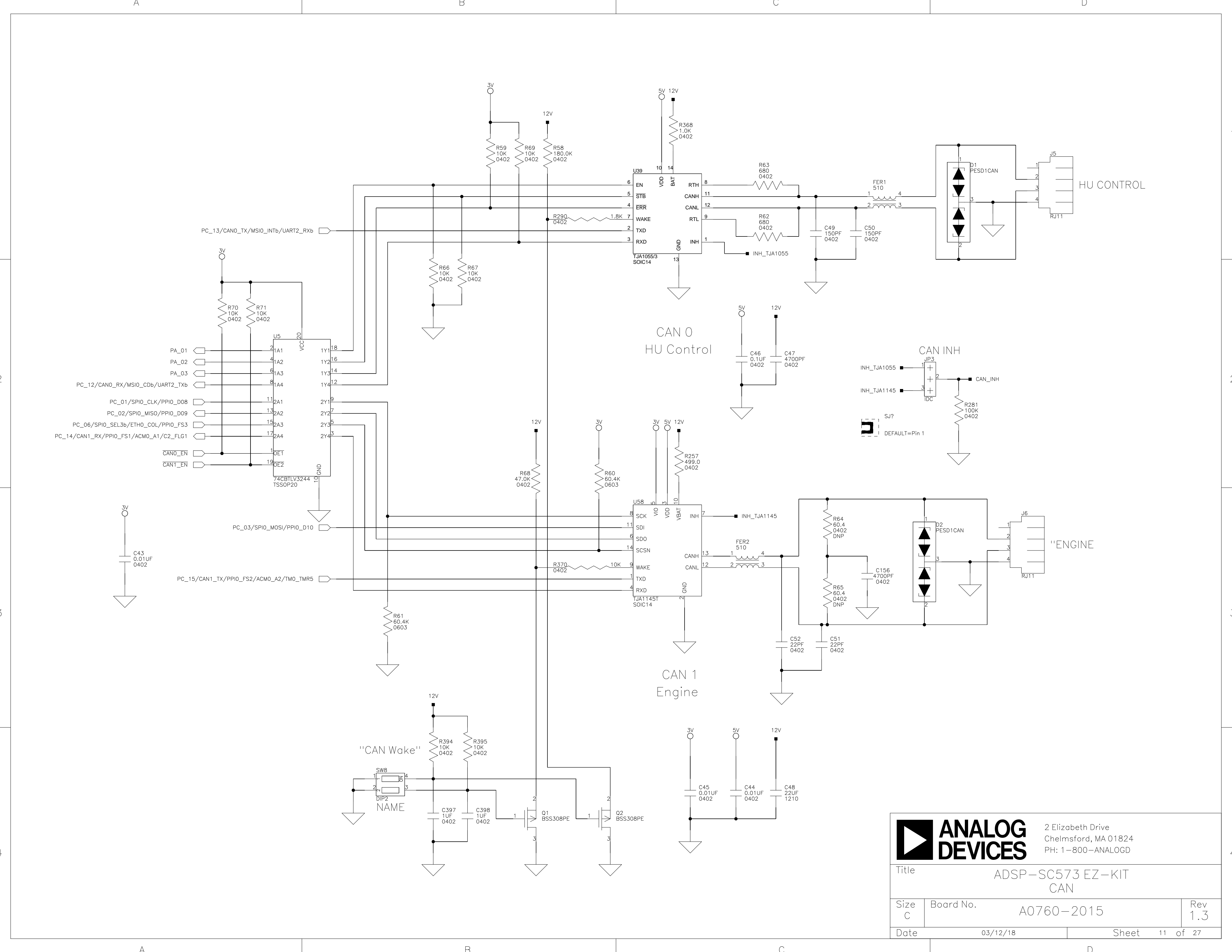
ANALOG DEVICES 2 Elizabeth Drive
Chelmsford, MA 01824
PH: 1-800-ANALOGD


Title ADSP-SC573 EZ-KIT
SPI Flash/MSI

Size C	Board No. A0760-2015	Rev 1.3
Date 03/12/18	Sheet 8 of 27	

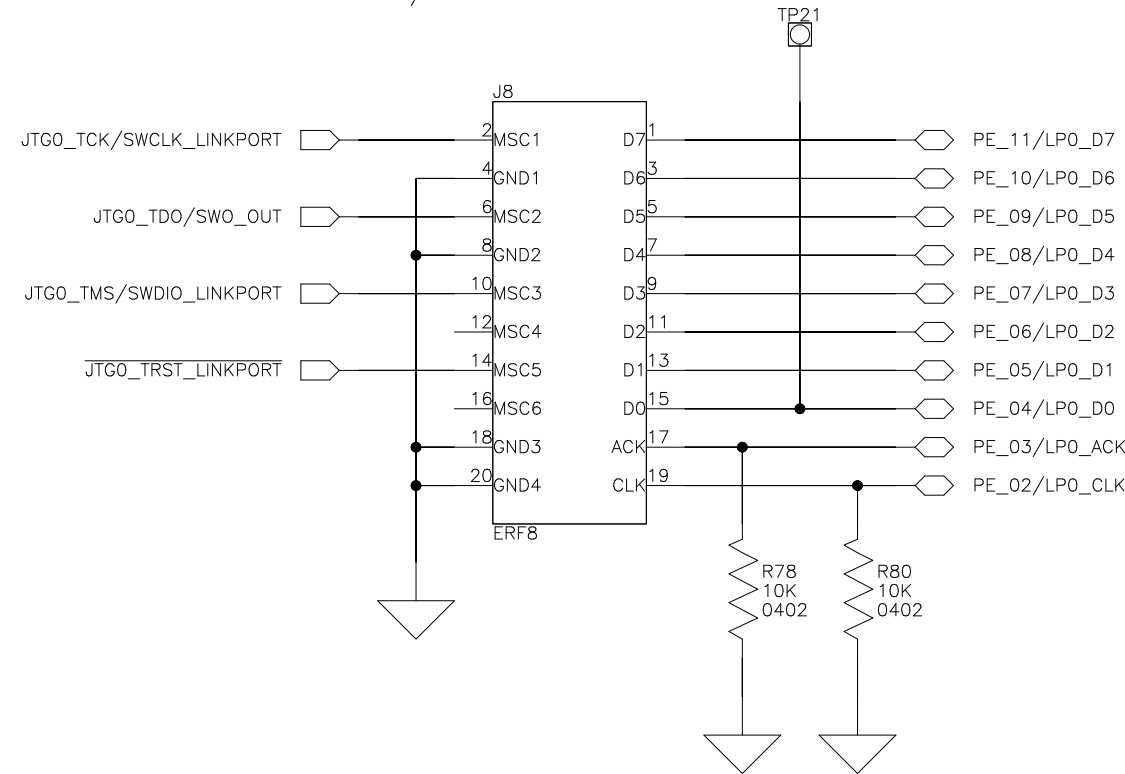


		2 Elizabeth Drive Chelmsford, MA 01824 PH: 1-800-ANALOGD	
		Title ADSP-SC573 EZ-KIT USB/ADC	
Size C	Board No. A0760-2015	Rev 1.3	
Date 03/12/18	Sheet 10 of 27		

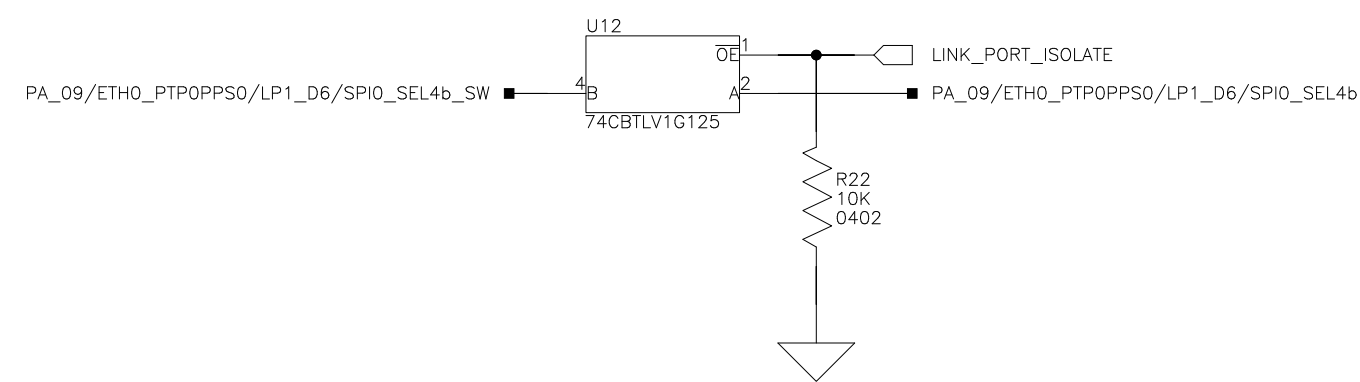
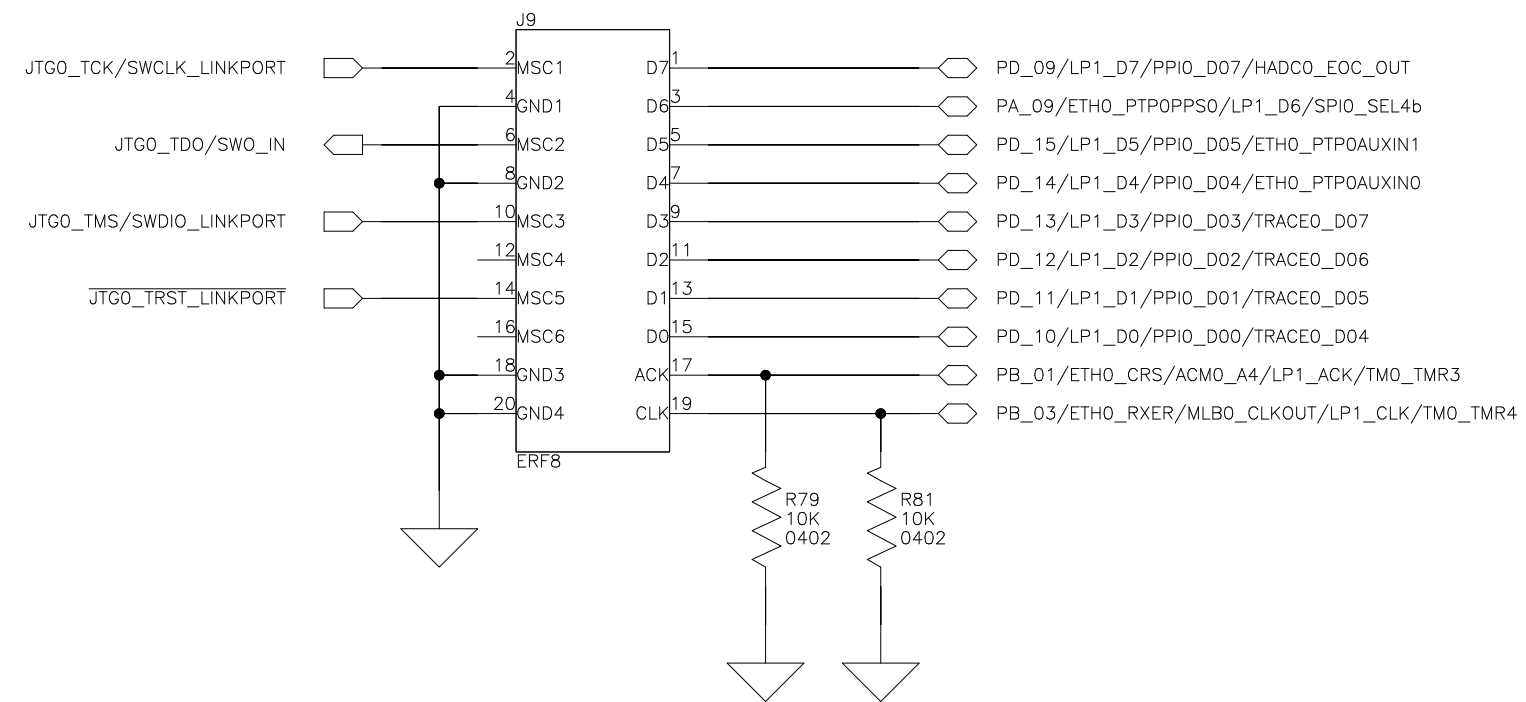


		2 Elizabeth Drive Chelmsford, MA 01824 PH: 1-800-ANALOGD	
		Title ADSP-SC573 EZ-KIT CAN	
Size C	Board No. A0760-2015	Rev 1.3	
Date 03/12/18	Sheet 11 of 27		

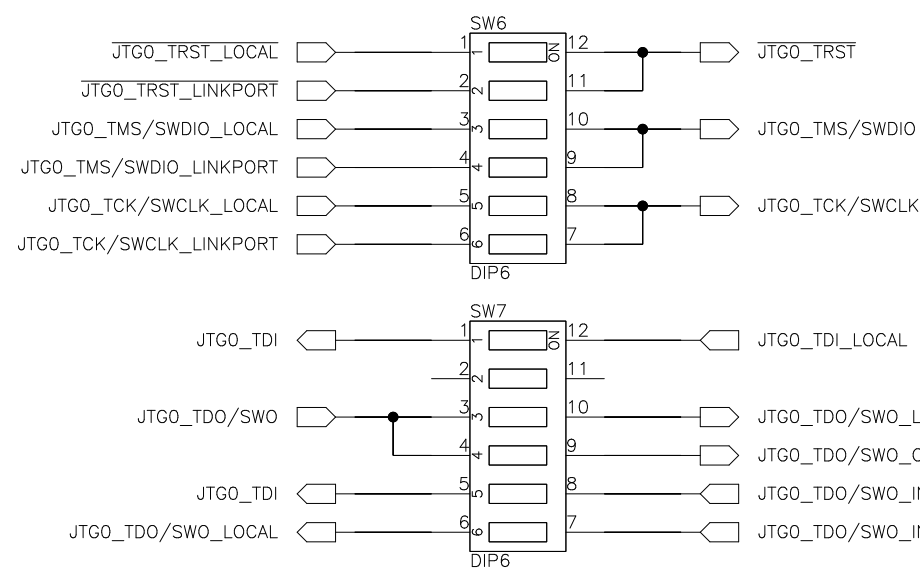
LINK PORT 0 / JTAG OUT



LINK PORT 1 / JTAG IN



JTAG SWITCHES

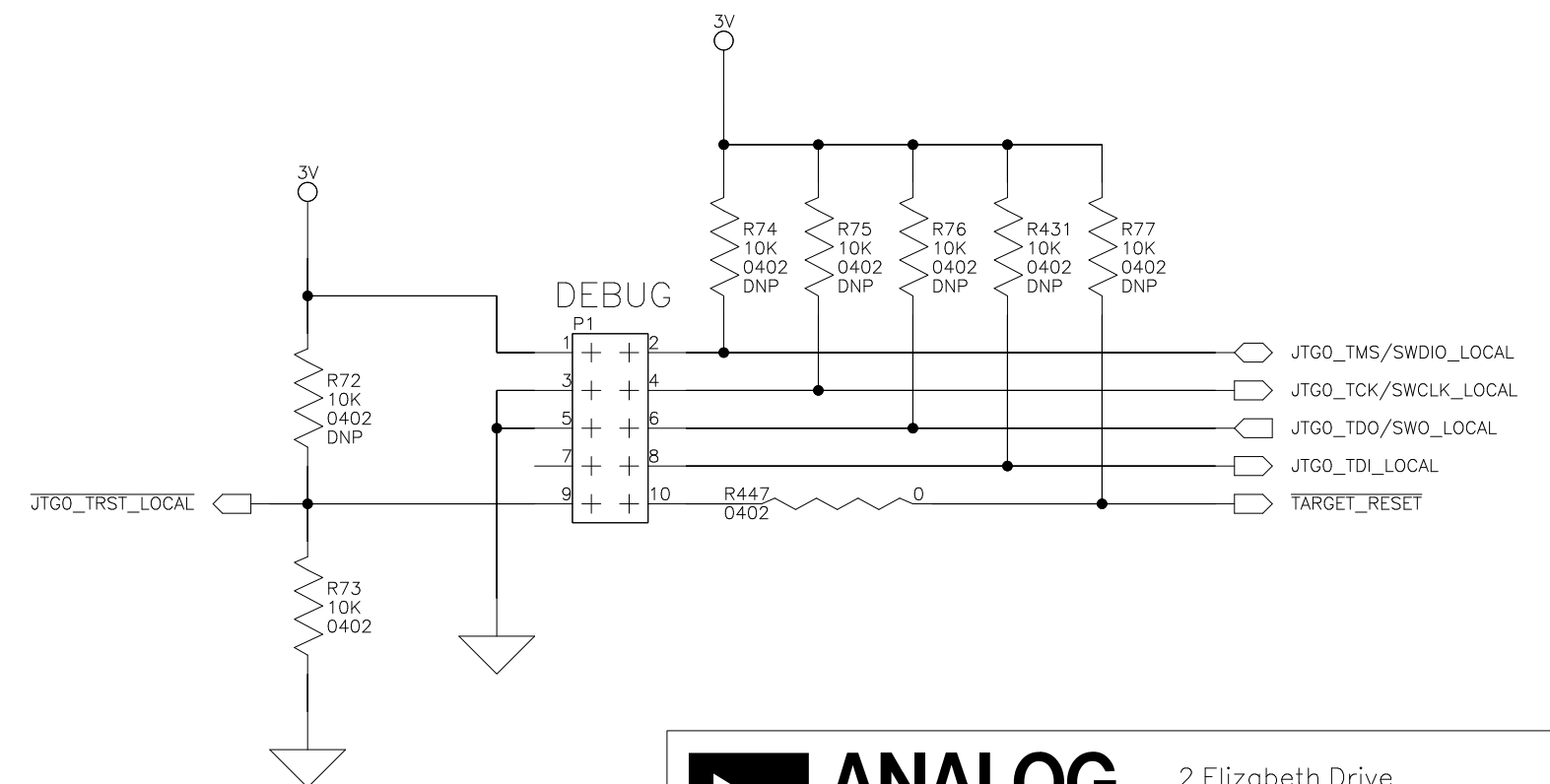
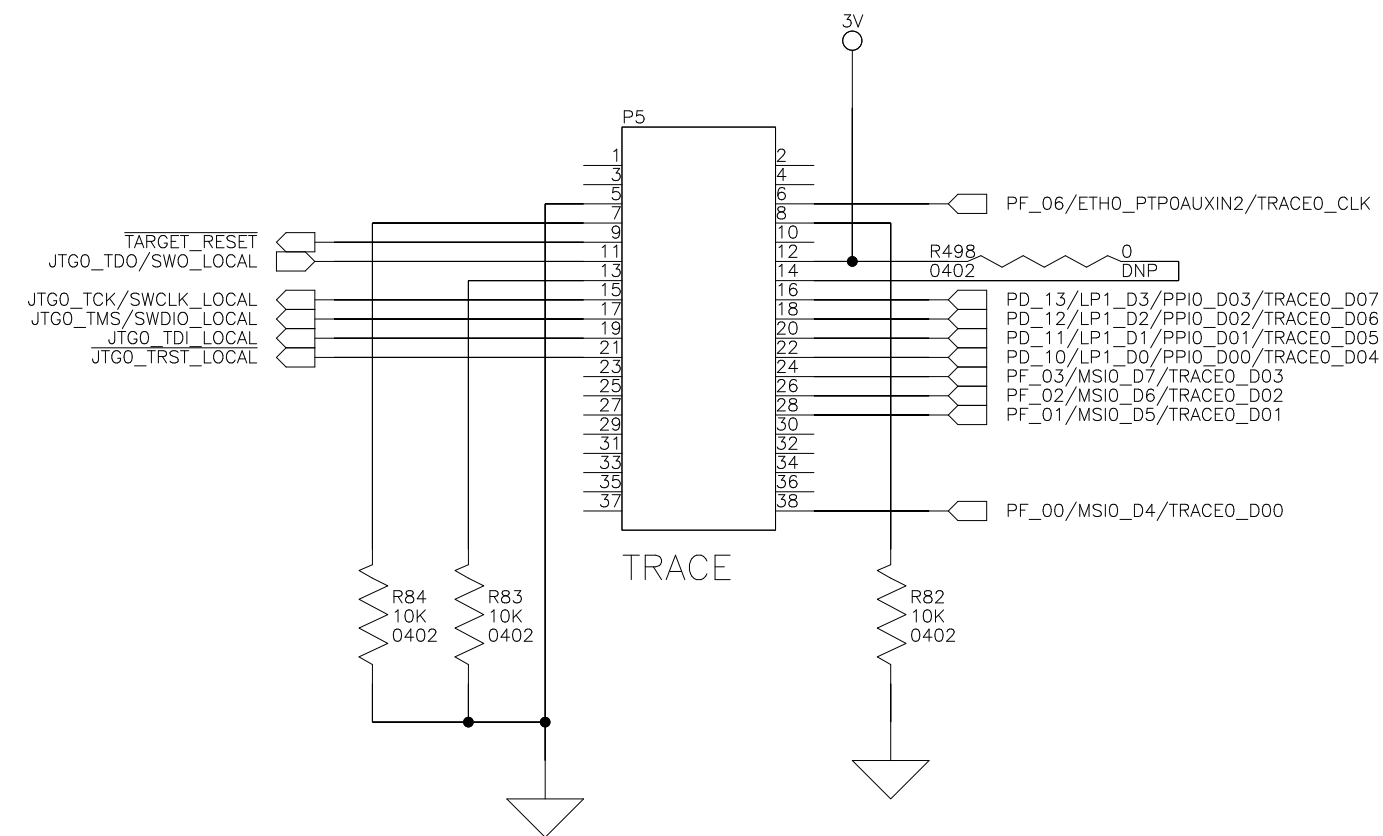


SINGLE PROCESSOR JTAG SETTINGS
EMULATOR OR DEBUG AGENT (DEFAULT)

SWITCH	BOARD ATTACHED TO EMULATOR
SW6.1	ON
SW6.2	OFF
SW6.3	ON
SW6.4	OFF
SW6.5	ON
SW6.6	OFF
SW7.1	ON
SW7.2	OFF
SW7.3	ON
SW7.4	OFF
SW7.5	OFF
SW7.6	OFF

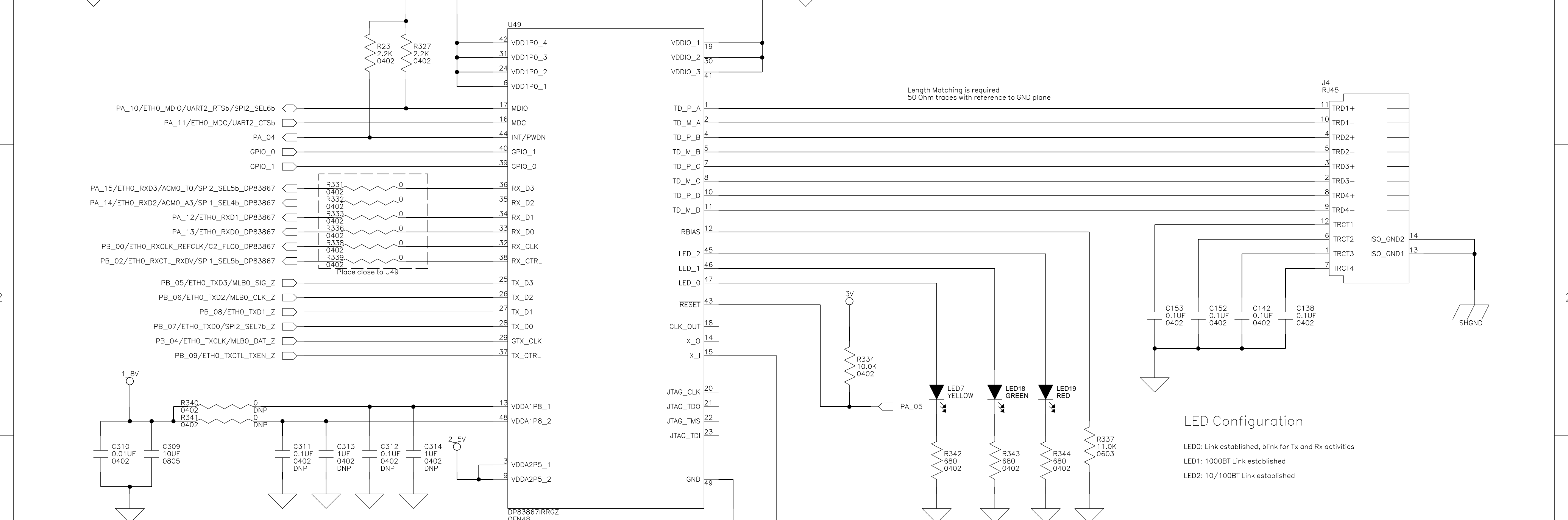
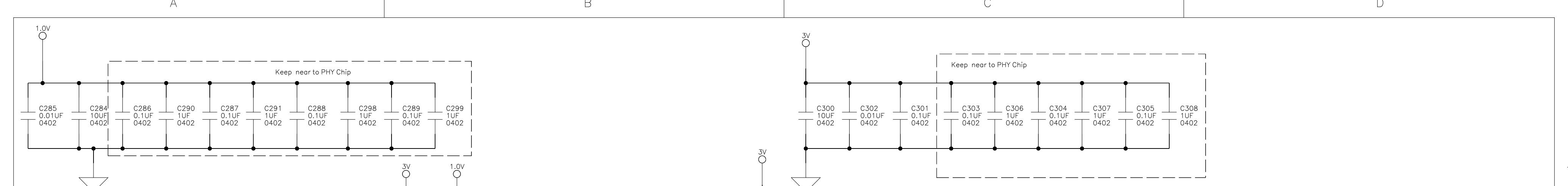
MULTI PROCESSOR JTAG SETTINGS
USING TWO OR MORE EZ-BOARDS (LINK PORT CABLES
REQUIRED FOR MORE THAN TWO BOARDS)

SWITCH	BOARD ATTACHED TO EMULATOR	BOARD(S) NOT ATTACHED TO EMULATOR
SW6.1	ON	OFF
SW6.2	ON	ON
SW6.3	ON	OFF
SW6.4	ON	ON
SW6.5	ON	OFF
SW6.6	ON	ON
SW7.1	ON	OFF
SW7.2	OFF	OFF
SW7.3	OFF	OFF
SW7.4	ON	ON
SW7.5	OFF	ON
SW7.6	ON	OFF



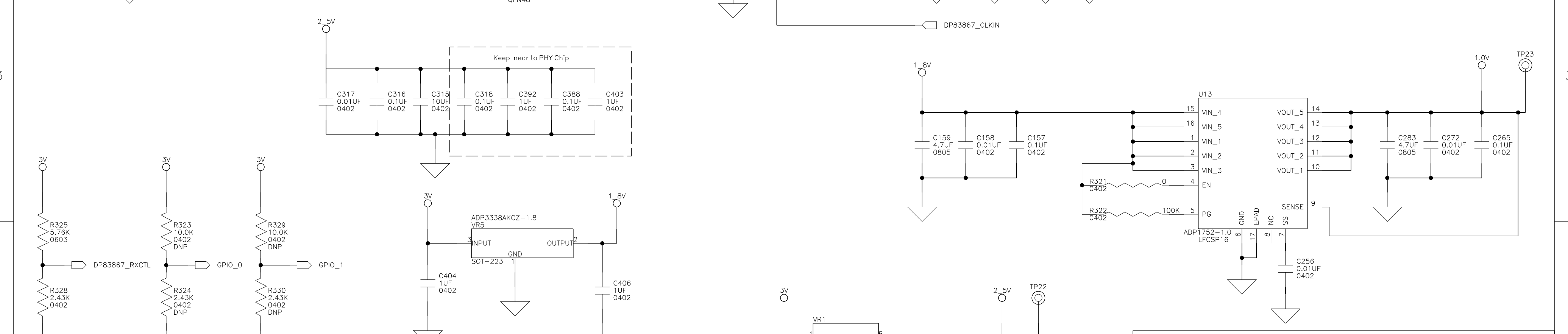
2 Elizabeth Drive
Chelmsford, MA 01824
PH: 1-800-ANALOGD

Title			ADSP-SC573 EZ-KIT Link Port/Debug		
Size	Board No.				Rev
C	A0760-2015				1.3
Date	03/12/18	Sheet	12	of	27



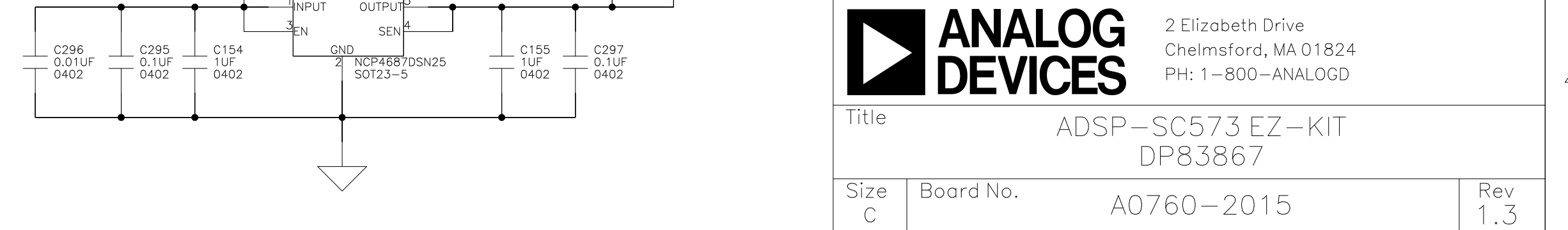
LED Configuration

LED0: Link established, blink for Tx and Rx activities
 LED1: 1000BT Link established
 LED2: 10/100BT Link established



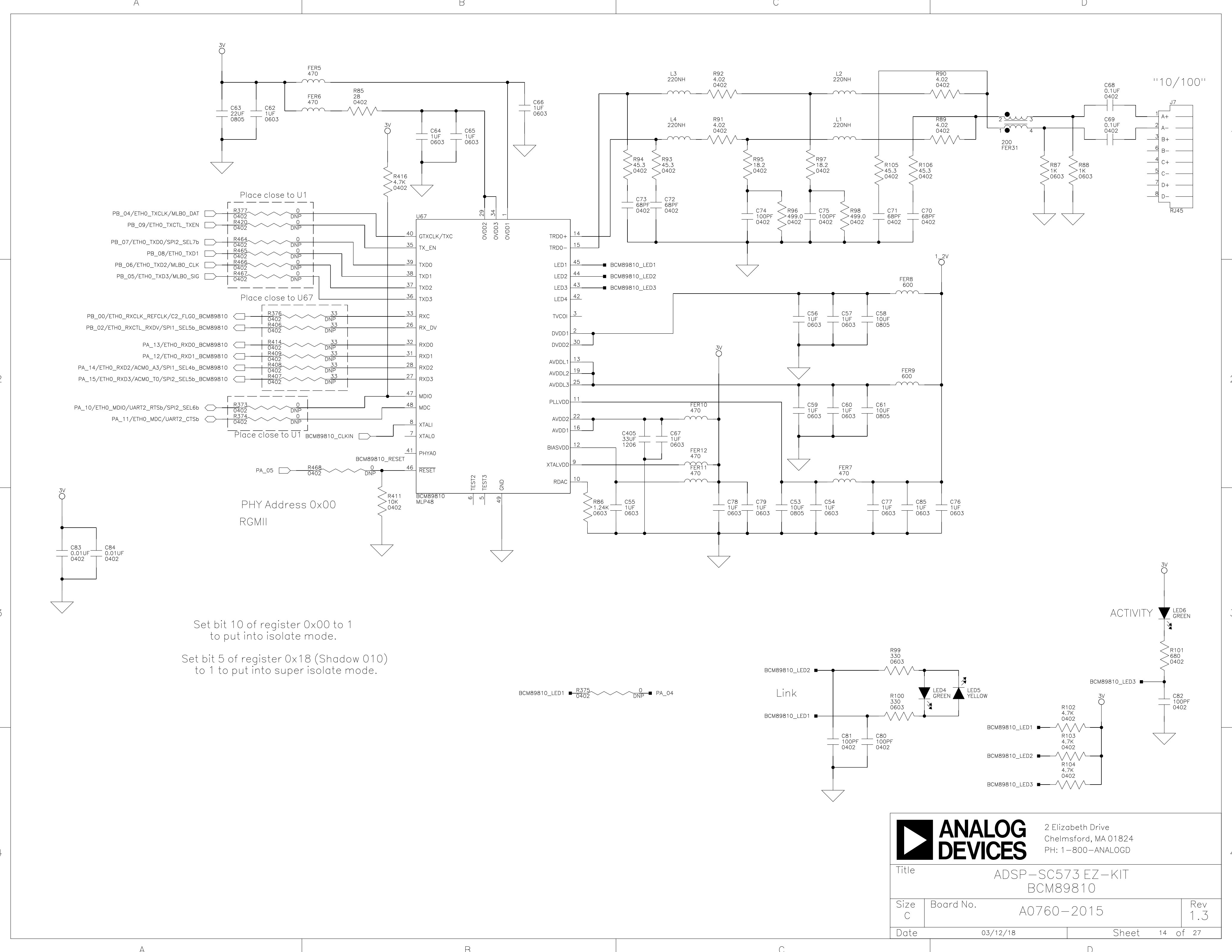
Strap Configuration

RX_D0: MODE 1	- PHY_ADD1 0 and PHY_ADD0 0
RX_D2: MODE 1	- PHY_ADD3 0 and PHY_ADD2 0
RX_DV/RX_CTRL	- MODE 3 - Autoneg Disable 0
GPIO_0: MODE 1	- RGMII Clock Skew RX[0] 0
GPIO_1: MODE 1	- RGMII Clock Skew RX[2] 0 and RX[1] 0 --> Clock Skew 2.0nS
LED0: MODE 1	- Mirror Enable 0
LED1: MODE 1	- ANEG_SEL 0 & RGMII Clock Skew TX[2] 0
LED2: MODE 1	- RGMII Clock Skew TX[1] 0 and TX[0] 0 --> Clock Skew 2.0nS



2 Elizabeth Drive
 Chelmsford, MA 01824
 PH: 1-800-ANALOGD

Title			ADSP-SC573 EZ-KIT DP83867		
Size	Board No.				Rev
C	A0760-2015				1.3
Date	03/12/18	Sheet	13	of	27

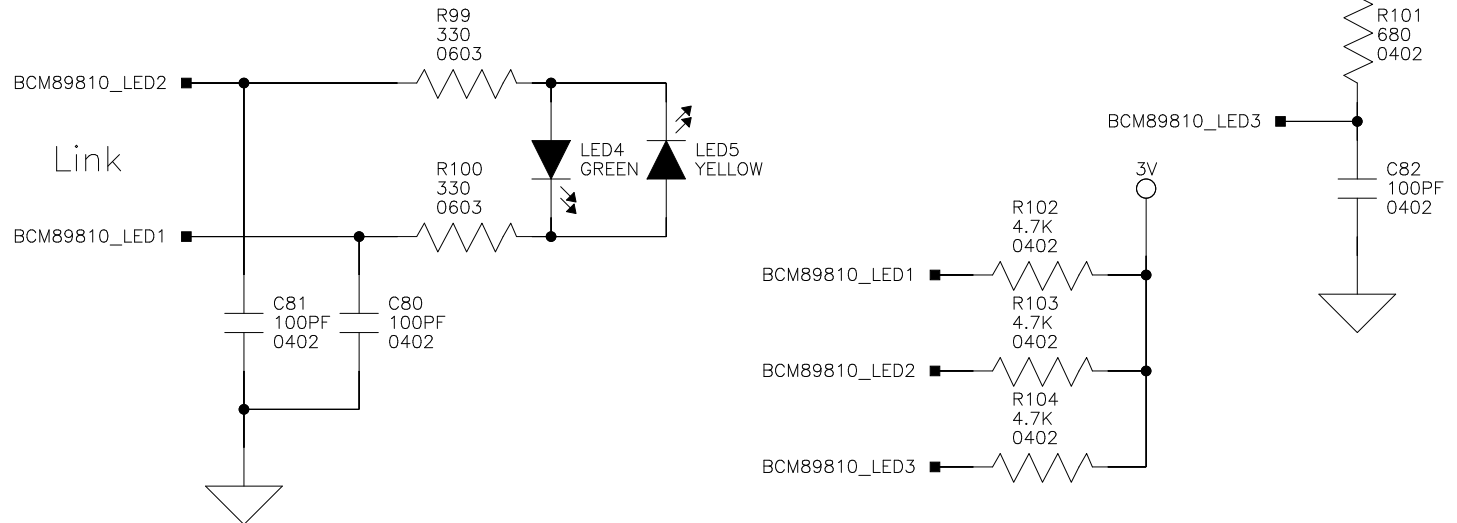


PHY Address 0x00
RGMII

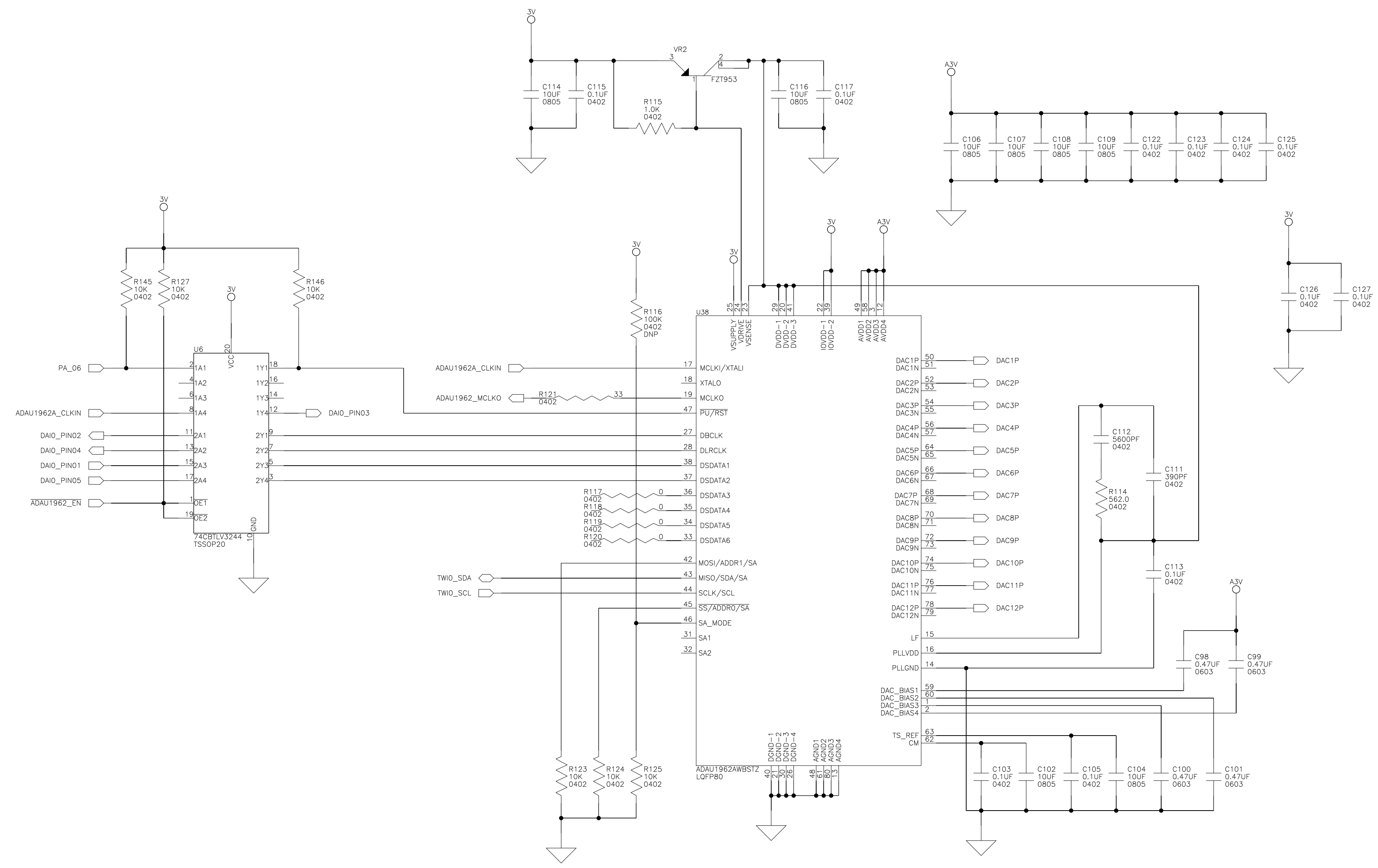
Set bit 10 of register 0x00 to 1
to put into isolate mode.

Set bit 5 of register 0x18 (Shadow 010)
to 1 to put into super isolate mode.

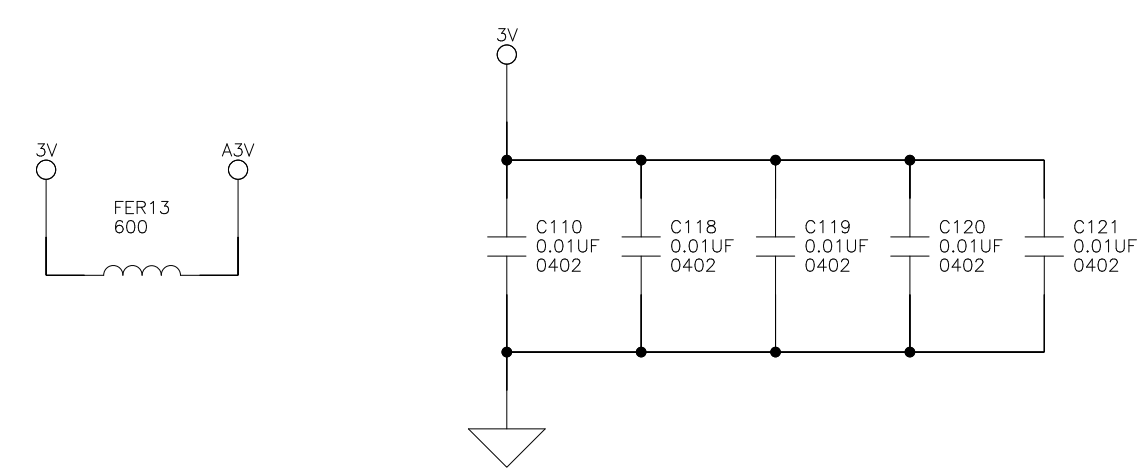
BCM89810_LED1 R375 4.7K 0402 PA_04




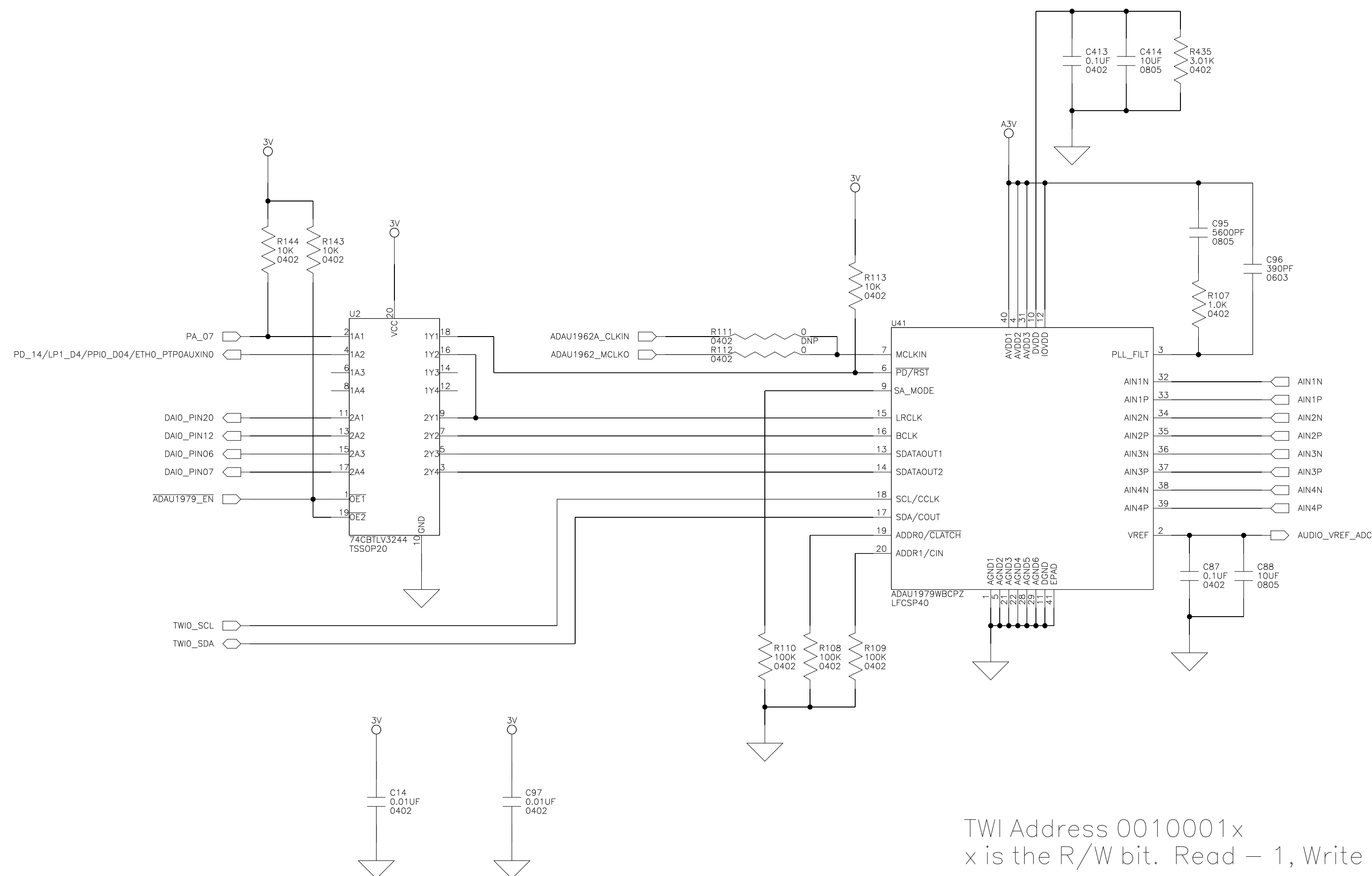
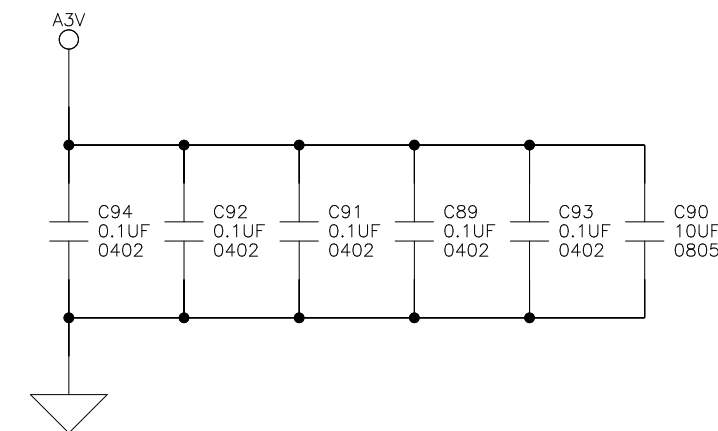
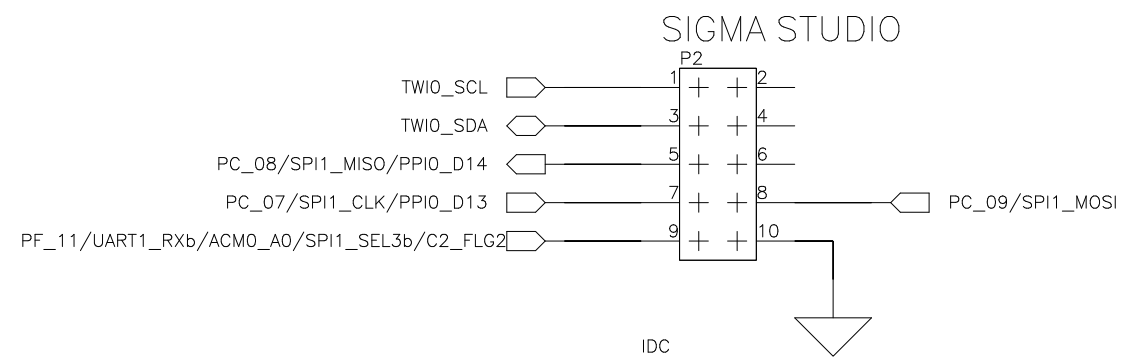
		2 Elizabeth Drive Chelmsford, MA 01824 PH: 1-800-ANALOGD	
		Title ADSP-SC573 EZ-KIT BCM89810	
Size C	Board No. A0760-2015	Rev 1.3	
Date 03/12/18	Sheet 14 of 27		



Check for extender conflict
 TWI Address 0000100x
 x is the R/W bit. Read - 1, Write - 0

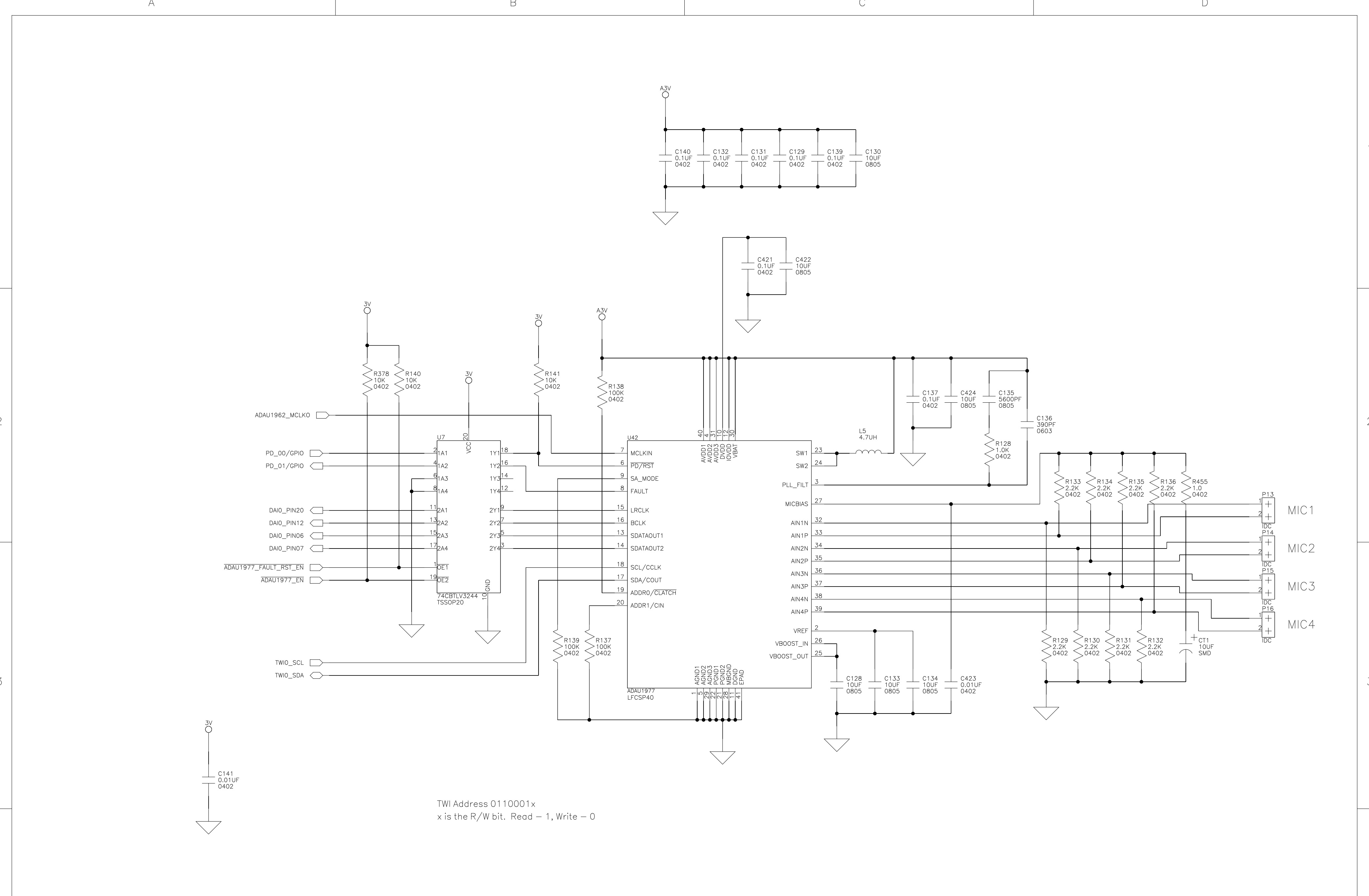


		2 Elizabeth Drive Chelmsford, MA 01824 PH: 1-800-ANALOGD	
		Title ADSP-SC573 EZ-KIT ADAU1962A	
Size C	Board No. A0760-2015	Rev 1.3	
Date 03/12/18	Sheet 15 of 27		




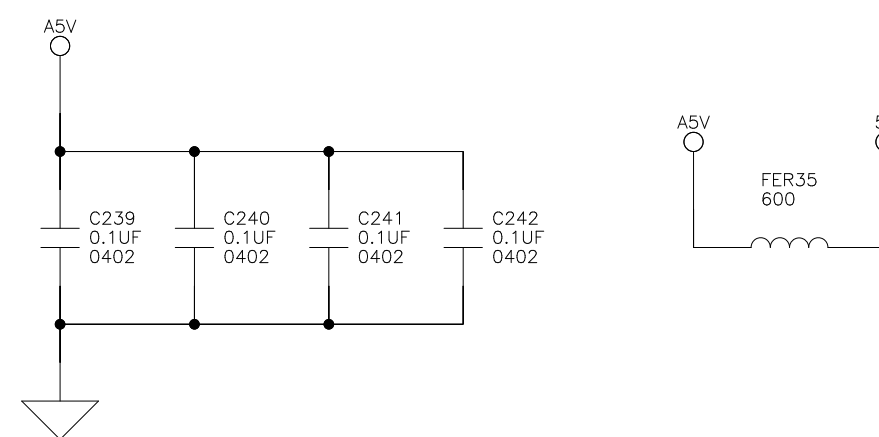
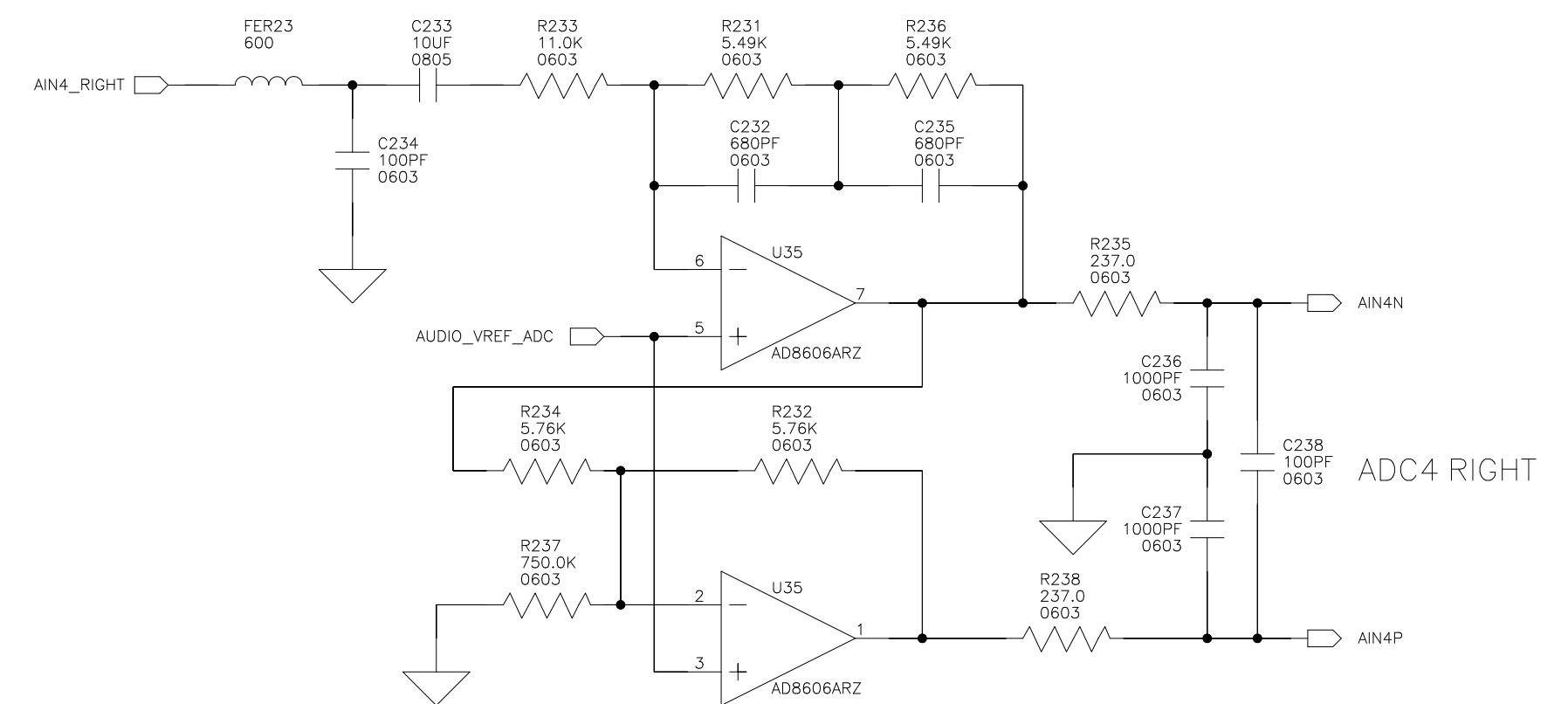
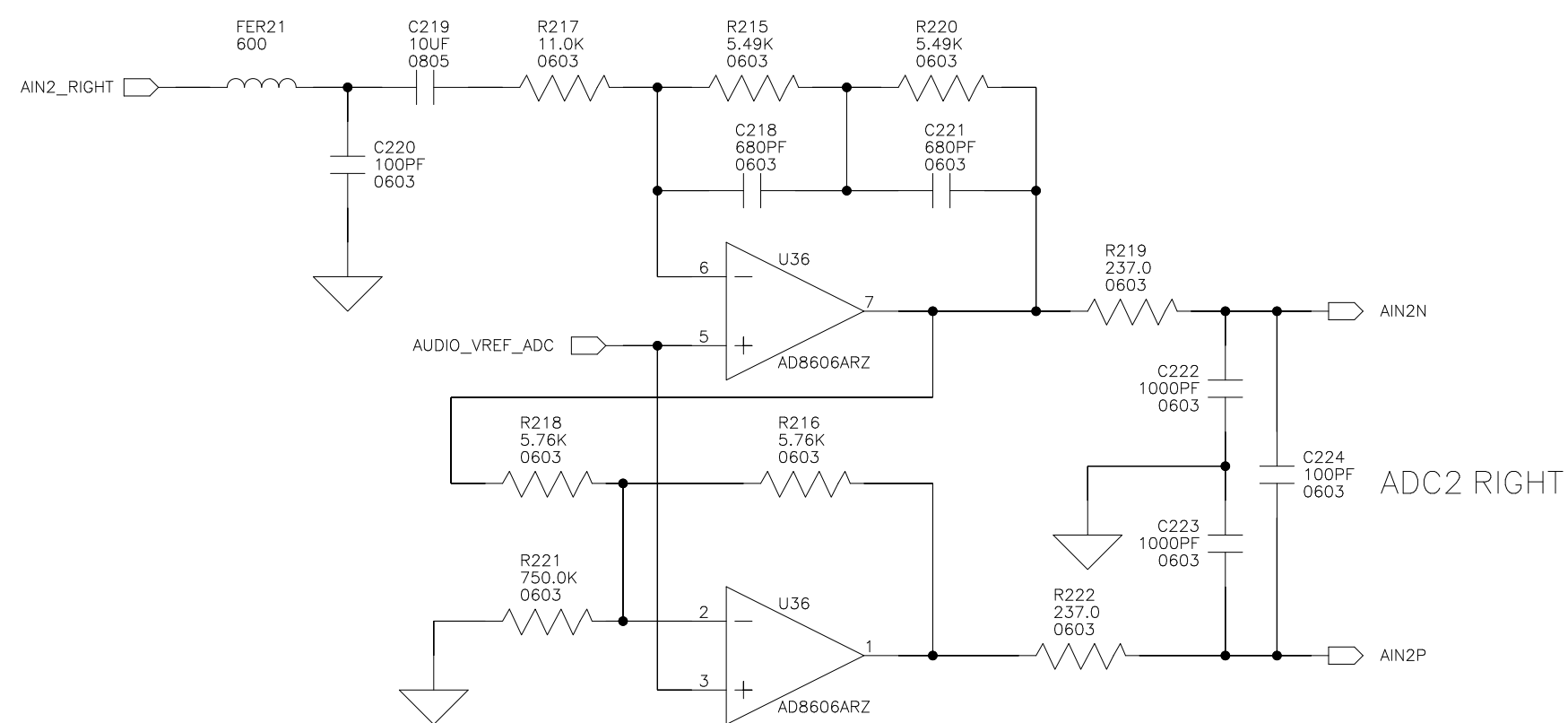
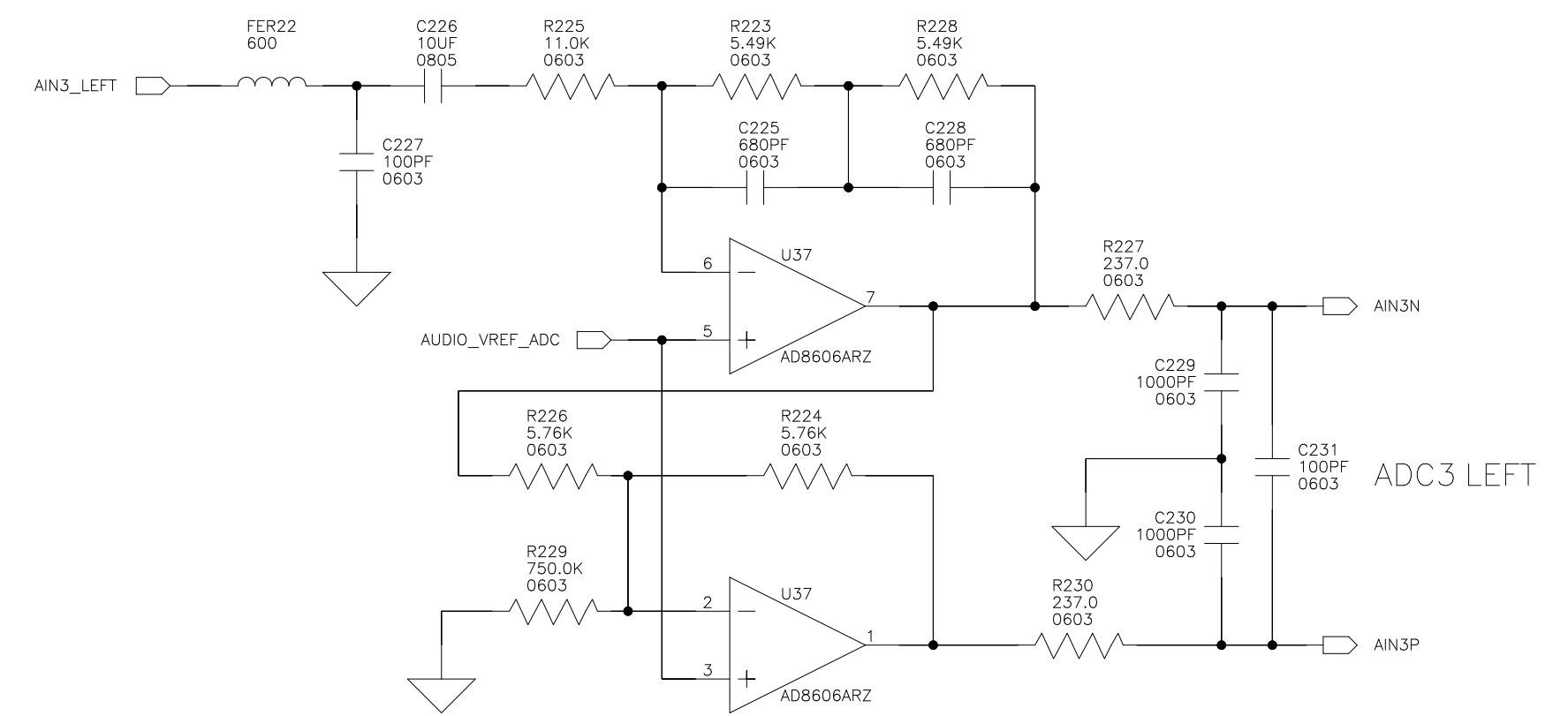
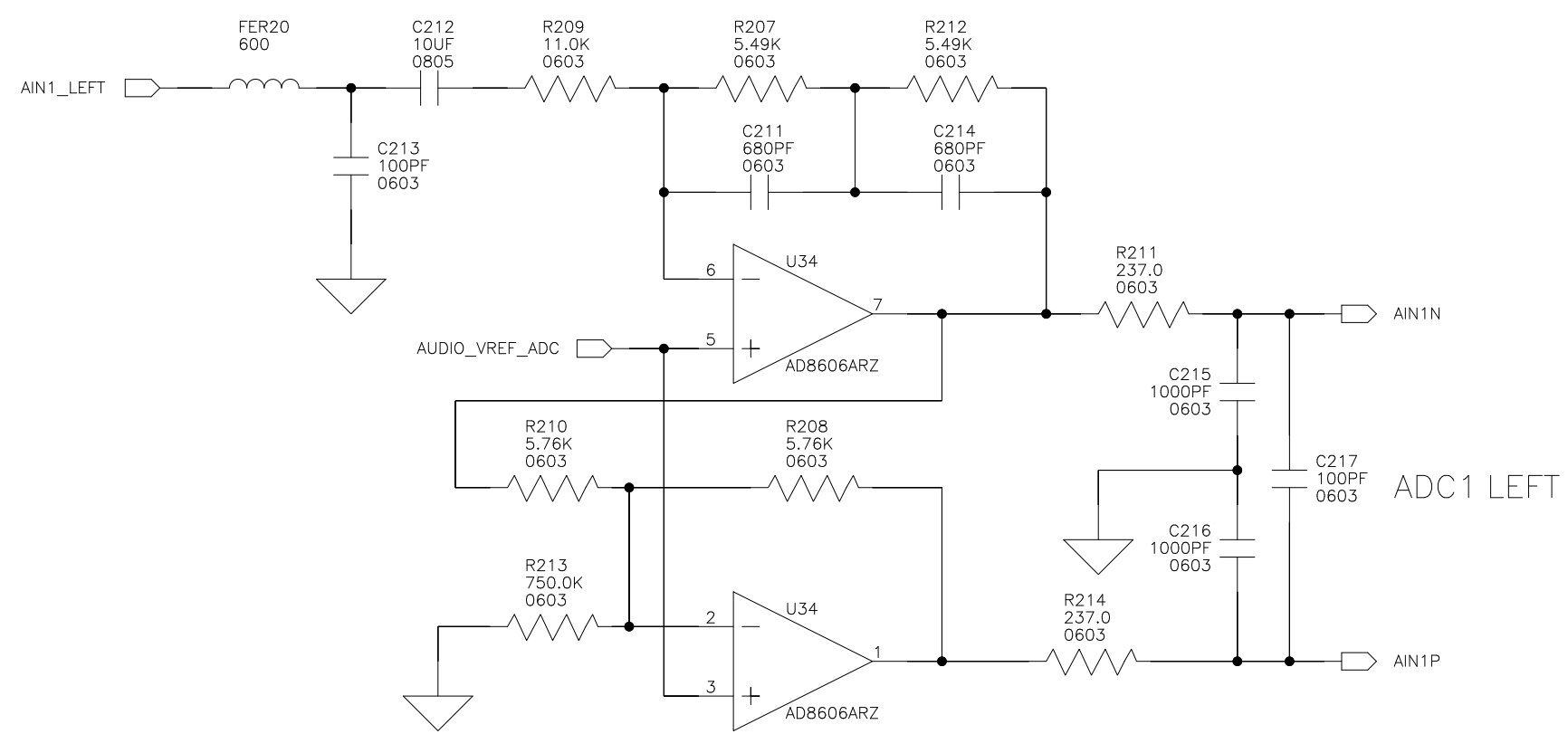
TWI Address 0010001x
x is the R/W bit. Read - 1, Write - 0


		2 Elizabeth Drive Chelmsford, MA 01824 PH: 1-800-ANALOGD	
Title ADSP-SC573 EZ-KIT ADAU1979			
Size C	Board No. A0760-2015	Rev 1.3	
Date 03/12/18	Sheet 16 of 27		

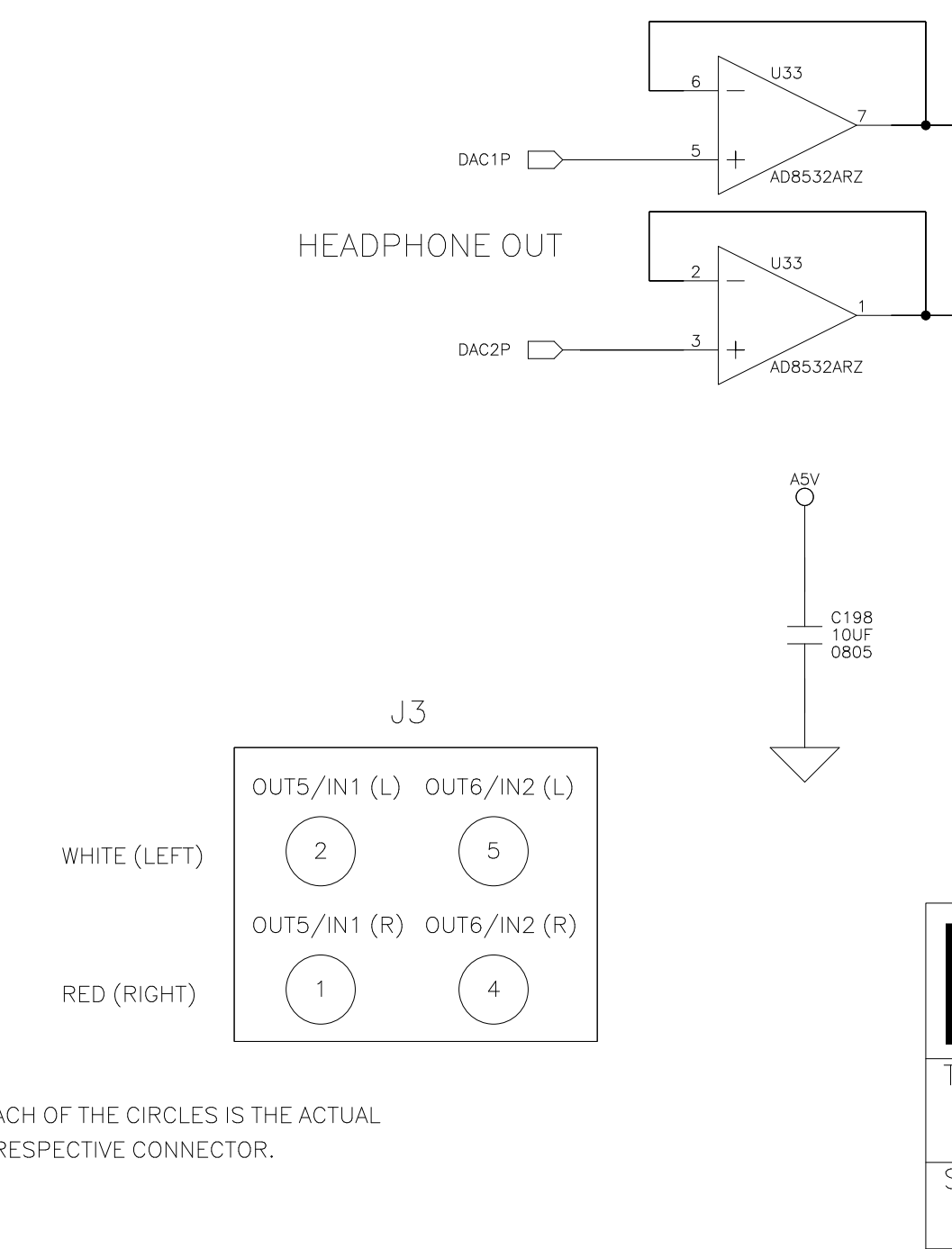
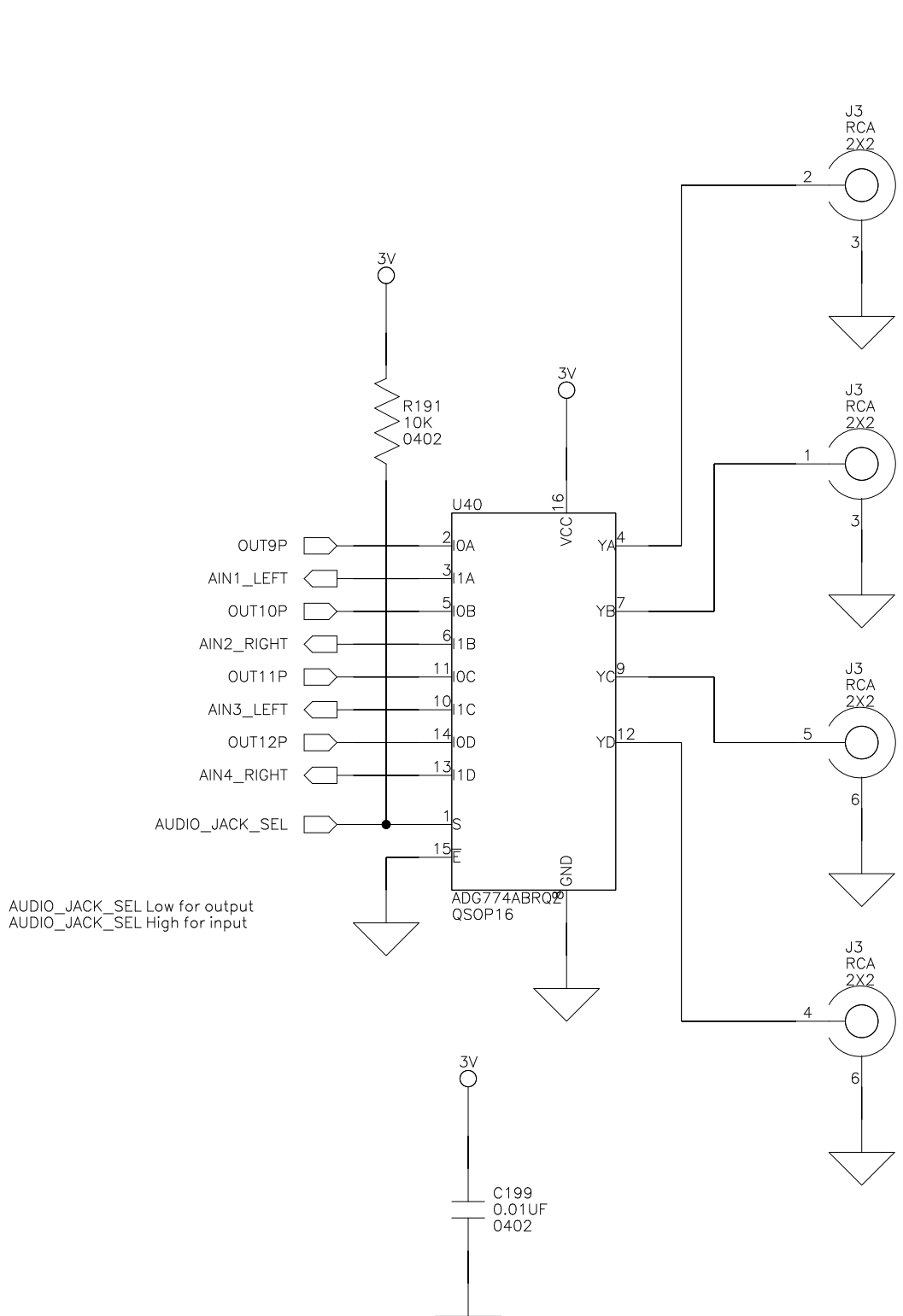
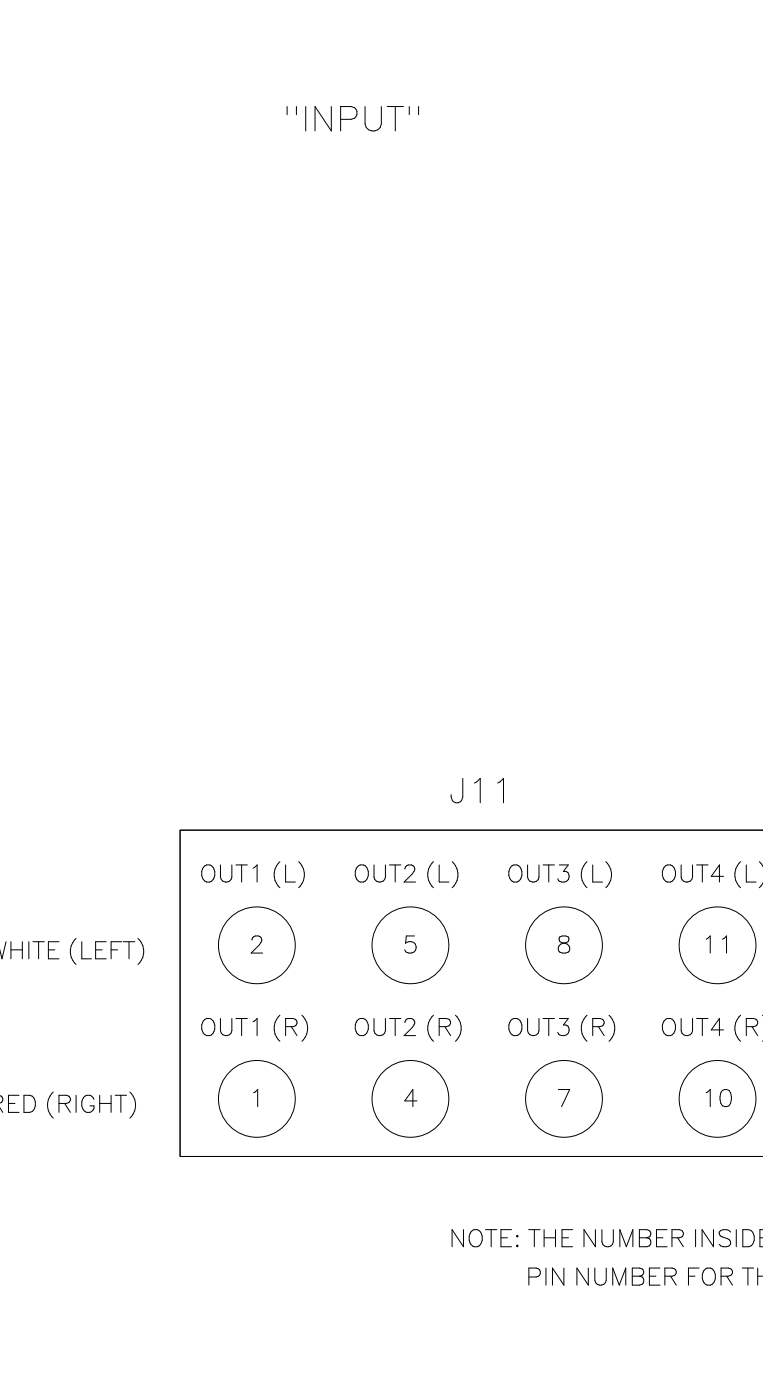
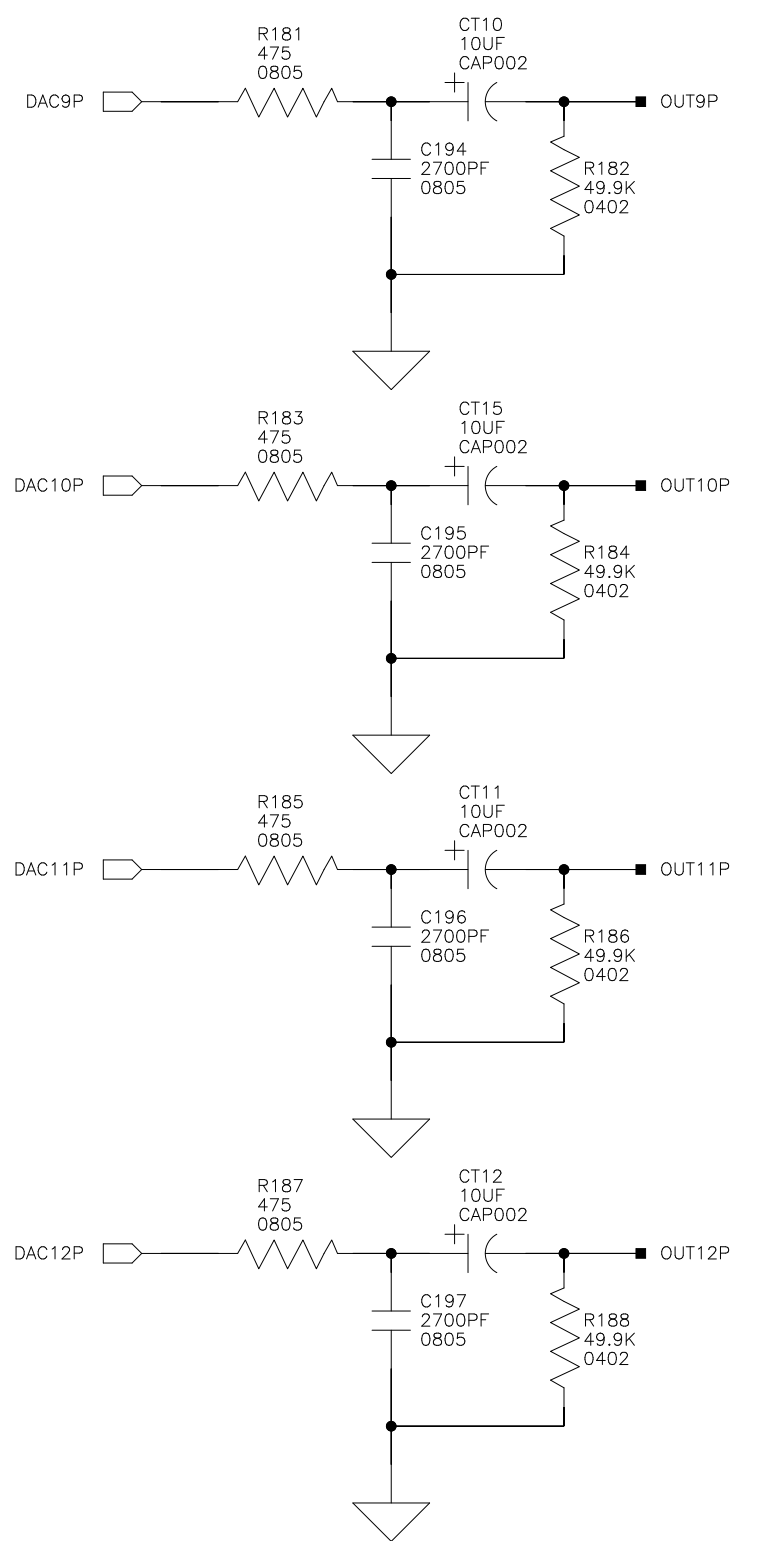
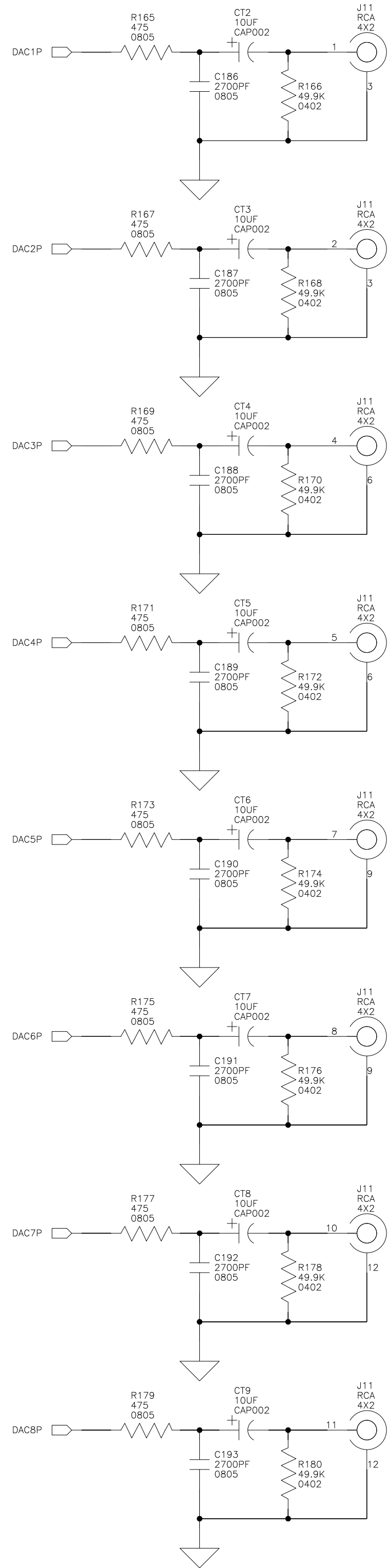


TWI Address 0110001x
 x is the R/W bit. Read - 1, Write - 0

		2 Elizabeth Drive Chelmsford, MA 01824 PH: 1-800-ANALOGD	
Title ADSP-SC573 EZ-KIT ADAU1977			
Size C	Board No. A0760-2015	Rev 1.3	
Date 03/12/18	Sheet 17 of 27		



		2 Elizabeth Drive Chelmsford, MA 01824 PH: 1-800-ANALOGD	
		Title ADSP-SC573 EZ-KIT Audio Input	
Size C	Board No. A0760-2015	Rev 1.3	
Date 03/12/18	Sheet 18 of 27		



"INPUT/OUTPUT"

"INPUT"

HEADPHONE OUT

NAME HEADPHONES"

J11

OUT1 (L)	OUT2 (L)	OUT3 (L)	OUT4 (L)
2	5	8	11
OUT1 (R)	OUT2 (R)	OUT3 (R)	OUT4 (R)
1	4	7	10

WHITE (LEFT)
RED (RIGHT)

J3

OUT5/IN1 (L)	OUT6/IN2 (L)
2	5
OUT5/IN1 (R)	OUT6/IN2 (R)
1	4

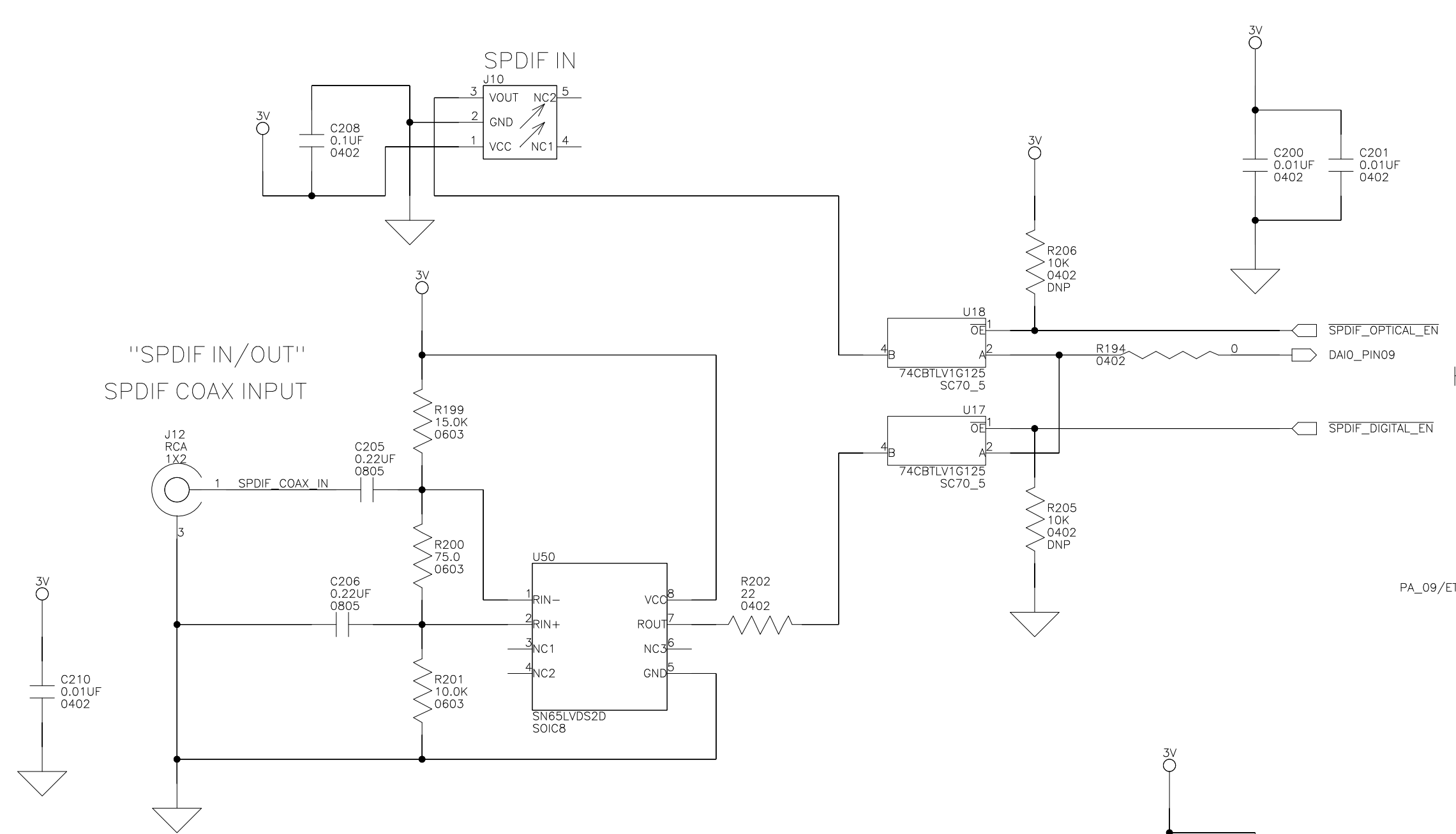
WHITE (LEFT)
RED (RIGHT)

NOTE: THE NUMBER INSIDE EACH OF THE CIRCLES IS THE ACTUAL PIN NUMBER FOR THE RESPECTIVE CONNECTOR.

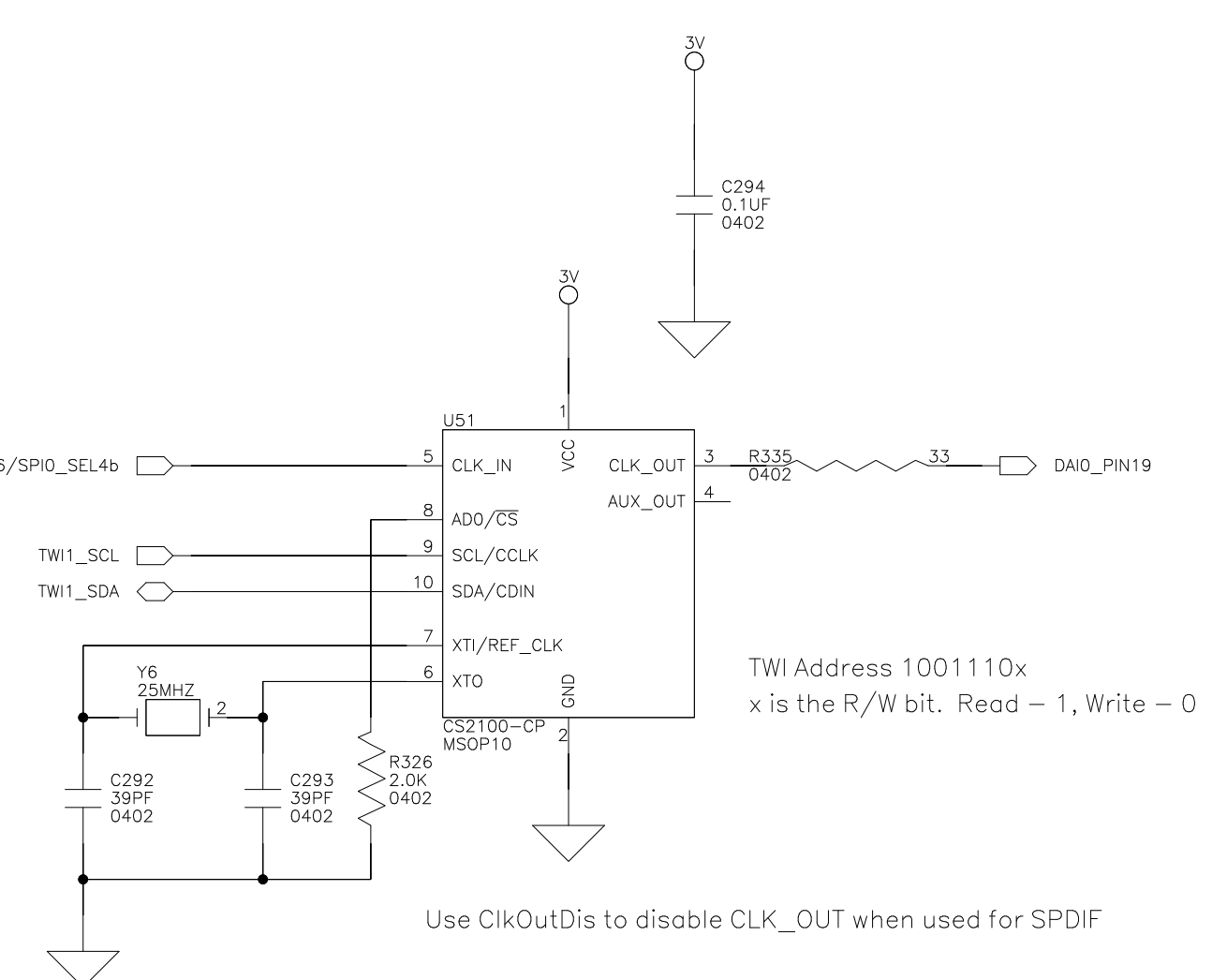
2 Elizabeth Drive
Chelmsford, MA 01824
PH: 1-800-ANALOGD

Title		ADSP-SC573 EZ-KIT Audio Connectors	
Size C	Board No.	A0760-2015	
Date	03/12/18	Sheet	19 of 27

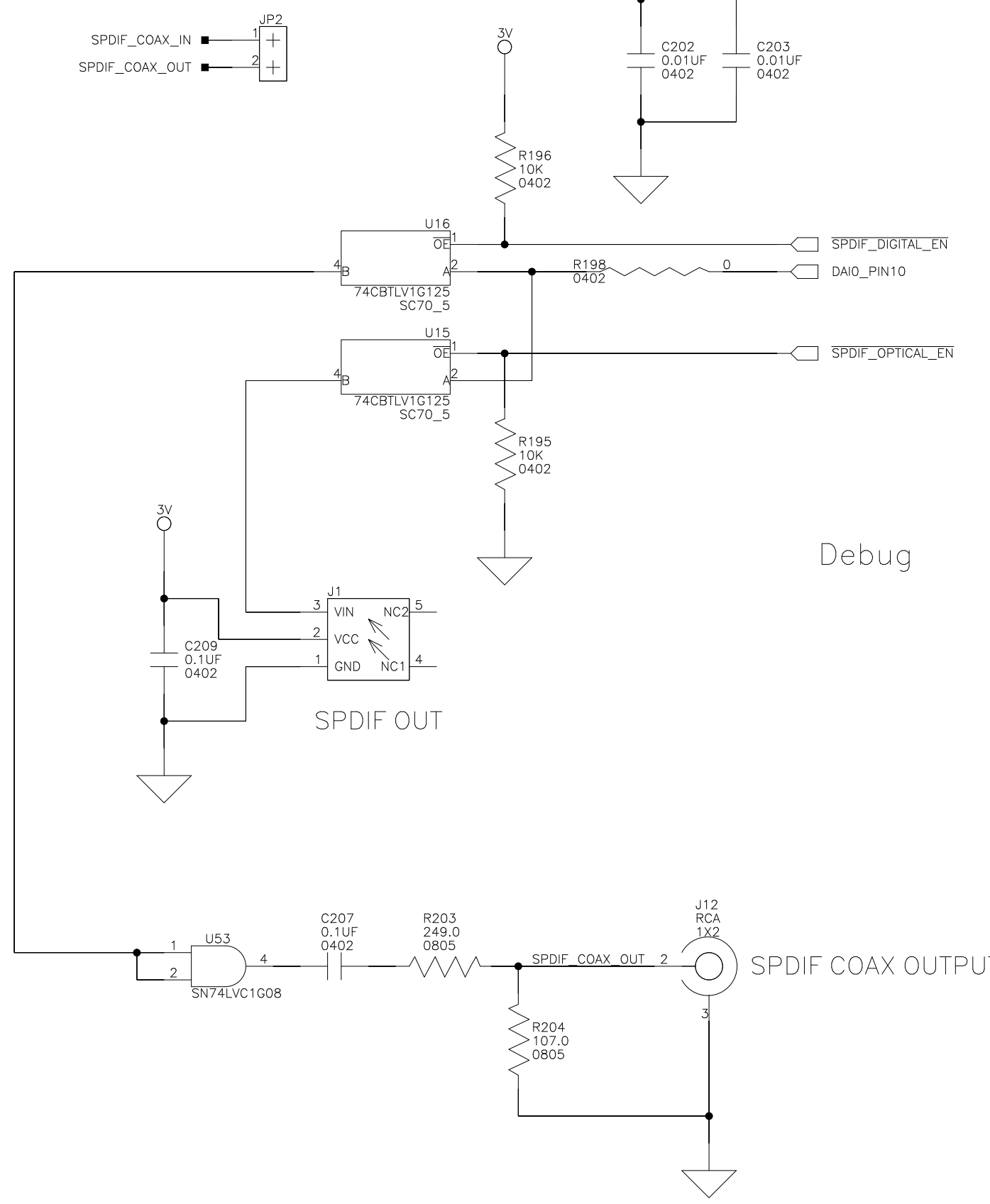
"SPDIF IN/OUT"
SPDIF COAX INPUT




HU Data

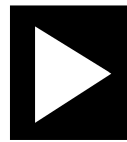


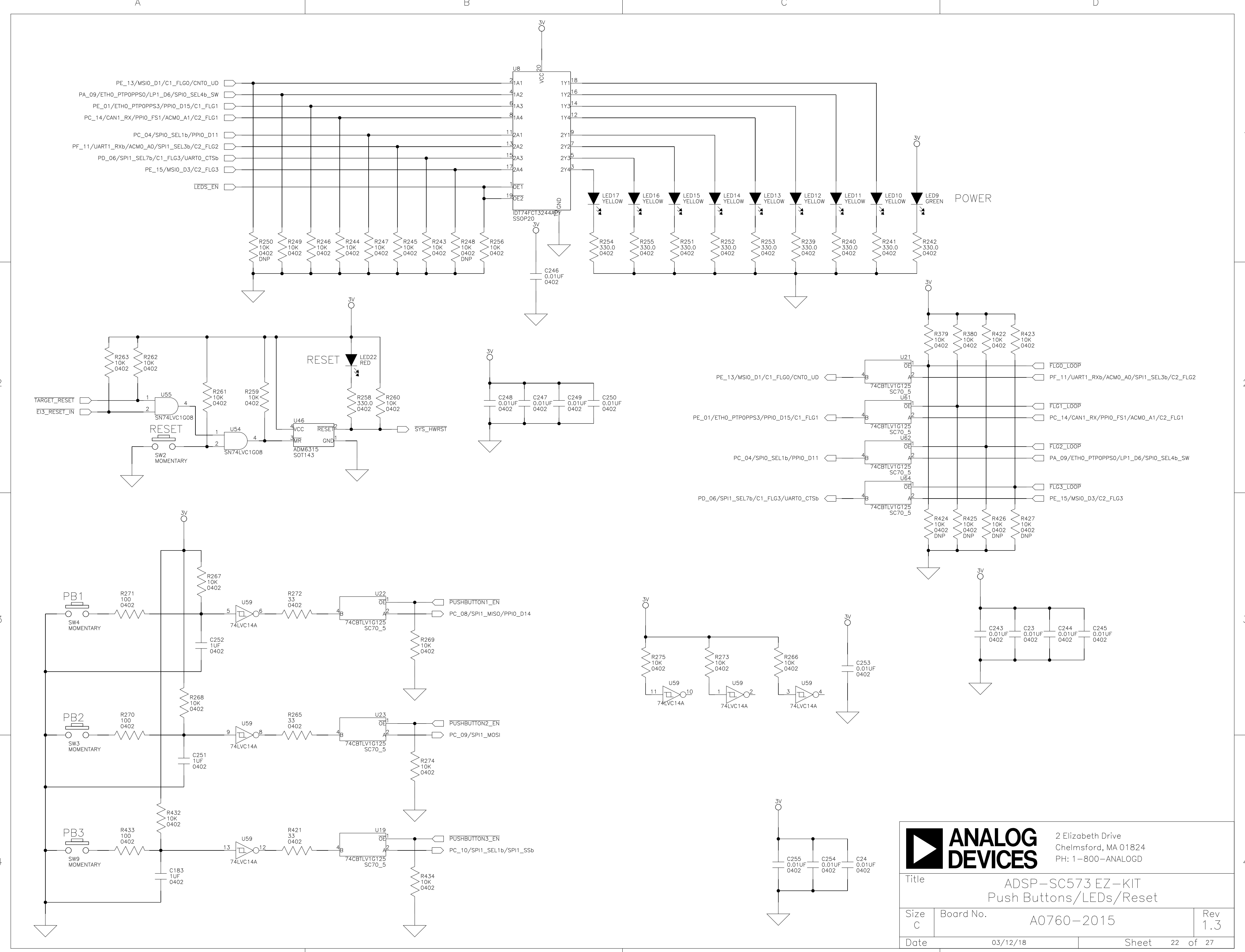
Debug



		2 Elizabeth Drive Chelmsford, MA 01824 PH: 1-800-ANALOGD	
		Title ADSP-SC573 EZ-KIT SPDIF	
Size C	Board No. A0760-2015	Rev 1.3	
Date 03/12/18	Sheet 20 of 27		

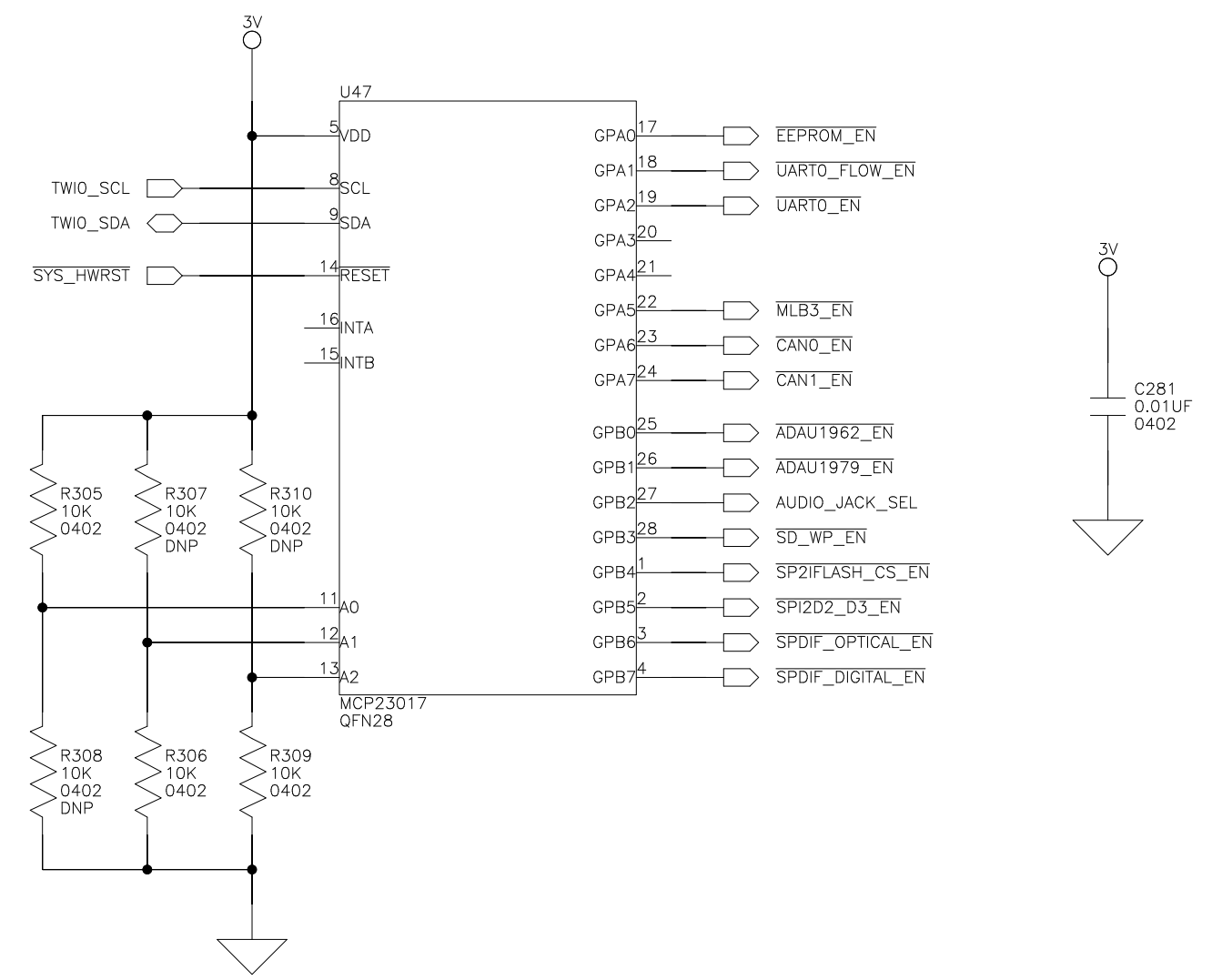
Please contact your nearest ADI sales office to complete the Non-Disclosure Agreement (NDA) required to receive additional A2B technical information.

 ANALOG DEVICES		2 Elizabeth Drive Chelmsford, MA 01824 PH: 1-800-ANALOGD	
Title		ADSP-SC573 EZ-KIT A2B Master/Slave Node A	
Size C	Board No.	A0760-2015	Rev 1.3
Date	03/12/18	Sheet	21 of 27

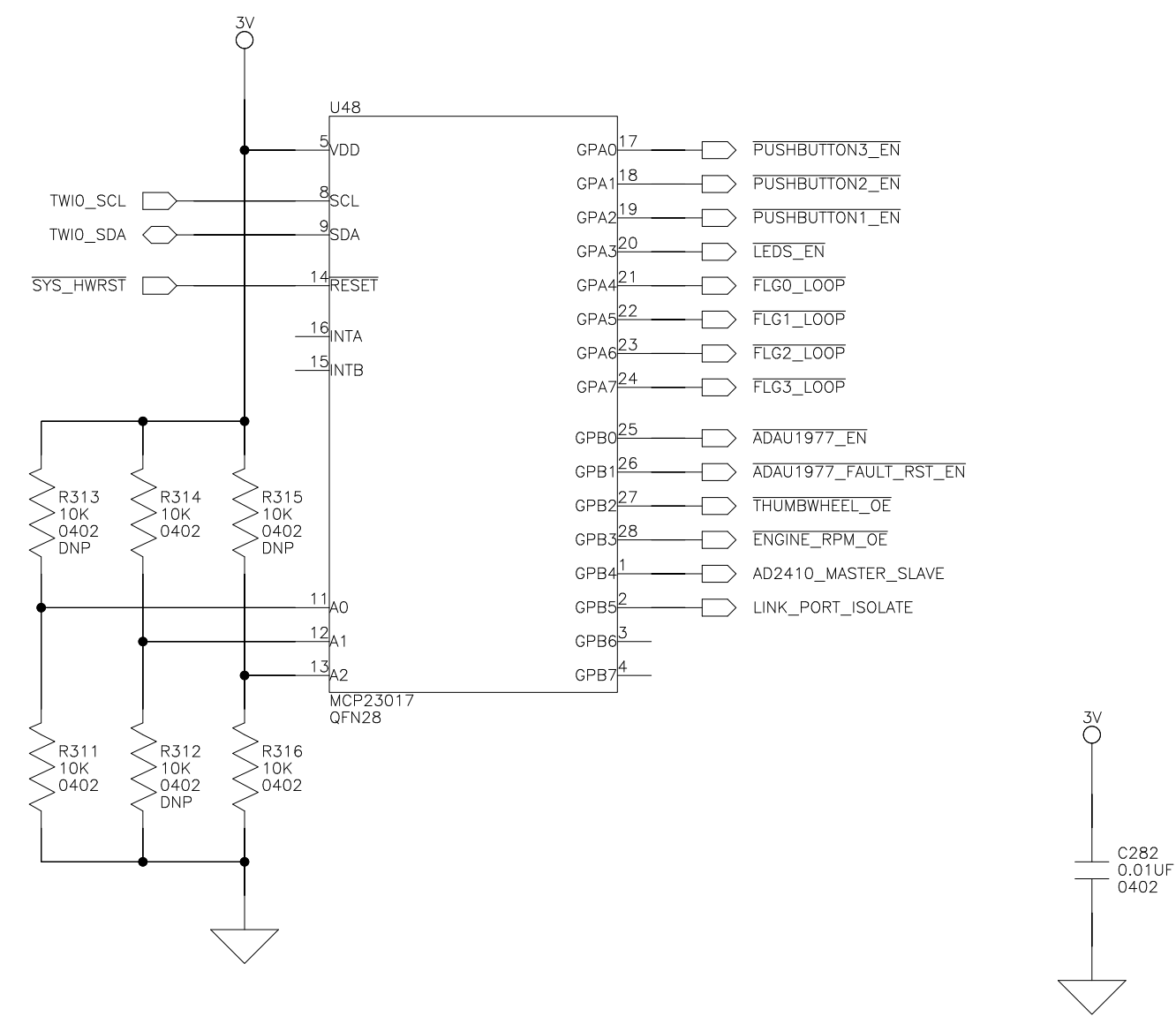



		2 Elizabeth Drive Chelmsford, MA 01824 PH: 1-800-ANALOGD	
		Title ADSP-SC573 EZ-KIT Push Buttons/LEDs/Reset	
Size C	Board No. A0760-2015	Rev 1.3	
Date 03/12/18	Sheet 22 of 27		

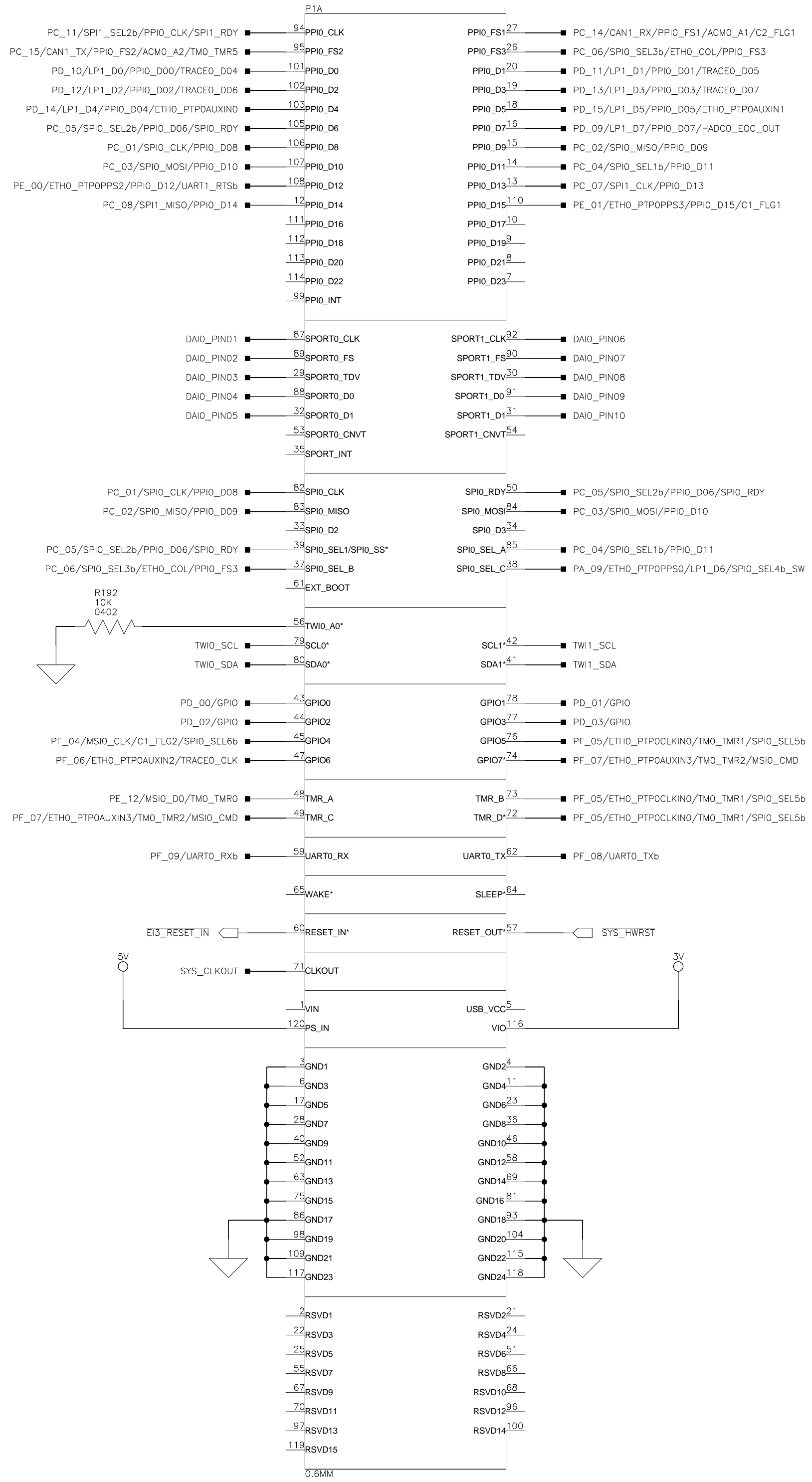
TWI Address 0100001x
 x is the R/W bit. Read - 1, Write - 0




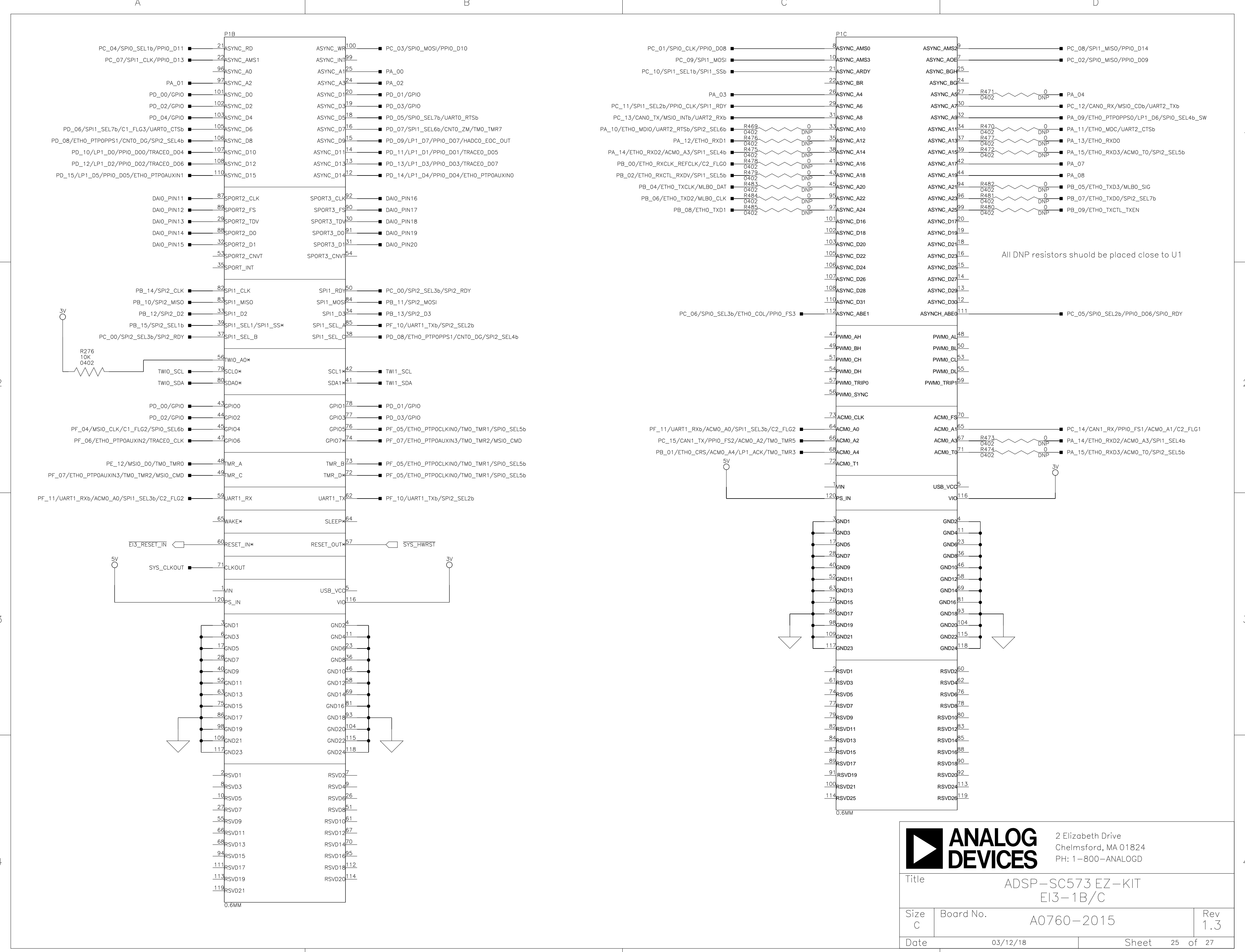
TWI Address 0100010x
 x is the R/W bit. Read - 1, Write - 0




		2 Elizabeth Drive Chelmsford, MA 01824 PH: 1-800-ANALOGD
Title ADSP-SC573 EZ-KIT SoftConfig		
Size C	Board No. A0760-2015	Rev 1.3
Date 03/12/18	Sheet 23 of 27	



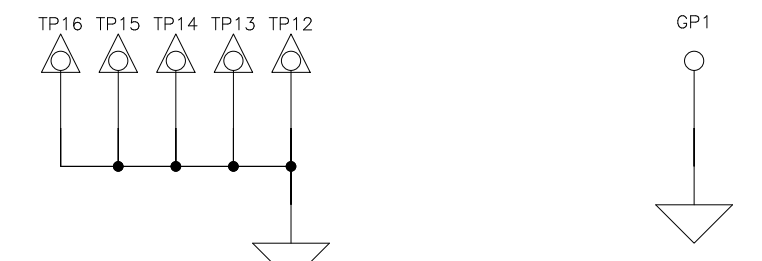
		2 Elizabeth Drive Chelmsford, MA 01824 PH: 1-800-ANALOGD	
Title		ADSP-SC573 EZ-KIT E13-1A	
Size C	Board No.	A0760-2015	Rev 1.3
Date	03/12/18	Sheet 24	of 27



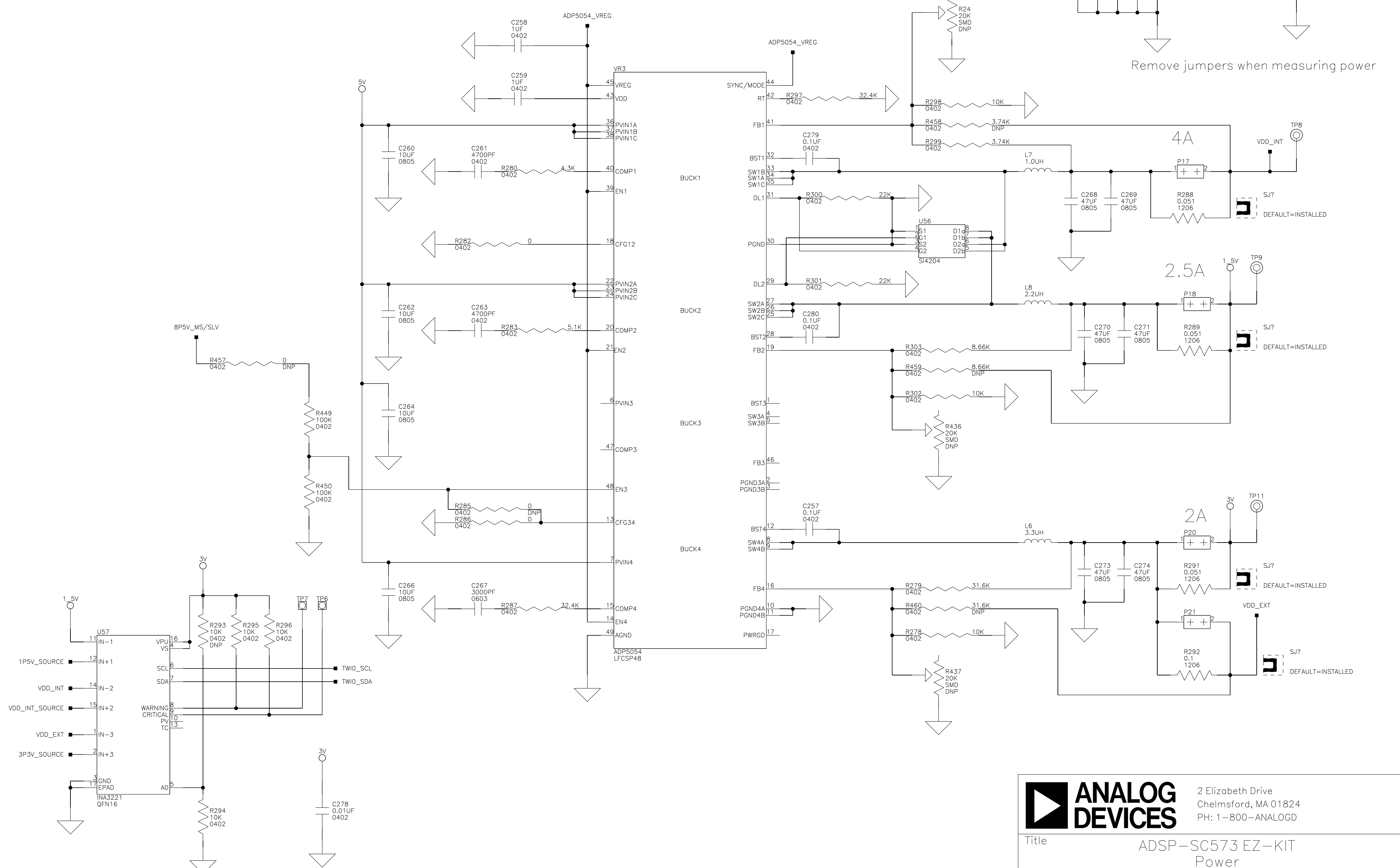
		2 Elizabeth Drive Chelmsford, MA 01824 PH: 1-800-ANALOGD	
		Title ADSP-SC573 EZ-KIT EI3-1B/C	
Size C	Board No. A0760-2015	Rev 1.3	
Date 03/12/18	Sheet 25	of 27	

GND Test Points are scattered on PCB for Test Measurement Purposes.

LABEL "GND" ON ALL TPs



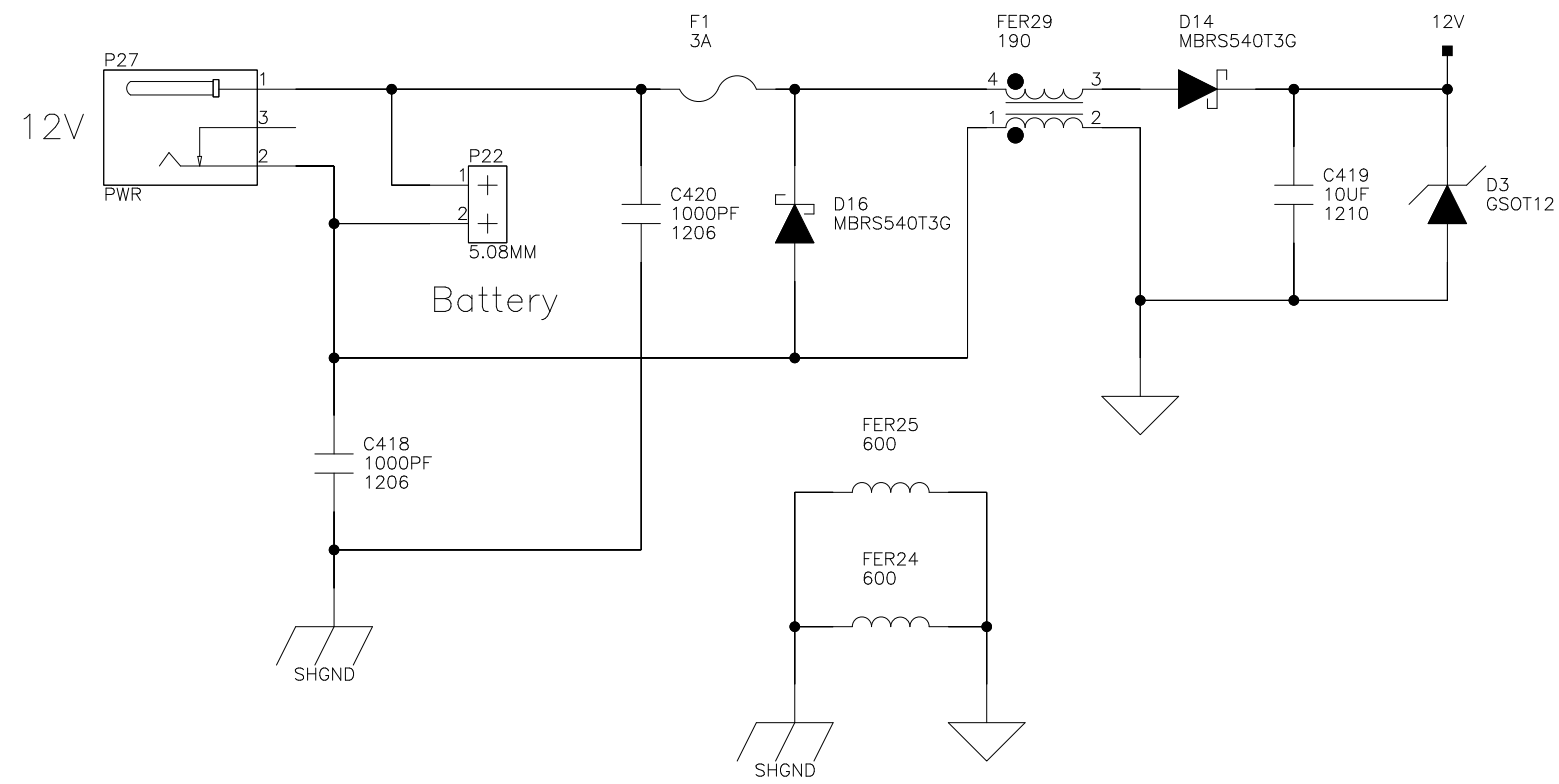
Remove jumpers when measuring power



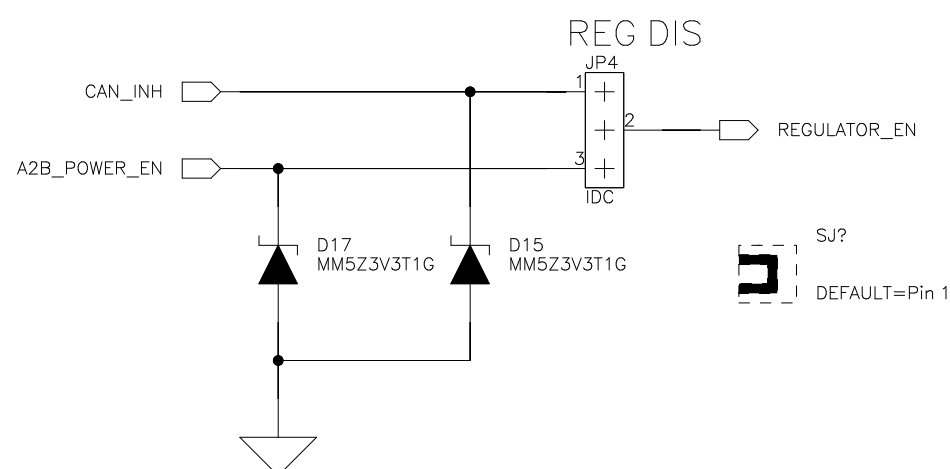
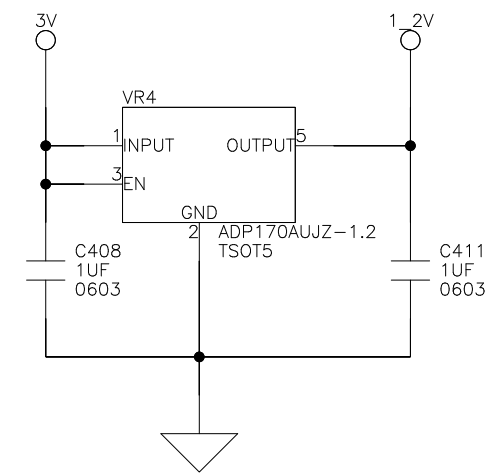
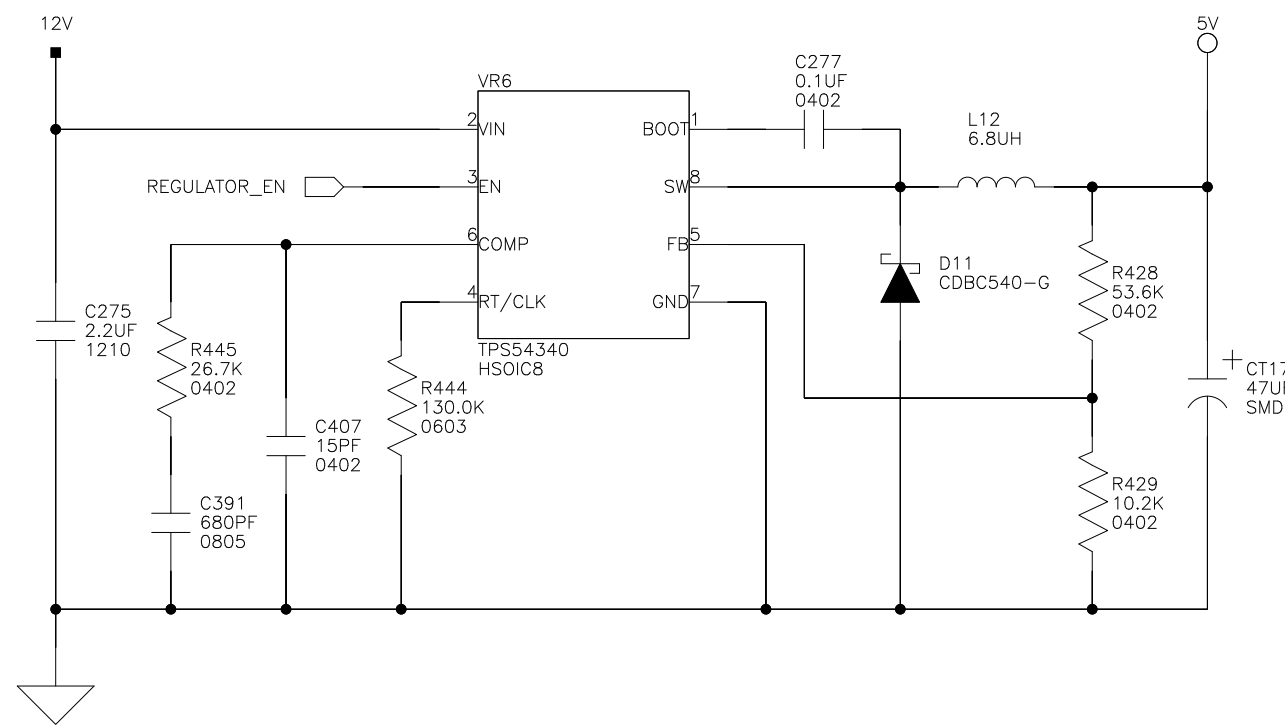
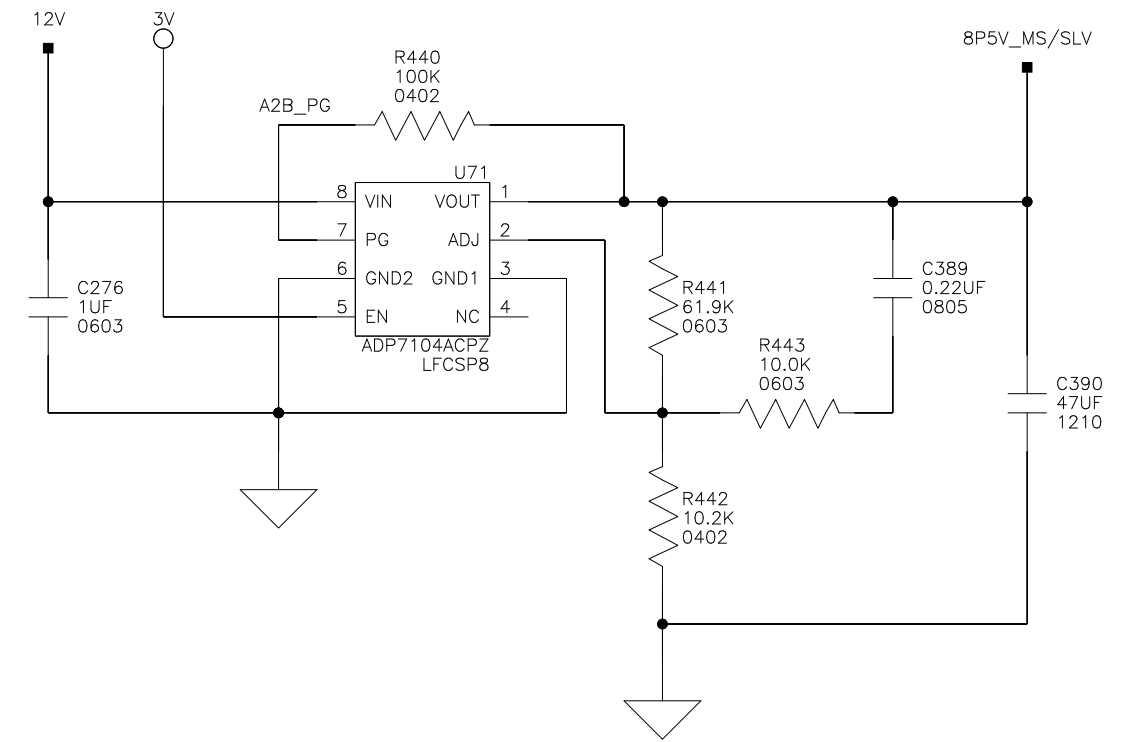
TWI address 1000 000x
where x is the R/W bit. Read - 1, Write - 0


ANALOG DEVICES 2 Elizabeth Drive
Chelmsford, MA 01824
PH: 1-800-ANALOGD

Title ADSP-SC573 EZ-KIT Power		
Size C	Board No. A0760-2015	Rev 1.3
Date 03/12/18	Sheet 26 of 27	



Power Sequence for A2B



		2 Elizabeth Drive Chelmsford, MA 01824 PH: 1-800-ANALOGD
Title ADSP-SC573 EZ-KIT Battery/CAN/A2B Power		
Size C	Board No. A0760-2015	Rev 1.3
Date 03/12/18	Sheet 27 of 27	