
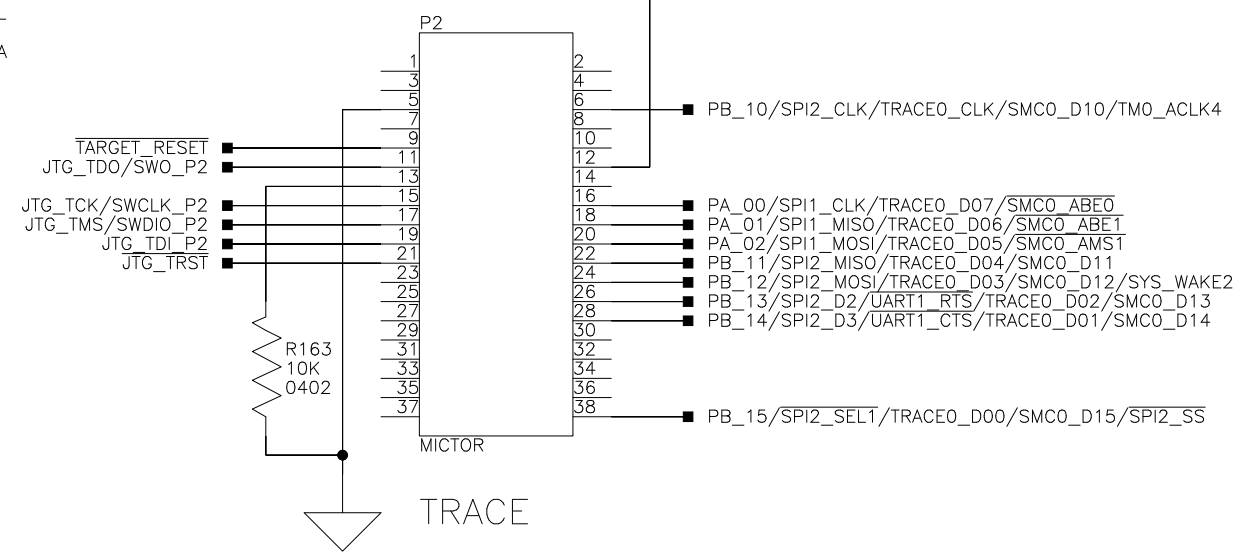
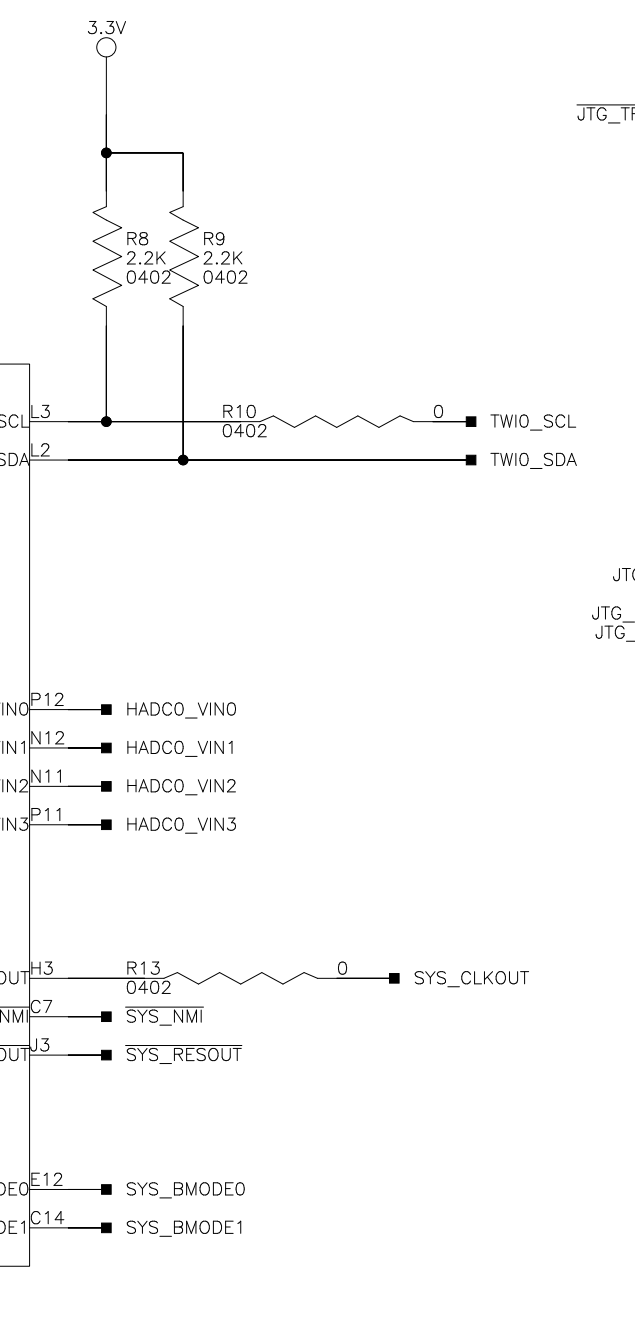
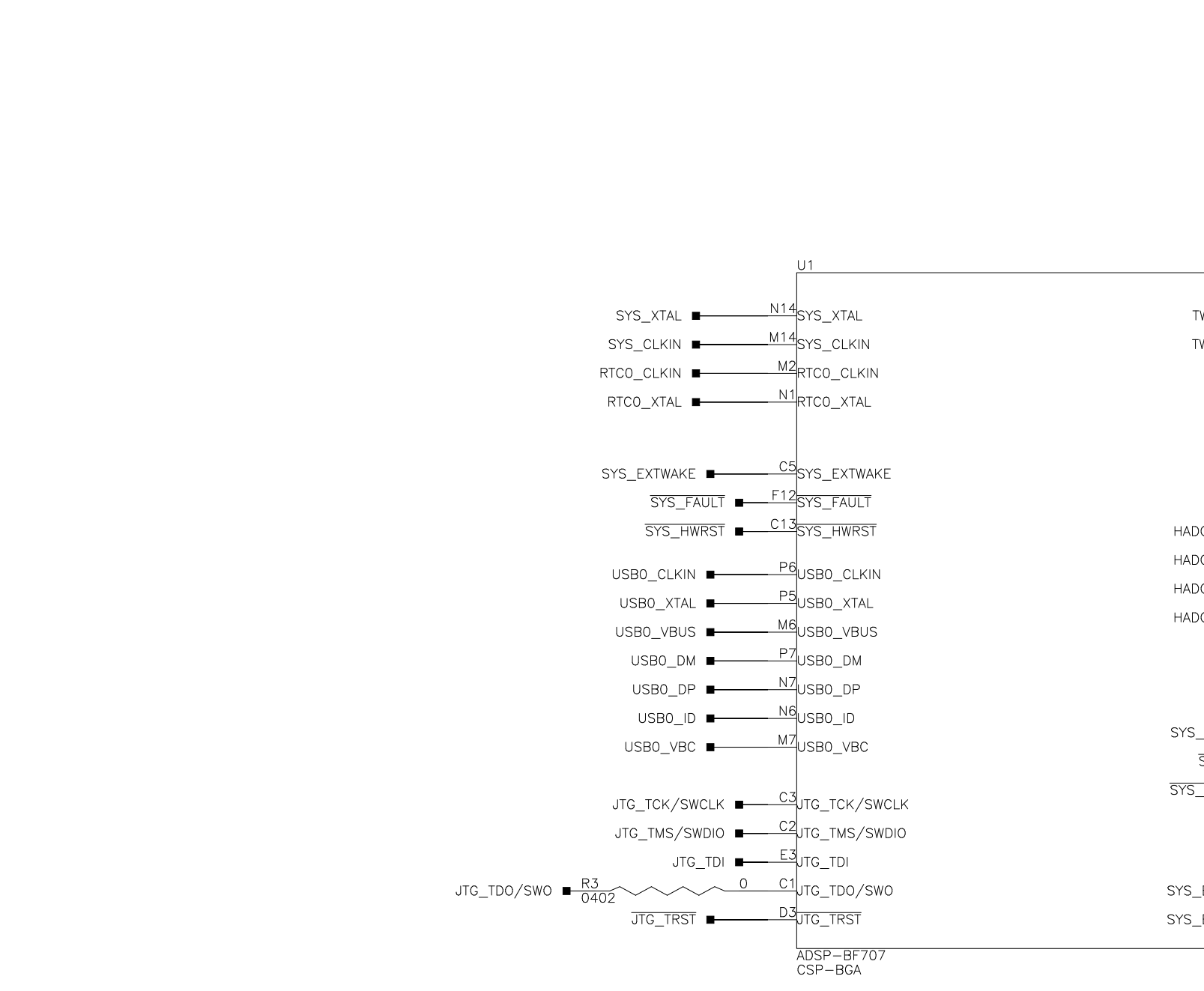
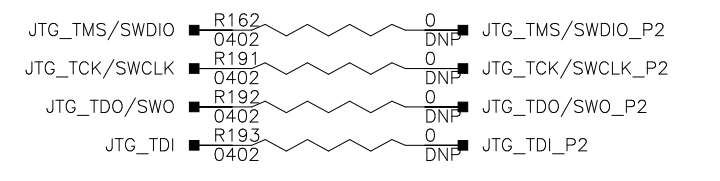
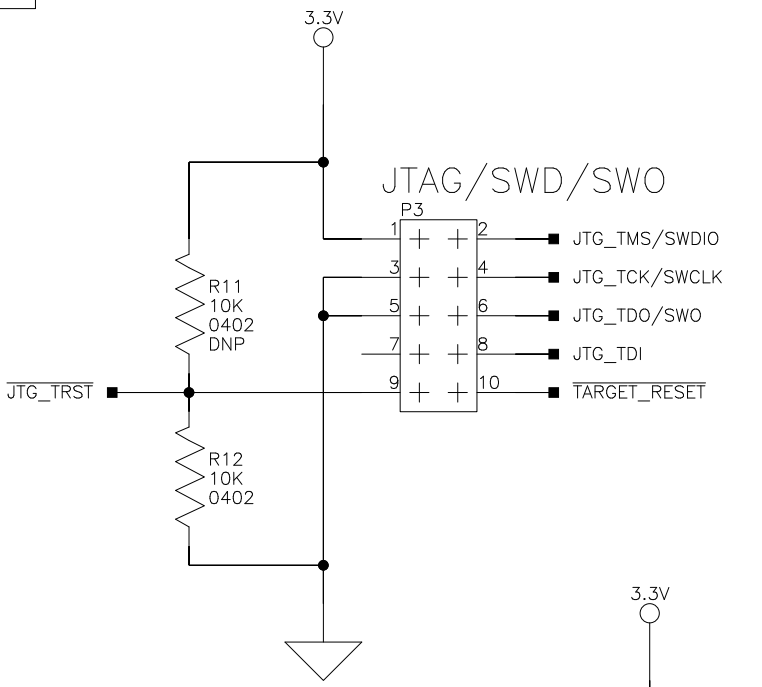
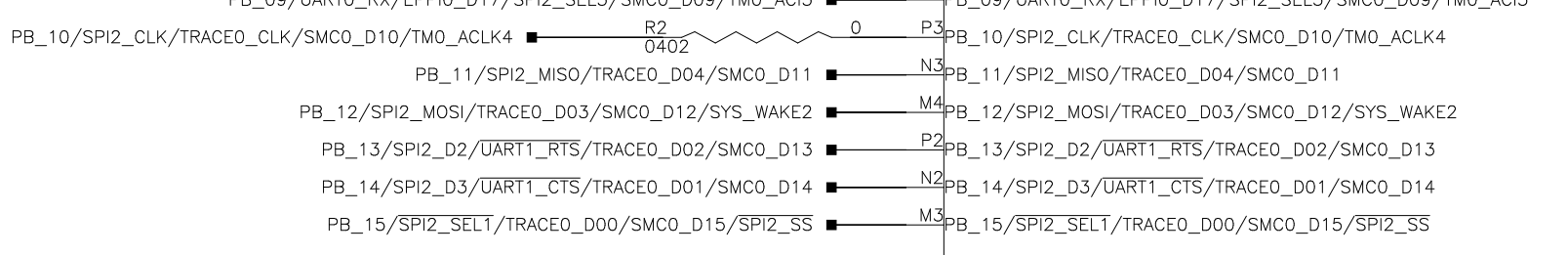
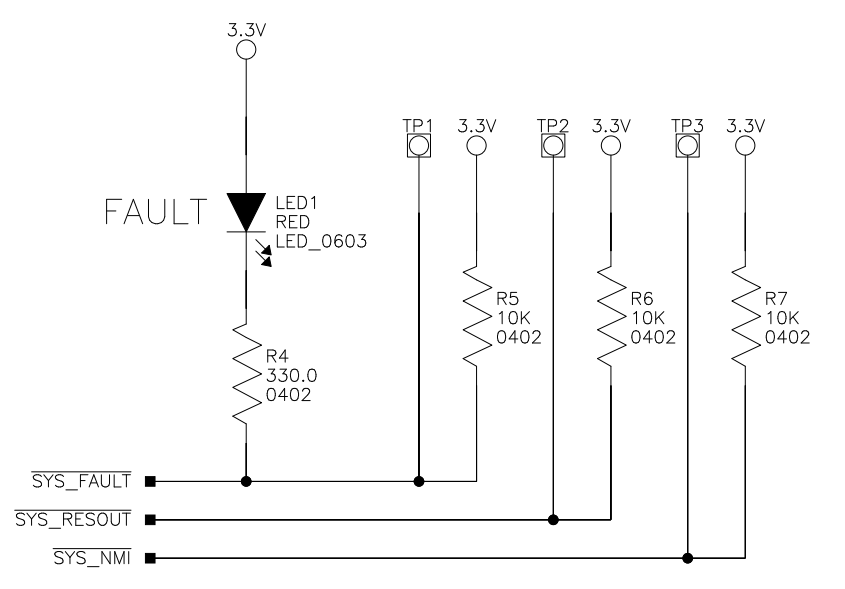
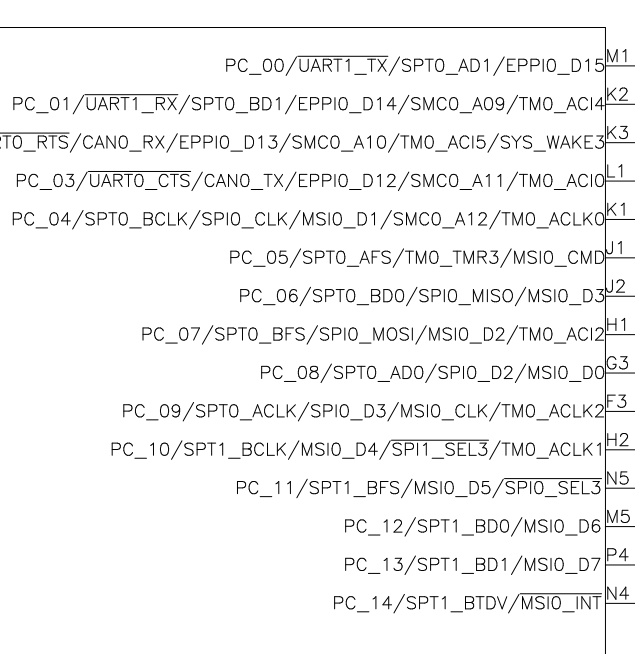
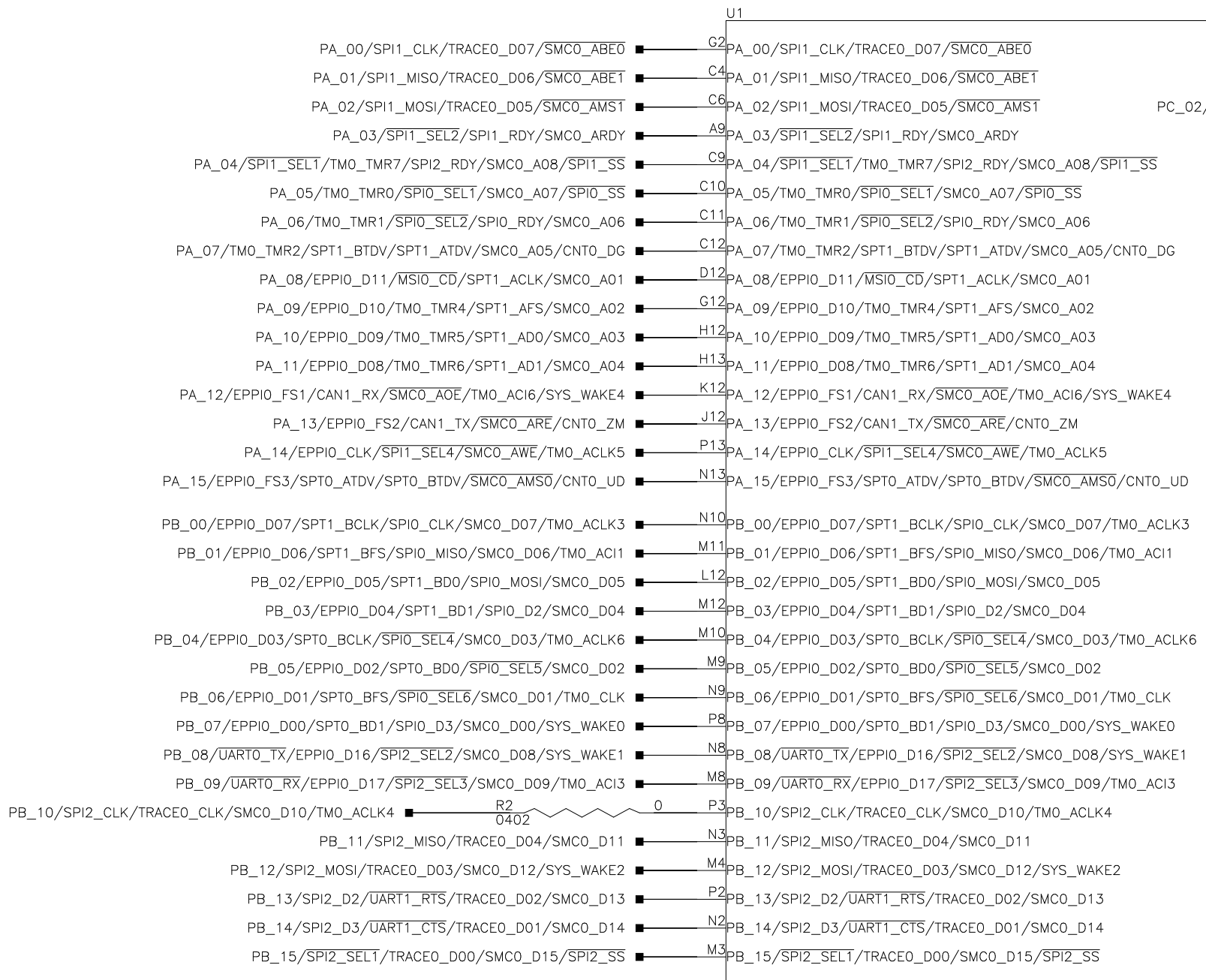


ADSP-BF707 EZ-Board SCHEMATIC

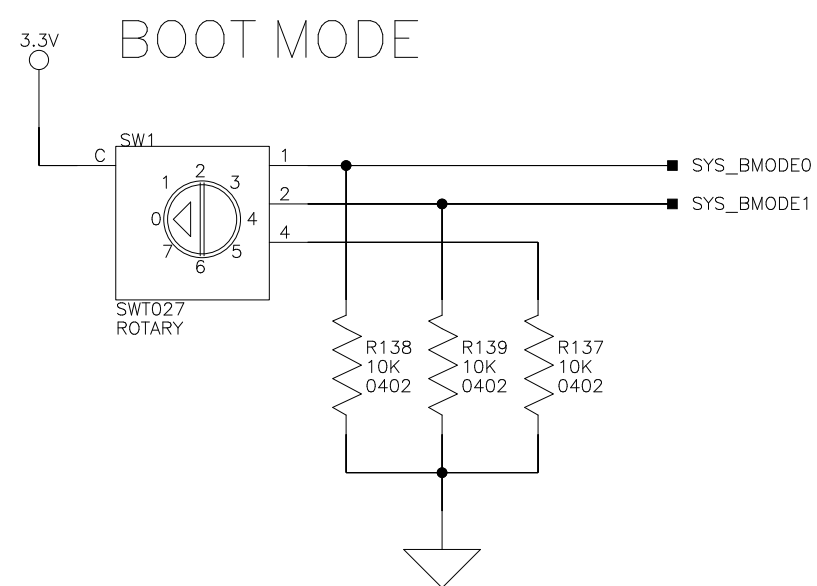
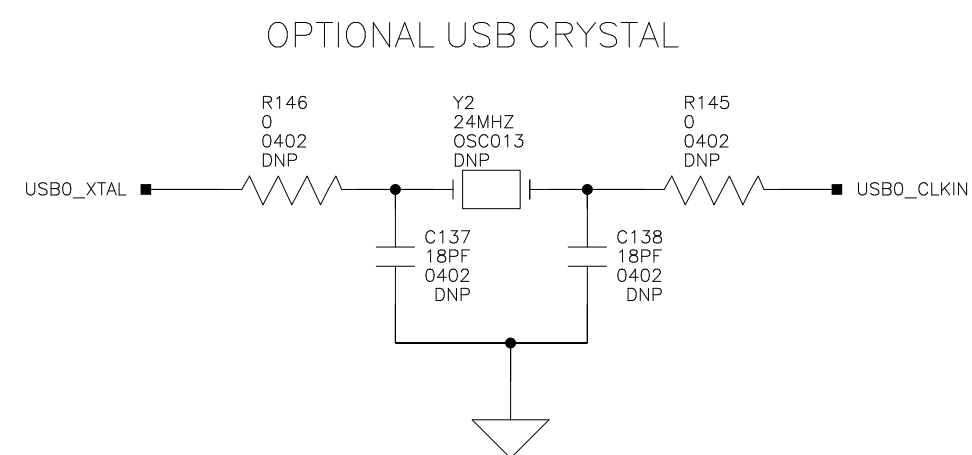
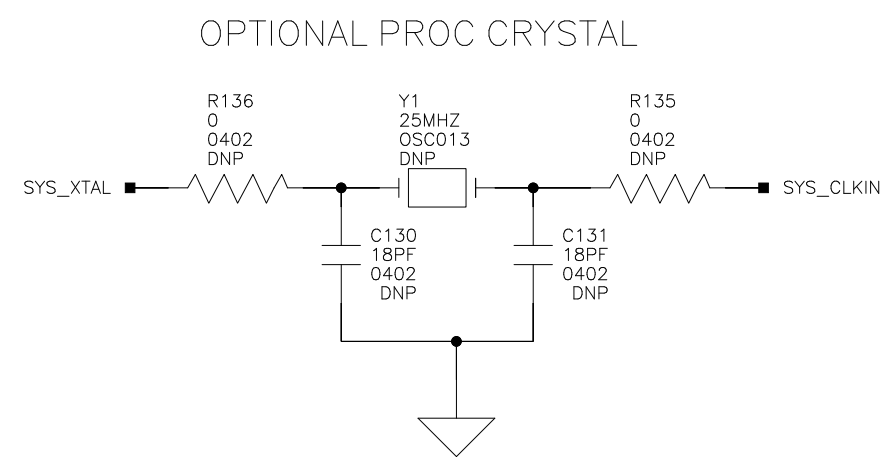
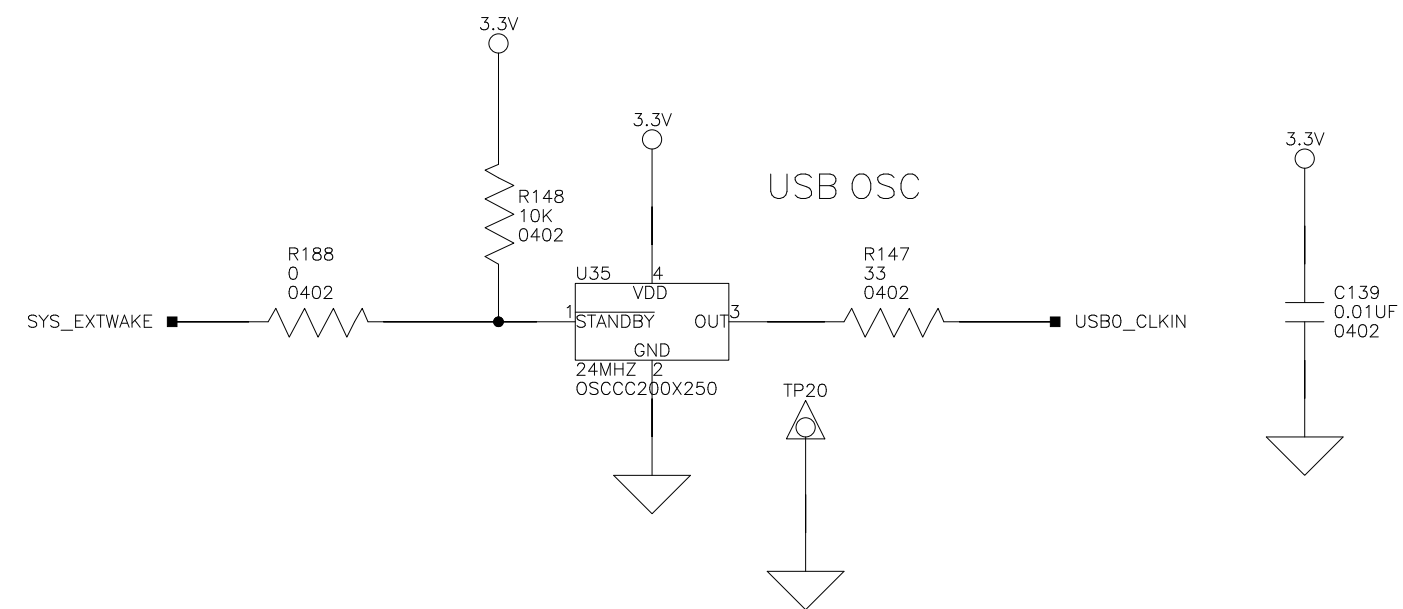
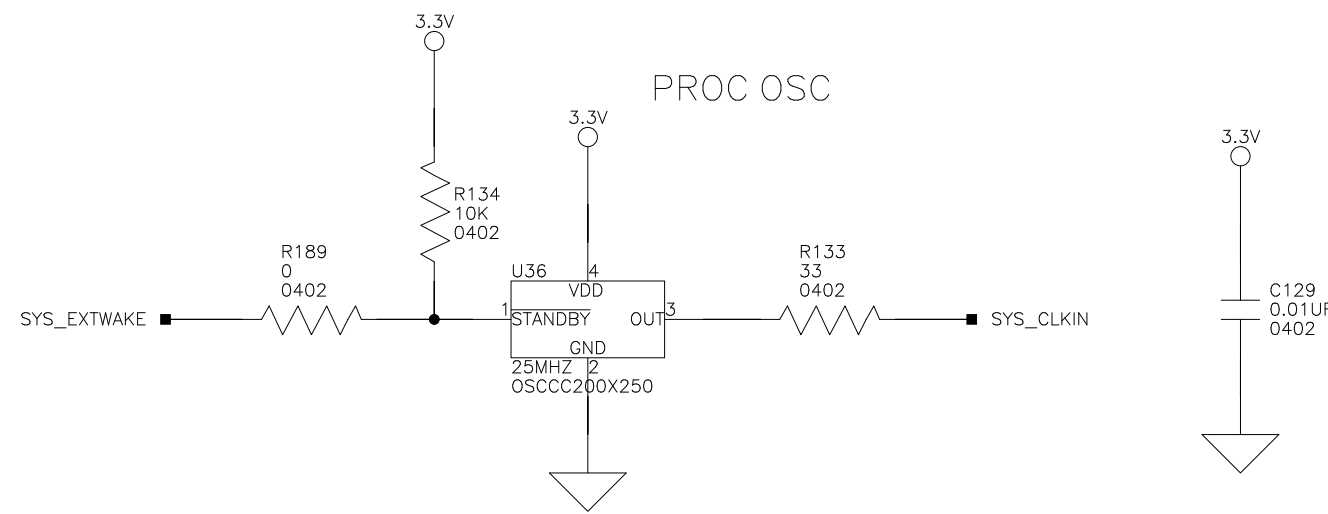
		2 Elizabeth Drive Chelmsford, MA 01824 PH: 1-800-ANALOGD	
Title		ADSP-BF707 EZ-BOARD	
		Title	
Size C	Board No.	A0450-2013	Rev 1.0B
Date	1/21/17	Sheet 1	of 14



2 Elizabeth Drive
Chelmsford, MA 01824
PH: 1-800-ANALOGD

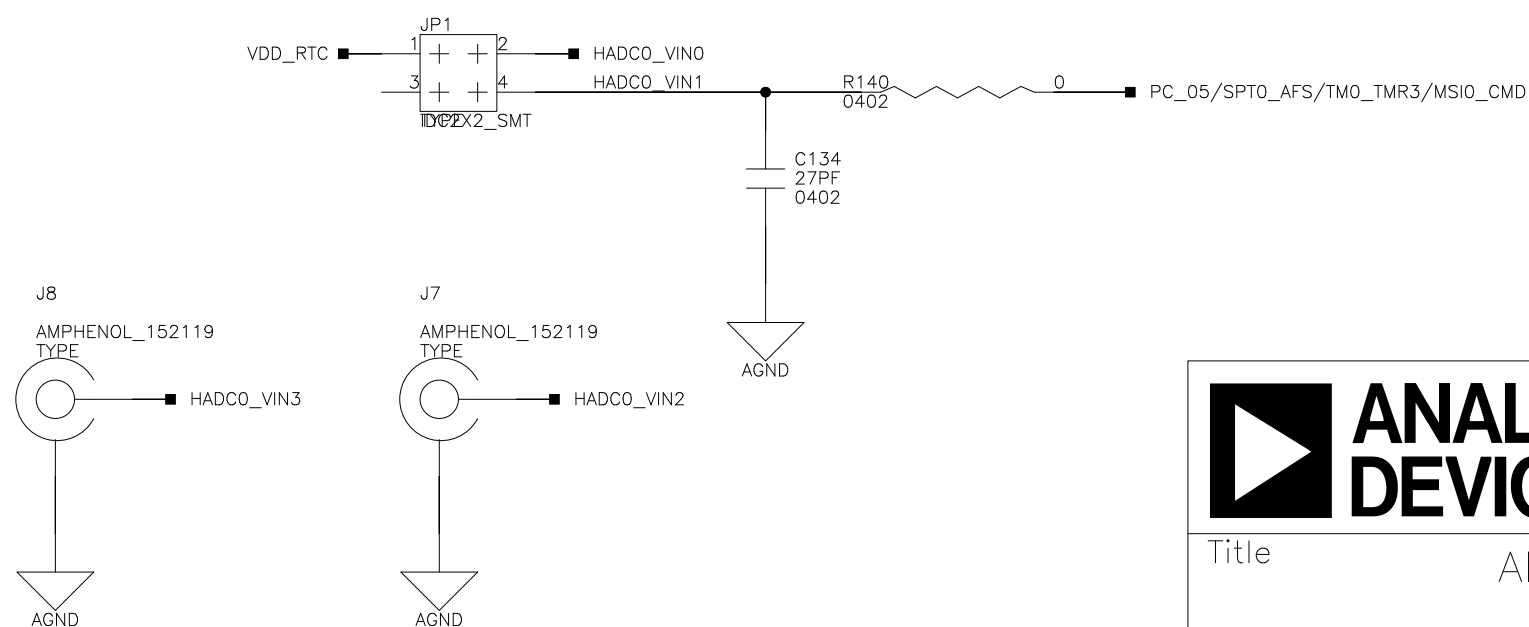
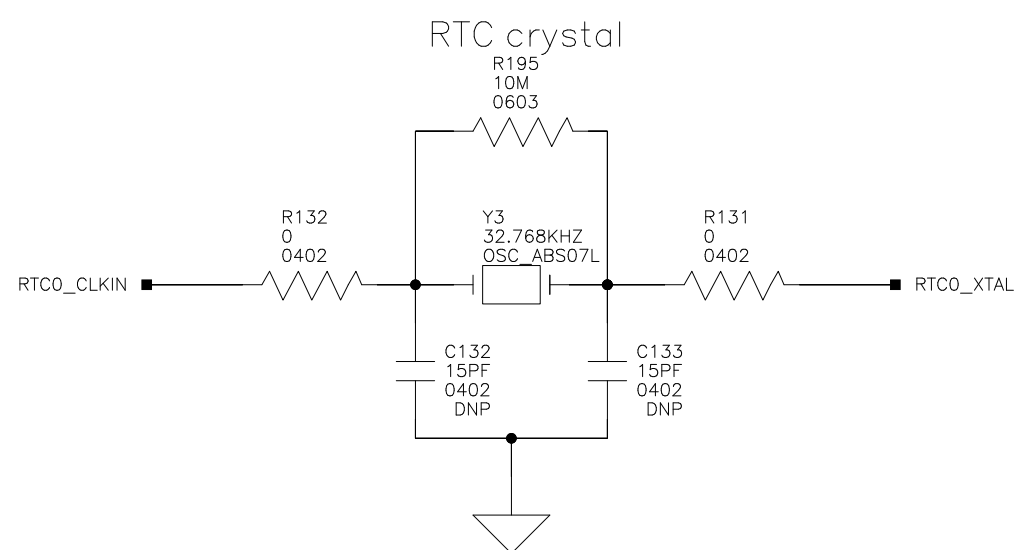
Title ADSP-BF707 EZ-BOARD
Proc Ports, JTAG, Trace

Size C	Board No. A0450-2013	Rev 1.0B
Date 1/21/17	Sheet 2	of 14



BOOT MODE SWITCH (SW1)

BOOT MODE (1:0)	
00	No Boot
01	SPI Master Boot (SPI2) DEFAULT
10	SPI Slave Boot (SPI2)
11	UART Boot (UART0)



ANALOG DEVICES 2 Elizabeth Drive
Chelmsford, MA 01824
PH: 1-800-ANALOGD

Title ADSP-BF707 EZ-BOARD
Clocks, Boot Mode

Size C	Board No. A0450-2013	Rev 1.0B
Date 1/21/17	Sheet 3 of 14	

DDR2 – 2Gb
16M x 16 x 8 banks

1

2

3

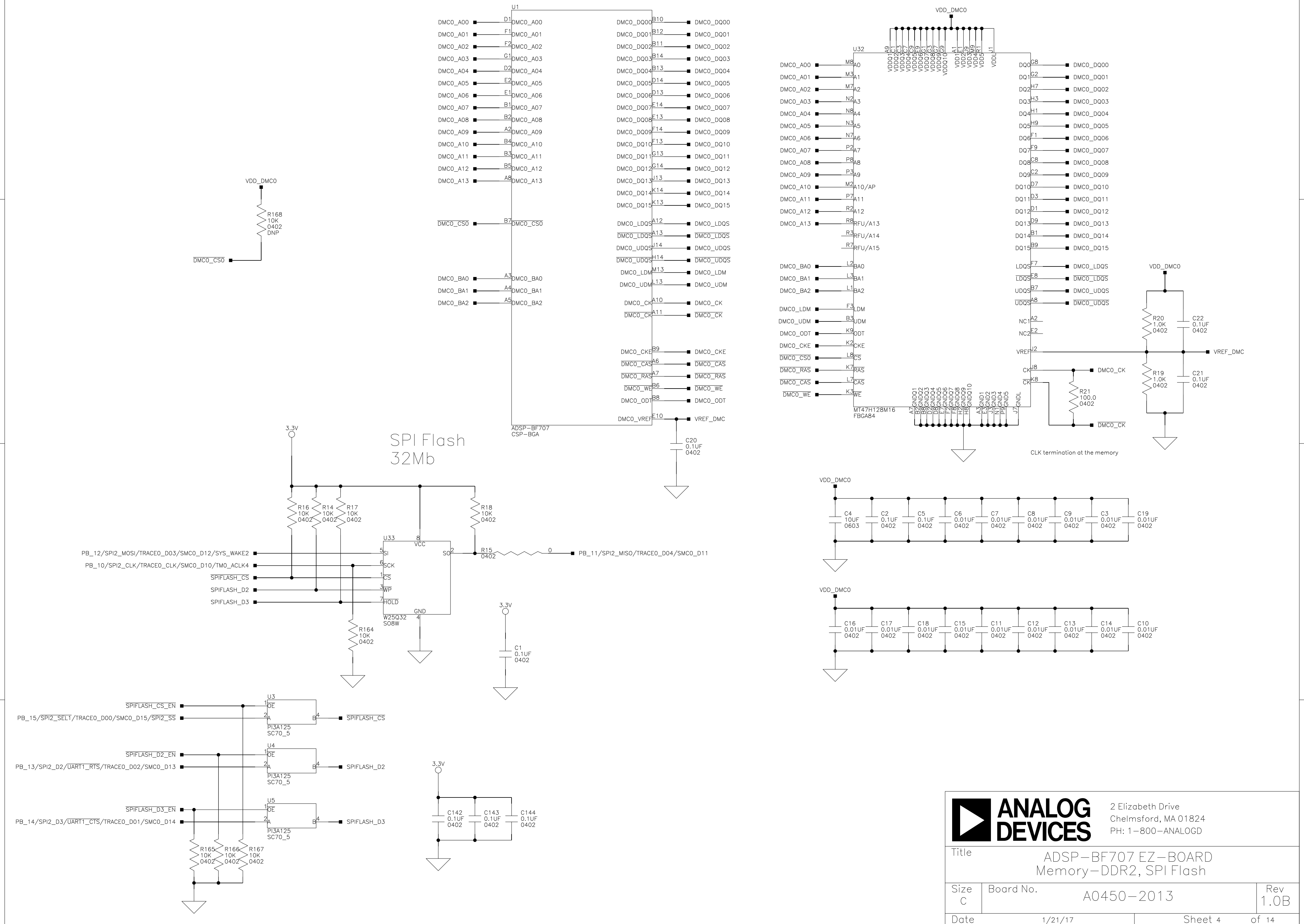
4

1

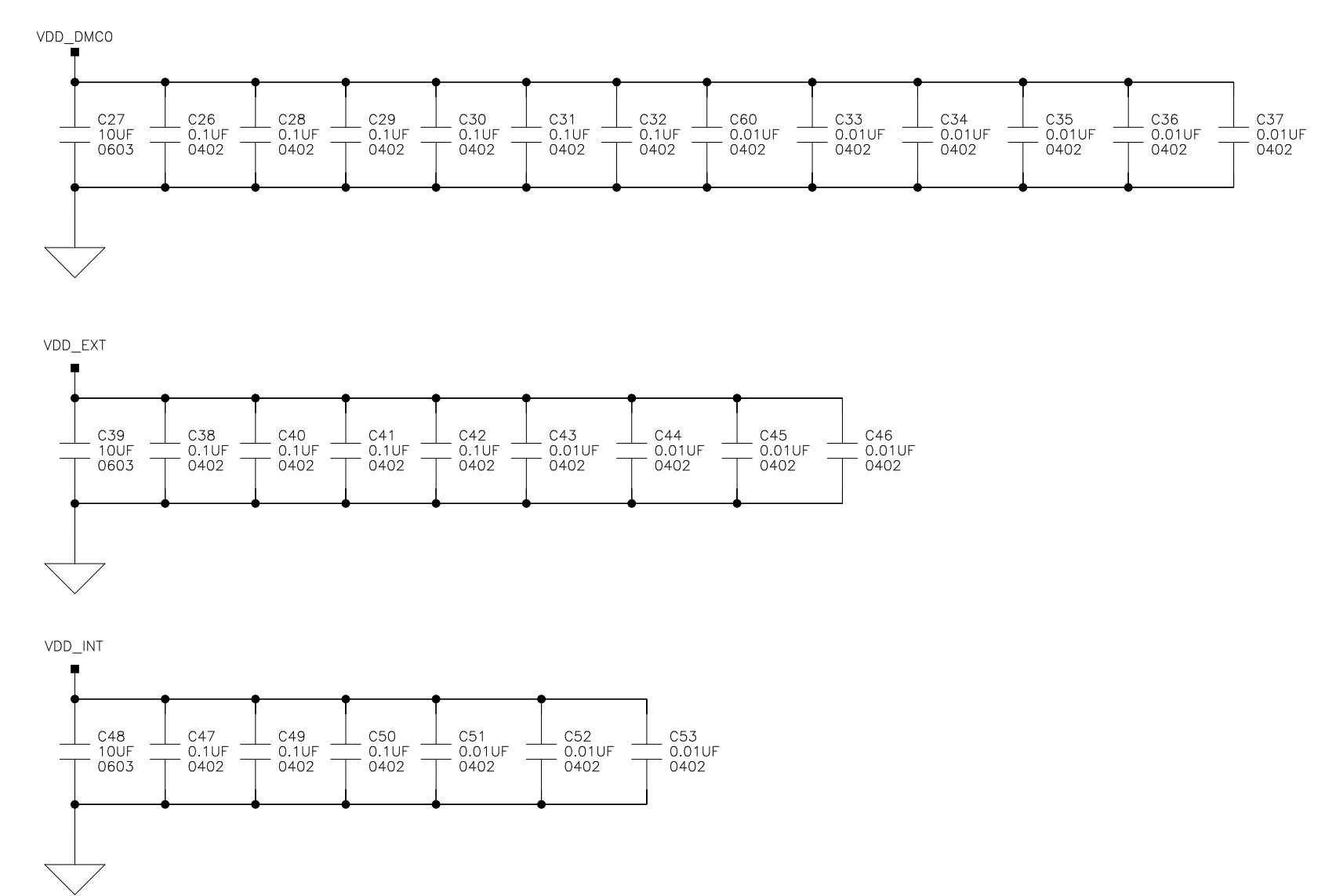
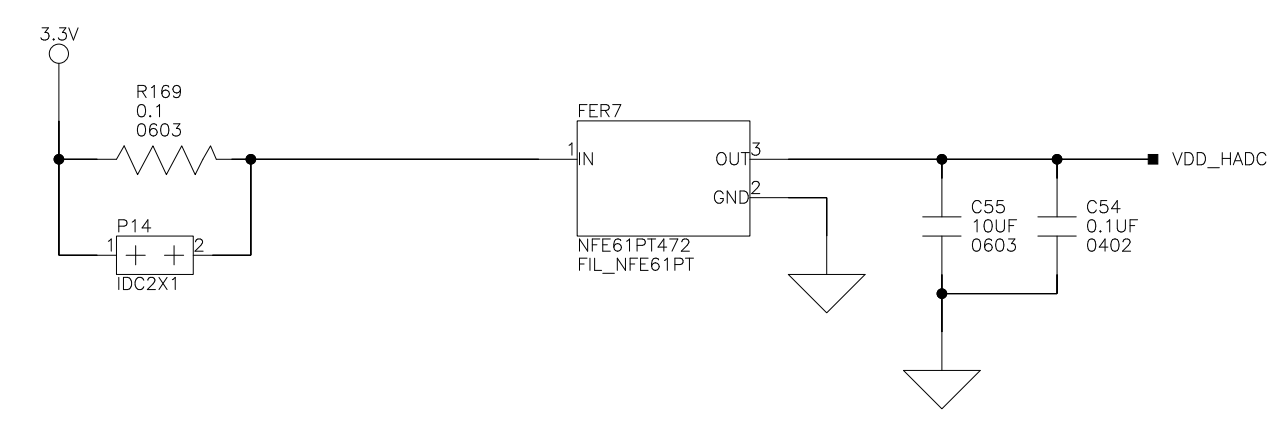
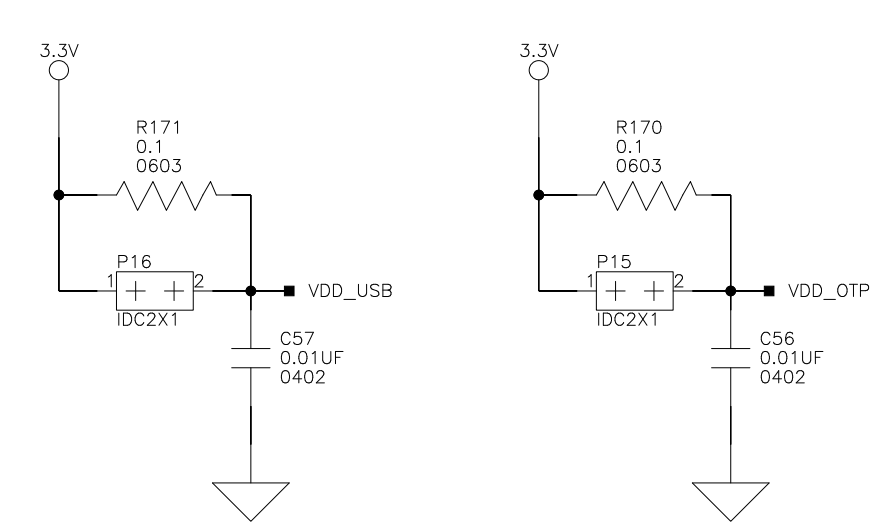
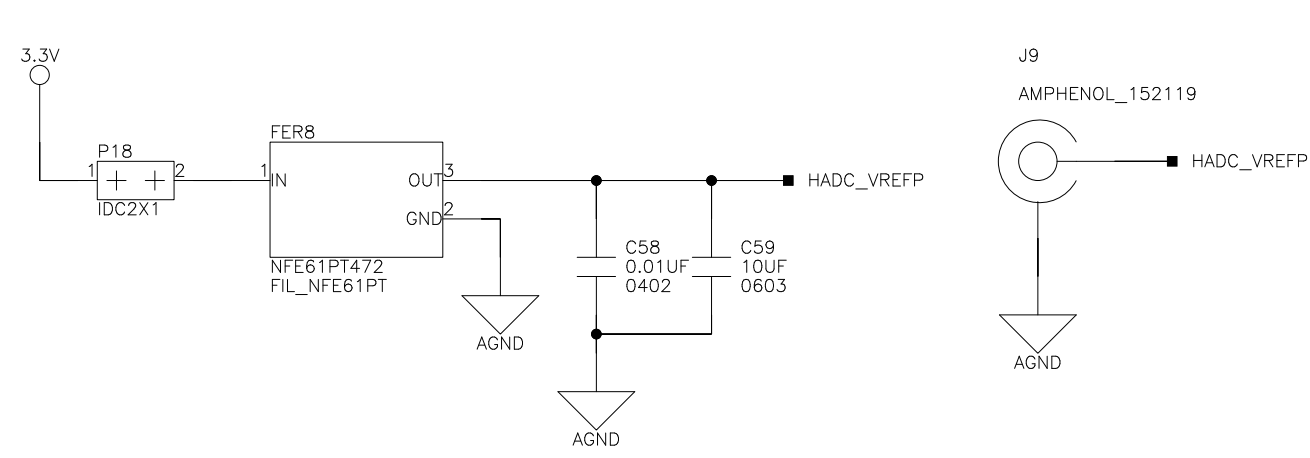
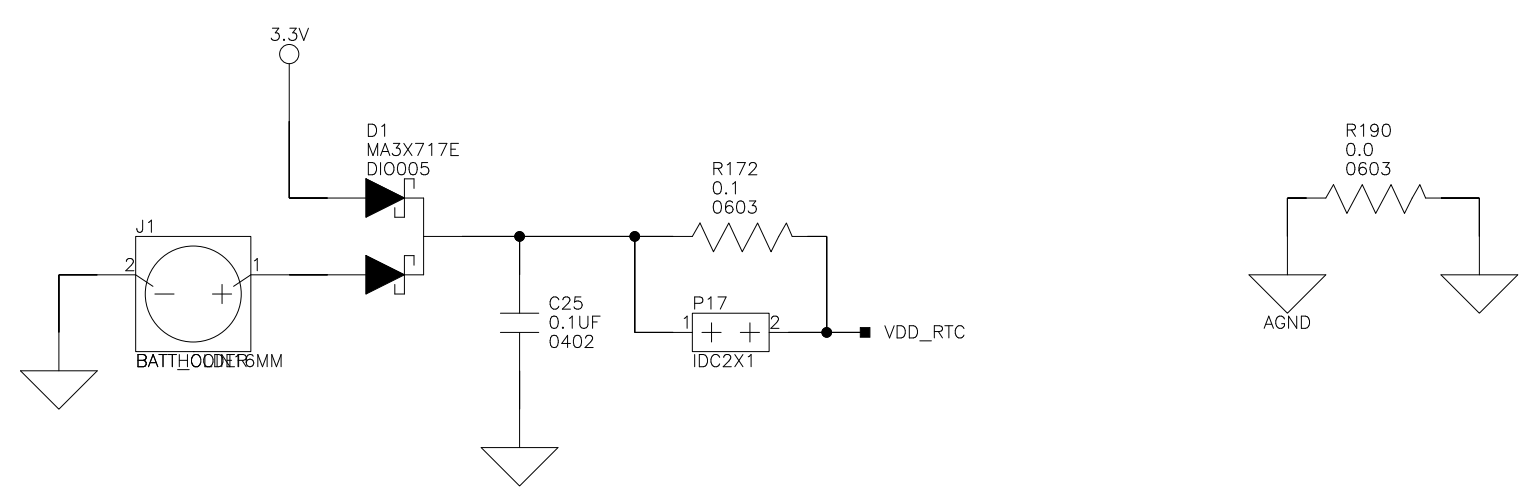
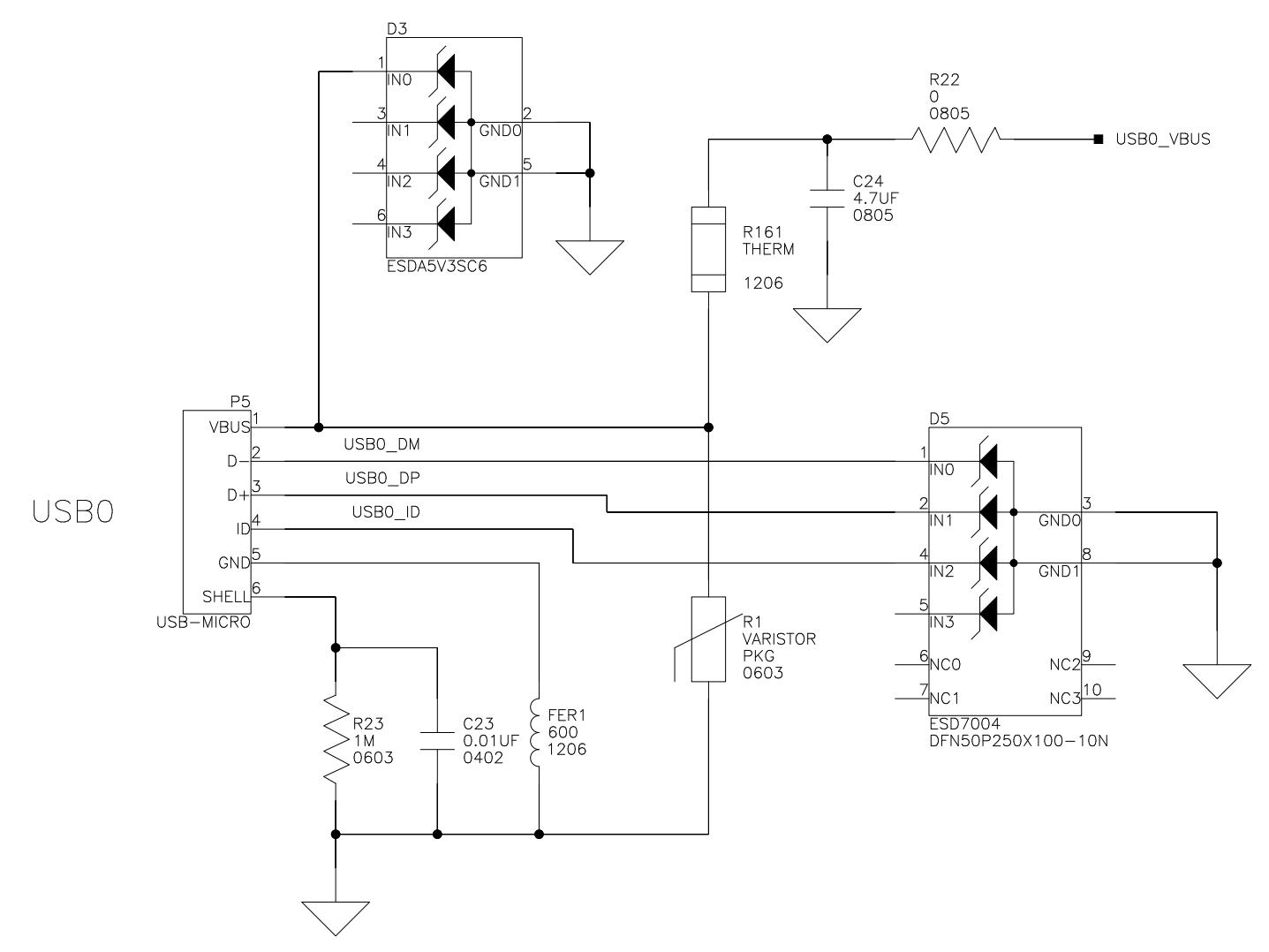
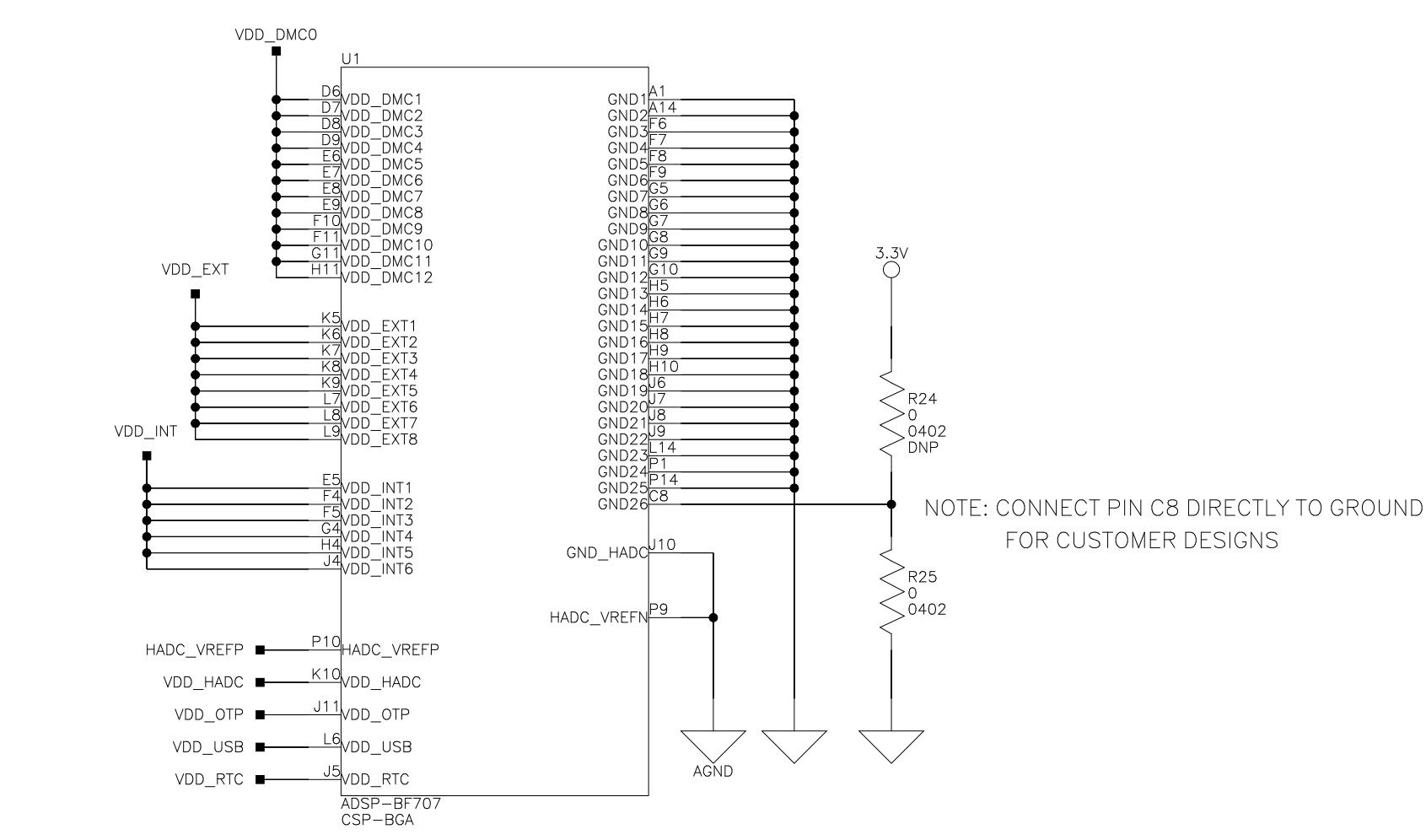
2

3

4



		2 Elizabeth Drive Chelmsford, MA 01824 PH: 1-800-ANALOGD	
		Title: ADSP-BF707 EZ-BOARD Memory-DDR2, SPI Flash	
Size: C	Board No.: A0450-2013	Rev: 1.0B	
Date: 1/21/17	Sheet 4 of 14		

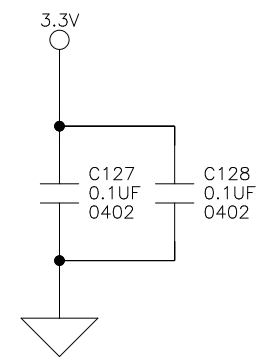
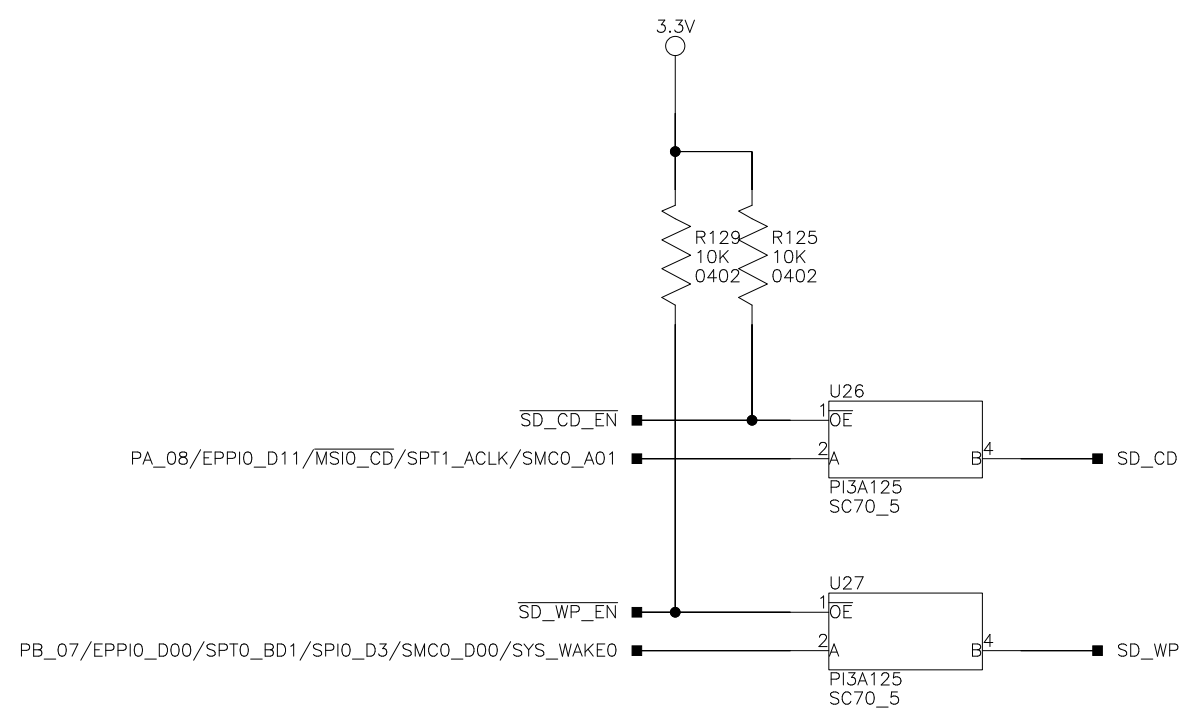
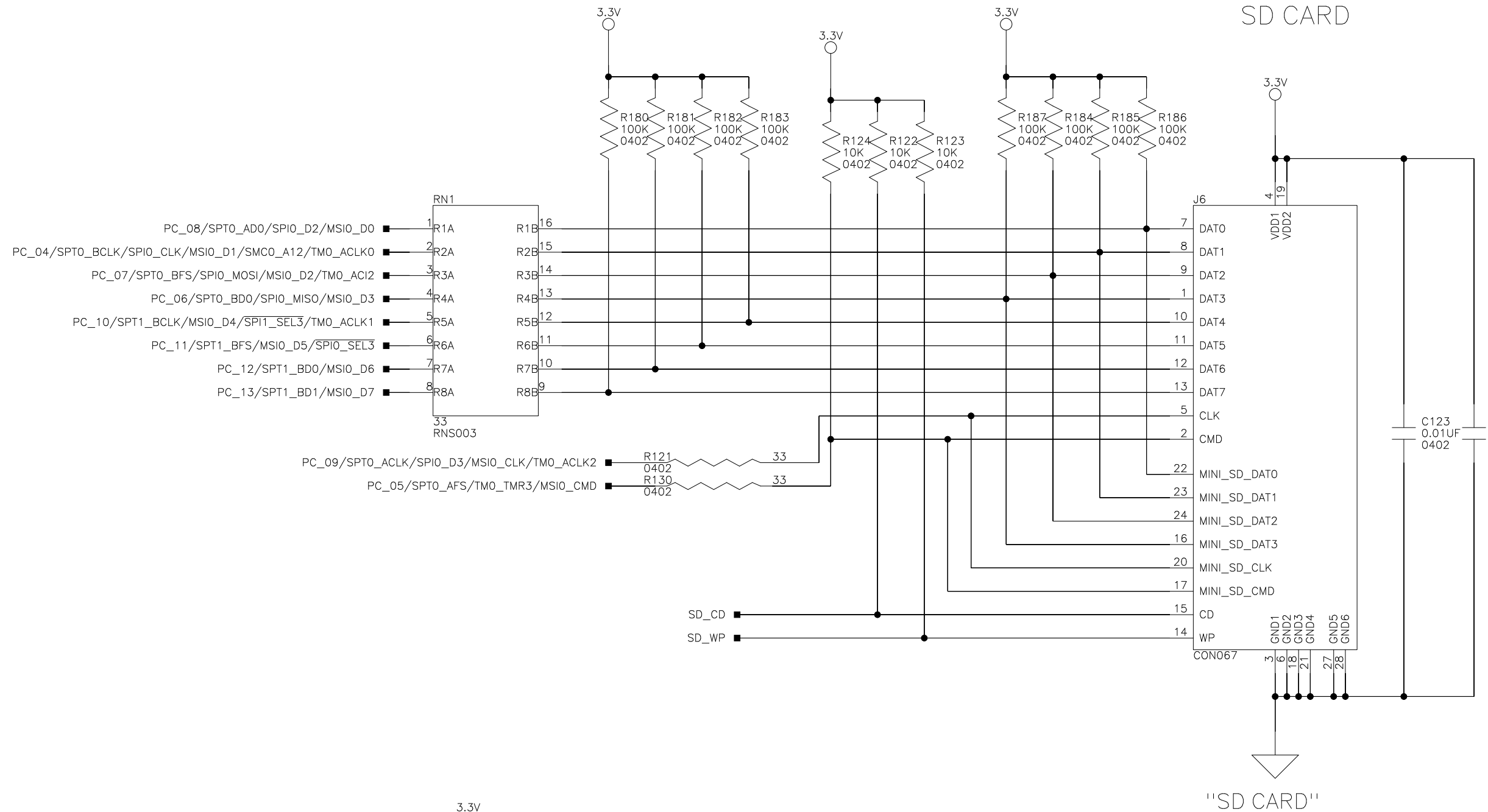
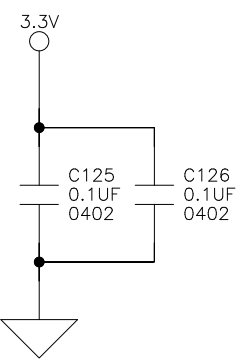
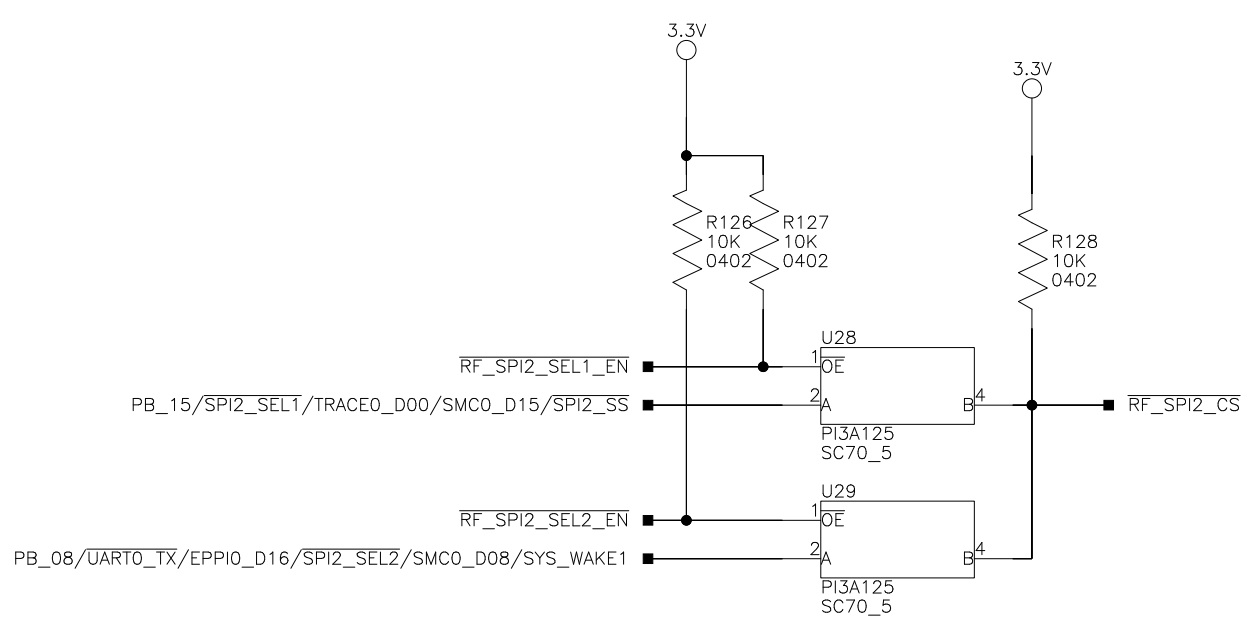
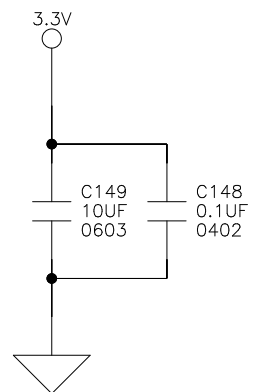
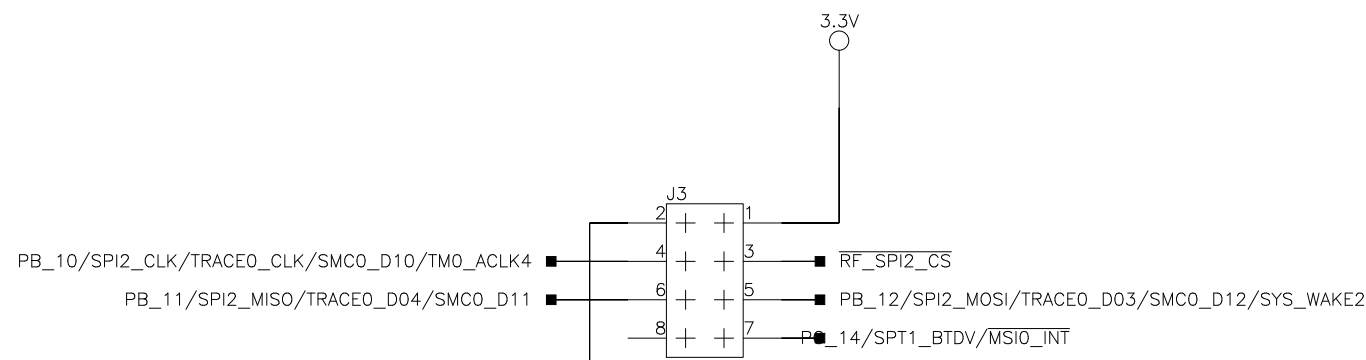



ANALOG DEVICES
 2 Elizabeth Drive
 Chelmsford, MA 01824
 PH: 1-800-ANALOGD

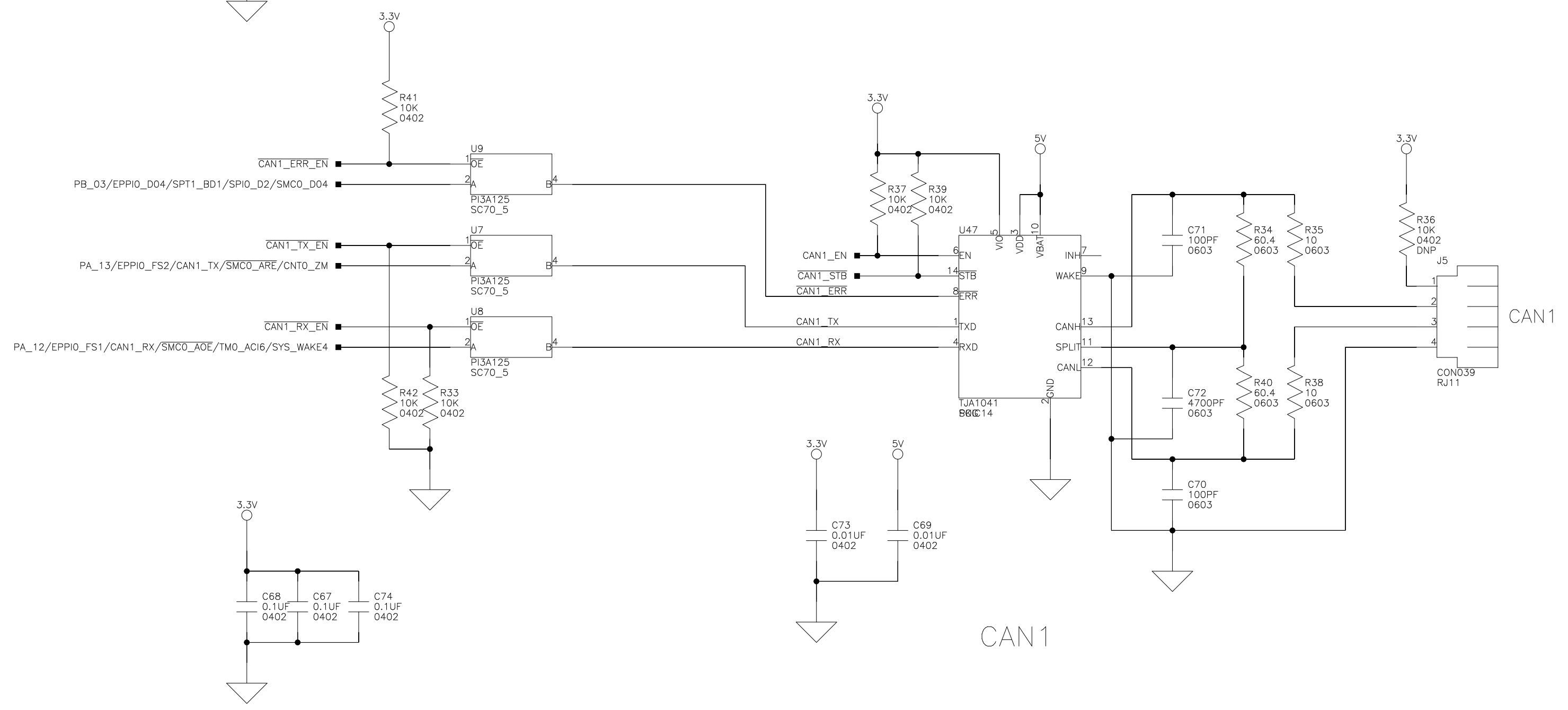
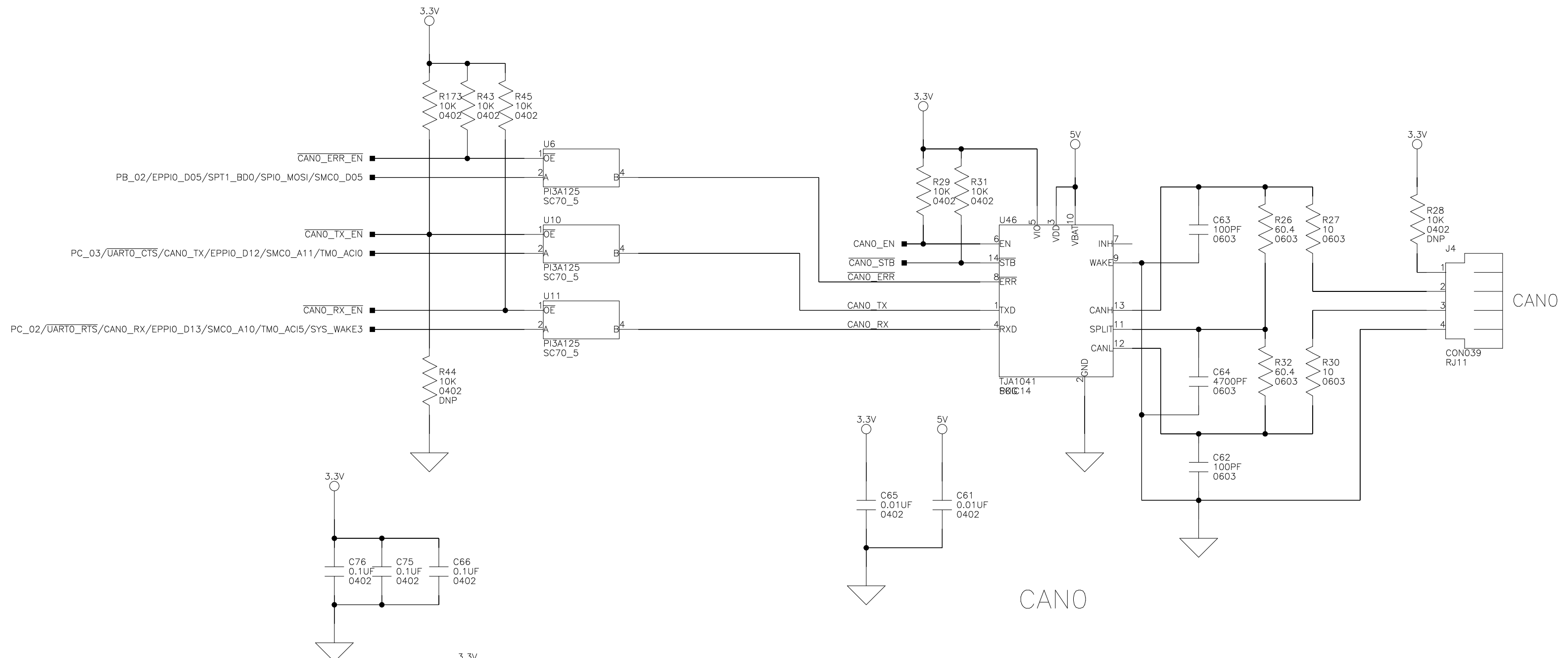
Title: ADSP-BF707 EZ-BOARD
 Proc Decoupling, USB0


Size C	Board No. A0450-2013	Rev 1.0B
Date 1/21/17	Sheet 5	of 14

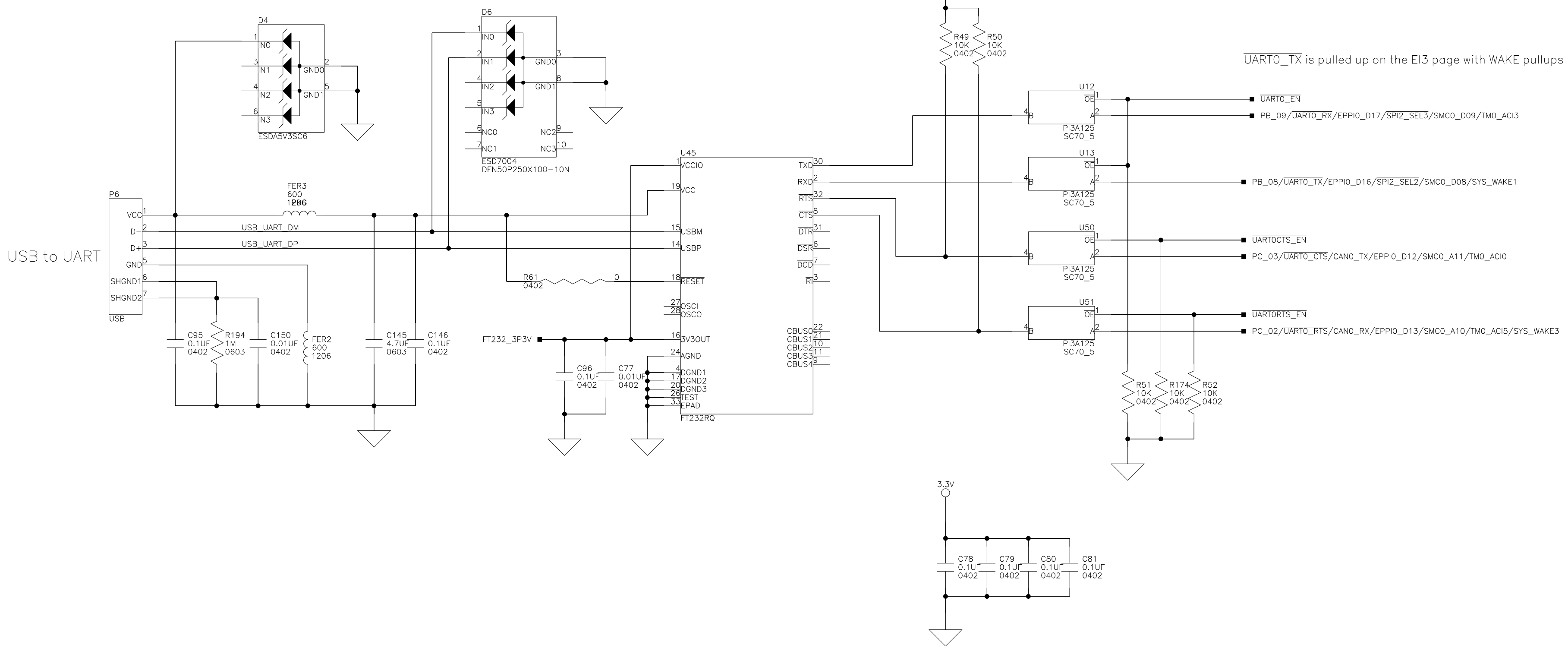
RF Wireless




		2 Elizabeth Drive Chelmsford, MA 01824 PH: 1-800-ANALOGD	
Title ADSP-BF707 EZ-BOARD RF Wireless, SD Conn			
Size C	Board No. A0450-2013	Rev 1.0B	
Date 1/21/17	Sheet 6		of 14

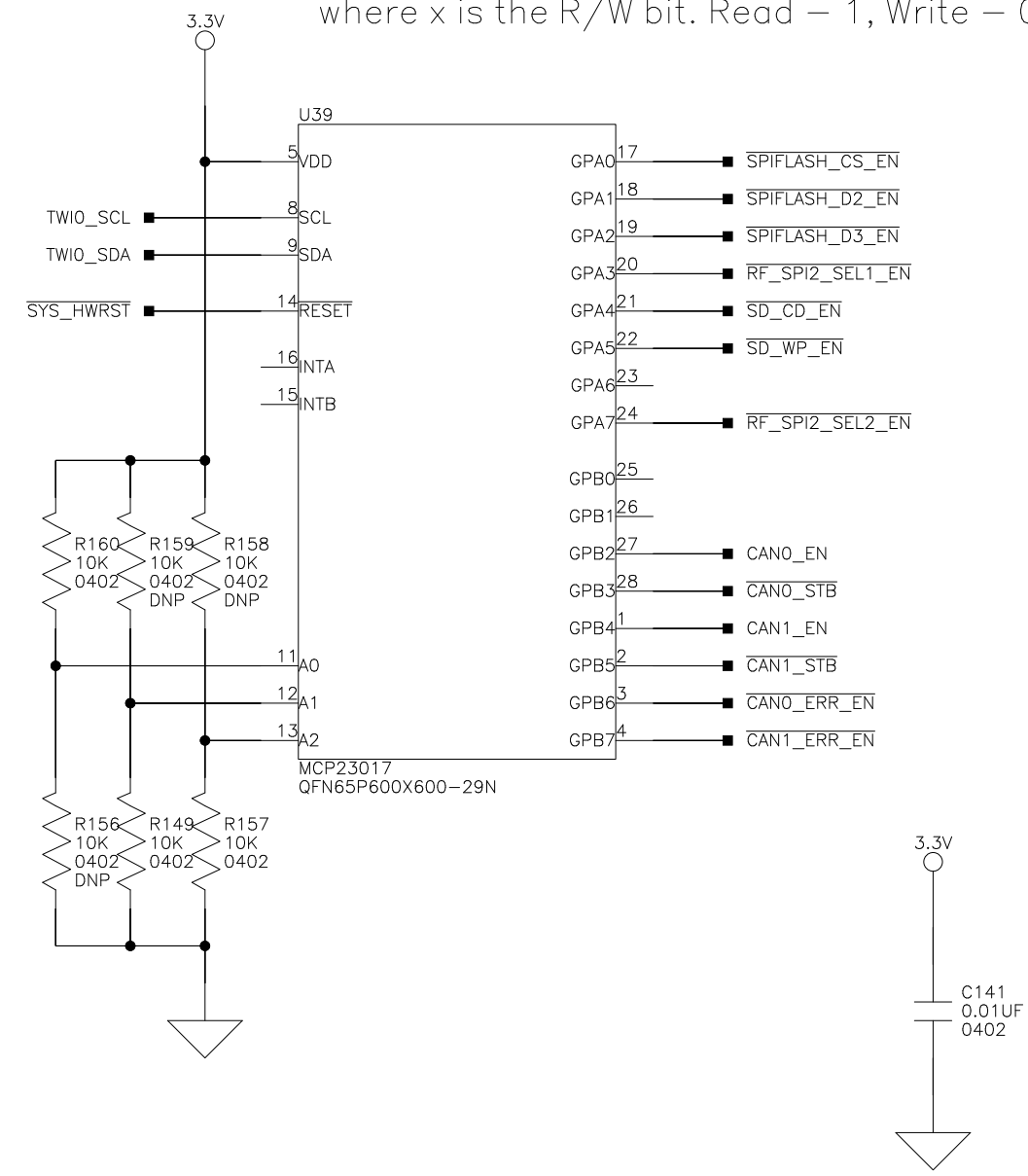


		2 Elizabeth Drive Chelmsford, MA 01824 PH: 1-800-ANALOGD	
		Title ADSP-BF707 EZ-BOARD CAN0, CAN1	
Size C	Board No. A0450-2013	Rev 1.0B	
Date 1/21/17	Sheet 7		of 14

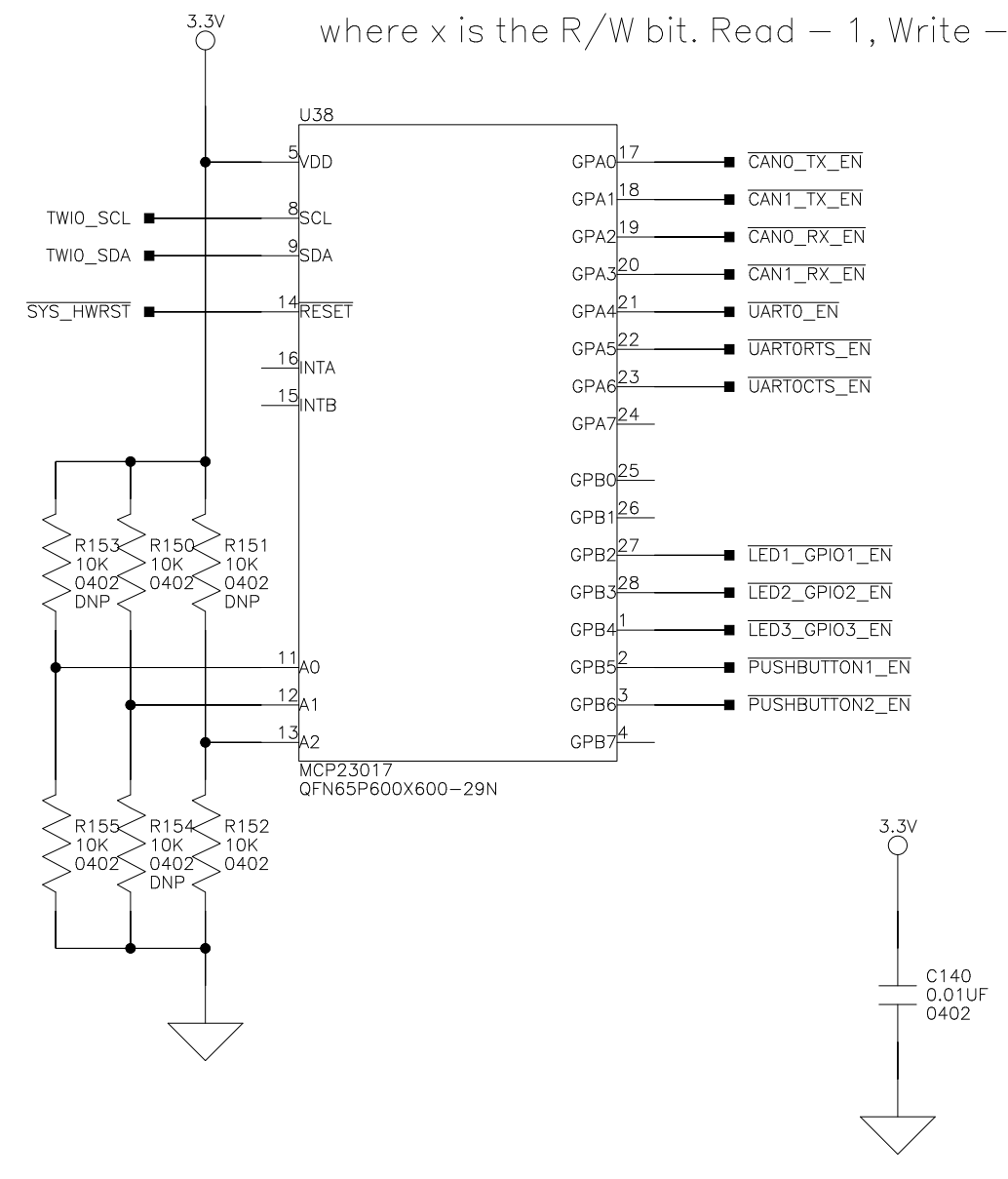



		2 Elizabeth Drive Chelmsford, MA 01824 PH: 1-800-ANALOGD	
Title ADSP-BF707 EZ-BOARD UART0, USB to UART			
Size C	Board No. A0450-2013	Rev 1.0B	
Date 1/21/17	Sheet 8		of 14

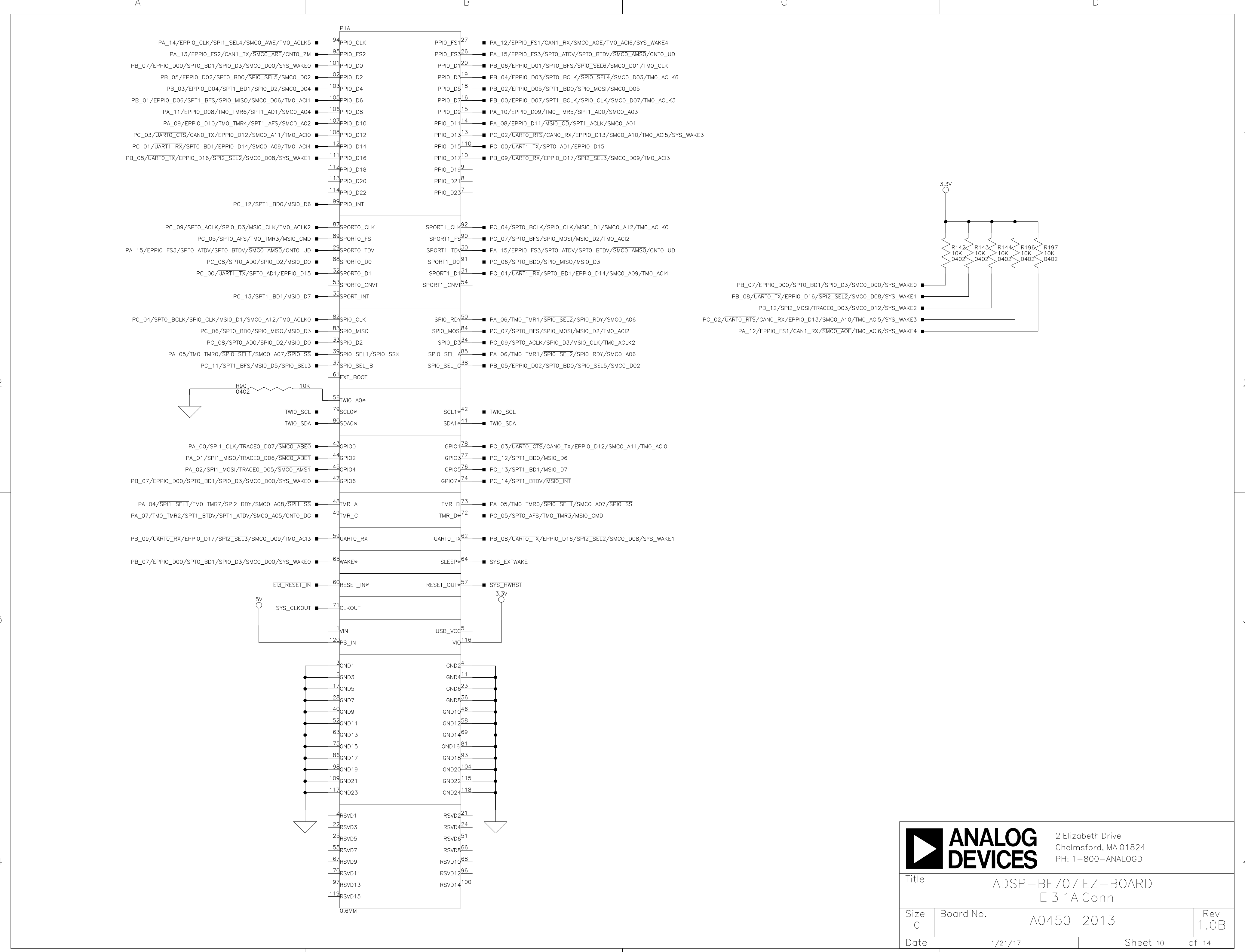
TWI address 0100 001x
 where x is the R/W bit. Read – 1, Write – 0




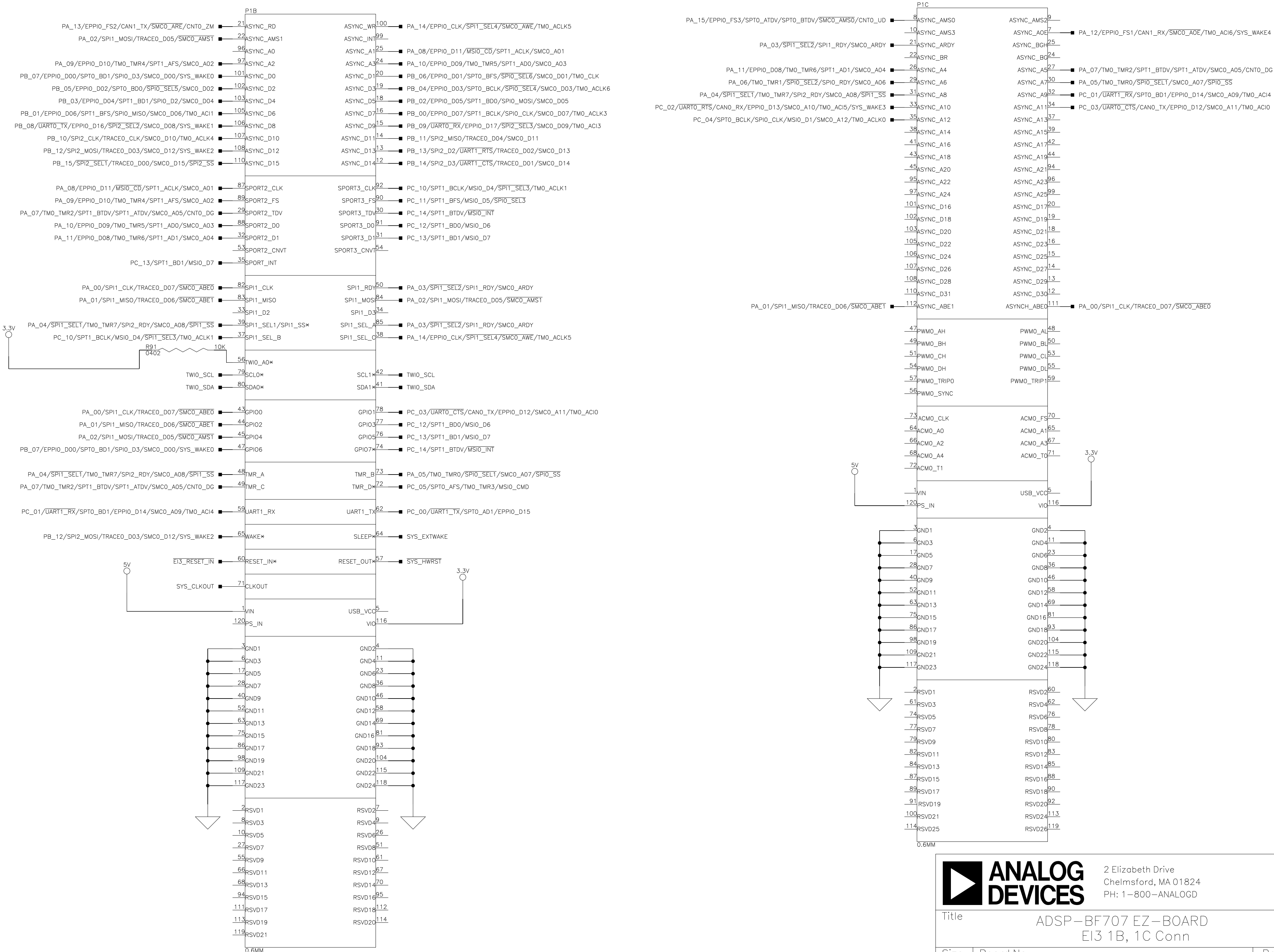
TWI address 0100 010x
 where x is the R/W bit. Read – 1, Write – 0




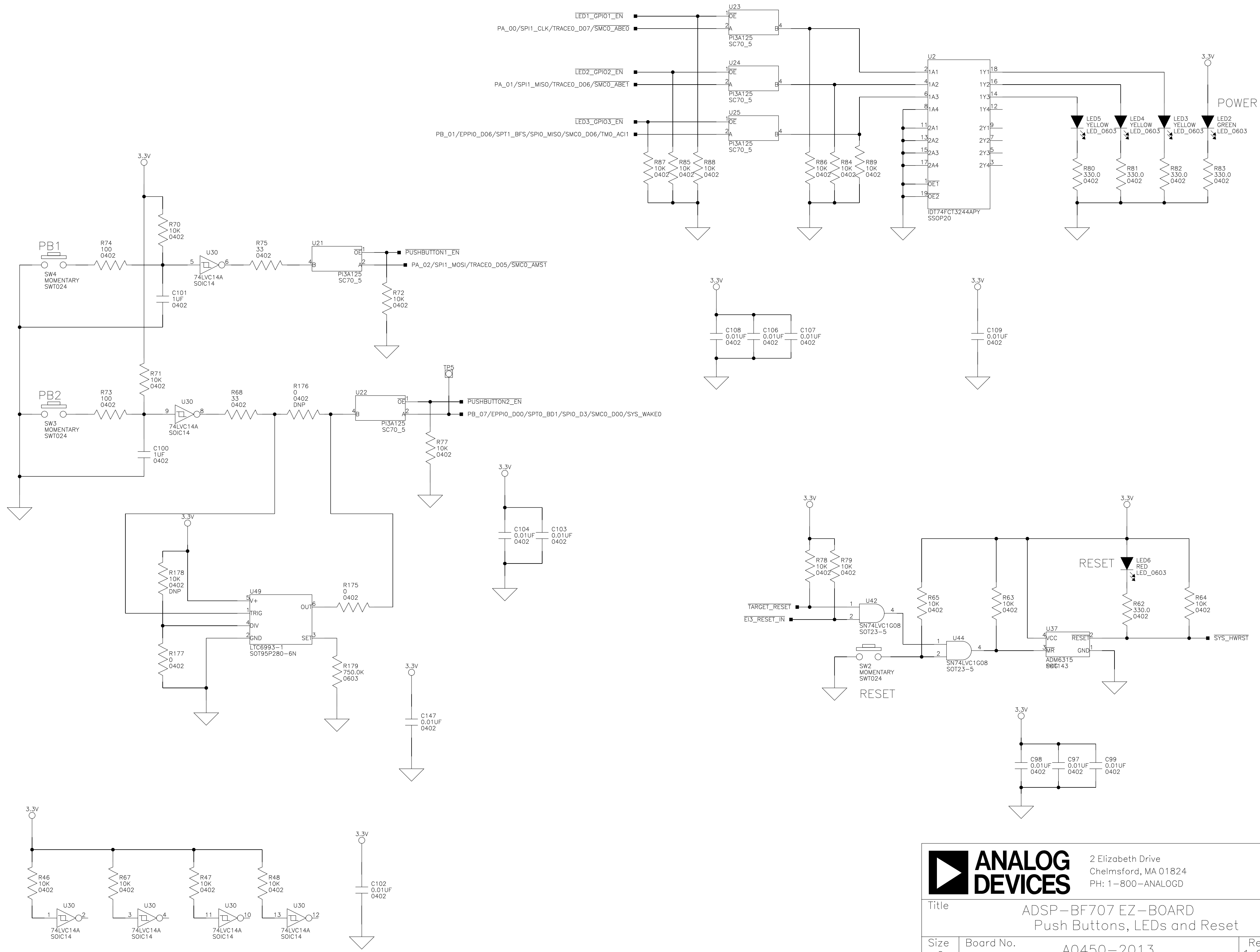
		2 Elizabeth Drive Chelmsford, MA 01824 PH: 1-800-ANALOGD	
		Title ADSP-BF707 EZ-BOARD SoftConfig	
Size C	Board No. A0450-2013	Rev 1.0B	
Date 1/21/17	Sheet 9		of 14




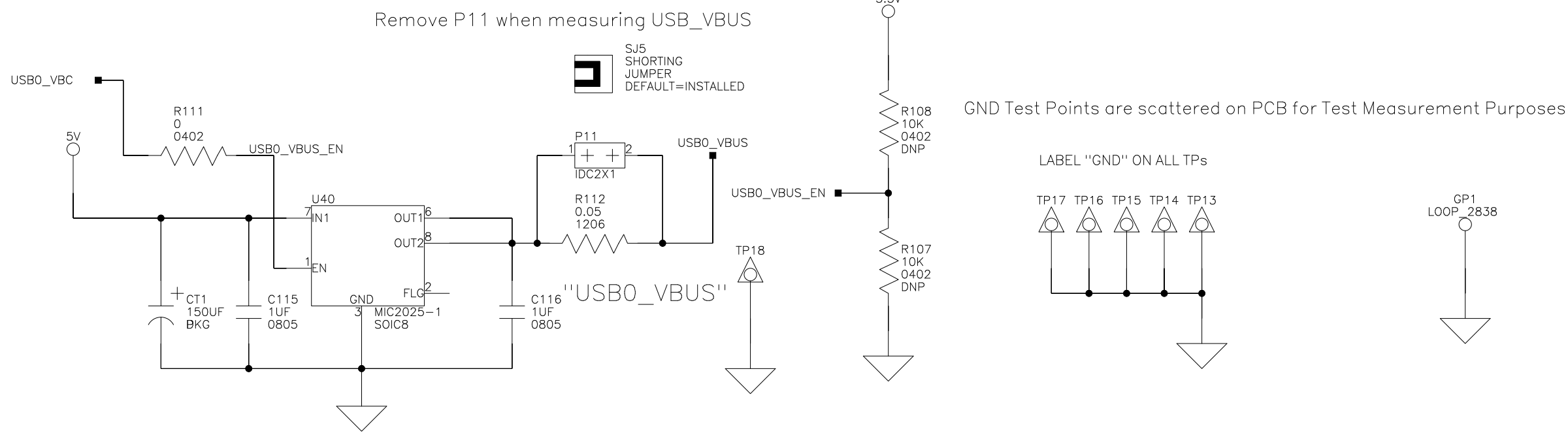
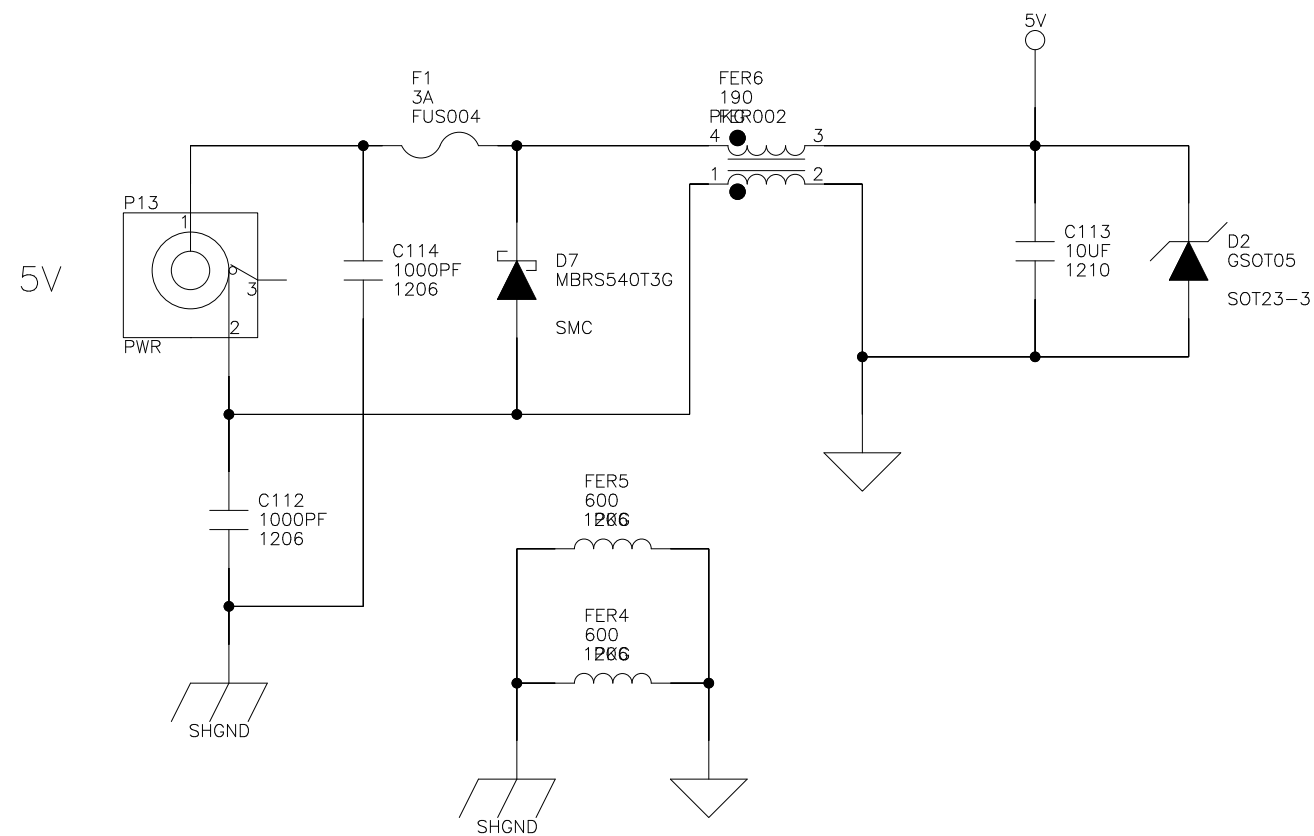
		2 Elizabeth Drive Chelmsford, MA 01824 PH: 1-800-ANALOGD	
		Title ADSP-BF707 EZ-BOARD E13 1A Conn	
Size C	Board No. A0450-2013	Rev 1.0B	
Date 1/21/17	Sheet 10		of 14



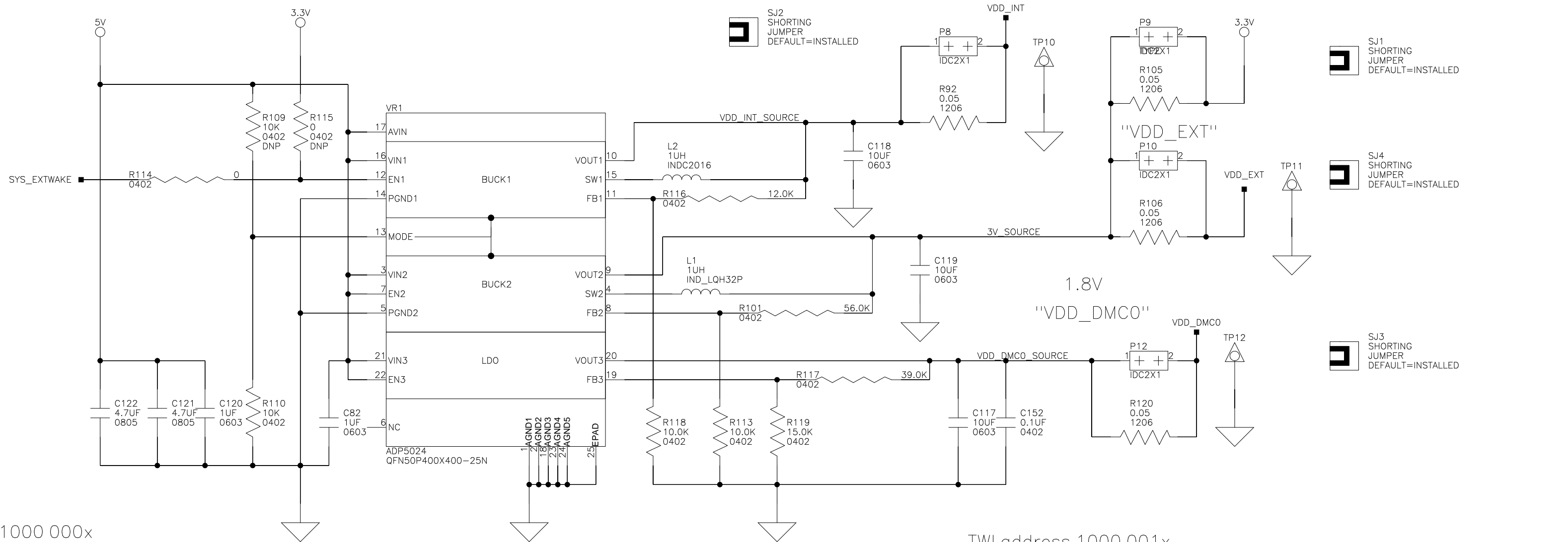
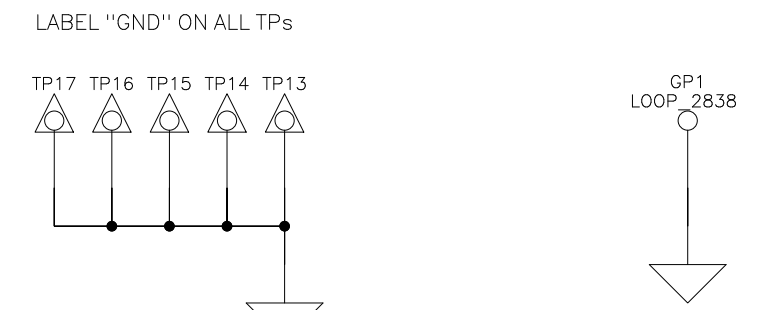
		2 Elizabeth Drive Chelmsford, MA 01824 PH: 1-800-ANALOGD	
		Title: ADSP-BF707 EZ-BOARD EI3 1B, 1C Conn	
Size C	Board No. A0450-2013	Rev 1.0B	
Date 1/21/17	Sheet 11		of 14



		2 Elizabeth Drive Chelmsford, MA 01824 PH: 1-800-ANALOGD	
Title ADSP-BF707 EZ-BOARD Push Buttons, LEDs and Reset			
Size C	Board No. A0450-2013	Rev 1.0B	
Date 1/21/17	Sheet 12 of 14		

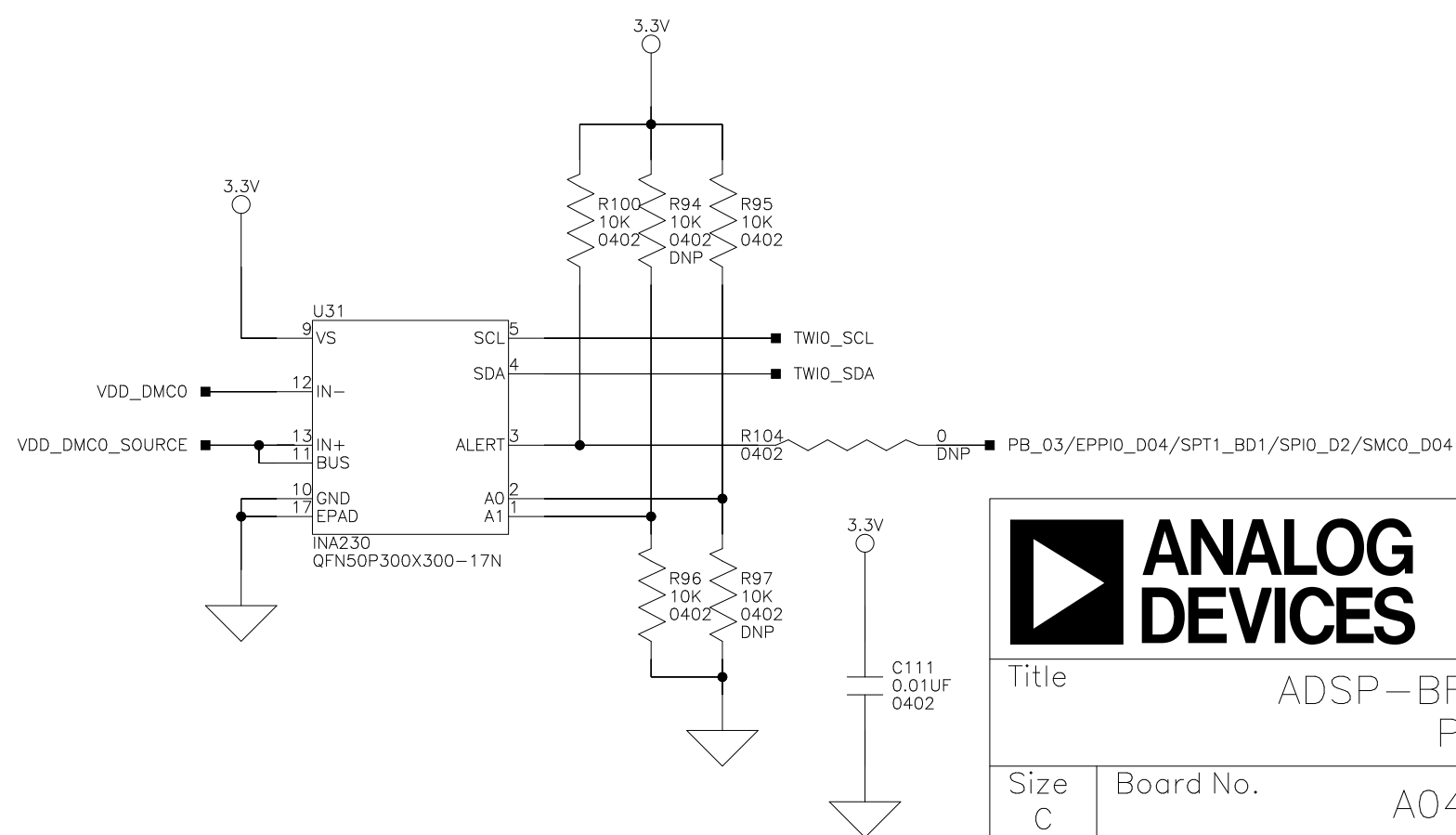
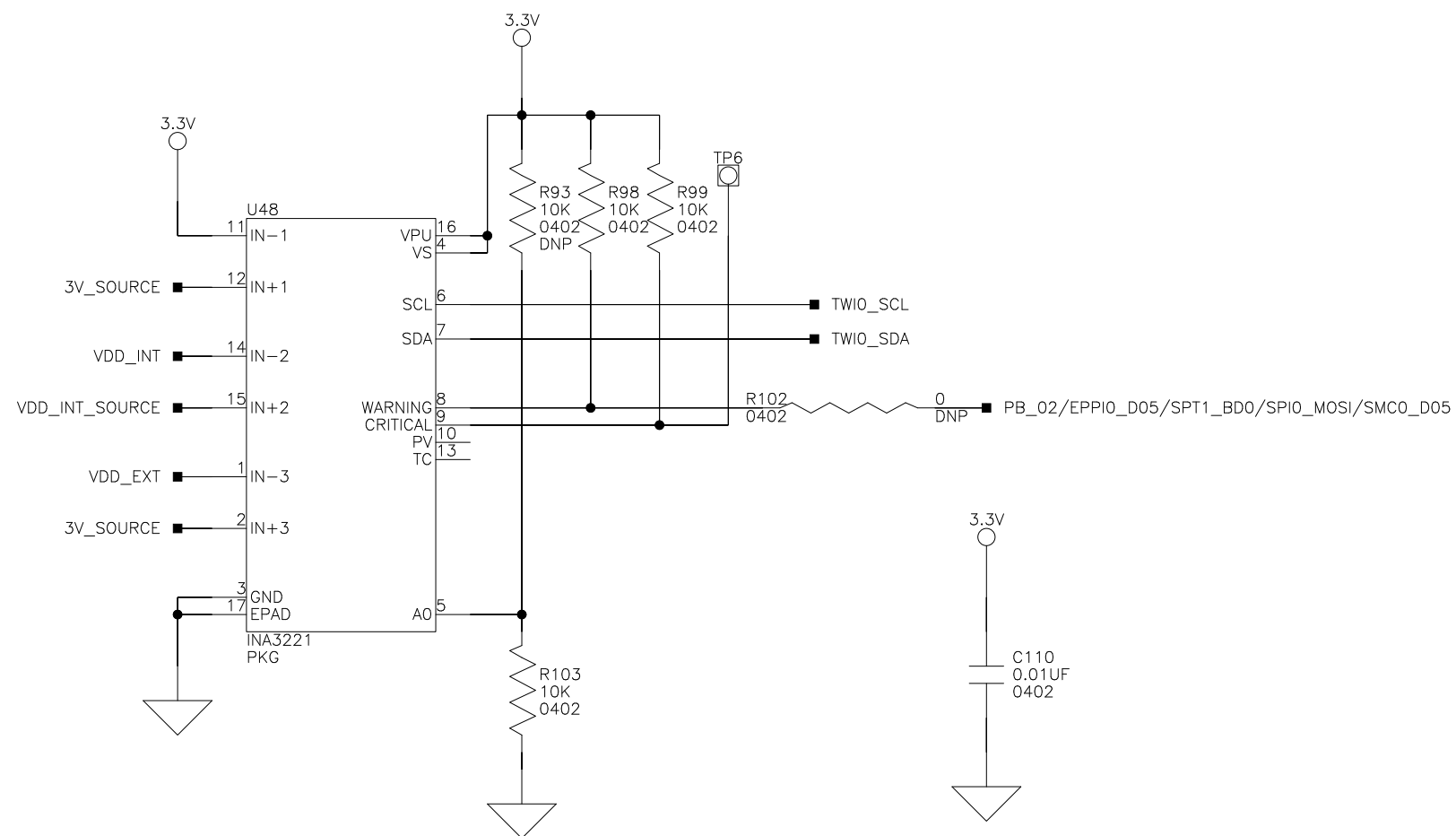


GND Test Points are scattered on PCB for Test Measurement Purposes.



TWI address 1000 000x
where x is the R/W bit. Read - 1, Write - 0

TWI address 1000 001x
where x is the R/W bit. Read - 1, Write - 0



		2 Elizabeth Drive Chelmsford, MA 01824 PH: 1-800-ANALOGD	
Title ADSP-BF707 EZ-BOARD Power			
Size C	Board No.	A0450-2013	Rev 1.0B
Date	1/21/17	Sheet 13	of 14