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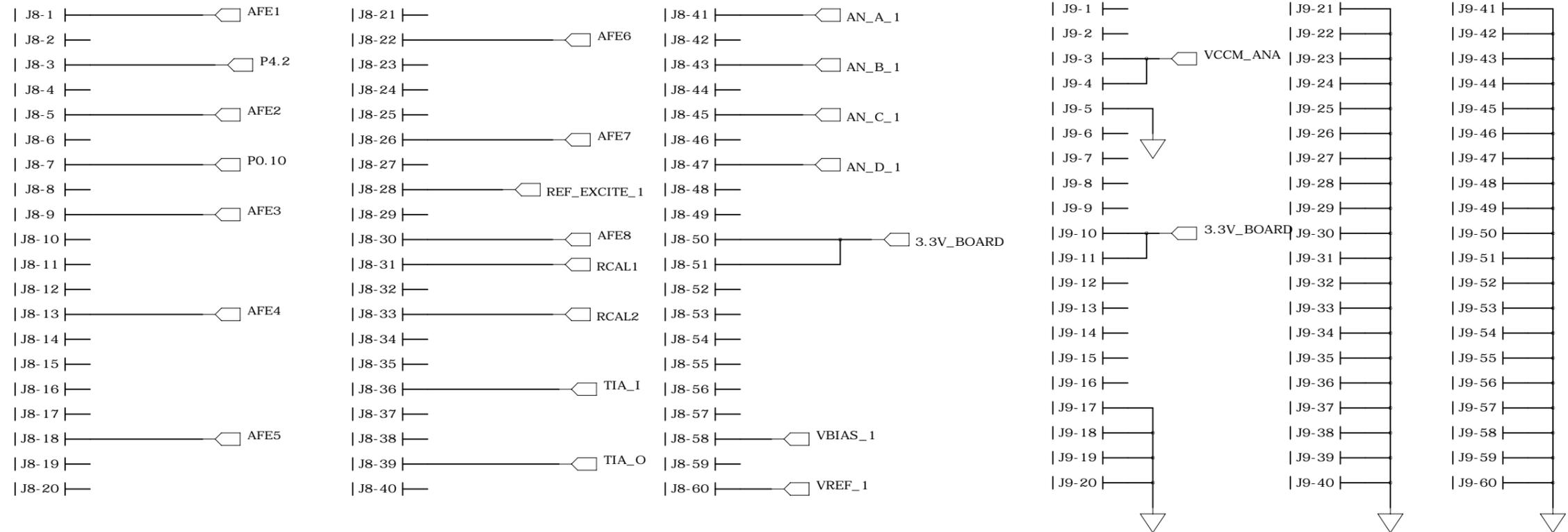
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REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

MotherBoard Connector - Allign J8 and J9.



COMPANY: **Analog Devices**

TITLE: **EV-ADuCM350-SMCZ**

DRAWN: E. Moloney	DATED: Nov. 2012	CODE:			SIZE:	DRAWING NO:	REV:
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QUALITY CONTROL: < QC By >	DATED: < QC Date >						
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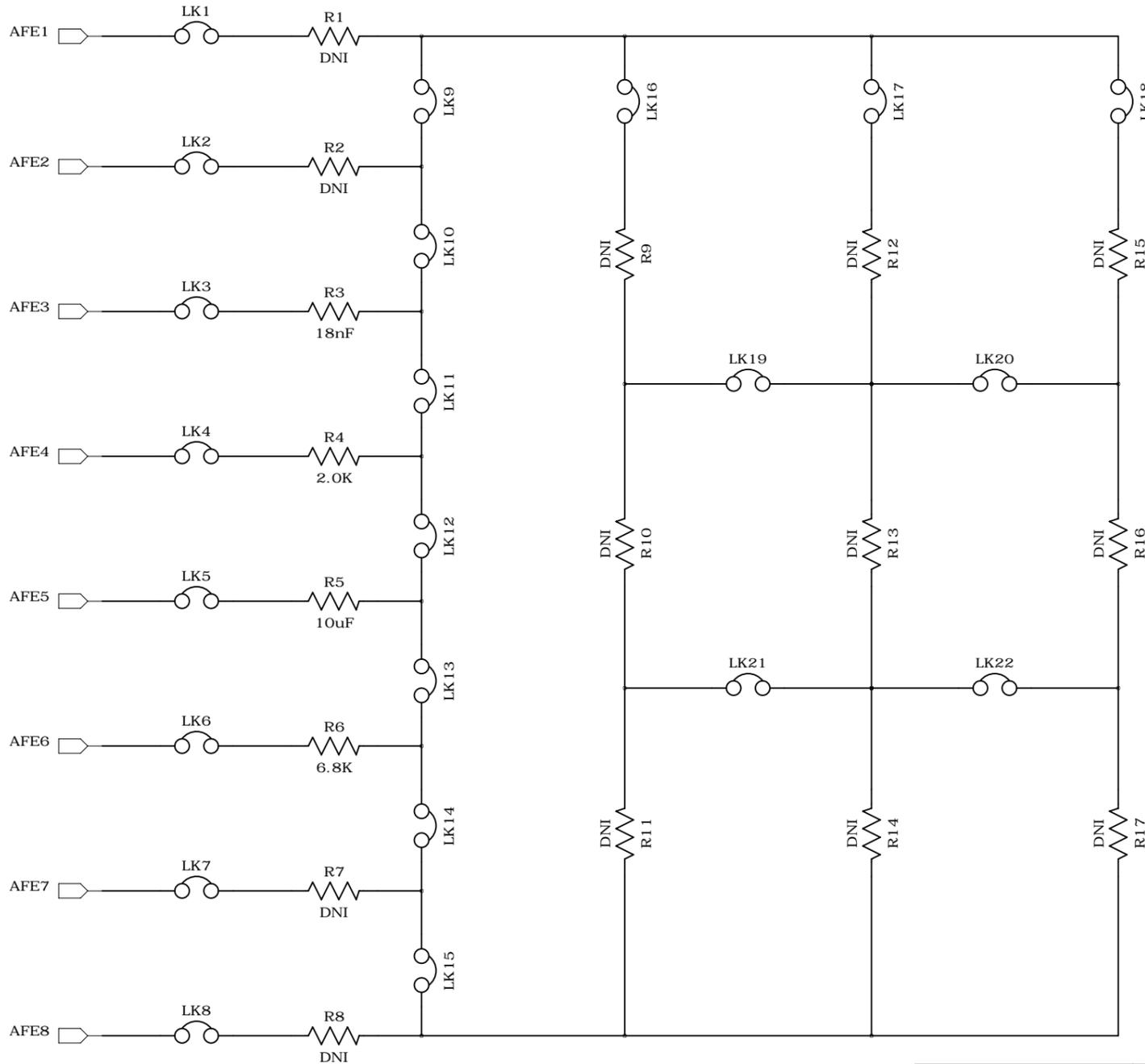
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1

Switch Matrix Connections

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:



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COMPANY: Analog Devices

TITLE: EV-ADuCM350-SMCZ

DRAWN: E. Moloney	DATED: Nov. 2012	CODE:		SIZE:	DRAWING NO:	REV:
CHECKED: < Checked By >	DATED: < Checked Date >	< Code >	A3	02-038363	A	SHEET: 2 OF 3
QUALITY CONTROL: < QC By >	DATED: < QC Date >					
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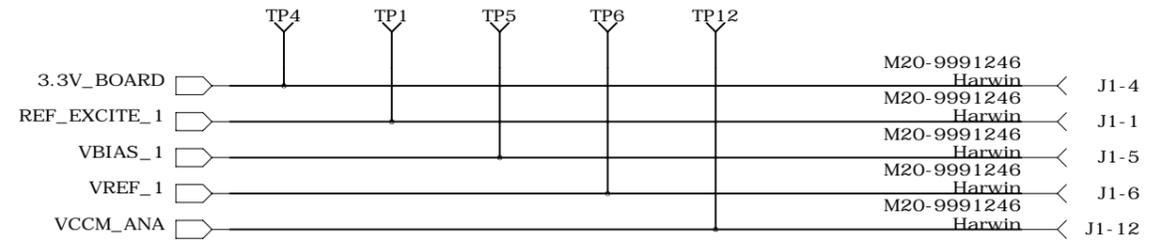
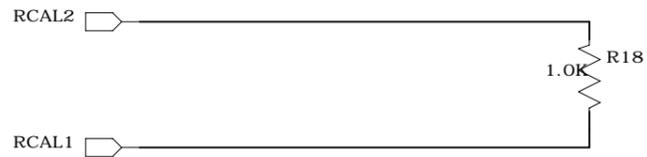
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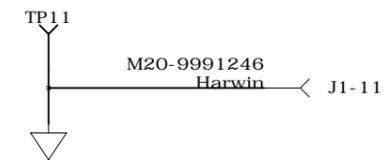
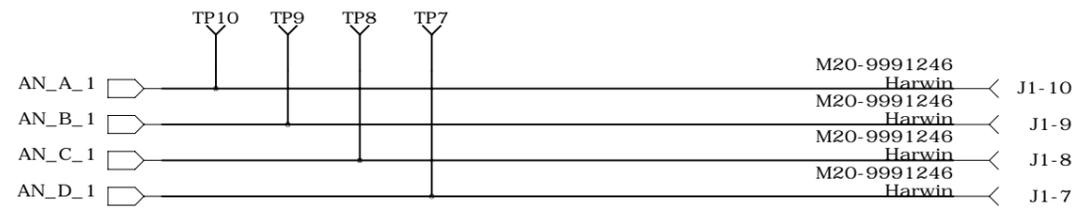
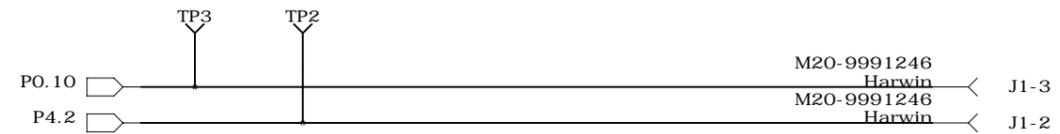
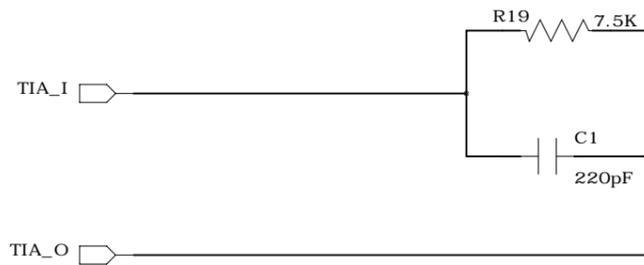
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REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

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COMPANY: **Analog Devices**

TITLE: **EV-ADuCM350-SMCZ**

DRAWN: E. Moloney	DATED: Nov. 2012	CODE:	SIZE:	DRAWING NO:	REV:
CHECKED: < Checked By >	DATED: < Checked Date >	< Code >	A3	02-038363	A
QUALITY CONTROL: < QC By >	DATED: < QC Date >				
RELEASED: < Released By >	DATED: < Release Date >	SCALE: < Scale >			SHEET: 3 OF 3

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