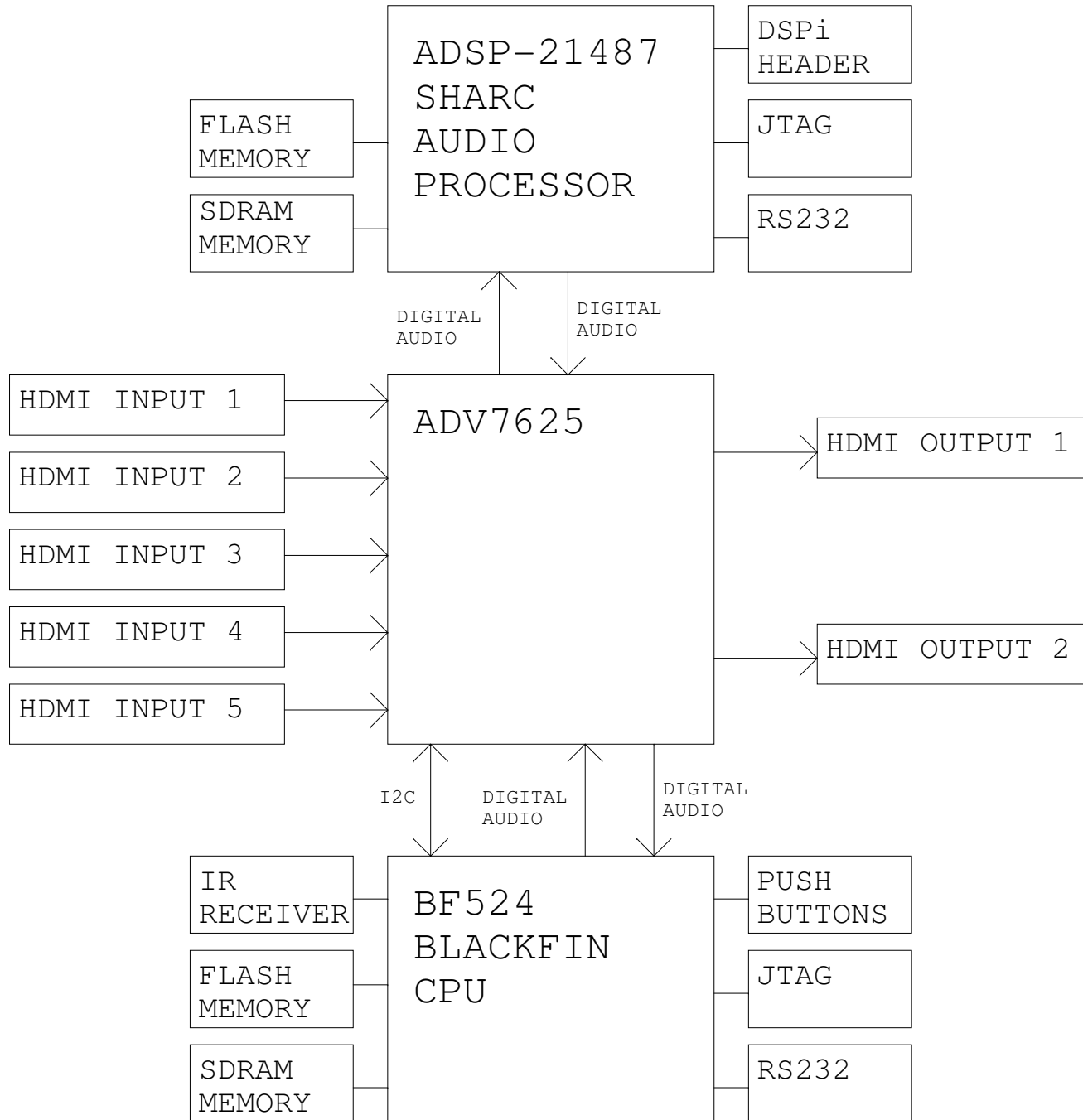


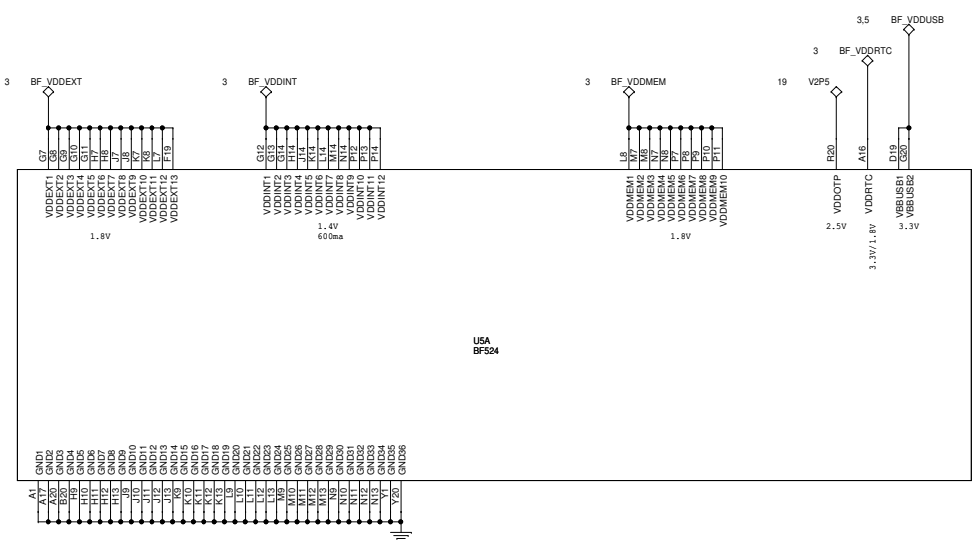
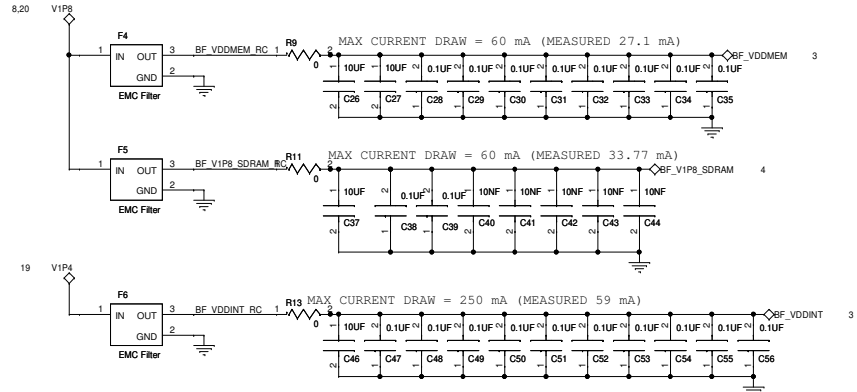
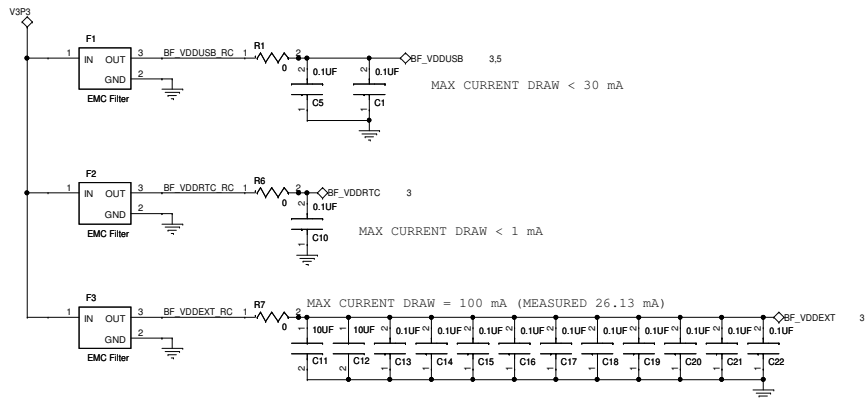
# Advantiv (TM) EVAL-ADV7625-SMZ Video Evaluation Board

## Change Log

Revision	Date	Description	Author
0.1	2012/07/16	Initial Release	MCP
0.1	2014/01/20	Public Release. Removed unnecessary notes from the schematic.	MCP

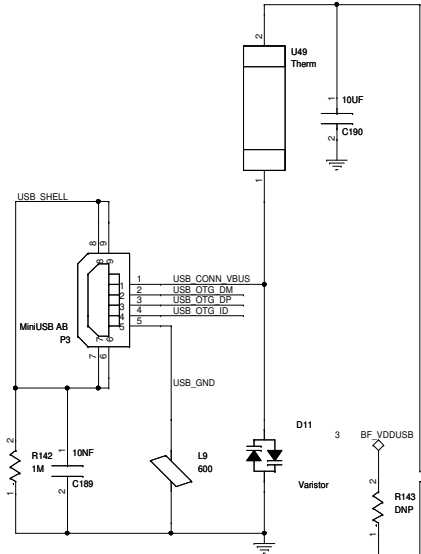
DIG AUDIO I/F  
(EZ-EXTENDER)



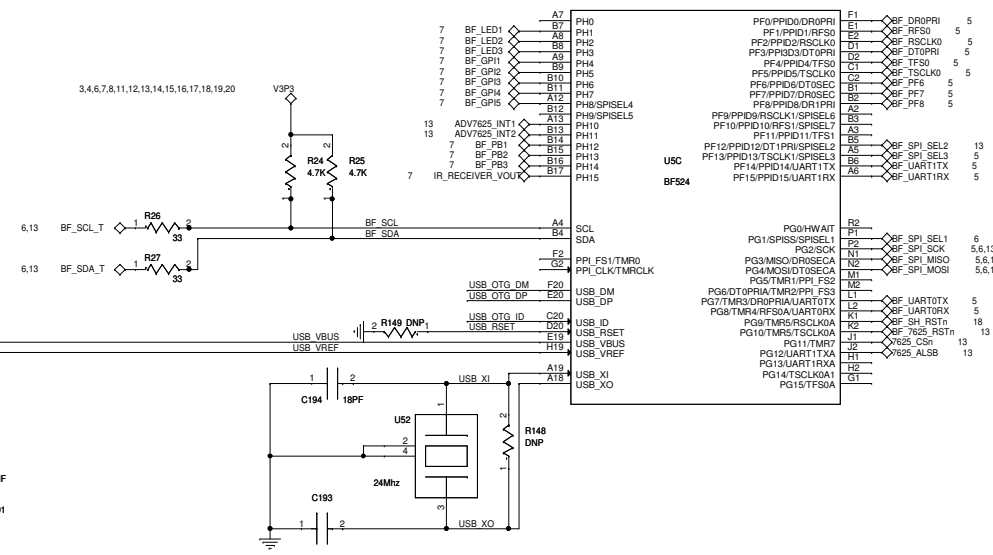
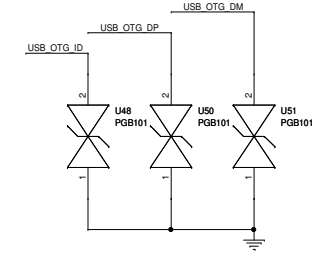


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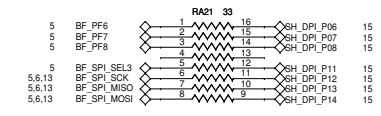
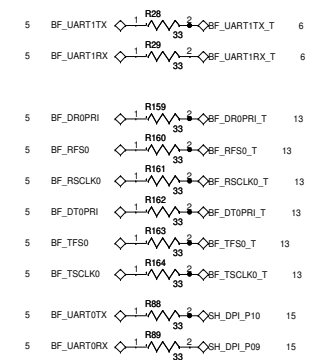


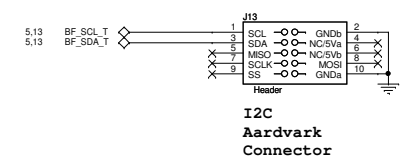
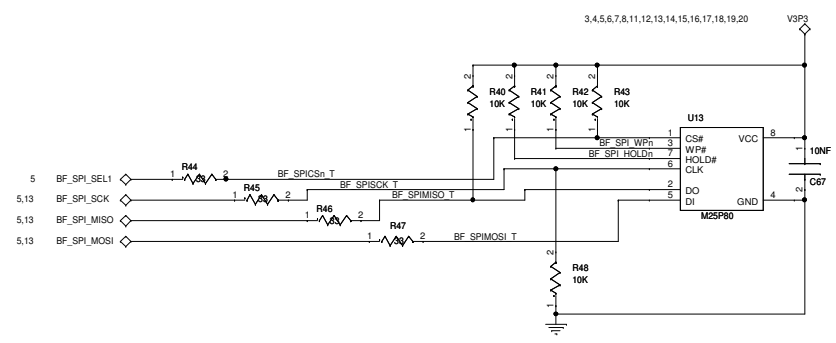
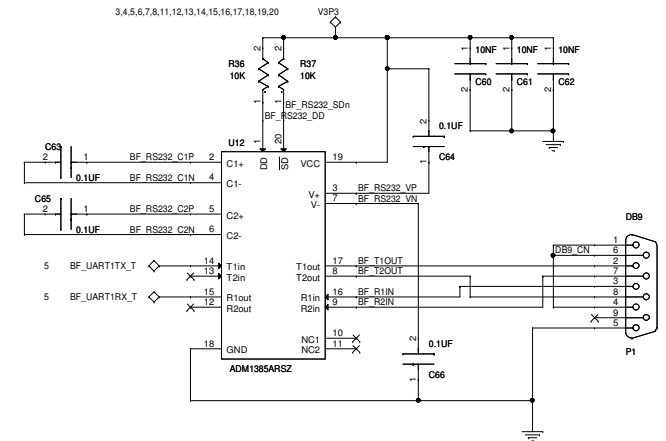
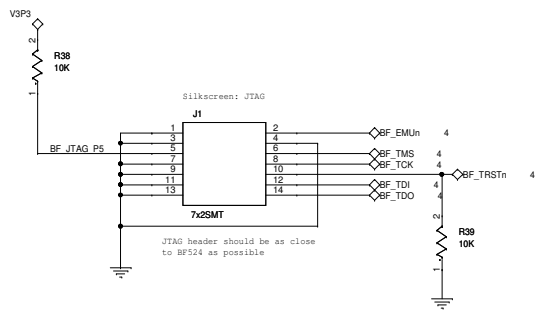


USB\_OTG\_DM and USB\_OTG\_DP are differential pairs with 90 ohm impedance and should be as short as possible

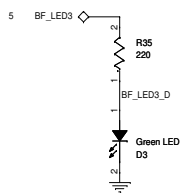
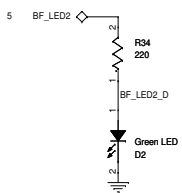
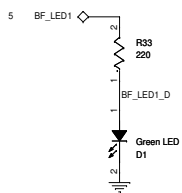
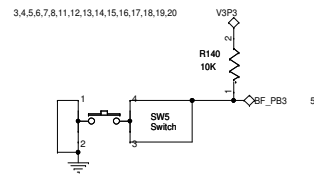
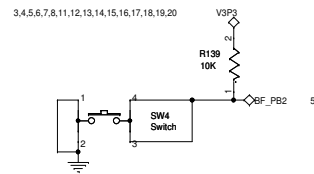
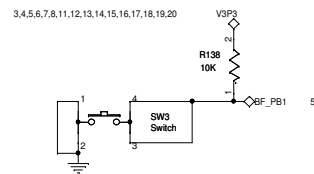
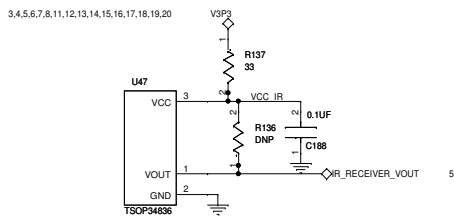
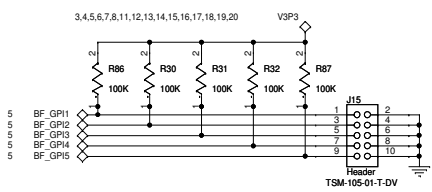


7	BF_LED1	A7	PH0	PF0/PPID0/DR0PRI	E1	BF_DR0PRI	5
7	BF_LED2	A8	PH1	PF1/PPID1/RFS0	E2	BF_RFS0	5
7	BF_LED3	A9	PH2	PF2/PPID2/RCLK0	D1	BF_RSCLK0	5
7	BF_GPI1	A9	PH3	PF3/PPID3/DT0PRI	D2	BF_DT0PRI	5
7	BF_GPI2	B9	PH4	PF4/PPID4/TF50	C1	BF_TF50	5
7	BF_GPI3	B10	PH5	PF5/PPID5/TSCLK0	C2	BF_TSCLK0	5
7	BF_GPI4	B11	PH6	PF6/PPID6/DT0SEC	B1	BF_FF6	5
7	BF_GPI5	B11	PH6	PF7/PPID7/DR0SEC	B2	BF_FF7	5
7	BF_GPI5	B12	PH7	PF8/PPID8/D11PRI	A2	BF_FF8	5
		B12	PH8/SPISEL4	PF9/PPID9/RSCLK1/SPISEL5	B3		
		B13	PH10	PF10/PPID10/RFS1/SPISEL7	A3		
		B14	PH11	PF11/PPID11/TF51	D5		
		B15	PH12	PF12/PPID12/DT1PRI/SPISEL2	A5	BF_SPI_SEL2	13
		B16	PH13	PF13/PPID13/TSCLK1/SPISEL3	B5	BF_SPI_SEL3	5
		B17	PH14	PF14/PPID14/UART1TX	B6	BF_UART1TX	5
		B17	PH15	PF15/PPID15/UART1RX	A6	BF_UART1RX	5

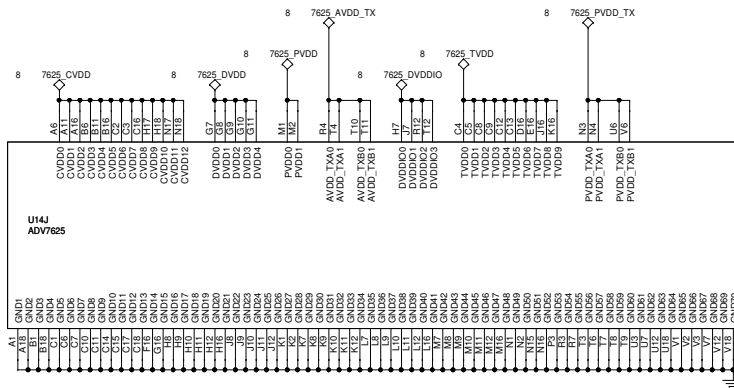
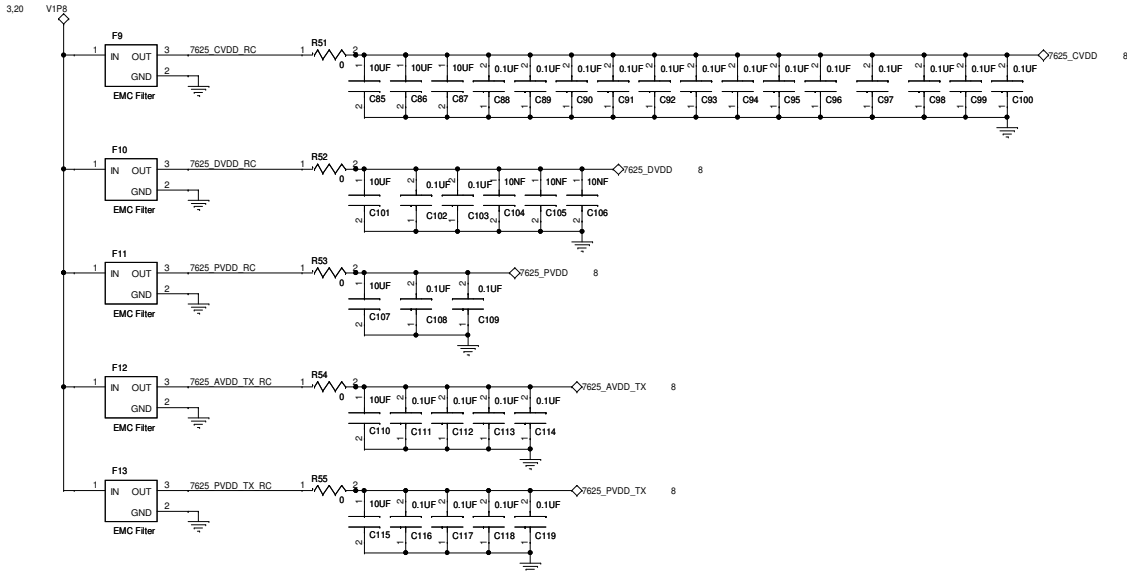
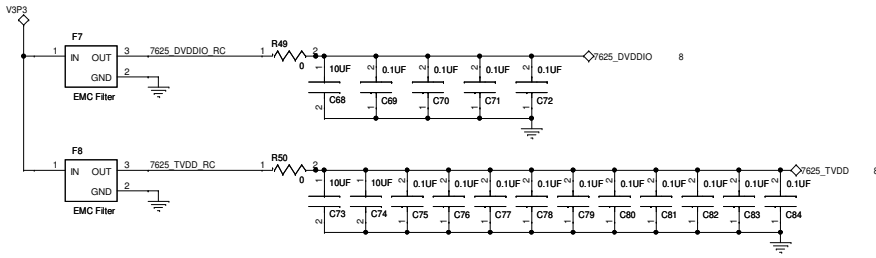




Title		
BF524 FLASH / JTAG / RS232		
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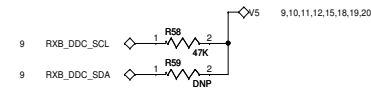
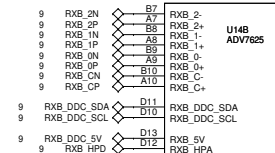
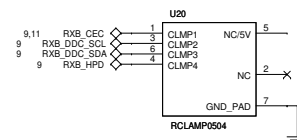
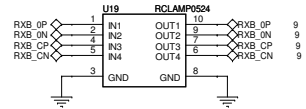
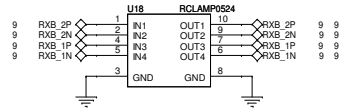
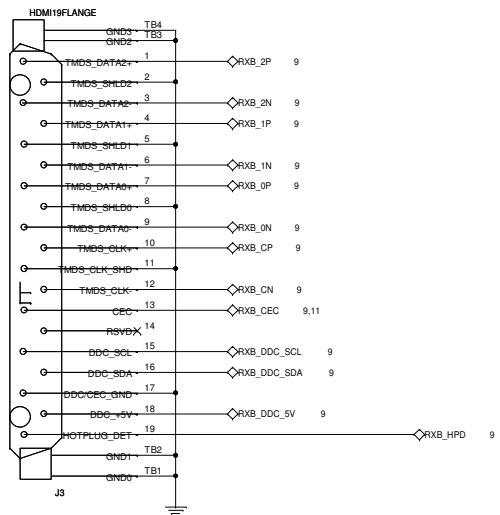
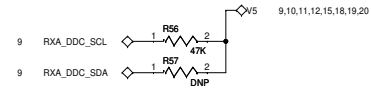
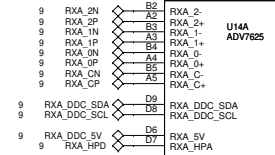
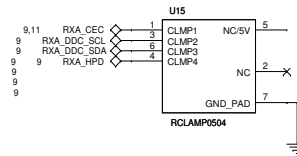
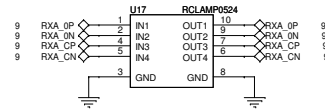
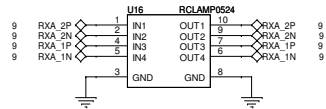
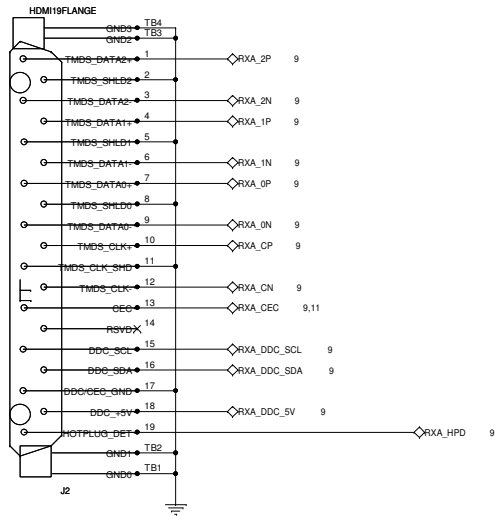


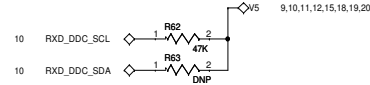
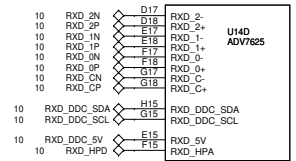
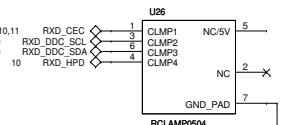
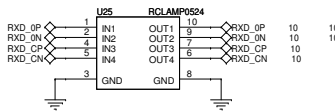
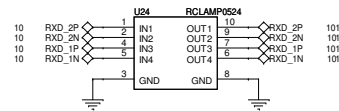
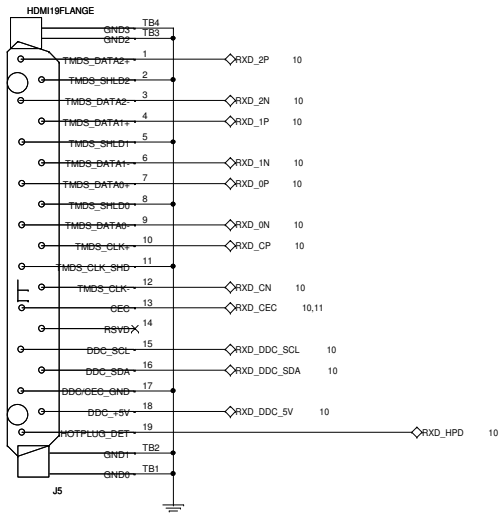
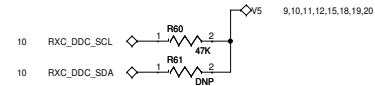
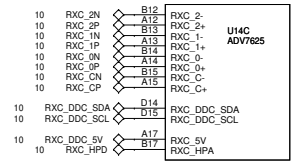
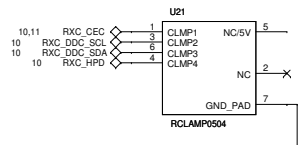
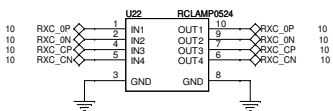
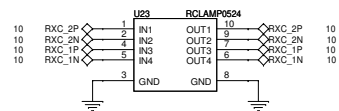
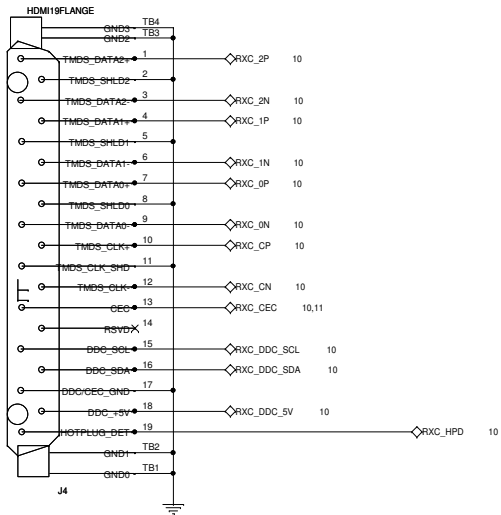
Title		
BF524 GP INPUTS / OUTPUTS		
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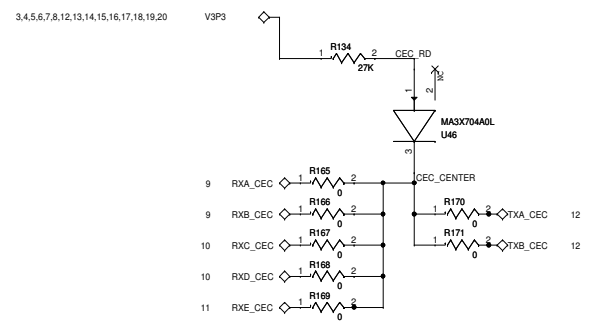
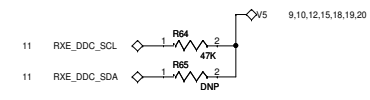
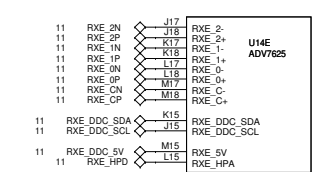
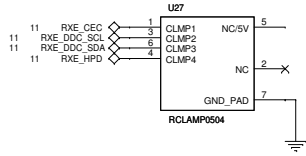
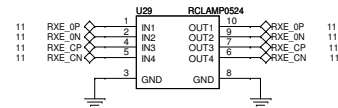
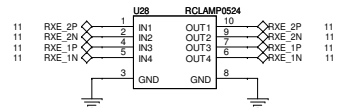
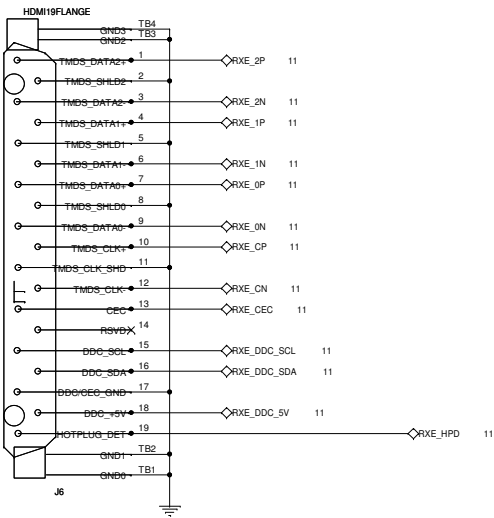


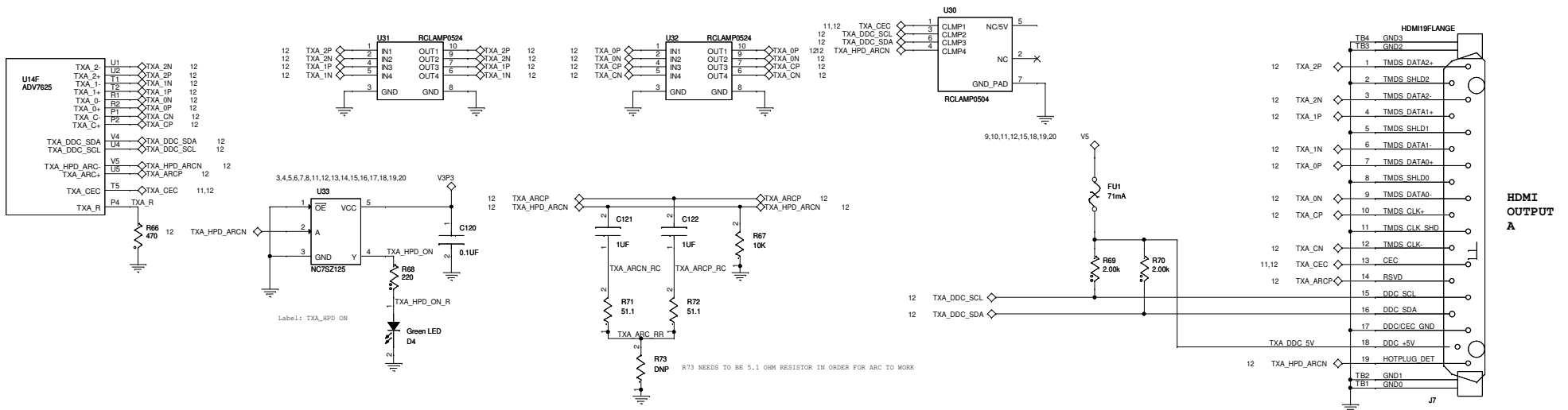
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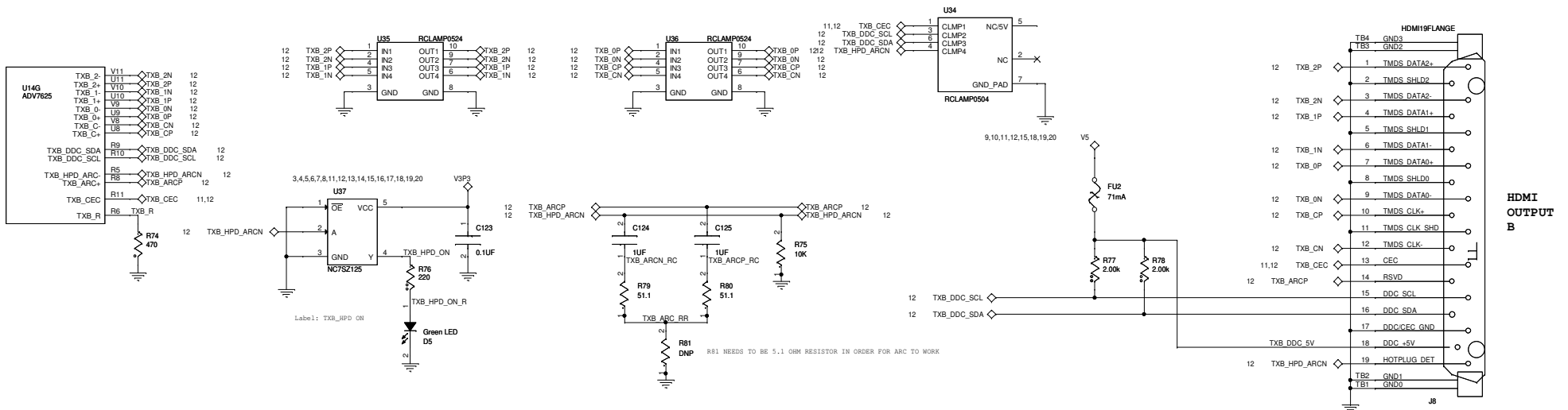






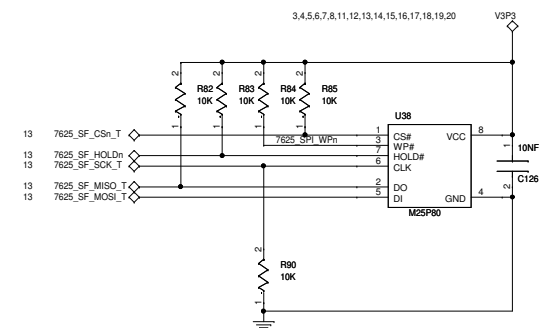
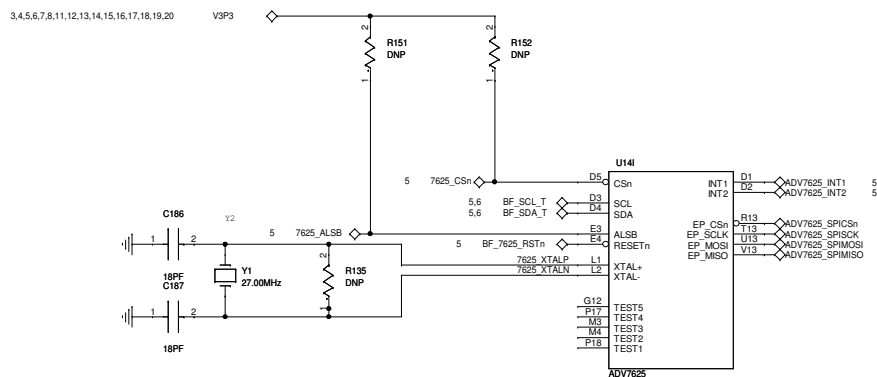
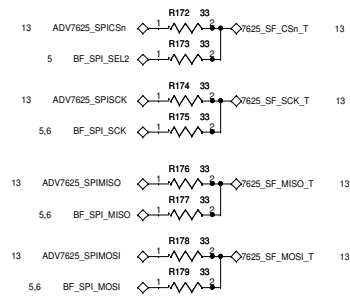
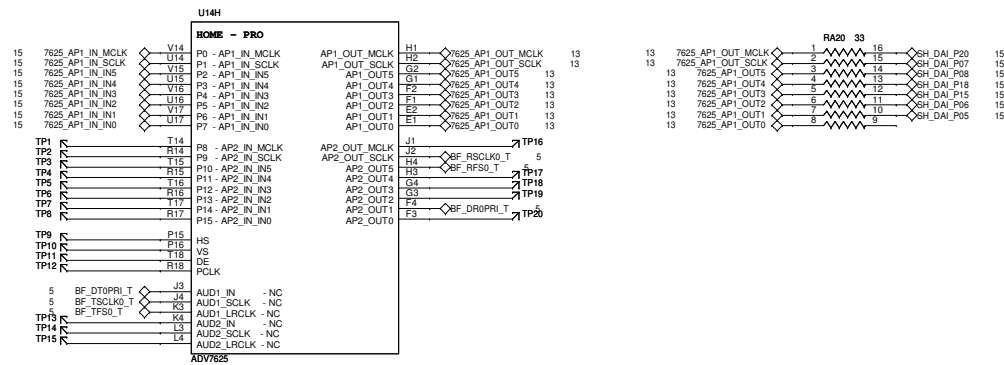


HDMI OUTPUT A



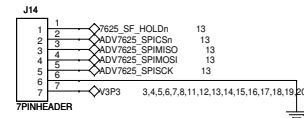
HDMI OUTPUT B

Title		
ADV7625 OUTPUTS A AND B		
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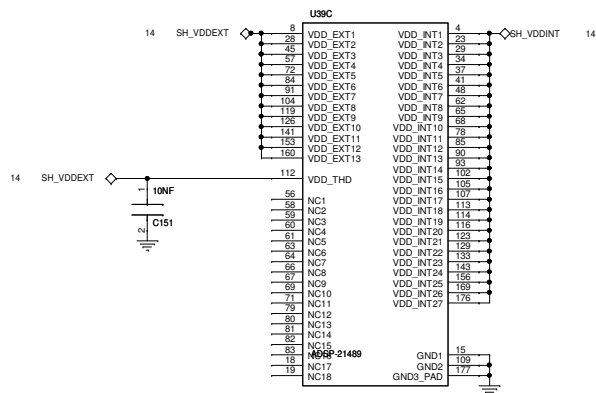
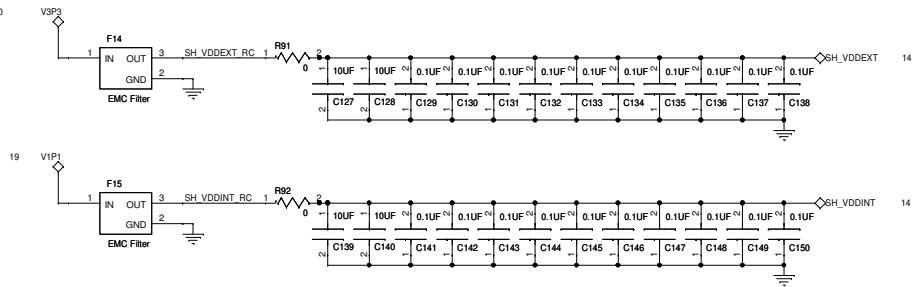


ADV7625 I2C ADDRESS:  
 ALSB Low : IO Map address is 0xB0 (default)  
 ALSB High : IO Map address is 0xB2

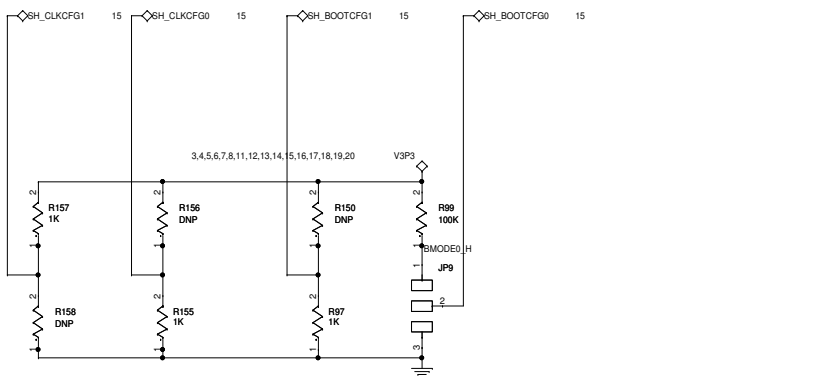
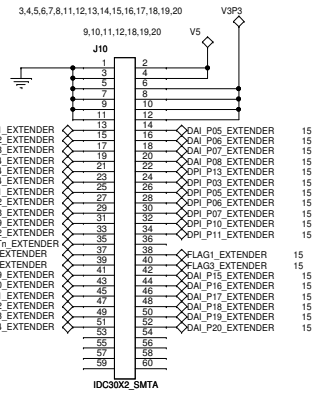
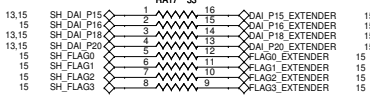
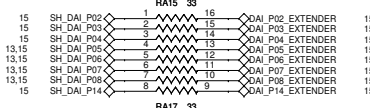
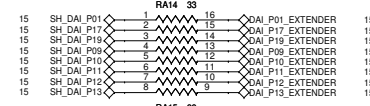
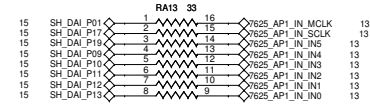
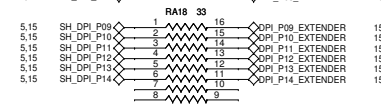
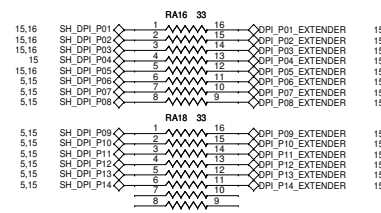
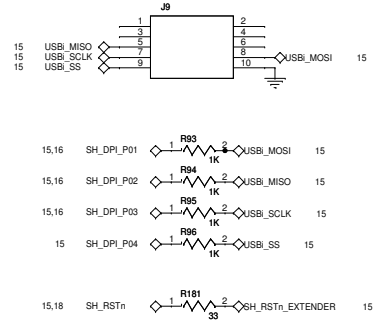
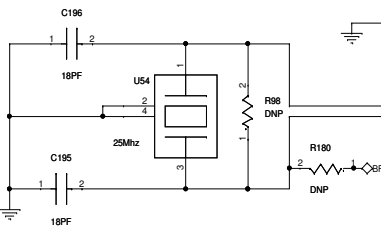
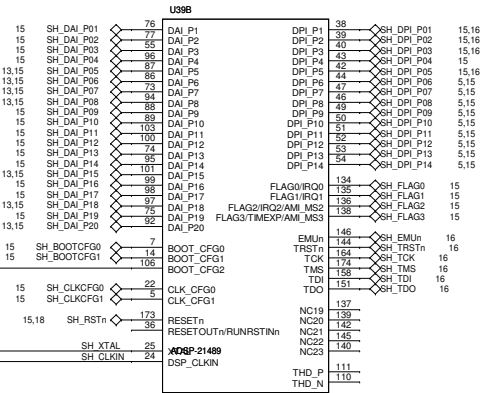
8003-STYLE MASTER SPI HEADER



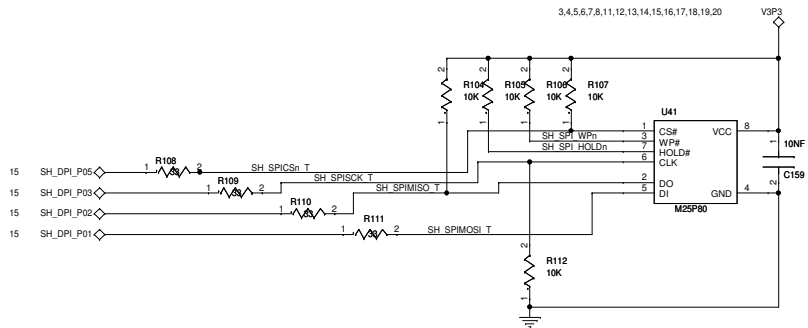
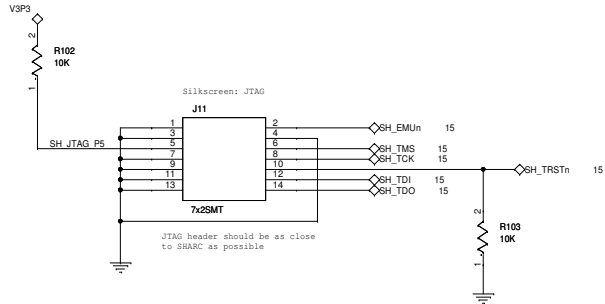
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SHARC POWER		
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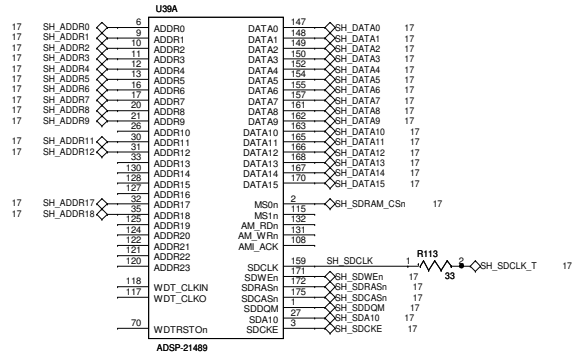


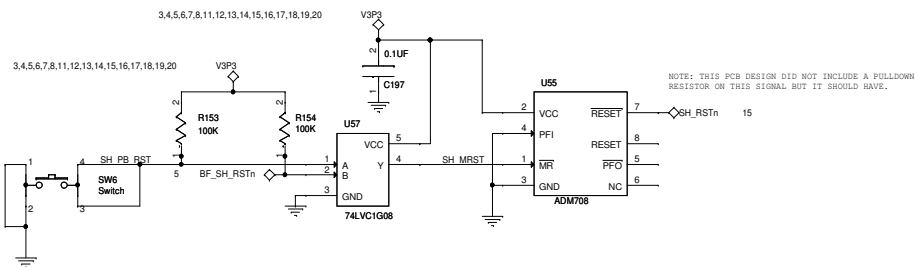
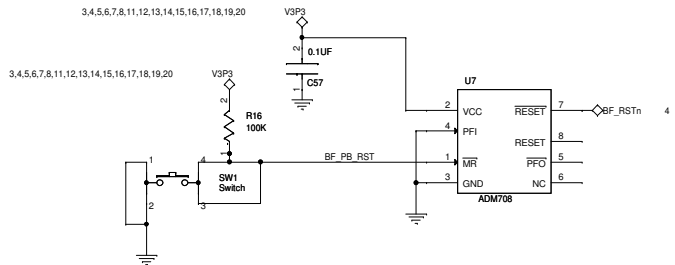
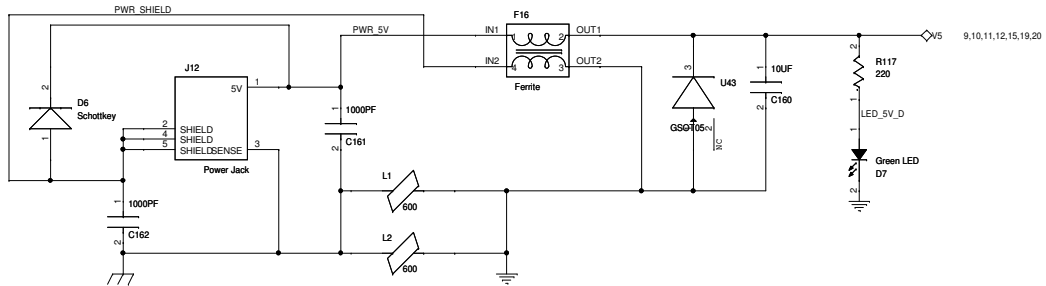
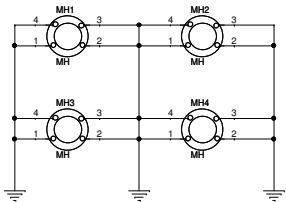
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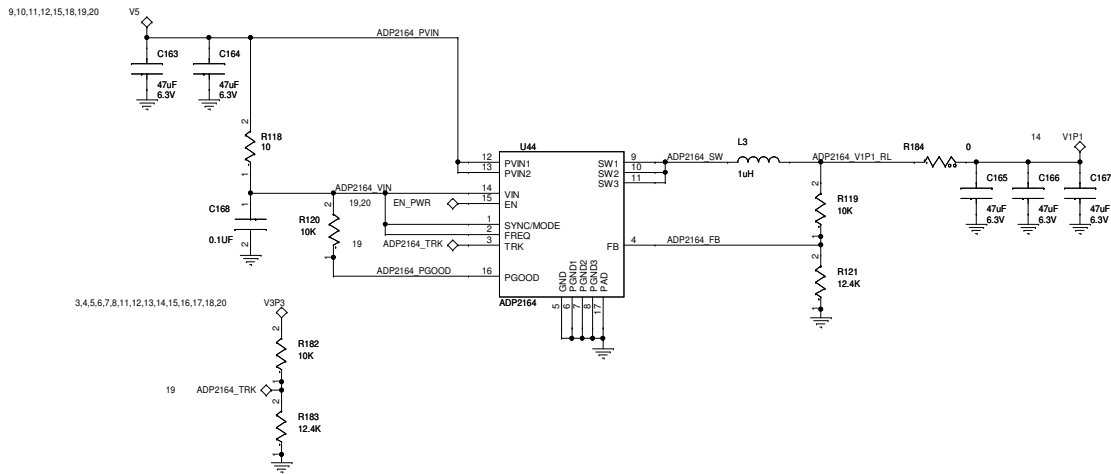
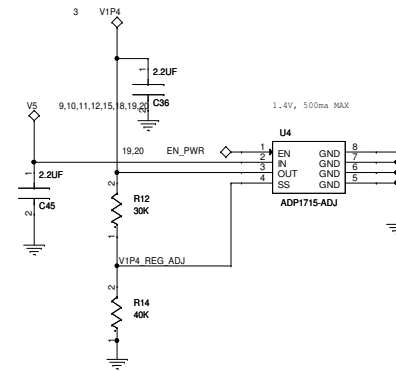
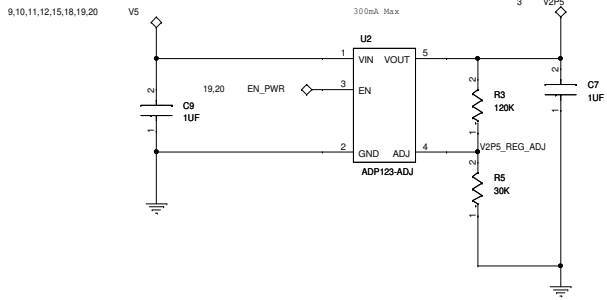
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SHARC FLASH / JTAG / RS232		
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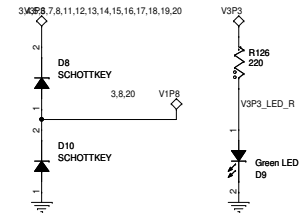
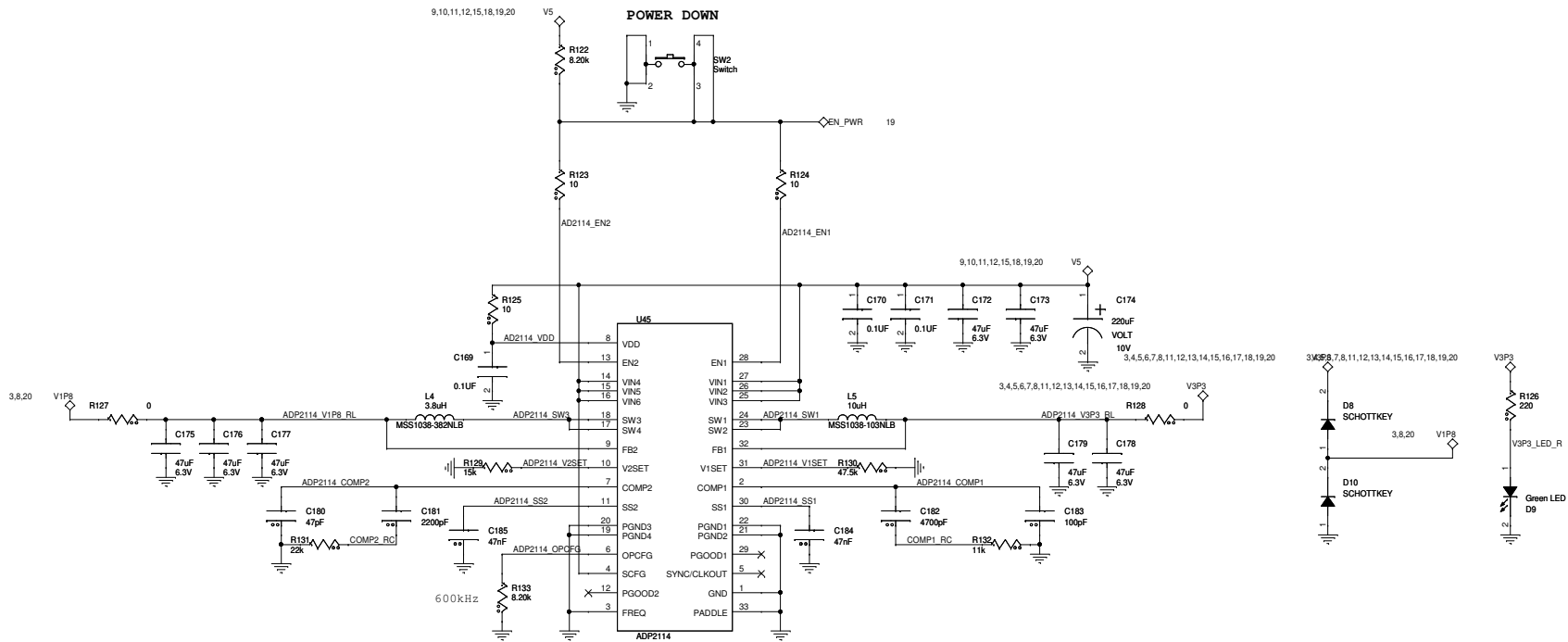






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BOARD POWER		
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