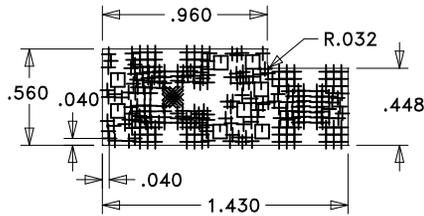


REVISION			
LTR	DESCRIPTION	DATE	APPROVED
A	INITIAL RELEASE	11/15/16	Y.AYDIN
B	PER ECR-066877	01/20/17	Y.AYDIN



044925

DRILL DRAWING

SIZE	QTY	SYM	PLATED	TOL
14	261	+	YES	+/-3
10	9	X	YES	FILLED +/-3
63	8	□	YES	+/-3

NOTES:  
UNLESS OTHERWISE SPECIFIED:

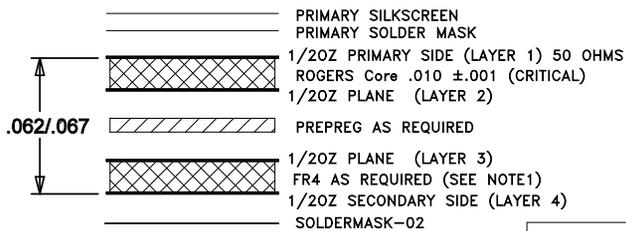
- MATERIAL: MULTILAYER. OVERALL STACKUP AS SHOWN. TYPE ROGERS 4350. HALF OUNCE COPPER BOTH SIDES. FR4 TO BE USED AS FILLER TO MEET CRITICAL OVERALL THICKNESS.
- FINISH: ENIG PER IPC-4552.
- PLATED THRU HOLES: .001 MINIMUM WALL THICKNESS.
- HOLE SIZES AND POSITIONS PER ARTWORK AND/OR DRILL FILE.
- ALL HOLES TO BE LOCATED WITHIN ±.003 OF THE CENTER OF THE PAD OR OTHER TRUE POSITION.
- FRONT TO BACK REGISTRATION ±.003 MAX.
- BOARD WARPAGE: <.010 PER LINEAR INCH.
- SILKSCREEN TOPSIDE ONLY WITH WHITE EPOXY INK.
- SOLDERMASK: LPI SOLDERMASK BOTH SIDES. COLOR: GREEN REGISTRATION: ±.004 MAX. SOLDERMASK THICKNESS ON THE TOP OF METAL MUST BE 0.00025 (6um) MIN., AND 0.0008 (20um) MAX.
- "SIZE" IN DRILL LEGEND IS IN MILS AND REFERS TO FINISHED HOLE SIZE.
- MANUFACTURE PER IPC-6012, CLASS 2.

SPECIAL REQUIREMENTS:

- LAYER 1 CRITICAL LINE WIDTH = .018 ±.001 ADJUST PROCESS TO ACHIEVE WIDTH.
- ALL .010 VIAS TO BE FILLED WITH NON-CONDUCTIVE VIA FILL AND OVERPLATED WITH Cu BOTH SIDES.

VENDOR NOTES:

- VENDOR MAY ADD E-TEST STAMP TO PCB. VENDOR SHALL NOT ADD NAME, LOGO, DATE CODE, OR ANY OTHER MARKING TO ANY VISIBLE LAYER.
- BOARDS MUST PASS VISUAL INSPECTION PER IPC-A-600, CLASS 2.



LAYER STACKUP

UNLESS OTHERWISE SPECIFIED:	
DIMENSIONS ARE IN INCHES [mm]	
DRAWING PRACTICES PER ASME Y14.100	
INTERPRET DIMENSIONS & TOLERANCES PER ASME Y14.5-2009	
TOLERANCES:	
.XX	± .01
.XXX	± .005
.XXXX	± .0020
ANGLES	± .5 DEG.

DRAWN BY Y.AYDIN	DATE DRN 11/15/16
CHECKED BY	
ENGINEER M.BATI	
THIRD ANGLE PROJECTION	
DO NOT SCALE DRAWING	



PROPRIETARY TO ANALOG DEVICES  
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Fax: 978-250-3373

TITLE FABRICATION HMC425ALP3E EVAL Z			
SIZE A	CODE ID NO. 1CN88	DWG NO. 09-044925	REV B
SCALE:1:1		WT:	SHEET:1 OF 1

NEXT ASSY	USED ON
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