



PCB DESIGN CHECKLIST

Engineer:	K. BOYSON	Part Number:	600-0039-00
PCB Designer:	V LY	Revision:	2
MECH Designer:	—	Board Name:	AA, EDGE LAUNCH
Checker:	M. GURDINIS	Review Date:	
ECR Number:	QC120996	Charge Number:	2012618

Designer	Checker	N/A	Layout Verification		
<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>	Spacing and Connectivity	NO ERRORS FOUND 16/13 & 18/13	
<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>	Impedance Control Rules		
<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>	Power/GND Planes		
<input type="checkbox"/>	<input type="checkbox"/>	<input checked="" type="checkbox"/>	VIA types		
<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>	GND VIAS		
<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>	IC center GND slugs		
<input type="checkbox"/>	<input type="checkbox"/>	<input checked="" type="checkbox"/>	Printed Components		
<input type="checkbox"/>	<input type="checkbox"/>	<input checked="" type="checkbox"/>	Unused pads		
<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>	Soldermask		
<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>	Silkscreen		
<input type="checkbox"/>	<input type="checkbox"/>	<input checked="" type="checkbox"/>	Fiducials		
Mechanical					
<input type="checkbox"/>	<input type="checkbox"/>	<input checked="" type="checkbox"/>	Cover Wall		Width/keep out/soldermask size
<input type="checkbox"/>	<input type="checkbox"/>	<input checked="" type="checkbox"/>	Cover & Mounting holes	Sizes and locations	
<input type="checkbox"/>	<input type="checkbox"/>	<input checked="" type="checkbox"/>	Critical comp. locations	Connectors/Dimensions/Bottom layer clearances	
<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>	Keep-out areas	Verify keep outs for components, VIAS, GND pour, etc	
<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>	Solder-mask openings	Screws-washers/Bottom around connectors	
<input type="checkbox"/>	<input type="checkbox"/>	<input checked="" type="checkbox"/>	DXF Verification		
Fabrication					
<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>	Specify IPC standard	6012 CU2	
<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>	PCB Dimensions	Overall/cutouts/radius/angles/edge plating/coupons	
<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>	Layer stack-up detail	Via cross-section/Layers/Thickness as Critical Dimension	
<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>	Material, copper weight	Stack-up to match notes	
<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>	Finish plating req.	Multi-finish/Wire-bonding/OSP	
<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>	Special requirements	Impedance-line widths/Blind/Back drilled vias/Edge pltg	
<input type="checkbox"/>	<input type="checkbox"/>	<input checked="" type="checkbox"/>	VIA fill requirements	Shown in cross-section detail and/or in notes	
<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>	Minimum annular ring	VIAS and thru holes	
<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>	Hole Chart	Match drill files/Partials/Merge drill sizes/Tolerances	
Gerber, Drill Data					
<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>	Artwork, output files	Appropriate labels & RS-274X output format)	
<input type="checkbox"/>	<input type="checkbox"/>	<input checked="" type="checkbox"/>	Printed Components	Proper size shapes and gaps/Metal-01 layer 20	
<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>	Soldermask	Oversize 8 mils/POSITIVE Image-must be clearly labeled	
<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	Stencil/Paste-mask	Undersize 2 mils/omit printed components	
<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>	NCDrill	3 files for each type: .rep, .drl, .lst	
<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>	GC-Prevue file	Engineer check/Design Review	
<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>	ZIP file	Readme.txt/output files/fab pdf/IPC Netlist/aperture.rep/ASCII file/Component XY/GC-Prevue/copy of this checklist	
Assembly Dwg					
<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>	Ref Des readable		
<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>	Notes		
<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	Detail Views		