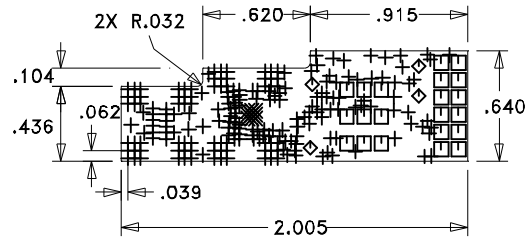


REVISION			
LTR	DESCRIPTION	DATE	APPROVED
1	ENGINEERING RELEASE	05/01/15	Y.AYDIN
2	LAYOUT UPDATED	04/01/16	Y.AYDIN



ANALOG DEVICES, INC.
600-01513-00-2
DRILL DRAWING

SIZE	QTY	SYM	PLATED	TOL
14	211	+	YES	+/-3
10	9	X	YES	FILLED +/-3
40	21	□	YES	+/-3
63	4	◇	YES	+/-3

NOTES: UNLESS OTHERWISE SPECIFIED:

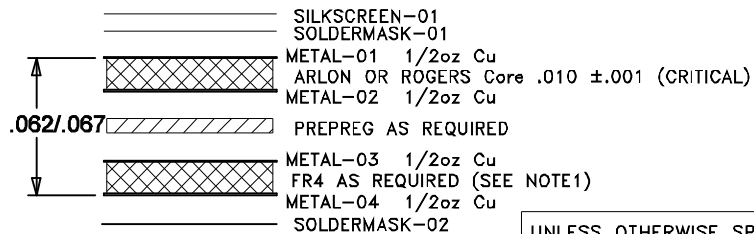
1. MATERIAL: MULTILAYER. OVERALL STACKUP AS SHOWN. TYPE ROGERS 4350 OR ARLON 25FR, HALF OUNCE COPPER BOTH SIDES, FR4 TO BE USED AS FILLER TO MEET CRITICAL OVERALL THICKNESS.
2. FINISH: ENIG PER IPC-4552.
3. PLATED THRU HOLES: .001 MINIMUM WALL THICKNESS.
4. HOLE SIZES AND POSITIONS PER ARTWORK AND/OR DRILL FILE.
5. ALL HOLES TO BE LOCATED WITHIN ± 0.003 OF THE CENTER OF THE PAD OR OTHER TRUE POSITION.
6. FRONT TO BACK REGISTRATION ± 0.003 MAX.
7. BOARD WARPAGE: < 0.010 PER LINEAR INCH.
8. SILKSCREEN TOPSIDE ONLY WITH WHITE EPOXY INK.
9. SOLDERMASK: LPI SOLDERMASK BOTH SIDES. COLOR: GREEN REGISTRATION: ± 0.004 MAX. SOLDERMASK THICKNESS ON THE TOP OF METAL MUST BE 0.00025 ($6\mu\text{m}$) MIN., AND 0.0008 ($20\mu\text{m}$) MAX.
10. "SIZE" IN DRILL LEGEND IS IN MILS AND REFERS TO FINISHED HOLE SIZE.
11. MANUFACTURE PER IPC-6012, CLASS 2.

SPECIAL REQUIREMENTS:

12. METAL-01 CRITICAL LINE WIDTH = $.018 \pm 0.001$ ADJUST PROCESS TO ACHIEVE WIDTH.
13. ALL .010 VIAS TO BE FILLED WITH NON-CONDUCTIVE VIA FILL AND OVERPLATED WITH Cu BOTH SIDES.

VENDOR NOTES:

14. VENDOR MAY ADD E-TEST STAMP TO PCB. VENDOR SHALL NOT ADD NAME, LOGO, DATE CODE, OR ANY OTHER MARKING TO ANY VISIBLE LAYER.
15. BOARDS MUST PASS VISUAL INSPECTION PER IPC-A-600, CLASS 2.



LAYER STACKUP

UNLESS OTHERWISE SPECIFIED:	
DIMENSIONS ARE IN INCHES [mm]	
DRAWING PRACTICES PER ASME Y14.100	
INTERPRET DIMENSIONS & TOLERANCES PER ASME Y14.5-2009	
TOLERANCES:	
.XX	$\pm .01$
.XXX	$\pm .005$
.XXXX	$\pm .0020$
ANGLES	$\pm .5$ DEG.

DRAWN BY Y.AYDIN	DATE DRN 05/01/15
CHECKED BY	
ENGINEER H.SAVCI	
THIRD ANGLE PROJECTION	

DO NOT SCALE DRAWING

PROPRIETARY TO ANALOG DEVICES



2 Elizabeth Drive
Chelmsford, MA 01824
978-250-3343
Fax: 978-250-3373

TITLE

PCB, EVAL

SIZE	CODE ID NO.	DWG NO.	REV
A	1CN88	600-01513-00	2
SCALE:1:1		WT:	SHEET:1 OF 1

NEXT ASSY

USED ON