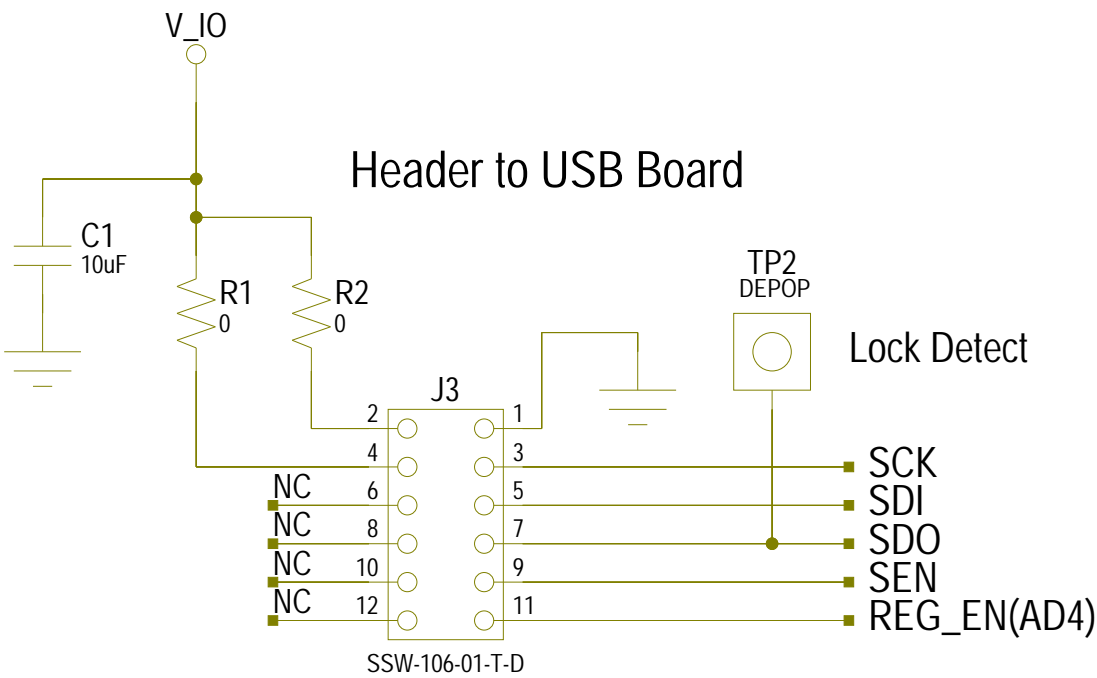



REVISIONS					
REV	ECN#	ZONE	DESCRIPTION	NAME	DATE
B	CP100519	-----	RELEASED PRODUCTION ECN CP100519	V. VADUVA	12/29/2010
C	CP121275	-----	CHANGE RELEASE PER CP121275	V. VADUVA	09/14/2012



HMC837 Fo & FO/2 Integrated PLL & VCO Eval



HITTITE MICROWAVE CORPORATION

20 Alpha Rd Chelmsford, MA 01824

TITLE

SCH, CUSTOMER EVALUATION PLL & VCO FO & FO/2

PROJECT

DRAWING #:

130-00034-00

SHEET

1

OF

2

DRAWN BY

D.YOUNG

DATE

11/08/10

CODE ID NO.

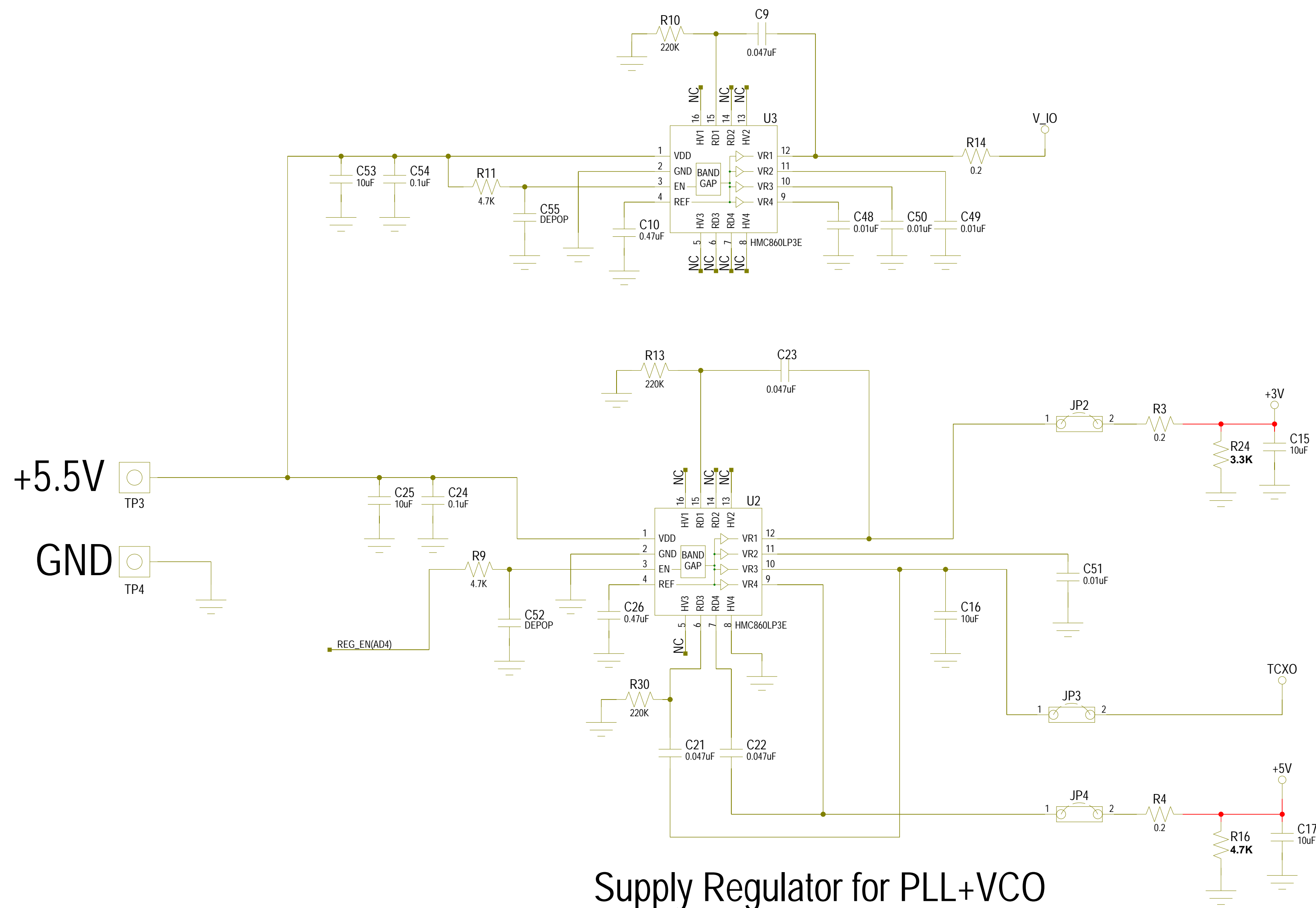
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SIZE

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REV

C



Supply Regulator for PLL+VCO

A recommended design practice is to connect the regulator Enable Pin #3 through a 4.7kOhm resistor to the system microcontroller/FPGA for power management control
Small series resistors required in VR1 and VR4 output paths, as shown