

D

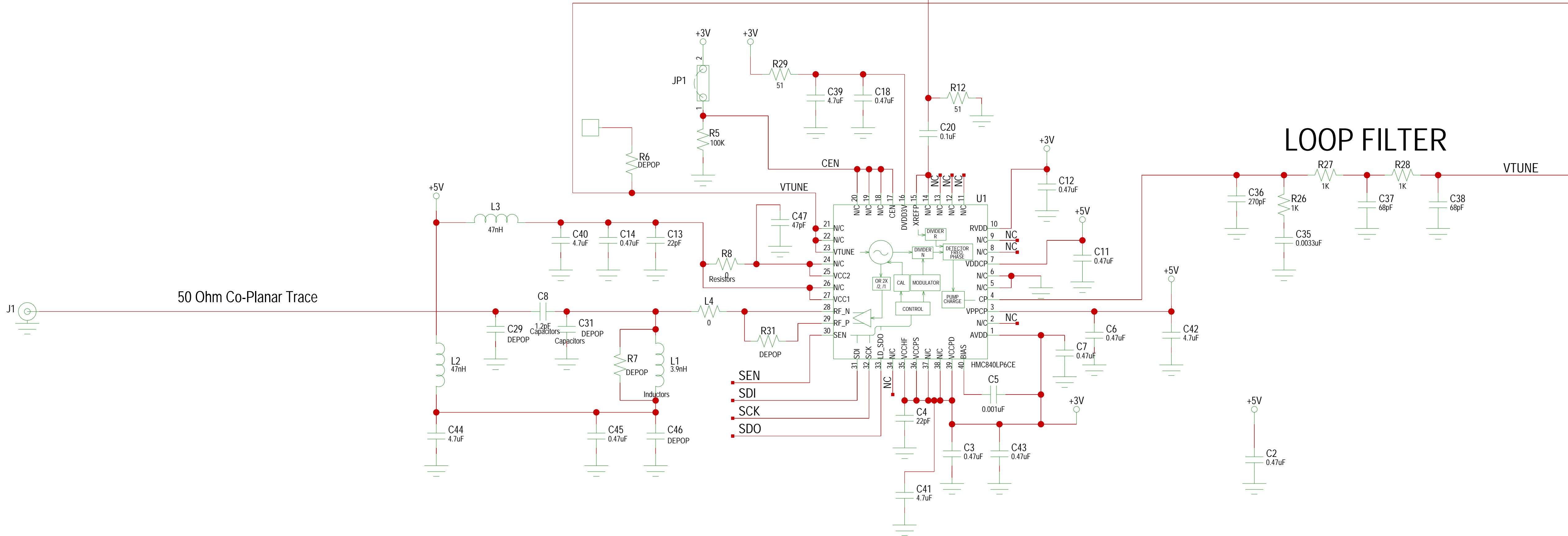
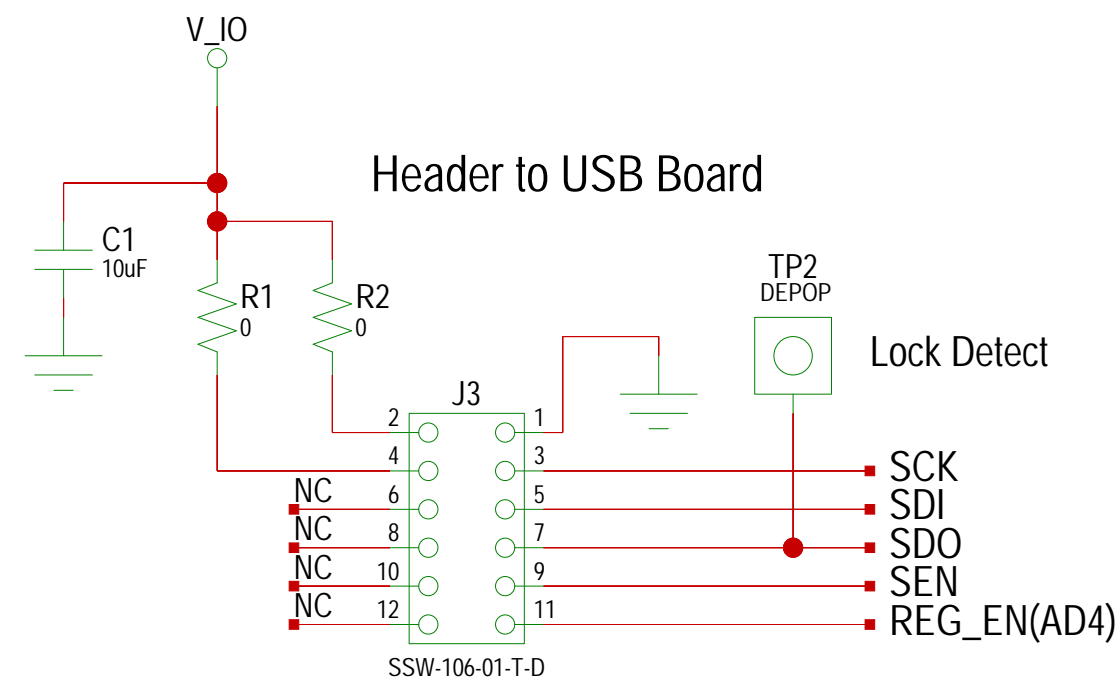
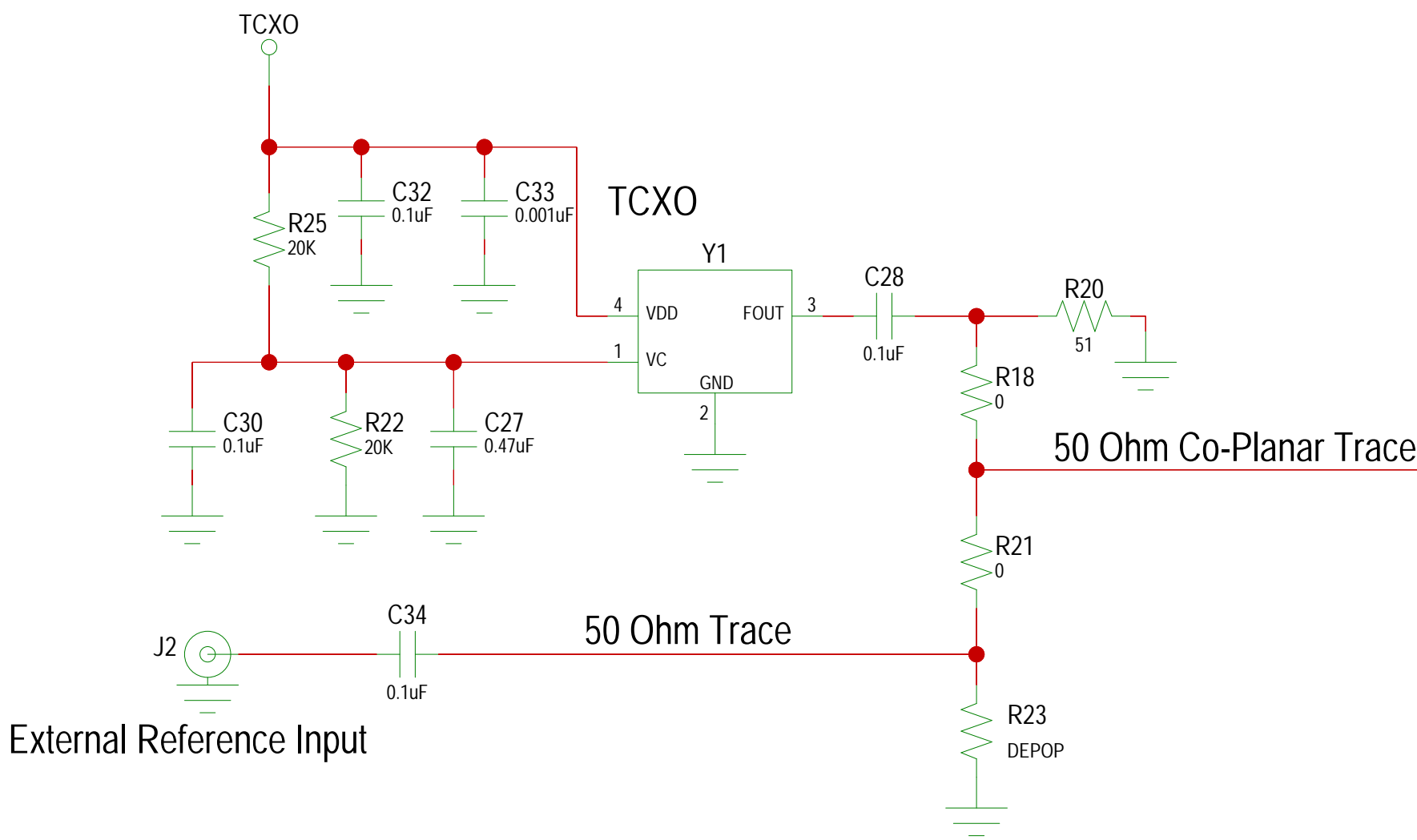
C

B

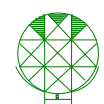
A

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REVISIONS					
REV	ECN#	ZONE	DESCRIPTION	NAME	DATE
C	CP121275	-----	CHANGE RELEASE PER CP121275	V. VADUVA	09/14/2012
D	CP140674	-----	CHANGE RELEASE PER CP140674	V. VADUVA	05/12/2014



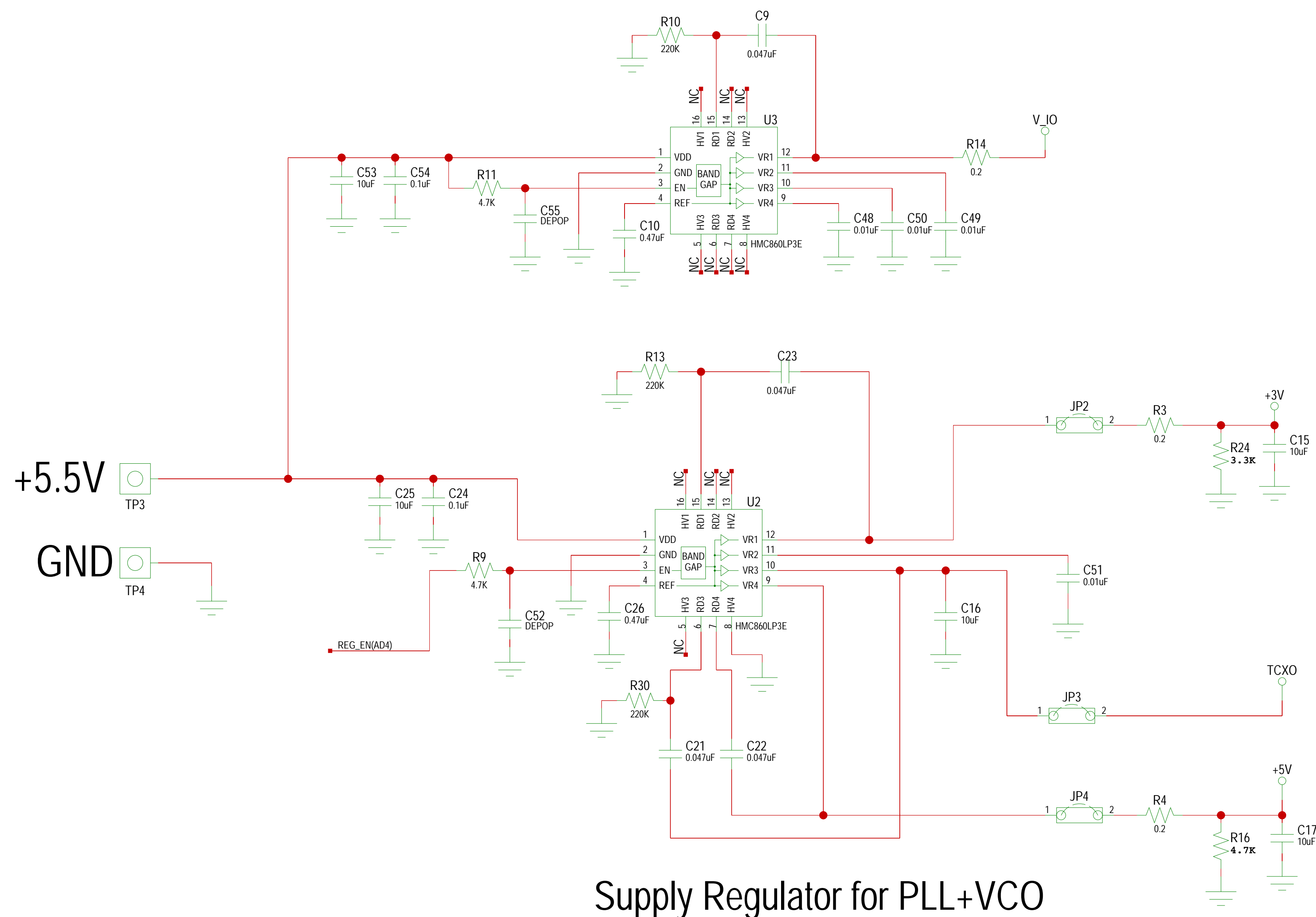
HMC840 Fo & FO/2 Integrated PLL & VCO Eval



HITTITE MICROWAVE CORPORATION
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TITLE
SCH, CUSTOMER EVALUATION PLL & VCO FO & FO/2

PROJECT		SHEET		1 OF 2	
DRAWING #:	129509	CODE ID NO.	1CN88	SIZE	D
DRAWN BY	D.YOUNG	DATE	11/08/10	REV	D



Supply Regulator for PLL+VCO

A recommended design practice is to connect the regulator Enable Pin #3 through a 4.7kOhm resistor to the system microcontroller/FPGA for power management control
Small series resistors required in VR1 and VR4 output paths, as shown