

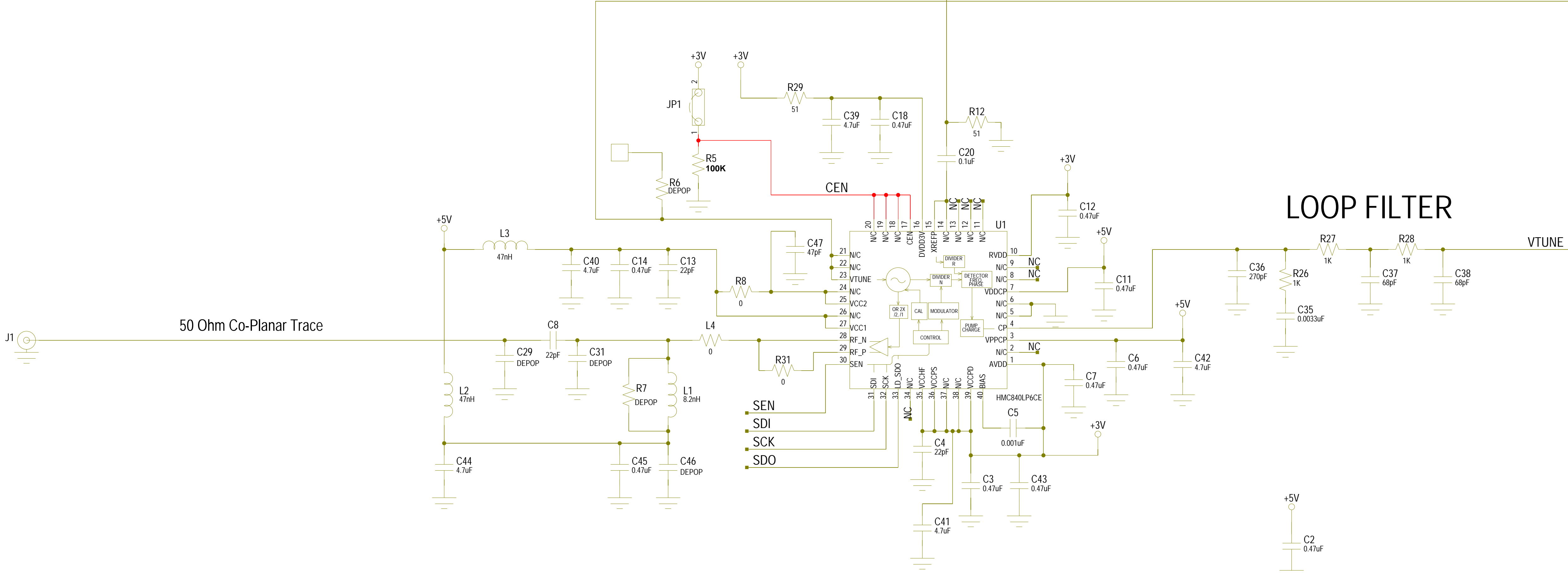
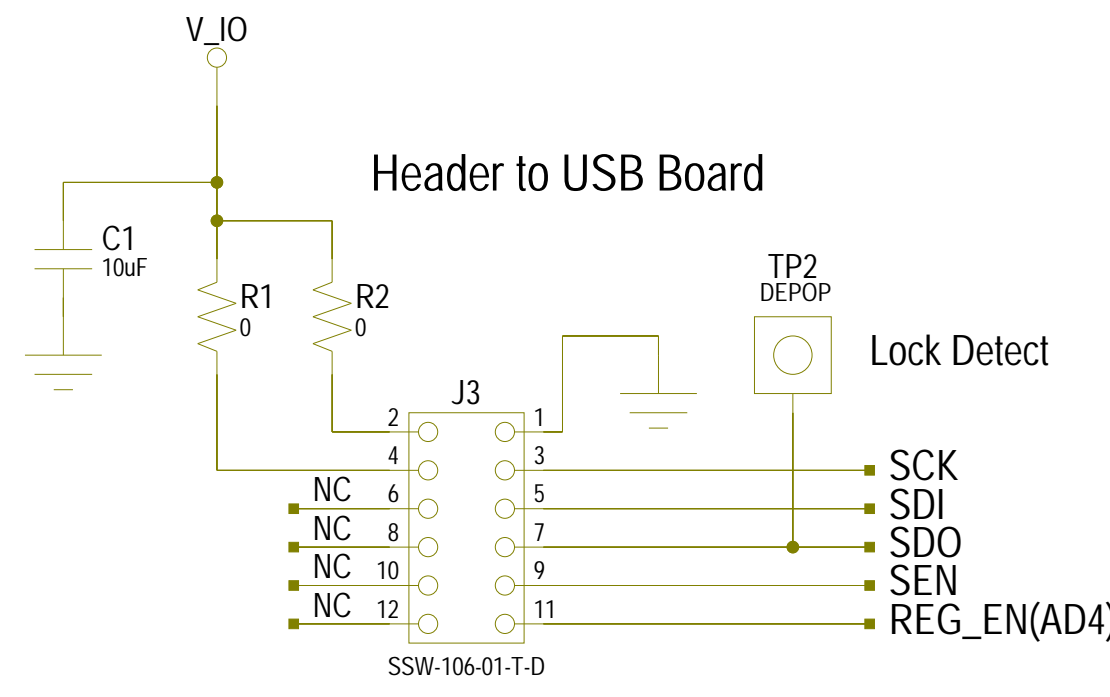
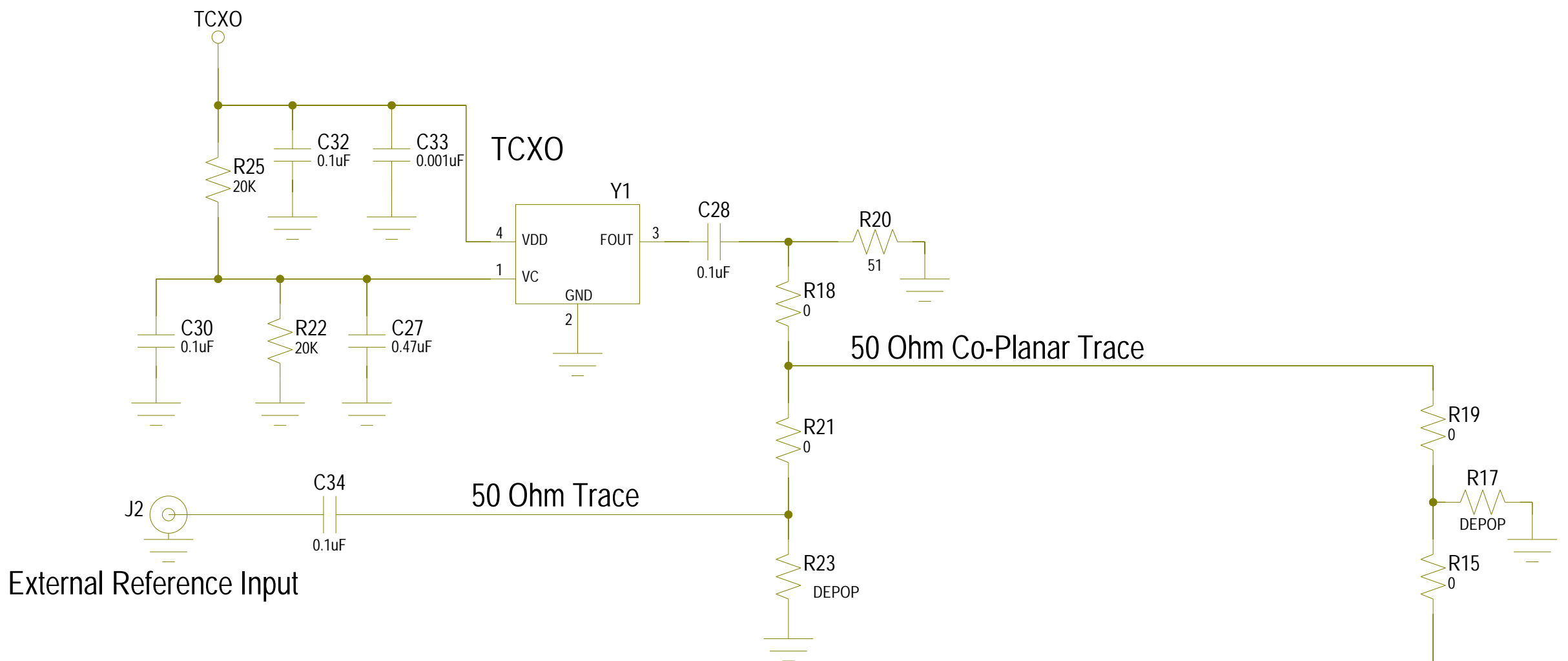
D

C

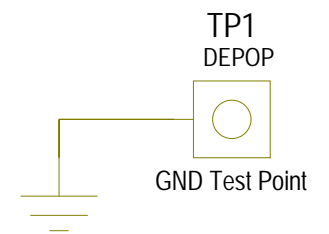
B


A

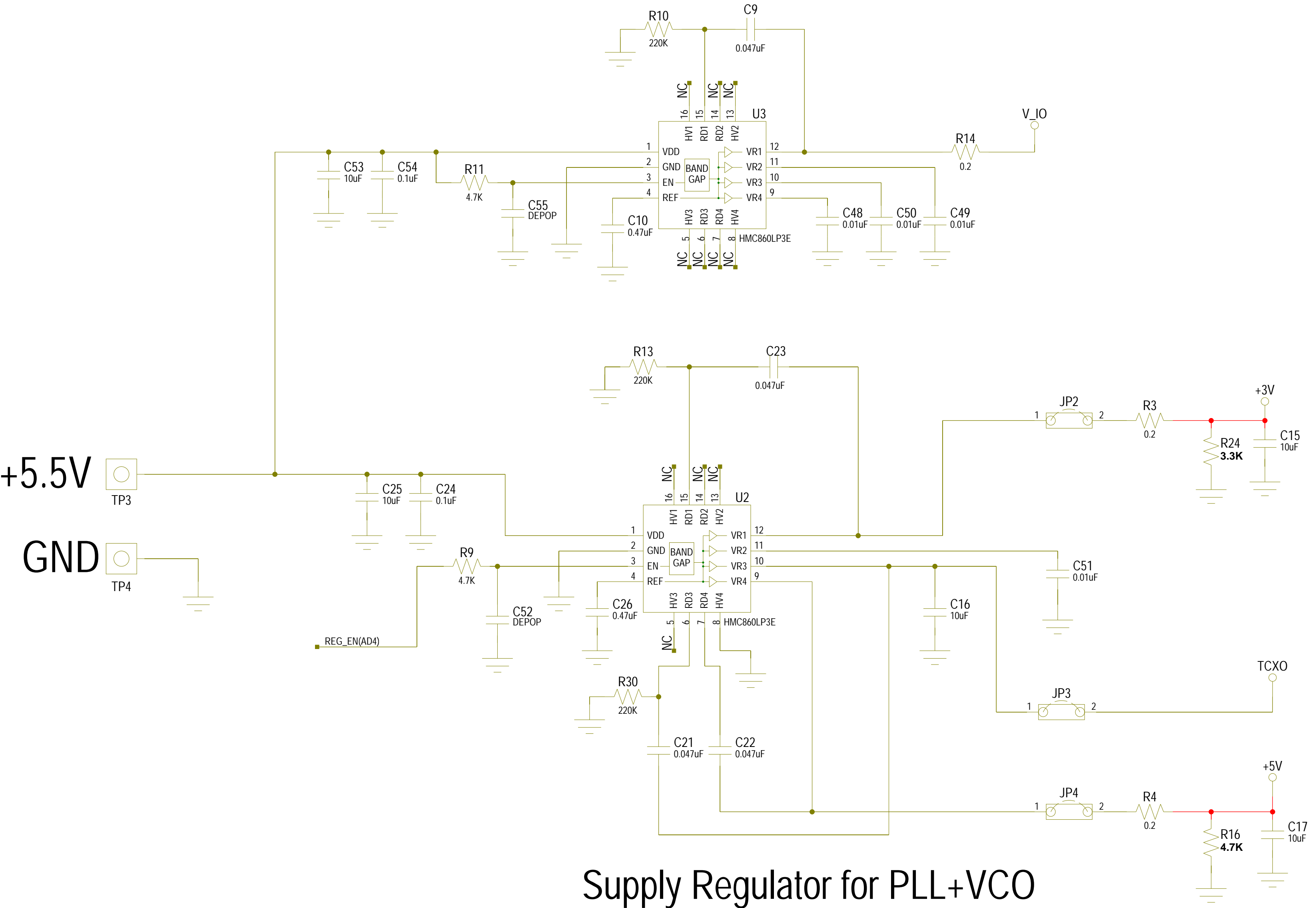
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HMC840 2xFo Integrated PLL & VCO Eval



 HITTITE MICROWAVE CORPORATION 20 Alpha Rd Chelmsford, MA 01824				
TITLE SCH, CUSTOMER EVALUATION PLL & VCO 2 X FO				
PROJECT				
DRAWING #:		129510		SHEET 1 OF 2
DRAWN BY		D.YOUNG		REV C
DATE		10/01/2010		SIZE 1CN88



Supply Regulator for PLL+VCO

A recommended design practice is to connect the regulator Enable Pin #3 through a 4.7kOhm resistor to the system microcontroller/FPGA for power management control
Small series resistors required in VR1 and VR4 output paths, as shown