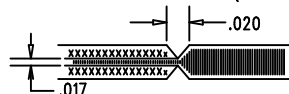


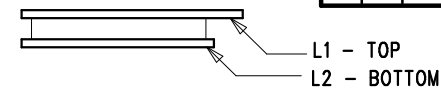
DRILL DRAWING LAYER
 LINEAR TECHNOLOGY DATE: 08-08-11
 DC417B

GENERAL PURPOSE BOARD FOR SINGLE PO AMP

8. SCORING FOR PANELIZED PCB (PRODUCTION FAB ONLY):



LAYER STRUCTURE



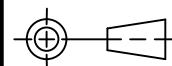
REVISION HISTORY				
ECO	REV	DESCRIPTION	APP. ENG.	DATE
-	1	PRODUCTION	GLEN B.	08-08-11

NOTES: UNLESS OTHERWISE SPECIFIED

- FAB PER IPC-A-600.
- MATERIAL: -LEAD FREE ASSEMBLY COMPLIANT, ISOLA FR-370HR OR EQUIVALENT.
 -FINISHED THICKNESS TO BE 0.062" +/- .005"
 -TOTAL OF 2 LAYERS WITH 2 OZ. CU ON THE OUTER
 -FLAMMABILITY RATING: 94 V-0 MINIMUM.
- SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.
 0.00" ARE PRIMARY DATUMS.
- DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, 0.001" THICK MIN.
 -ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
 -HOLE LOCATION TOLERANCES ARE +/-0.003" IN RELATION TO CENTER
- FINISH: -SMOBC USING LPI BOTH SIDES, COLOR GREEN.
 -GOLD IMMERSION BOTH SIDES.
 -FOR SILKSCREENS: USE WHITE NON-CONDUCTIVE INK.
- DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
 PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
- PCBS ARE TO BE RoHS COMPLIANT.

SIZE	QTY	SYM	PLATED	TOL
0.125	8	+	YES	+/-0.003
0.052	24	X	YES	+/-0.003
0.059	6	□	YES	+/-0.003
0.01	149	◇	YES	+/-0.003
0.094	18	⊗	YES	+/-0.003
0.035	19	⊠	YES	+/-0.003
0.037	34	+ ^A	YES	+/-0.003

UNLESS OTHERWISE SPECIFIED
 DIMENSIONS ARE IN INCHES
 TOLERANCES:
 0.XX" = ±0.01"
 0.XXX" = ±0.005"
 INTERPRET DIM AND TOL
 PER ASME Y14.5M-1994
 THIRD ANGLE PROJECTION



APPROVALS

PCB DES.	AK
APP ENG.	GLEN B.
SCALE = NONE	



TITLE: FABRICATION DRAWING
 GENERAL PURPOSE BOARD
 FOR SINGLE PO AMP

SIZE N/A	IC NO. DEMO CIRCUIT 417B	REV 1
FILENAME: DC417B-1.PCB		SHT 1 OF 1