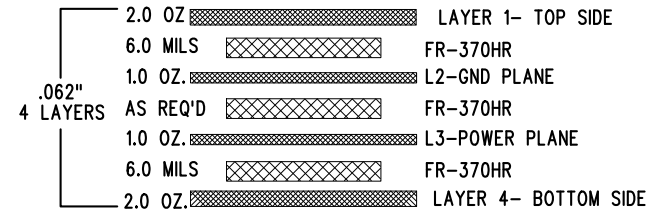


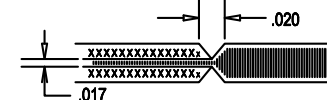
SIZE	QTY	SYM	PLATED	TOL
0.07	2	+	NO	+/-0.003
0.064	4	X	YES	+/-0.003
0.02	25	□	YES	+/-0.003
0.035	18	◇	YES	+/-0.003

FIGURE 1- STACKUP DETAILS



## NOTES: UNLESS OTHERWISE SPECIFIED

- FAB PER IPC-A-600.
- MATERIAL: -LEAD FREE ASSEMBLY COMPLIANT, ISOLA FR-370HR OR EQUIVALENT.  
-FINISHED THICKNESS TO BE 0.062 +/- .005  
-TOTAL OF 4 LAYERS WITH 2 OZ. CU ON THE OUTER LAYERS AND 1 OZ. CU ON THE INNER LAYERS.  
-FLAMMABILITY RATING: 94 V-0 MINIMUM.
- SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.  
0.00" ARE PRIMARY DATUMS.
- DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, 0.001 THICK MIN.  
-ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.  
-HOLE LOCATION TOLERANCES ARE +/-0.003 IN RELATION TO CENTER  
-FOR VIAS >0.01", VIAS HOLES NEED TO BE PLUGGED AND COVERED WITH SOLDERMASK.
- FINISH: -SMOBC USING LPI BOTH SIDES, COLOR GREEN.  
-GOLD IMMERSION BOTH SIDES.  
-FOR SILKSCREENS: USE WHITE NON-CONDUCTIVE INK.
- DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.  
PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
- PCBS ARE TO BE RoHS COMPLIANT.
- SCORING FOR PANELIZED PCB (PRODUCTION FAB ONLY):



### ADDITIONAL REQUIREMENT FOR PROTOTYPE FAB ONLY:

- OUTGOING INSPECTION REPORT (BASED ON ACTUAL MEASUREMENTS AND CROSS SECTION).

### ADDITIONAL REQUIREMENTS FOR PRODUCTION FAB ONLY:

- PROVIDE COMPLIANCE CERTIFICATES FOR RoHS, REACH AND CONFLICT-FREE MINERALS.
- SOLDERABILITY BOARD WITH TEST RESULTS.
- OUTGOING INSPECTION REPORT (BASED ON ACTUAL MEASUREMENTS AND CROSS SECTION).
- VACUUM PACKED WITH DESICCANT.
- FULL PANEL WITH NO REJECT

UNLESS OTHERWISE SPECIFIED			
DIMENSIONS ARE IN INCHES TOLERANCES: 0.XX" = ±0.01" 0.XXX" = ±0.005" INTERPRET DIM AND TOL PER ASME Y14.5M-1994 THIRD ANGLE PROJECTION		www.analog.com TITLE: FABRICATION DRAWING <b>500mA LOW DROPOUT REGULATOR</b>	
SIZE	IC NO.	LT1763CS8	REV
N/A		DC368B	1
FILENAME: DC368B-1.PCB		SHT 1 OF 1	