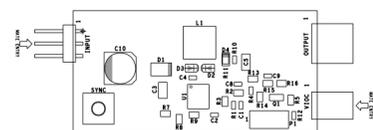


REVISIONS			
REV	DESCRIPTION	DATE	APPROVED
A	INITIAL RELEASE	21NOV18	J. VIERNES
B	AS PER ECR-086386	07MAY19	J. VIERNES

ASSEMBLY NOTES:

- BOARD ACCEPTABILITY PER ANALOG DEVICES, INC. SPECIFICATION TST00119 (LATEST REVISION).
- REPAIRS PER IPC-7711/21(LATEST REVISION) ARE ALLOWED.
- REPAIRS ARE NOT ALLOWED IN SOLDERMASK FREE AREAS ON EITHER SIDE OF THE BOARD.
- RoHS COMPLIANCE: ASSEMBLY VENDOR SHOULD ASSURE COMPLIANCE WITH LEAD-FREE AND RoHS PCB ASSEMBLY STANDARDS (EU RoHS DIRECTIVE 2002/95/EC).
- INSTALL SOLDER BLOB USING THE TABLE BELOW (SEE DETAIL A) AND PLACE MARK ON TICK BOX IN THE PRIMARY SIDE



BOM	PARTS	TICK BOX
05-051160-01-b	R17, R18, R19, R23, R24, R25, R29, R30, R31	BUCK
05-051160-02-b	R20, R21, R22, R26, R27, R28	INVERTER

DETAIL A  
SCALE FACTOR: 1:4

PRIMARY SIDE

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES DECIMALS FRACTIONS ANGLES .XX -.010 --1/32 --2 .XXX -.005 .XXXX -.0050	APPROVAL	DATE	 WWM DIVISION 804 WOBURN STREET WILMINGTON, MA 01887			
	TEMPLATE ENGINEER	-				
MATERIAL	HARDWARE SERVICES	-	TITLE ASSEMBLY SCP-LT1956 EVALZ			
	HARDWARE SYSTEMS	-				
	TEST ENGINEER	-				
	COMPONENT ENGINEER	-				
FINISH	TEST PROCESS	-	SIZE	FSCM NO	DRAWING NUMBER	REV
	HARDWARE RELEASE	-				
	DESIGNER	21NOV18				
PTD ENGINEER	21NOV18					
CHECKER	21NOV18					
DO NOT SCALE DWG			SCALE	1/1		SHEET 1 OF 1

EVALUATION BOARD