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REVISIONS			
REV	DESCRIPTION	DATE	APPROVED
A	INITIAL RELEASE	06DEC18	J.VIERNES
B	AS PER ECR-086420	07MAY19	J.VIERNES

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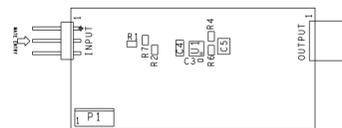
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ASSEMBLY NOTES:

1. BOARD ACCEPTABILITY PER ANALOG DEVICES, INC. SPECIFICATION TST00119 (LATEST REVISION).
2. REPAIRS PER IPC-7711/21(LATEST REVISION) ARE ALLOWED.
3. REPAIRS ARE NOT ALLOWED IN SOLDERMASK FREE AREAS ON EITHER SIDE OF THE BOARD.
4. RoHS COMPLIANCE: ASSEMBLY VENDOR SHOULD ASSURE COMPLIANCE WITH LEAD-FREE AND RoHS PCB ASSEMBLY STANDARDS (EU RoHS DIRECTIVE 2002/95/EC).

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EVALUATION BOARD

PRIMARY SIDE

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES DECIMALS FRACTIONS ANGLES .XX -.010 --1/32 --2 .XXX -.005 .XXXX -.0050	APPROVAL	DATE	 WWM DIVISION 804 WOBURN STREET WILMINGTON, MA 01887			
	TEMPLATE ENGINEER	N/A				
	HARDWARE SERVICES	N/A				
	HARDWARE SYSTEMS	N/A				
MATERIAL	TEST ENGINEER	DATE	TITLE ASSEMBLY SCP-LT1964 EVALZ			
	COMPONENT ENGINEER	DATE				
	TEST PROCESS	DATE				
	HARDWARE RELEASE	DATE				
FINISH	DESIGNER	DATE	SIZE	FSCM NO	DRAWING NUMBER	REV
	PTD ENGINEER	DATE	C	24355	01-051166	B
DO NOT SCALE DWG	CHECKER	DATE	SCALE	1/1		SHEET 1 OF 2
	J.VIERNES	10OCT18				

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