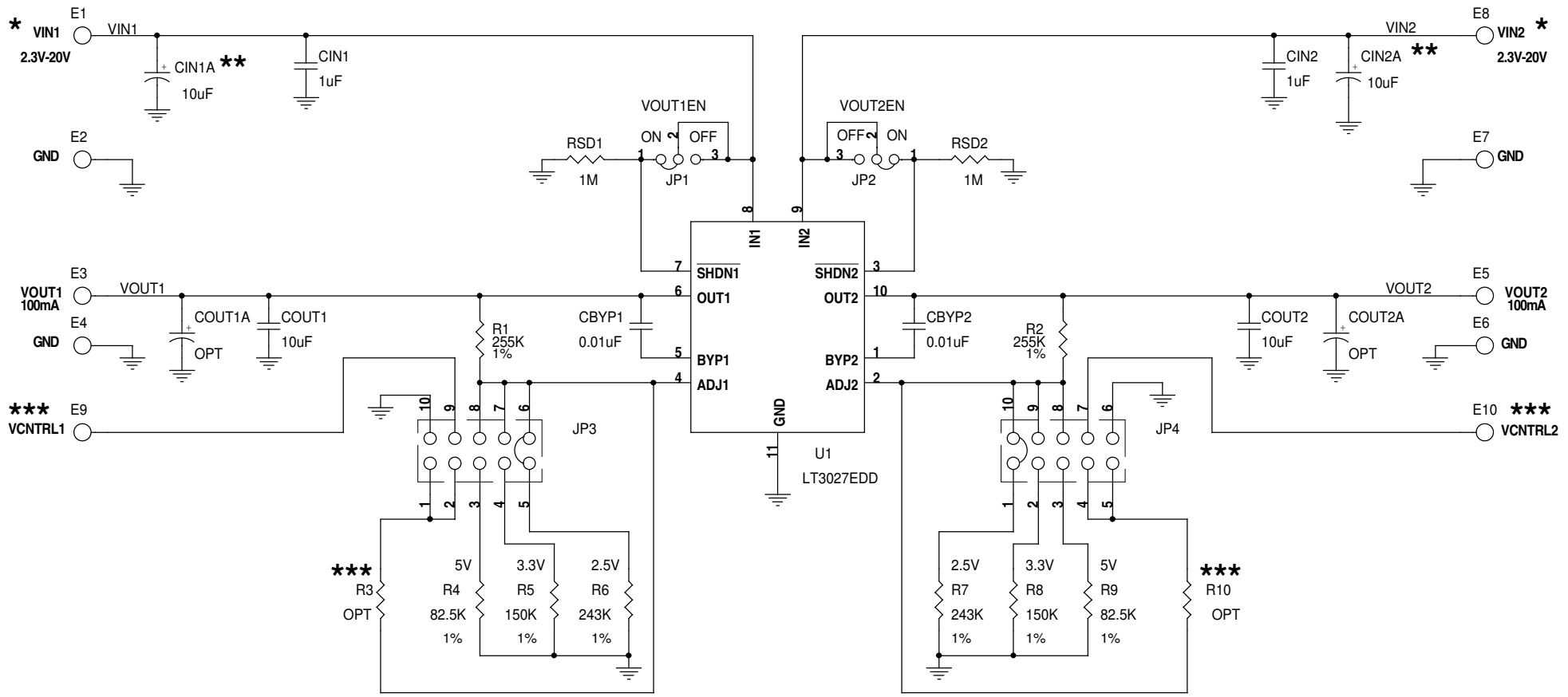



This circuit is proprietary to Linear Technology and supplied for use with Linear Technology parts.
Customer Notice: Linear Technology has made a best effort to design a circuit that meets customer-supplied specifications; however, it remains the customer's responsibility to verify proper and reliable operation in the actual application. Component substitution and printed circuit board layout may significantly affect circuit performance or reliability. Contact Linear Applications Engineering for assistance.

REVISION HISTORY				
ECO	REV	DESCRIPTION	DATE	APPROVED
	2	PROTO	07/28/04	



- * 2.3V IS THE MINIMUM INPUT VOLTAGE THAT LT3027EDD WILL OPERATE AT. THE MINIMUM INPUT VOLTAGE FOR A SPECIFIC REGULATOR CIRCUIT DEPENDS ON THE OUTPUT VOLTAGE PLUS THE DROP OUT VOLTAGE.
- ** CIN1A AND CIN2A ARE OPTIONAL CAPACITORS. THEY ARE ASSEMBLED ON THE DC811A TO DAMPEN THE (POSSIBLE) RINGING VOLTAGE DUE TO THE USE OF LONG INPUT LEADS. ON A NORMAL, TYPICAL PCB, WITH SHORT TRACES, CIN1A AND CIN2A ARE NOT NEEDED.
- *** THERE ARE TWO CHOICES WHEN UTILIZING USER SELECT. THE FIRST CHOICE IS A RESISTOR TO GROUND FOR SETTING AN OUTPUT VOLTAGE BETWEEN VIN AND THE REFERENCE VOLTAGE. THIS IS THE USUAL CONFIGURATION. THE SECOND CHOICE IS TO INSERT A RESISTOR TO VIN AND AN EXTERNAL CONTROL VOLTAGE, WHICH ALLOWS THE OUTPUT VOLTAGE TO BE SET AT VOLTAGES BELOW VREF. THE CIRCUIT NEEDS A MINIMUM LOAD CURRENT OF 1mA TO OPERATE PROPERLY IN THIS CONFIGURATION, SO SET R1 TO 1.21K.

CONTRACT NO.		 1630 McCarthy Blvd. Milpitas, CA 95035 Phone: (408)432-1900 Fax: (408)434-0507	
APPROVALS	DATE		
DRAWN mei	06/08/04	TITLE	
CHECKED		SCH, LT3027EDD, DUAL OUTPUT LOW NOISE LDO REGULATOR	
APPROVED		SIZE	CAGE CODE
ENGINEER		DWG NO	
DESIGNER		DC811A	
			REV 2
Friday, October 01, 2004	SCALE: NONE	FILENAME: 811A-2.DSN	SHEET 1 OF 1