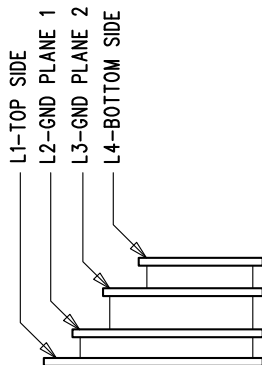
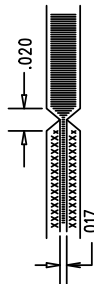


LAYER STRUCTURE



NOTES: UNLESS OTHERWISE SPECIFIED

1. FAB PER IPC-A-600.
2. MATERIAL: -EPOXY FIBERGLASS, NEMA GRADE FR-4
-FINISHED THICKNESS TO BE 0.062" \pm / - .005"
-TOTAL OF 4 LAYERS WITH 2 OZ. CU ON THE OUTER LAYERS
AND 1 OZ. CU ON THE INNER LAYERS.
-FLAMMABILITY RATING: 94 V-0 MINIMUM.
3. SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.
0.00" ARE PRIMARY DATUMS.
4. DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH
HOLES WITH COPPER, 0.001" THICK MIN.
-ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
-HOLE LOCATION TOLERANCES ARE \pm / -0.003"
IN RELATION TO CENTER
-FOR VIAS SIZE $>$ 0.01". VIAS HOLES NEED TO BE PLUGGED
AND COVERED WITH SOLDERMASK.
5. FINISH: -SNOBC USING LPI BOTH SIDES, COLOR GREEN.
-GOLD IMMERSION BOTH SIDES.
(LEAD FREE SOLDER CAN BE USED FOR PROTOTYPE)
-FOR SILKSCREEN: BOTH SIDES USE WHITE NON-CONDUCTIVE INK.
6. DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
7. PCBs ARE TO BE ROHS COMPLIANT.
8. SCORING FOR PANELIZED PCB:



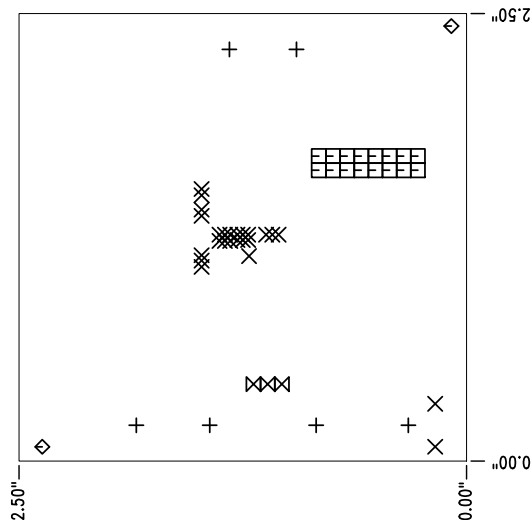
UNLESS OTHERWISE SPECIFIED
DIMENSIONS ARE IN INCHES
TOLERANCES:
0.XX" = \pm 0.01"
0.XXX" = \pm 0.005"
INTERPRET DIM AND TOL
PER ASME Y14.5M-1994
THIRD ANGLE PROJECTION



LINEAR
TECHNOLOGY
4330 MCCARTHY BLVD
MURFreesboro, TN 37132
PH: (615) 892-1000
WWW.LINEARTECH.COM
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TITLE: FABRICATION DRAWING
45V, 500mA MICROPOWER LDO WITH
PRECISION CURRENT LIMIT NAD POWER GOOD

SIZE	IC NO.	LT3065EDD	REV
N/A	DEMOCIRCUIT	2072A	2
FILENAME: DC2072A-2.PCB			SHT 1 OF 1



SIZE	QTY	SYM	PLATED	TOL
0.094	6	+	YES	\pm / -0.003
0.01	25	X	YES	\pm / -0.003
0.035	16	□	YES	\pm / -0.003
0.07	2	◇	NO	\pm / -0.003
0.031	3	⊗	YES	\pm / -0.003