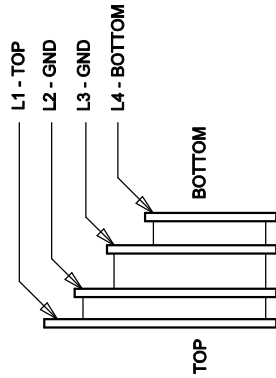
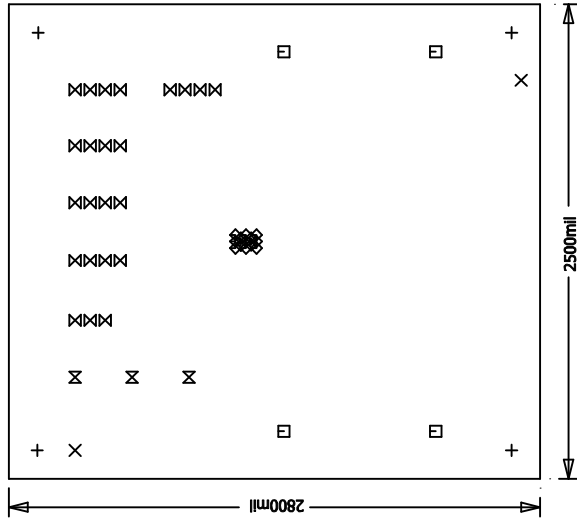


Linear Tech. Corp.
Demo Circuit 1433A

Component Side

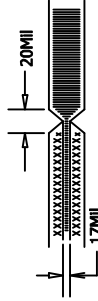


LAYER CONSTRUCTION

REVISION HISTORY			
ECO	REV	DESCRIPTION	APPR
-	2	PRODUCTION	MO/YZ
			DATE
			09/11/09

NOTES: UNLESS OTHERWISE SPECIFIED

1. FAB PER IPC-A-600
2. MATERIAL: -EPOXY FIBERGLASS, NEMA GRADE FR-4
-FINISHED THICKNESS TO BE 62MIL +/- 5MIL
-TOTAL OF 4 LAYERS WITH 2 OZ. CU ON THE
OUTER LAYERS AND 1 OZ. CU ON THE INNER LAYERS.
-FLAMMABILITY RATING: 94 V-0 MINIMUM .
3. SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.
0.00 ARE PRIMARY DATUMS.
4. DRILLING: DRILL HOLES PER SCHEDULE. PLATE THROUGH
HOLES WITH COPPER, 1MIL THICK MIN.
ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
HOLE LOCATION TOLERANCES ARE +/-3MIL
IN RELATION TO CENTER
5. FINISH: -SMOBC USING LPT BOTH SIDES, COLOR GREEN.
-GOLD IMMERSION BOTH SIDES.
-FOR SILKSCREEN: BOTH SIDES USE WHITE NON-CONDUCTIVE INK.
6. DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
7. PCBs ARE TO BE RoHS COMPLIANT.
8. SCORING FOR PANELIZED PCB:



SIZE	QTY	SYM	PLATED	TOL
187	4	+	YES	+/-3MIL
70	2	X	NO	+/-3MIL
205	4	□	YES	+/-3MIL
10	13	◇	YES	+/-3MIL
31	23	⊗	YES	+/-3MIL
94	3	⊗	YES	+/-3MIL

APPROVALS			
PCB DES.	INIT	DATE	
ENG.	MO/YZ	09/11/09	
UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES ON ANGLE ±1° 0.XX = ± 0.01 0.XXX = ± 0.005 INTERPRET DIM AND TOL PER ASME Y14.5M-1994			
THIRD ANGLE PROJECTION			
DO NOT SCALE DRAWING			
SCALE: NONE			
TITLE: FABRICATION DRAWING			
5A PROGRAMMABLE VERY LOW DROPOUT REGULATOR			
L160 MCCARTHY BLVD MILPITAS, CA 95035 PH: (408)432-1900 LTC CONFIDENTIAL- FOR CUSTOMER USE ONLY			
LINEAR TECHNOLOGY			
SIZE A			
IC NO. LT3070EUF			
DEMO CIRCUIT 1433A			
REV. 2			
FILENAME: DC1433A-2.PCB			
SHEET 1 of 1			