REVISIONS			
REV	DESCRIPTION	DATE	APPROVED
A	INITIAL RELEASE	18DEC18	J.VIERNES
B	AS PER ECR-086562	07MAY19	J.VIERNES

ASSEMBLY NOTES:

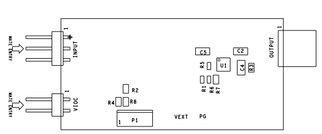
1. BOARD ACCEPTABILITY PER ANALOG DEVICES, INC. SPECIFICATION TST00119 (LATEST REVISION).

2. REPAIRS PER IPC-7711/21(LATEST REVISION) ARE ALLOWED.

3. REPAIRS ARE NOT ALLOWED IN SOLDERMASK FREE AREAS ON EITHER SIDE OF THE BOARD.

4. RoHS COMPLIANCE: ASSEMBLY VENDOR SHOULD ASSURE COMPLIANCE WITH LEAD-FREE AND RoHS PCB ASSEMBLY STANDARDS (EU RoHS DIRECTIVE 2002/95/EC).

VIEW FROM PRIMARY SIDE



VIEW FROM SECONDARY SIDE

EVALUATION BOARD

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES DECIMALS .XX +-.010 .XXX +-.005 .XXXX +-.0050 FRACTIONS +-1/32 ANGLES +- 2	APPROVAL	DATE	ANALOG DEVICES WWM DIVISION 804 WOBURN STREET WILMINGTON, MA 01887			
	TEMPLATE ENGINEER N/A	N/A				
	HARDWARE SERVICES N/A	N/A				
	HARDWARE SYSTEMS N/A	N/A	TITLE ASSEMBLY SCP-LT3094 EVALZ			
MATERIAL	TEST ENGINEER N/A	N/A				
	COMPONENT ENGINEER A.CAUILAN-AGENA	18DEC18				
	TEST PROCESS N/A	N/A				
	HARDWARE RELEASE K.JABATAN	18DEC18				
FINISH	DESIGNER J.M.PLAZA	18DEC18	SIZE C	FSCM NO 24355	DRAWING NUMBER 01-051372	REV B
	PTD ENGINEER J.VIERNES	18DEC18				
	CHECKER J.VIERNES	18DEC18				
DO NOT SCALE DWG			SCALE 1/1		SHEET 1 OF 1	