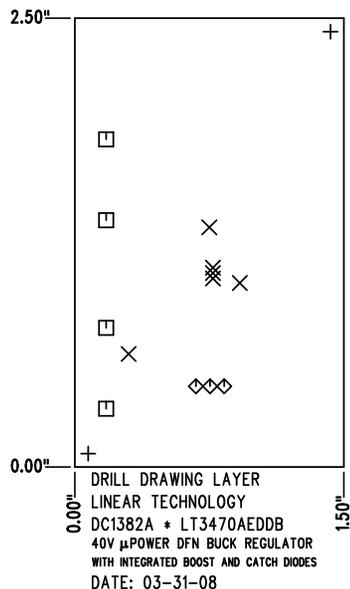


REVISIONS			
REV	DESCRIPTION	APPR	DATE
A	FINAL FABRICATION	KS	03-31-08

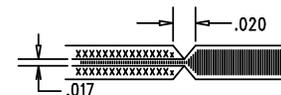


SHOWN FROM COMPONENT SIDE

SIZE	QTY	SYM	PLATED	TOL
0.07	2	+	NO	+/-0.003
0.01	6	X	YES	+/-0.003
0.094	4	□	YES	+/-0.003
0.035	3	◇	YES	+/-0.003

NOTES: UNLESS OTHERWISE SPECIFIED

- FAB PER IPC-A-600
- MATERIAL: EPOXY FIBERGLASS, NEMA GRADE FR-4
 FINISHED THICKNESS TO BE 0.062" +/- .005"
 TOTAL OF 4 LAYERS WITH 2 OZ. CU ON THE
 OUTER LAYERS AND 1 OZ. CU ON THE INNER LAYERS.
 FLAMMABILITY RATING: 94 V-0 MINIMUM .
- SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.
 0.00" ARE PRIMARY DATUMS.
- DRILLING: DRILL HOLES PER SCHEDULE. PLATE THROUGH
 HOLES WITH COPPER, 0.001" THICK MIN.
 ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
 HOLE LOCATION TOLERANCES ARE +/-0.003"
 IN RELATION TO CENTER
- FINISH: SMOBC USING LPI BOTH SIDES, COLOR GREEN.
 GOLD IMMERSION. USE LEAD FREE SOLDER FOR PROTOTYPE.
 FOR SILKSCREEN: USE WHITE NON-CONDUCTIVE INK.
- DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
 PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
- PCBS ARE TO BE RoHS COMPLIANT.
- SCORING FOR PANELIZED PCB:



APPROVALS			 LINEAR TECHNOLOGY 1630 MCCARTHY BLVD MILPITAS, CA 95035 PH: (408)432-1900 LTC CONFIDENTIAL - FOR CUSTOMER USE ONLY
DRAWN	INIT	DATE	
CHECK			TITLE: FABRICATION DRAWING 40V μPOWER DFN BUCK REGULATOR WITH INTEGRATED BOOST AND CATCH DIODES
DESIGN	KIM T.	03-31-08	
ENGR	KEITH S.	03-31-08	SIZE DEMO A DC1382A * LT3470AEDDB REV. A
SCALE = NONE			SHT 1 of 1