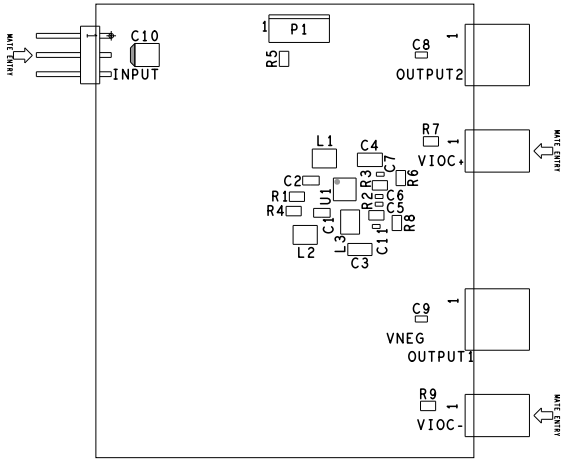



REVISIONS			
REV	DESCRIPTION	DATE	APPROVED
A	INITIAL RELEASE	02JUL19	A. SERQUINA

ASSEMBLY NOTES:

1.
- BOARD ACCEPTABILITY PER ANALOG DEVICES, INC. SPECIFICATION TST00119 (LATEST REVISION).
2.
- REPAIRS PER IPC-7711/21(LATEST REVISION) ARE ALLOWED.
3.
- REPAIRS ARE NOT ALLOWED IN SOLDERMASK FREE AREAS ON EITHER SIDE OF THE BOARD.
4.
- RoHS COMPLIANCE: ASSEMBLY VENDOR SHOULD ASSURE COMPLIANCE WITH LEAD-FREE AND RoHS PCB ASSEMBLY STANDARDS (EU RoHS DIRECTIVE 2002/95/EC).



PRIMARY SIDE

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES			APPROVAL		DATE		<div><div></div><div>ANALOG DEVICES</div></div> <div>WWM DIVISION 804 WOBURN STREET WILMINGTON, MA 01887</div> <div>TITLE ASSEMBLY SCP-LT3472 EVAL - Z</div>							
TOLERANCES			TEMPLATE ENGINEER		-									
DECIMALS FRACTIONS ANGLES			HARDWARE SERVICES		-									
.XX .-.010 .-.1/32 .-. 2			HARDWARE SYSTEMS		-									
.XXX .-.005														
.XXXX .-.0050														
MATERIAL			TEST ENGINEER		-									
			COMPONENT ENGINEER		-									
			TEST PROCESS		-									
			HARDWARE RELEASE		-									
FINISH			DESIGNER		02JUL19		SIZE		FSCM NO		DRAWING NUMBER		REV	
			C. BAUTISTA				C		24355		01-057520		A	
			PTD ENGINEER		02JUL19									
			A. SERQUINA											
			CHECKER		02JUL19									
			A. SERQUINA											
DO NOT SCALE DWG							SCALE		1 / 1				SHEET 1 OF 1	

EVALUATION BOARD - Z