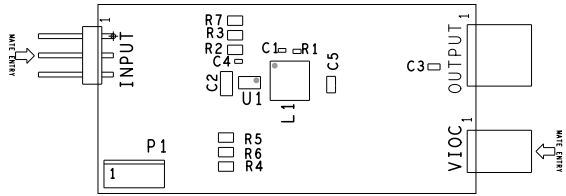



REVISIONS			
REV	DESCRIPTION	DATE	APPROVED
A	INITIAL RELEASE	27NOV18	J.VIERNES
B	AS PER ECR-086417	07MAY19	J.VIERNES

ASSEMBLY NOTES:

1.
- BOARD ACCEPTABILITY PER ANALOG DEVICES, INC. SPECIFICATION TST00119 (LATEST REVISION).
2.
- REPAIRS PER IPC-7711/21(LATEST REVISION) ARE ALLOWED.
3.
- REPAIRS ARE NOT ALLOWED IN SOLDERMASK FREE AREAS ON EITHER SIDE OF THE BOARD.
4.
- RoHS COMPLIANCE: ASSEMBLY VENDOR SHOULD ASSURE COMPLIANCE WITH LEAD-FREE AND RoHS PCB ASSEMBLY STANDARDS (EU RoHS DIRECTIVE 2002/95/EC).



PRIMARY SIDE

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES  TOLERANCES  DECIMALS    FRACTIONS    ANGLES .XX   -.010    --1/32    -- 2 .XXX   -.005 .XXX   -.0050			APPROVAL		DATE		<div><div></div><div>ANALOG DEVICES</div></div> <div>WWM DIVISION 804 WOBURN STREET WILMINGTON, MA 01887</div>					
			TEMPLATE ENGINEER		N/A							
			HARDWARE SERVICES		N/A							
			HARDWARE SYSTEMS G.AGUIRRI		N/A							
MATERIAL			TEST ENGINEER		N/A		TITLE  ASSEMBLY  SCP-LT3483   EVALZ					
			COMPONENT ENGINEER A.AGENA		27NOV18							
			TEST PROCESS N/A		N/A							
			HARDWARE RELEASE K.JABATAN		27NOV18							
FINISH			DESIGNER J.M.PLAZA		27NOV18		SIZE	FSCM NO	DRAWING NUMBER	REV		
			PTD ENGINEER J.VIERNES		27NOV18							
			CHECKER J.VIERNES		27NOV18							
DO NOT SCALE DWG							SCALE	1 / 1		SHEET	1 OF 1	

EVALUATION BOARD