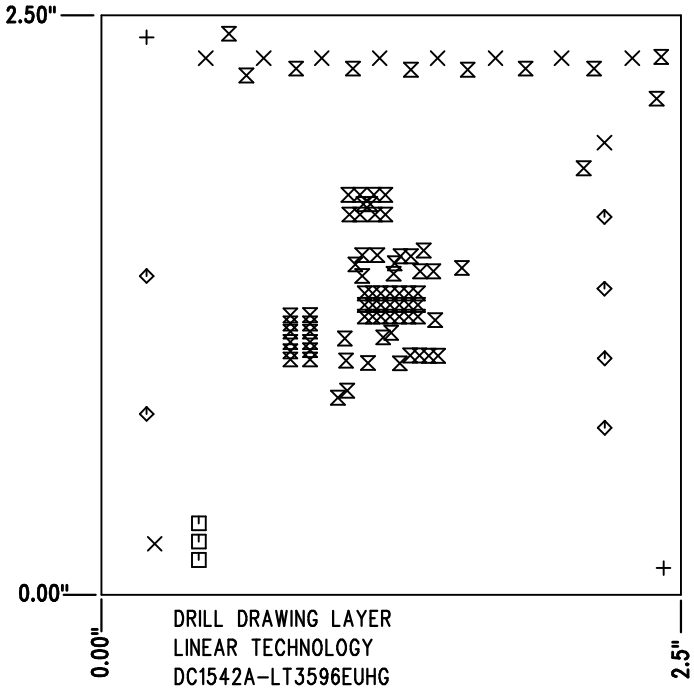
REVISION HISTORY				
ECO	REV	DESCRIPTION	APP. ENG.	DATE
–	2	PRODUCTION	WALKER B	05-21-10

LAYER STRUCTURE

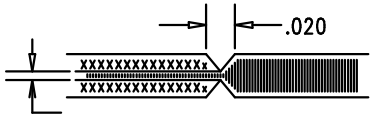
- LAYER 1 (COMPONENT SIDE)
- LAYER 2 (GND)
- LAYER 3 (GND)
- LAYER 4 (SOLDER SIDE)



DRILL DRAWING LAYER
LINEAR TECHNOLOGY
DC1542A-LT3596EUHG
60V STEP-DOWN 3-CHANNEL LED DRIVER
DATE: 05-21-10

NOTES: UNLESS OTHERWISE SPECIFIED

1. FAB PER IPC-A-600.
2. MATERIAL: -EPOXY FIBERGLASS, NEMA GRADE FR-4
   -FINISHED THICKNESS TO BE 0.062" +/- .005"
   -TOTAL OF 4 LAYERS WITH 2 OZ. CU ON THE OUTER LAYERS AND 1 OZ. CU ON THE INNER LAYERS.
   -FLAMMABILITY RATING: 94 V-0 MINIMUM.
3. SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN. 0.00" ARE PRIMARY DATUMS.
4. DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, 0.001" THICK MIN.
   -ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
   -HOLE LOCATION TOLERANCES ARE +/-0.003" IN RELATION TO CENTER
5. FINISH: -SMOBC USING LPI BOTH SIDES, COLOR GREEN.
   -GOLD IMMERSION BOTH SIDES.
   -FOR SILKSCREEN: TOP SIDE USE WHITE NON-CONDUCTIVE INK.
6. DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE. PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
7. PCBS ARE TO BE RoHS COMPLIANT.
8. SCORING FOR PANELIZED PCB:



SIZE	QTY	SYM	PLATED	TOL
0.07	2	+	NO	+/-0.003
0.065	10	×	YES	+/-0.003
0.031	3	□	YES	+/-0.003
0.094	6	◇	YES	+/-0.003
0.01	79	⧖	YES	+/-0.003

UNLESS OTHERWISE SPECIFIED
DIMENSIONS ARE IN INCHES
TOLERANCES:
0.XX" = ±0.01"
0.XXX" = ±0.005"
INTERPRET DIM AND TOL PER ASME Y14.5M-1994
THIRD ANGLE PROJECTION

APPROVALS	
PCB DES.	AK
APP ENG.	WALKER B
SCALE = NONE	

LINEAR TECHNOLOGY
1630 MCCARTHY BLVD
MILPITAS, CA 95035
PH: (408)432-1900
www.linear.com

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TITLE: FABRICATION DRAWING

60V STEP-DOWN 3-CHANNEL LED DRIVER

SIZE	IC NO.	REV
N/A	LT3596EUHG DEMO CIRCUIT 1542A	2

FILENAME: DC1542A-2.PCB

SHT 1 OF 1