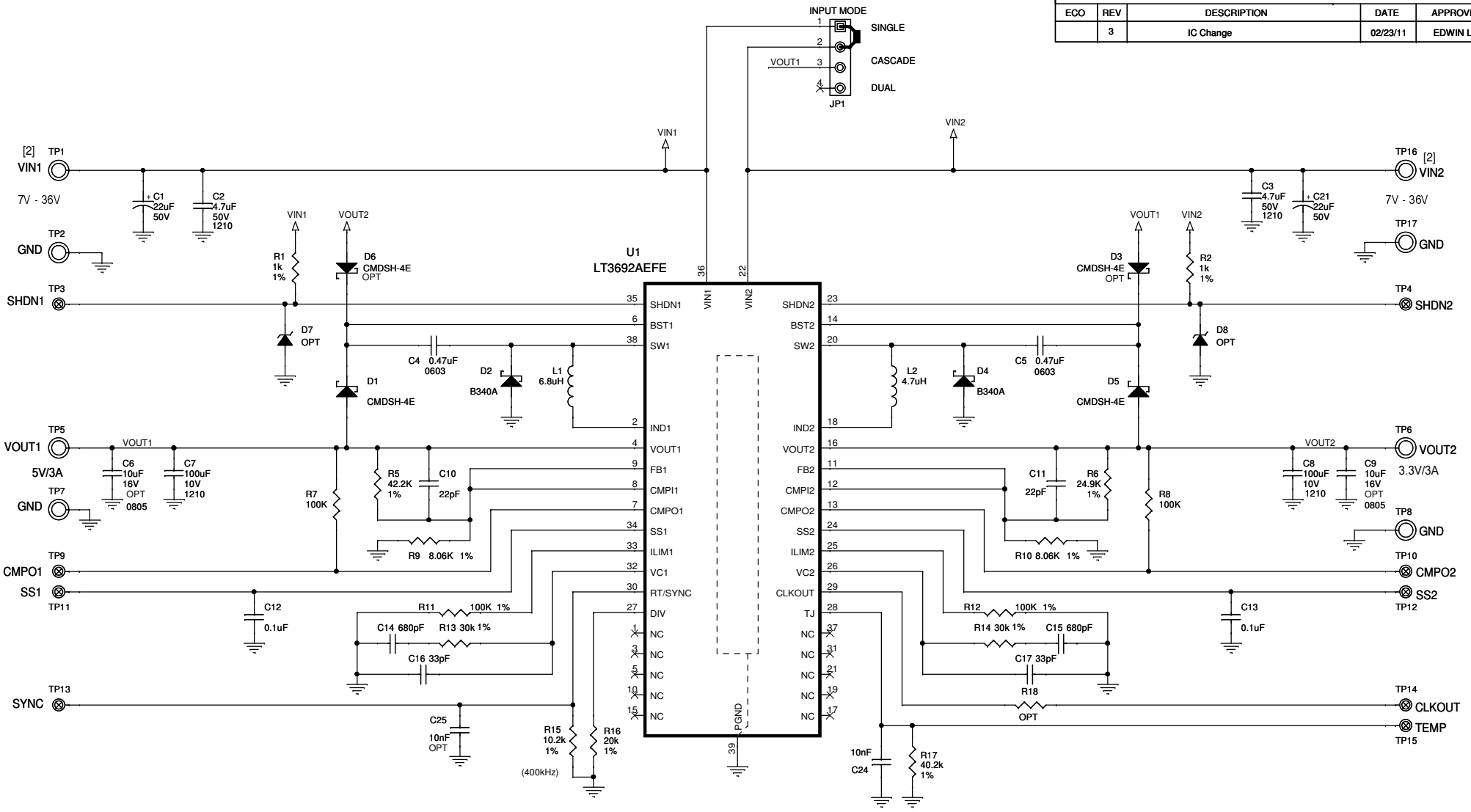
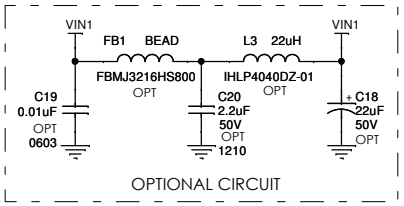
REVISION HISTORY				
ECO	REV	DESCRIPTION	DATE	APPROVED
	3	IC Change	02/23/11	EDWIN L.





NOTES: UNLESS OTHERWISE SPECIFIED,

1. ALL CAPACITORS AND RESISTORS ARE 0402.
2. [2] SEE QUICK START GUIDE FOR DETAILS ON JP1 SETTING AND INPUT VOLTAGE IMPLEMENTATION ORDER.

CUSTOMER NOTICE

LINEAR TECHNOLOGY HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE.

THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.

APPROVALS	
PCB DES.	MI
APP ENG.	EDWIN L.

SCALE = NONE

LINEAR TECHNOLOGY

1630 McCarthy Blvd.
Milpitas, CA 95035
Phone: (408)432-1900 www.linear.com
Fax: (408)434-0507

TITLE: SCHEMATIC

DUAL MONOLITHIC STEP-DOWN CONVERTER

SIZE	IC NO.	REV.
N/A	LT3692AEFE DEMO CIRCUIT 1403A	3

DATE: Wednesday, February 23, 2011 SHEET 1 OF 1