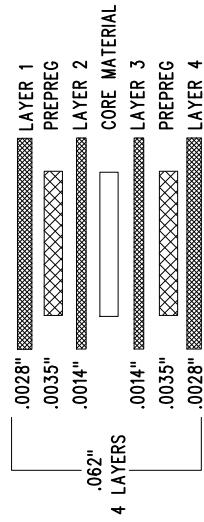
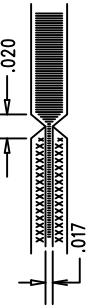


LAYER STRUCTURE



NOTES: UNLESS OTHERWISE SPECIFIED

1. FAB PER IPC-A-600.
2. MATERIAL: -LEAD FREE ASSEMBLY COMPLIANT, ISOLA FR-370HR OR EQUIVALENT.
-FINISHED THICKNESS TO BE 0.062 ± 0.005
-TOTAL OF 4 LAYERS WITH 2 OZ. CU ON THE OUTER LAYERS AND 1 OZ. CU ON THE INNER LAYERS.
-FLAMMABILITY RATING: 94 V-0 MINIMUM.
3. SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.
0.00 ARE PRIMARY DATUMS.
4. DRILLING: -DRILL HOLES PER SCHEDULE, PLATE THROUGH HOLES WITH COPPER, 0.001 THICK MIN.
-ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
-HOLE LOCATION TOLERANCES ARE $+/-0.003$ IN RELATION TO CENTER
-GOLD IMMERSION BOTH SIDES.
-FOR SILKSCREEN: USE WHITE NON-CONDUCTIVE INK.
5. FINISH: -SMOBC USING LPI BOTH SIDES, COLOR GREEN.
-GOLD IMMERSION BOTH SIDES.
6. DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
7. PCB'S ARE TO BE ROHS COMPLIANT.
8. SCORING FOR PANELIZED PCB



UNLESS OTHERWISE SPECIFIED		1630 MCCARTHY BLVD MILPITAS, CA 95035 PH: (408)432-1900 www.linaro.com LTC CONFIDENTIAL—	
DIMENSIONS ARE IN INCHES		FOR CUSTOMER USE ONLY	
TOLERANCES:		TITLE: FABRICATION DRAWING	
0.XX = ± 0.01	0.XXX = ± 0.005	USB 3.5V 2.5A BUCK	
INTERPRET DIM AND TOL PER ASME Y14.5M-1994	THIRD ANGLE PROJECTION	WITH CABLE DROP COMPENSATION	
N/A	IC NO.	L73697EMSE	REV
	FILENAME:	DEMO CIRCUIT 1893A	3
		DC1893A-3.PCB	SHT 1 OF 1

- ADDITIONAL REQUIREMENT FOR PROTOTYPE FAB ONLY:
1. OUTGOING INSPECTION REPORT (BASED ON ACTUAL MEASUREMENTS AND CROSS SECTION).
 - ADDITIONAL REQUIREMENTS FOR PRODUCTION FAB ONLY:
 1. PROVIDE COMPLIANCE CERTIFICATES FOR ROHS, REACH AND CONFLICT-FREE MINERALS.
 2. SOLDERABILITY BOARD WITH TEST RESULTS.
 3. OUTGOING INSPECTION REPORT (BASED ON ACTUAL MEASUREMENTS AND CROSS SECTION).
 4. VACUUM PACKED WITH DESCANTIC.
 5. FULL PANEL WITH NO REJECT.