

REVISION HISTORY																																																										
ECO	REV	DESCRIPTION	APPROVAL																																																							
	1	PRODUCTION	GEORGE Q.																																																							
DATE Apr 08, 2013																																																										
<p><b>NOTES: UNLESS OTHERWISE SPECIFIED,</b></p> <ol style="list-style-type: none"> <li>1. ARTWORK P/N DC1816B REV 1.</li> <li>2. FAB PER IPC-A-600</li> <li>3. PCB SHALL MEET RoHS COMPLIANCE.</li> <li>4. MATERIAL:           <ul style="list-style-type: none"> <li>A. LAMINATE: EPOXY FIBERGLASS, NEMA GRADE FR-4</li> <li>B. THICKNESS: 1.6 +/- 0.12 mm</li> <li>C. CLAD: 2 OZ. (50μm) EXTERNAL LAYERS.</li> </ul> </li> <li>5. FINISH:           <ul style="list-style-type: none"> <li>A. COATING: SOLDERMASK OVER BARE COPPER, COLOR, GREEN LPI.</li> <li>B. PLATING: GOLD IMMERSION BOTH SIDES.</li> <li>C. SILKSCREEN: COMPONENT SIDE WITH WHITE NON-CONDUCTIVE INK.</li> </ul> </li> <li>6. TOLERANCES:           <ul style="list-style-type: none"> <li>A. WARPAGE: (0.075mm) MAX. ALONG THE LONGEST DIAG. AS PER IPC-TM650 METHOD 2.4.22.</li> <li>B. ETCHING: +0.03mm, -0.3mm OF MASTER PATTERN.</li> <li>C. REGISTRATION: 0.075mm MAX.</li> <li>D. MASK THICKNESS: 0.025mm MIN., 0.075mm MAX.</li> <li>E. HOLE PLATING: 22μm +/- 3μm.</li> </ul> </li> <li>7. SIZE:           <ul style="list-style-type: none"> <li>A. CUT TO DIMENSIONS AND TOLERANCES SHOWN.</li> <li>0.00 ARE PRIMARY DATUMS.</li> <li>B. HOLE DIAMETERS ARE POST-PLATED SIZES.</li> <li>C. DEBURR AND BREAK ALL SHARP EDGES.</li> <li>D. DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.</li> </ul> </li> </ol>																																																										
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