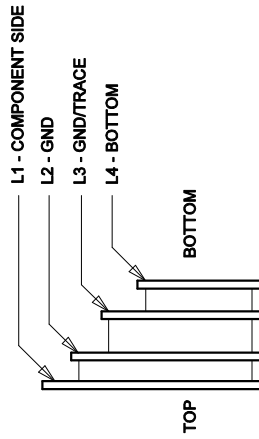


LINEAR TECHNOLOGY CORPORATION COMPONENT SIDE SHOWN
DEMO CIRCUIT 1569A LT3958EUEH 01/12/10
L1-COMPONENT SIDE

HIGH VOLTAGE BOOST, FLYBACK, SEPIC, AND INVERTING CONVERTER

SIZE	QTY	SYM	PLATED	TOL
0.2032	13	⊗	YES	+/-0.08
0.254	33	⊗	YES	+/-0.08
0.9	3	⊗	YES	+/-0.08
1.8	2	□	NO	+/-0.13
2.4	10	◇	YES	+/-0.13

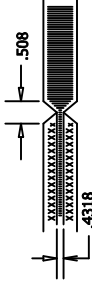


LAYER CONSTRUCTION

REVISION HISTORY			
ECO	REV	DESCRIPTION	APPR DATE
-	1	PRODUCTION	Keith S. 04/14/10

NOTES: UNLESS OTHERWISE SPECIFIED,

1. FAB PER IPC-A-600
2. MATERIAL: -EPOXY FIBERGLASS, NEMA GRADE FR-4
-FINISHED THICKNESS TO BE 1.6mm +/- 0.127mm
-TOTAL OF 4 LAYERS WITH 2 OZ. CU ON THE
OUTER LAYERS AND 1 OZ. CU ON THE INNER LAYERS.
-FLAMMABILITY RATING: 94 V-0 MINIMUM.
3. SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.
0.00" ARE PRIMARY DATUMS.
4. DRILLING: DRILL HOLES PER SCHEDULE. PLATE THROUGH
HOLES WITH COPPER, 0.0254mm THICK MIN.
ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
HOLE LOCATION TOLERANCES ARE +/-0.08mm
IN RELATION TO CENTER
5. FINISH: -SMBGC USING LPI BOTH SIDES. COLOR GREEN.
-GOLD IMMERSION BOTH SIDES.
-FOR SILKSCREEN: BOTH SIDES USE WHITE NON-
CONDUCTIVE INK.
6. DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
7. PCBs ARE TO BE RoHS COMPLIANT.
8. SCORING FOR PANELIZED PCBs:




UNLESS OTHERWISE SPECIFIED
DIMENSIONS ARE IN MILLIMETERS

TOLERANCES:


$0.00^{\circ} = \pm 0.25$
 $0.000^{\circ} = \pm 0.127$

INTERPRET DIM AND TOL
PER ASME Y14.5M-1994

THIRD ANGLE PROJECTION



APPROVALS	
PCB DES.	RJB
APP ENG.	Keith S.
SCALE = NONE	



LINEAR

TECHNOLOGY

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FOR CUSTOMER USE ONLY

TITLE: FABRICATION DRAWING

HIGH VOLTAGE BOOST, FLYBACK,
SEPIC, AND INVERTING CONVERTER

SIZE	IC NO. LT9558UEH	REV
N/A	DEMO CIRCUIT 1569A	1

FILENAME:	DC1569A-1.POB	SHT 1 OF 1
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