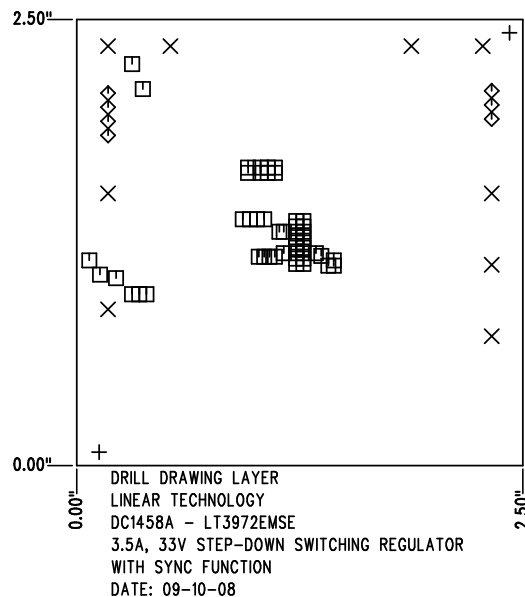
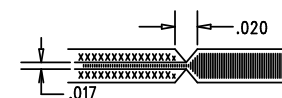


REVISIONS			
REV	DESCRIPTION	APPR	DATE
A	1ST PROTOTYPE	KH	09-10-08


SIZE	QTY	SYM	PLATED	TOL
0.07	2	+	NO	+/-0.003"
0.094	9	×	YES	+/-0.003"
0.012	50	□	YES	+/-0.003"
0.035	7	◇	YES	+/-0.003"

NOTES: UNLESS OTHERWISE SPECIFIED

1. FAB PER IPC-A-600
2. MATERIAL: EPOXY FIBERGLASS, NEMA GRADE FR-4
FINISHED THICKNESS TO BE 0.062" +/- .005"
TOTAL OF 4 LAYERS WITH 2 OZ. CU ON THE
OUTER LAYERS AND 1 OZ. CU ON THE INNER LAYERS.
FLAMMABILITY RATING: 94 V-0 MINIMUM .
3. SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.
0.00" ARE PRIMARY DATUMS.
4. DRILLING: DRILL HOLES PER SCHEDULE. PLATE THROUGH
HOLES WITH COPPER, 0.001" THICK MIN.
ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
HOLE LOCATION TOLERANCES ARE +/-0.003"
IN RELATION TO CENTER
5. FINISH: SMOBC USING LPI BOTH SIDES, COLOR GREEN.
GOLD IMMERSION. USE LEAD FREE SOLDER FOR PROTOTYPE.
FOR SILKSCREEN: USE WHITE NON-CONDUCTIVE INK.
6. DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
7. PCBs ARE TO BE RoHS COMPLIANT.
8. SCORING FOR PANELIZED PCB:



SHOWN FROM COMPONENT SIDE

APPROVALS <table border="1"> <tr> <th></th> <th>INIT</th> <th>DATE</th> </tr> <tr> <td>DRAWN</td> <td></td> <td></td> </tr> <tr> <td>CHECK</td> <td></td> <td></td> </tr> <tr> <td>DESIGN</td> <td>KIM T.</td> <td>09-10-08</td> </tr> <tr> <td>ENGR</td> <td>KEVIN H.</td> <td>09-10-08</td> </tr> </table>			INIT	DATE	DRAWN			CHECK			DESIGN	KIM T.	09-10-08	ENGR	KEVIN H.	09-10-08	 LINEAR TECHNOLOGY 1630 MCCATHY BLVD MILPITAS, CA 95035 PH: (408)432-1900 LTC CONFIDENTIAL- FOR CUSTOMER USE ONLY	
			INIT	DATE														
DRAWN																		
CHECK																		
DESIGN	KIM T.	09-10-08																
ENGR	KEVIN H.	09-10-08																
TITLE: FABRICATION DRAWING 3.5A, 33V STEP-DOWN SWITCHING REGULATOR WITH SYNC FUNCTION																		
SCALE = NONE		SIZE A	DEMO DC1458A-LT3972EMSE															
		REV. A	SHT 1 of 1															