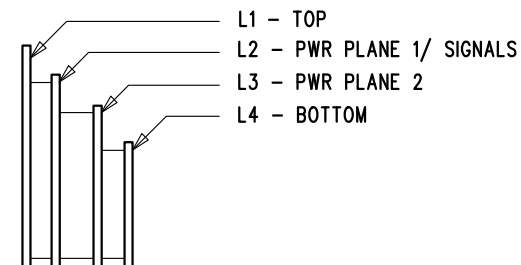


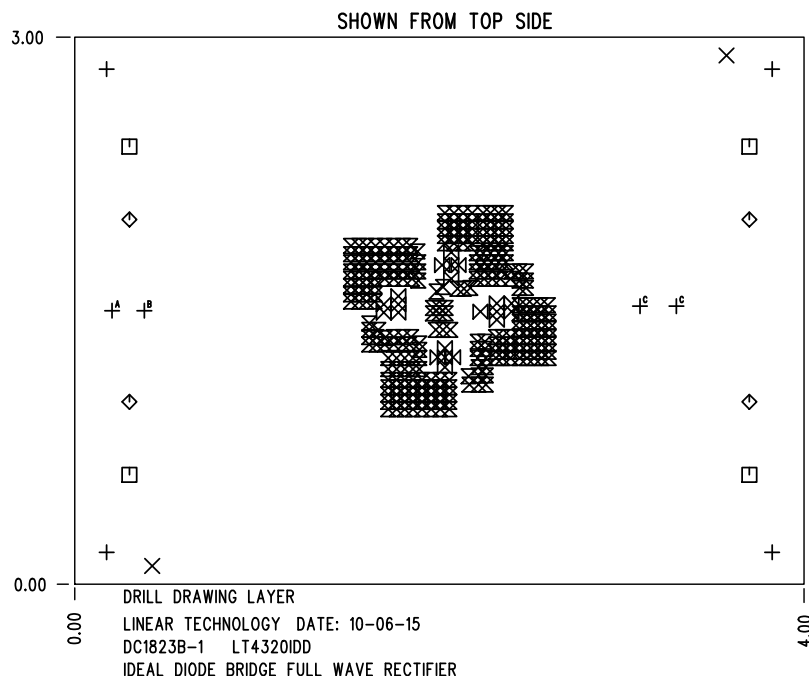
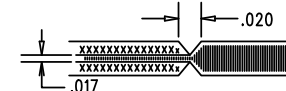
REVISION HISTORY				
ECO	REV	DESCRIPTION	APP. ENG.	DATE
-	1	REBUILD WITH CHANGE	KAUGH H.	10-06-15

LAYER STRUCTURE


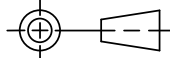


NOTES: UNLESS OTHERWISE SPECIFIED

- FAB PER IPC-A-600.
- MATERIAL: -LEAD FREE ASSEMBLY COMPLIANT, ISOLA FR-370HR OR EQUIVALENT.
-FINISHED THICKNESS TO BE 0.062 +/- .005
-TOTAL OF 4 LAYERS WITH 2 OZ. CU ON THE OUTER LAYERS AND 2 OZ. CU ON THE INNER LAYERS.
-FLAMMABILITY RATING: 94 V-0 MINIMUM.
- SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.
0/0 ARE PRIMARY DATUMS.
- DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, 0.001 THICK MIN.
-ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
-HOLE LOCATION TOLERANCES ARE +/-0.003 IN RELATION TO CENTER
- FINISH: -SMOBC USING LPI BOTH SIDES, COLOR GREEN.
-GOLD IMMERSION BOTH SIDES.
-FOR SILKSCREENS: USE WHITE NON-CONDUCTIVE INK.
- DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
- PCBS ARE TO BE RoHS COMPLIANT.
- SCORING FOR PANELIZED PCB:



SIZE	QTY	SYM	PLATED	TOL
0.19	4	+	NO	+/-0.003"
0.07	2	X	NO	+/-0.003"
0.205	4	□	YES	+/-0.003"
0.094	4	◇	YES	+/-0.003"
0.012	269	⊗	YES	+/-0.003"
0.008	21	⊗	YES	+/-0.003"
0.065	1	+ ^A	NO	+/-0.003"
0.072	1	+ ^B	NO	+/-0.003"
0.035	2	+ ^C	YES	+/-0.003"

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES: XX = ±0.01 XXX = ±0.005 INTERPRET DIM AND TOL PER ASME Y14.5M-1994 THIRD ANGLE PROJECTION	APPROVALS		 LINEAR TECHNOLOGY 1630 MCCARTHY BLVD MILPITAS, CA 95035 PH: (408)432-1900 www.linear.com LTC CONFIDENTIAL- FOR CUSTOMER USE ONLY
	PCB DES.	KIM T.	
	APP ENG.	KAUGH H.	TITLE: FABRICATION DRAWING
			IDEAL DIODE BRIDGE FULL WAVE RECTIFIER
	SCALE = NONE		SIZE IC NO. LT4320IDD N/A DEMO CIRCUIT 1823B FILENAME: DC1823B-1.PCB SHT 1 OF 1