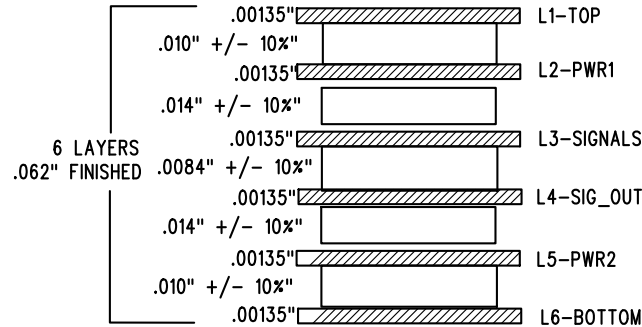


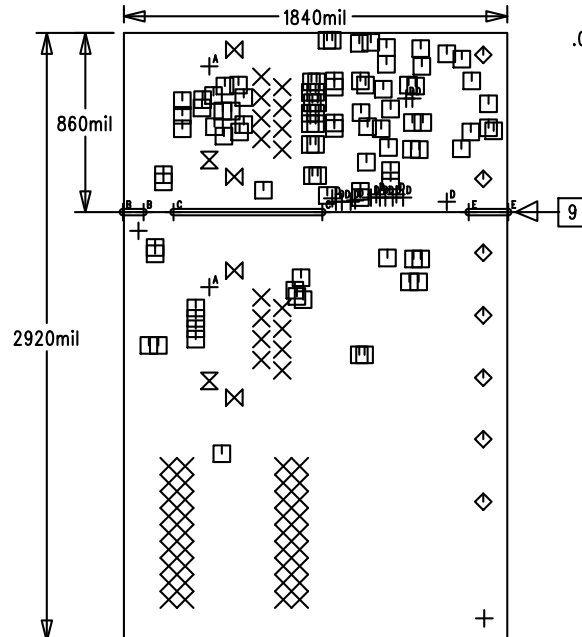
LAYER STRUCTURE

CONTROLLED DIELECTRIC FOR ALL LAYERS



SIZE	QTY	SYM	PLATED	TOL
0.07	2	+	NO	+/-0.003"
0.035	52	X	YES	+/-0.003"
0.01	92	□	YES	+/-0.003"
0.094	7	◇	YES	+/-0.003"
0.128	2	⊗	YES	+/-0.003"
0.065	4	⊠	YES	+/-0.003"
0.128	2	+ ^A	NO	+/-0.003"
0.031 x 0.13	1	+ ^B	NO	+/-0.003"
0.031 x 0.745	1	+ ^C	NO	+/-0.003"
0.006	15	+ ^D	YES	+/-0.003"
0.031 x 0.22	1	+ ^E	NO	+/-0.003"

SHOWN FROM TOP SIDE



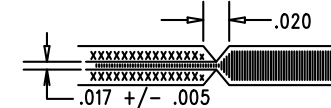
DRILL DRAWING LAYER DATE: 10-19-18
ANALOG DEVICES
DC2911A-1, LT4293IDD, LT4321IUF
IEEE 802.3bt COMPLIANT, LTPoE++
INTEROPERABLE POWERED DEVICE

REVISION HISTORY

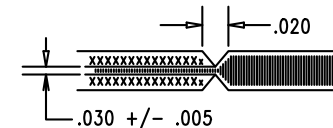
ECO	REV	DESCRIPTION	APP. ENG.	DATE
-	1	1ST PROTOTYPE	ERIC H.	10-19-18

NOTES: UNLESS OTHERWISE SPECIFIED

- FAB PER IPC-A-600.
- MATERIAL: -LEAD FREE ASSEMBLY COMPLIANT, ISOLA FR-370HR OR EQUIVALENT.
-FINISHED THICKNESS TO BE 0.062" +/- .005"
-TOTAL OF 6 LAYERS WITH 1 OZ. CU FOR ALL LAYERS.
-FLAMMABILITY RATING: 94 V-0 MINIMUM.
- SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN. 0.00" ARE PRIMARY DATUMS.
- DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, 0.001" THICK MINIMUM EXCEPT SLOT HOLES INDICATED BY THE ARROW #9
-ALL HOLE SIZES ARE SPECIFIED AFTER PLATING EXCEPT SLOT HOLES INDICATED BY THE ARROW #9
-HOLE LOCATION TOLERANCES ARE +/-0.003" IN RELATION TO CENTER
- FINISH: -SMOBC USING LPI BOTH SIDES, COLOR GREEN.
-FOR SILKSCREENS: USE WHITE NON-CONDUCTIVE INK.
-GOLD IMMERSION BOTH SIDES.
- DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE. PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
- PCBS ARE TO BE RoHS COMPLIANT.
- SCORING FOR PANELIZED PCB (PRODUCTION FAB ONLY):

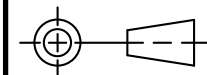


9. ADD A SCORE LINE AS INDICATED BY THE ARROW WITH THE PARAMETERS AS SHOWN BELOW.
SCORING ANGLE IS 60 DEGREE MEASURED FROM THE SURFACE OF THE BOARD.



UNLESS OTHERWISE SPECIFIED

DIMENSIONS ARE IN INCHES
TOLERANCES:
0.XX" = ±0.01"
0.XXX" = ±0.005"
INTERPRET DIM AND TOL
PER ASME Y14.5M-1994
THIRD ANGLE PROJECTION



APPROVALS

PCB DES. KIM T.
APP ENG. ERIC H.

SCALE = NONE



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1630 MCCARTHY BLVD.
MILPITAS, CA 95035
TEL: (408)432-1900

TITLE: FABRICATION DRAWING
IEEE 802.3bt COMPLIANT, LTPoE++
INTEROPERABLE POWERED DEVICE

SIZE N/A IC NO. LT4293IDD, LT4321IUF
DEMO CIRCUIT 2911A REV. 1

FILENAME: DC2911A-1.PCB SHT 1 OF 1