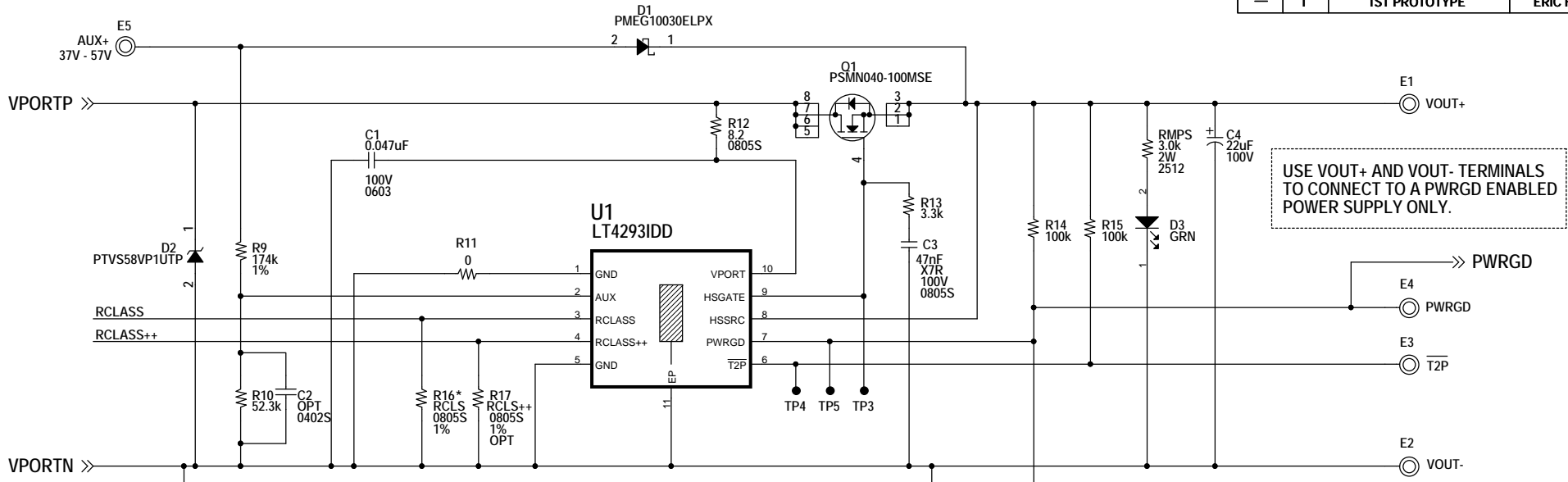
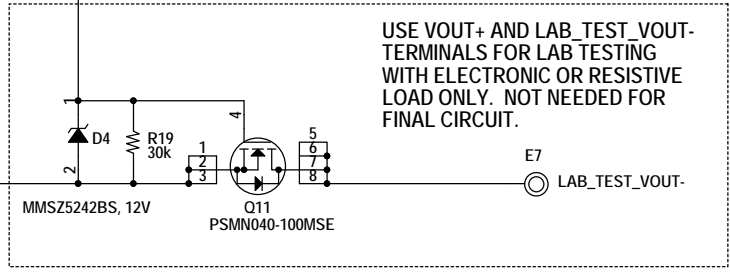


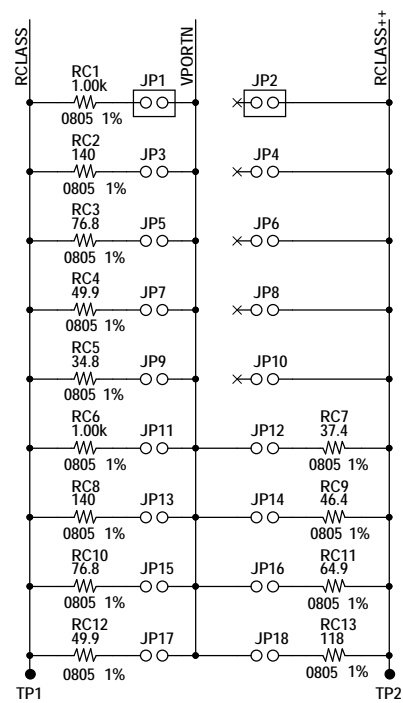
REVISION HISTORY				
ECO	REV	DESCRIPTION	APPROVED	DATE
—	1	1ST PROTOTYPE	ERIC H.	11-29-18



USE VOUT+ AND VOUT- TERMINALS TO CONNECT TO A PWRGD ENABLED POWER SUPPLY ONLY.



USE VOUT+ AND LAB_TEST_VOUT- TERMINALS FOR LAB TESTING WITH ELECTRONIC OR RESISTIVE LOAD ONLY. NOT NEEDED FOR FINAL CIRCUIT.



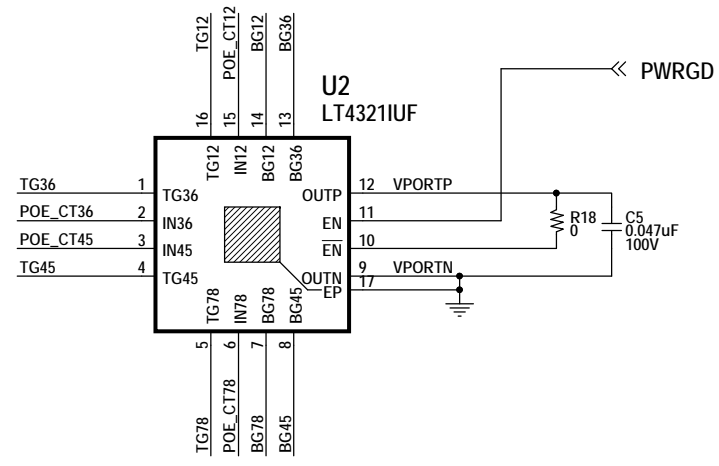
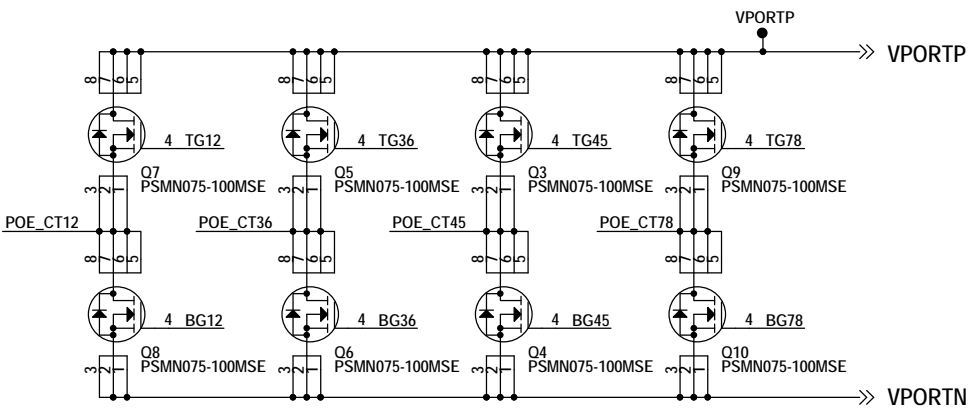
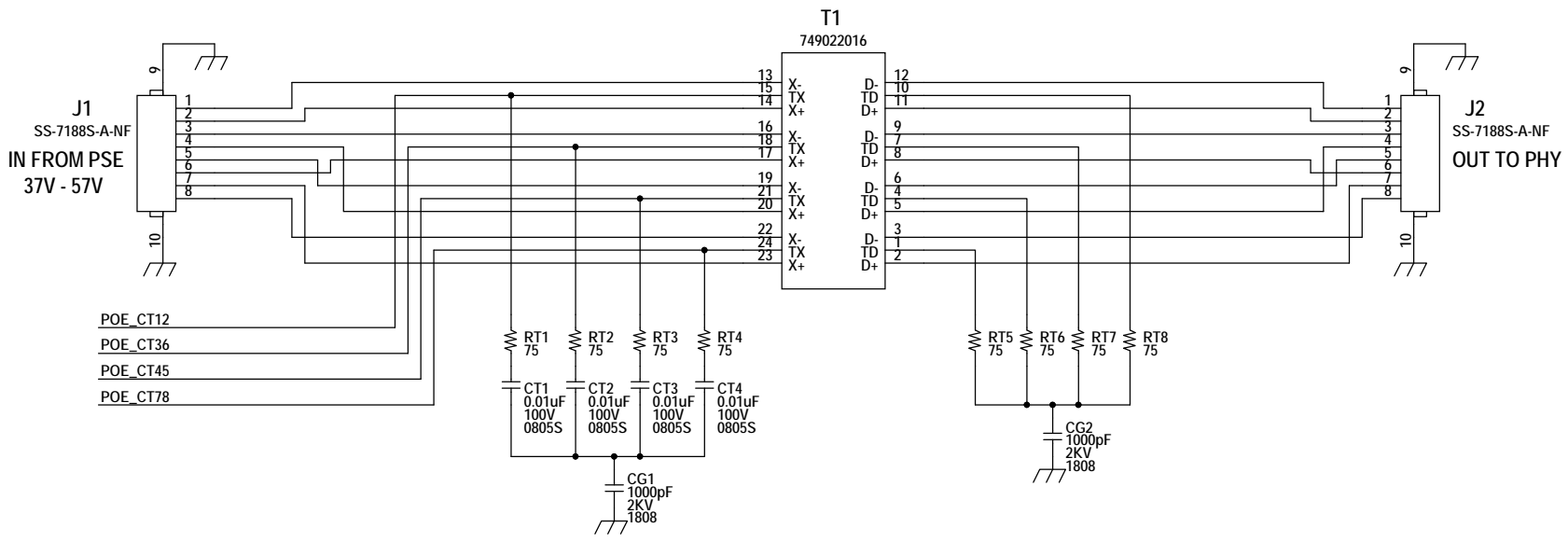
RCLASS JUMPERS

JUMPER SELECTIONS				
PD CLASS	802.3bt REQ. POWER	LTPoE++ REQ. POWER	RCLASS JUMPERS	
0	13W		JP1	JP2
1	3.84W		JP3	JP4
2	6.49W		JP5	JP6
3	13W		JP7	JP8
4	25.5W		JP9	JP10
5	40W	38.7W	JP11	JP12
6	51W	52.7W	JP13	JP14
7	62W	70W	JP15	JP16
8	71.3W	90W	JP17	JP18

NOTE 1: UNLESS OTHERWISE SPECIFIED

1. ALL RESISTORS AND CAPACITORS ARE 0603
2. ALL RESISTORS TOLERANCE RATING ARE 5%

CUSTOMER NOTICE		APPROVALS		ANALOG DEVICES		POWER BY LINEAR		1630 MCCARTHY BLVD. MILPITAS, CA 95035 TEL: (408) 432-1900	
ADI HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT ADI APPLICATIONS ENGINEERING FOR ASSISTANCE.		PCB DES.	KIM T.	www.analog.com		www.linear.com		TITLE: SCHEMATIC	
		APP ENG.	ERIC H.					IEEE 802.3bt COMPLIANT, LTPoE++ INTEROPERABLE POWERED DEVICE	
		SIZE	N/A	IC NO.	LT4293IDD, LT4321IUF		REV. 1		
THIS CIRCUIT IS PROPRIETARY TO ADI AND SUPPLIED FOR USE WITH ADI PARTS.		SCALE	= NONE		DATE:	Thursday, November 29, 2018		SHEET 1 OF 2	



CUSTOMER NOTICE		APPROVALS		ANALOG DEVICES		POWER BY LINEAR		1630 MCCARTHY BLVD. MILPITAS, CA 95035 TEL: (408) 432-1900	
ADI HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT ADI APPLICATIONS ENGINEERING FOR ASSISTANCE.		PCB DES.	KIM T.	www.analog.com		www.linear.com		IEEE 802.3bt COMPLIANT, LTPoE++ INTEROPERABLE POWERED DEVICE	
		APP ENG.	ERIC H.	TITLE: SCHEMATIC		SIZE IC NO. LT4293IDD, LT4321IUF DEMO CIRCUIT 2911A			
		THIS CIRCUIT IS PROPRIETARY TO ADI AND SUPPLIED FOR USE WITH ADI PARTS.		SCALE = NONE		DATE: Thursday, November 29, 2018	SHEET 2 OF 2		REV. 1