

**LT4295, LT4321**
**High Efficiency IEEE 802.3bt (PoE++, Type 4, 62W/71W) PD with Flyback DC/DC Converter and Auxiliary Supply Input**
**DESCRIPTION**

Demonstration Circuit 2476A is an Ethernet Alliance™ Gen 2 PoE certified IEEE 802.3bt compliant power over ethernet (PoE) powered device (PD). It features the **LT®4295** PD interface and switching regulator controller and the **LT4321** PoE ideal diode bridge controller.

The LT4295 provides IEEE 802.3af (PoE, Type 1), IEEE 802.3at (PoE+, Type 2), and IEEE 802.3bt (PoE++, Type 3 and 4) compliant interfacing and power supply control. It utilizes an external, low  $R_{DS(on)}$  (30mΩ typical) N-channel FET for the hot swap function to improve efficiency. The LT4295 controls a DC/DC converter that utilizes a highly efficient flyback topology with synchronous rectification.

The LT4321 controls eight low  $R_{DS(on)}$  (30mΩ typical) N-channel FETs to further improve end-to-end power

delivery efficiency and ease thermal design. This solution replaces the eight diodes typically found in a passive PoE rectifier bridge.

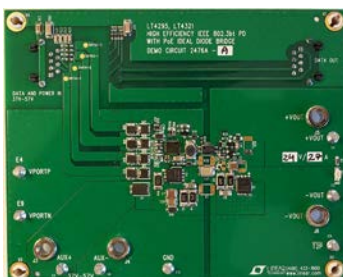
The DC2476A-A accepts up to 71.3W of delivered power from a power sourcing equipment (PSE) via the RJ45 connector (J1) or a local 48V DC power supply using the auxiliary supply input. When both supplies are connected, the auxiliary supply input has priority over the PoE input. The DC2476A-A supplies a 24V output at up to 2.7A as a Class 8 (71.3W) PD and up to 2.4A as a Class 7 (62W) PD application.

**Design files for this circuit board are available.**

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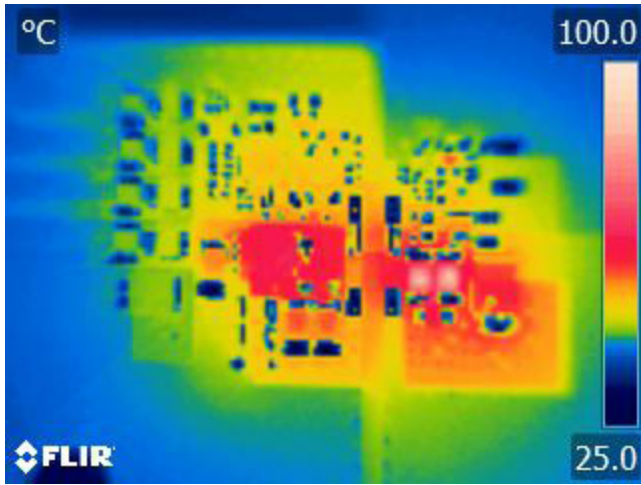
**PERFORMANCE SUMMARY**

PARAMETER	CONDITIONS	VALUE
Port Voltage ( $V_{PORT}$ )	At RJ45	37V to 57V
Auxiliary Voltage	From AUX+ to AUX- Terminals	37V to 57V
Output Voltage ( $V_{OUT}$ )		24V (Typical)
Output Current ( $I_{OUT}$ )	Class 8 (Default) Class 7 ( $R_{20} = 80.60\Omega$ , $R_{19} = 64.90\Omega$ )	2.7A (Max) 2.4A (Max)
Output Voltage Ripple	$V_{PORT} = 41.2V$ , $I_{OUT} = 2.7A$	200mV <sub>P-P</sub> (Typical)
Load Regulation		0.5% (Typical)
Efficiency	$V_{PORT} = 50V$ , $I_{OUT} = 2.7A$ , End-to-End	91.5% (Typical)
Switching Frequency		250kHz (Typical)

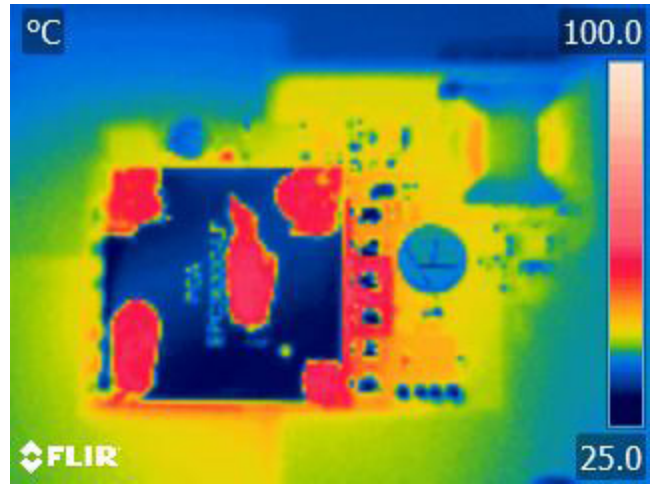
**BOARD PHOTO**

**Top Side**

**Bottom Side**


## TYPICAL PERFORMANCE CHARACTERISTICS



Top Side



Bottom Side

Figure 1. Thermal Pictures (Conditions:  $V_{PORT} = 57V$ ,  $V_{OUT} = 24V$ ,  $I_{OUT} = 2.7A$ )

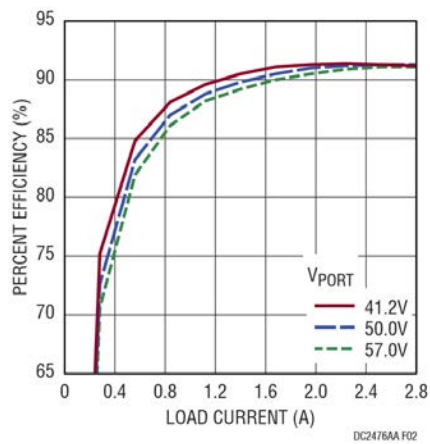


Figure 2. Efficiency (End-to-End)

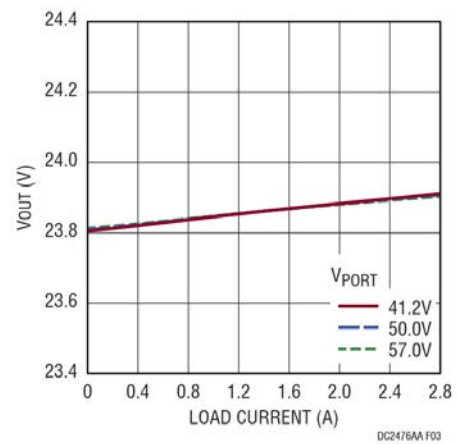


Figure 3. Load Regulation

TYPICAL PERFORMANCE CHARACTERISTICS

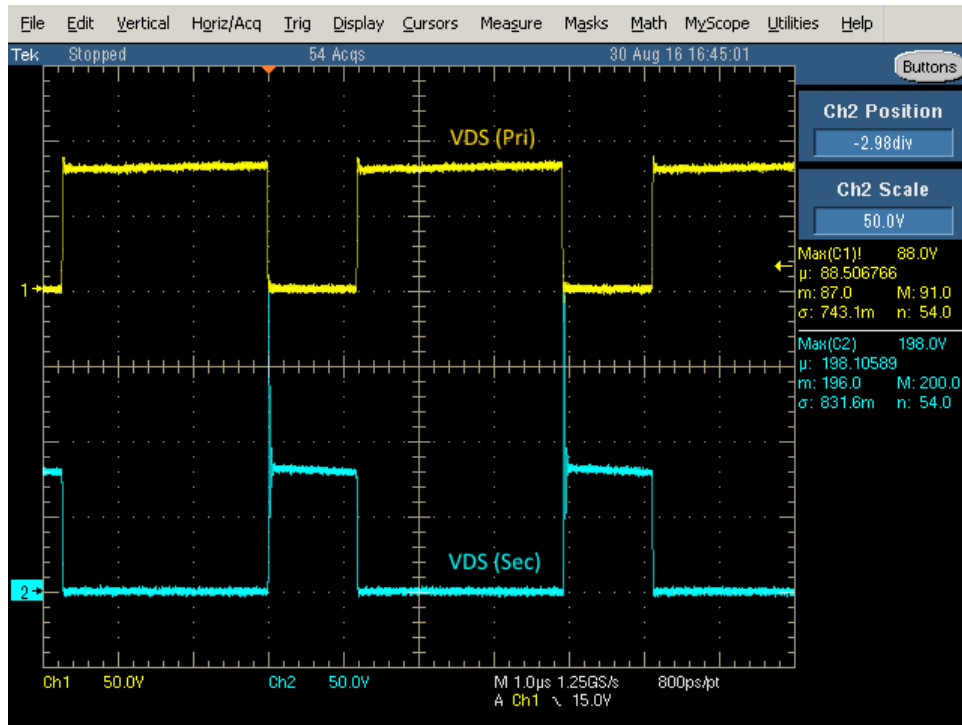


Figure 4. Switch Node Waveforms (Conditions:  $V_{PORT} = 57V$ ,  $V_{OUT} = 24V$ ,  $I_{OUT} = 2.7A$ )

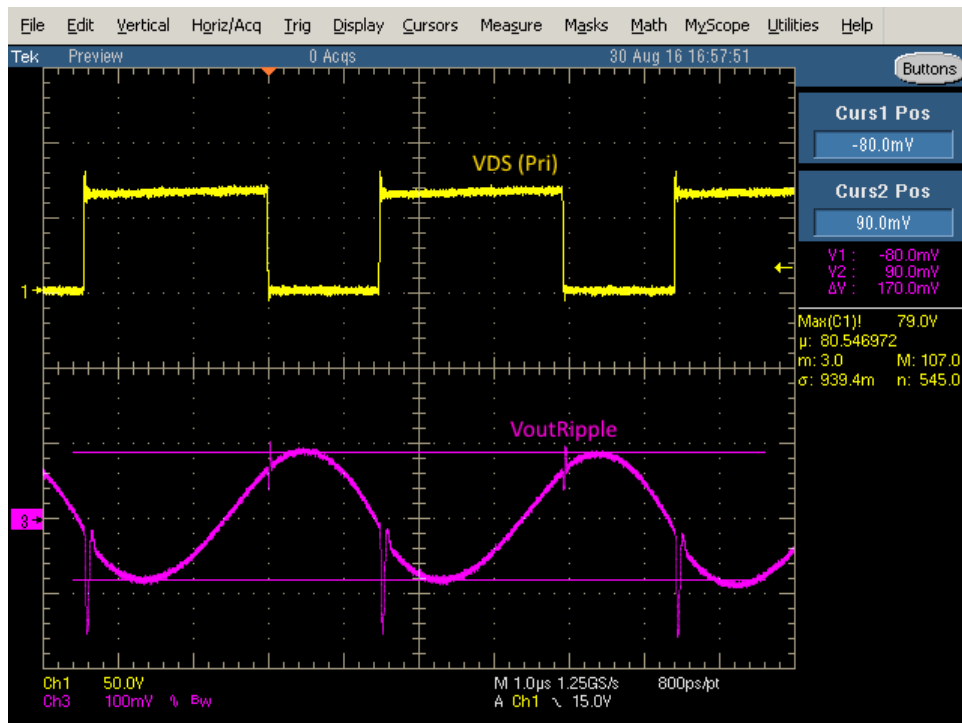


Figure 5. Output Voltage Ripple (Conditions:  $V_{PORT} = 41.2V$ ,  $V_{OUT} = 24V$ ,  $I_{OUT} = 2.7A$ )

## TYPICAL PERFORMANCE CHARACTERISTICS



Figure 6. Load Transient Response (Conditions:  $V_{PORT} = 41.2V$ , Load Step: 1.35A to 2.7A to 1.35A)

**TYPICAL PERFORMANCE CHARACTERISTICS**

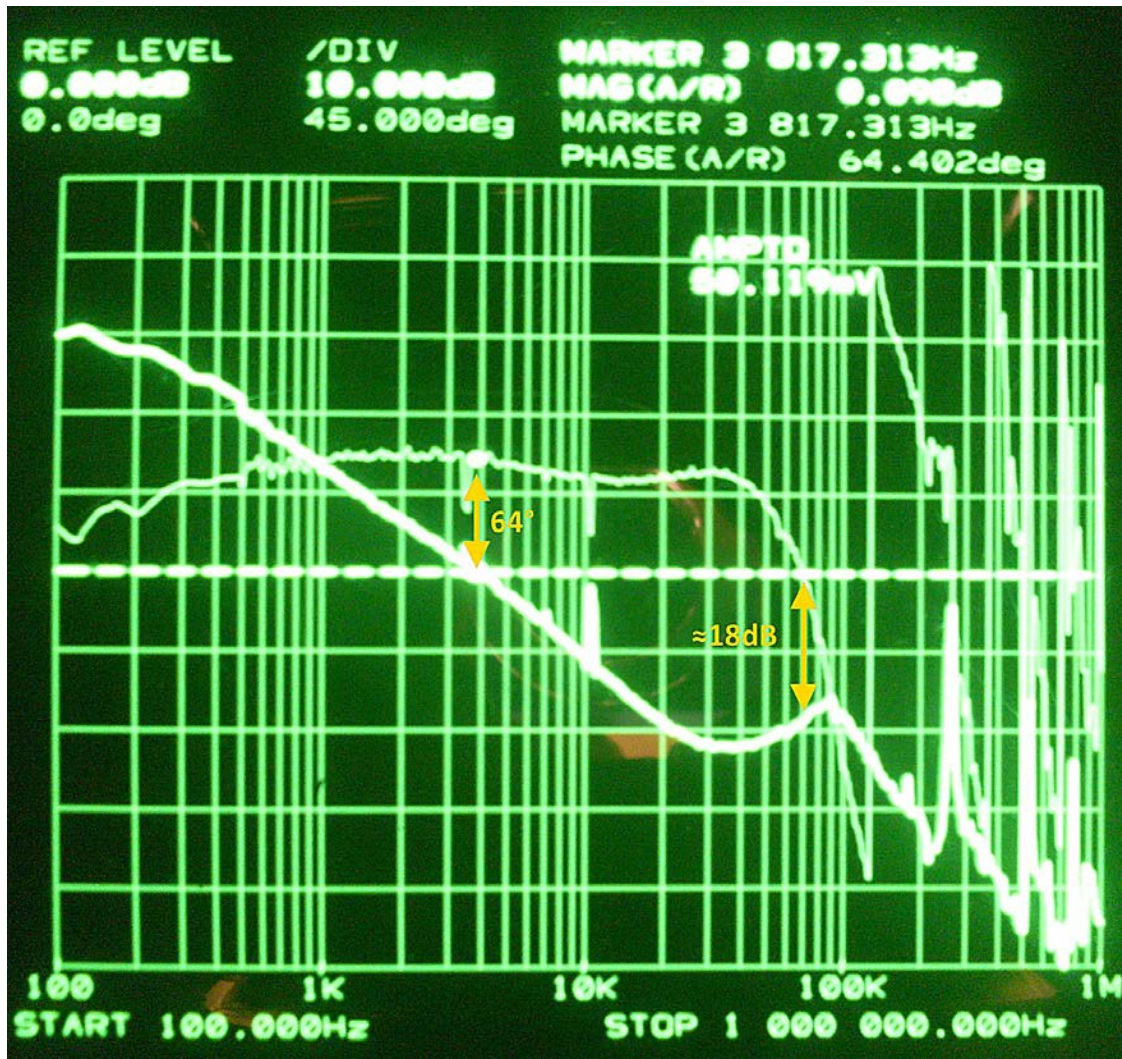


Figure 7. Gain and Phase Margin of the Flyback DC/DC Converter (Conditions:  $V_{PORT} = 57V$ ,  $V_{OUT} = 24V$ ,  $I_{OUT} = 2.7A$ )

CROSSOVER FREQUENCY	GAIN MARGIN	PHASE MARGIN
3.8kHz	18dB	64°

## QUICK START PROCEDURE

### Power over Ethernet (PoE) Input

1. Disconnect auxiliary supply if it is connected to AUX+ and AUX- inputs of the DC2476A-A.
2. Place and connect test equipment (voltmeter, ammeter, oscilloscope and electronic load) as shown in Figure 8.
3. Turn down the electronic load to a minimum value and turn off the electronic load.
4. Connect the output of the IEEE 802.3bt compliant PSE to the RJ45 connector (J1) of the DC2476A using a CAT5e or CAT6 Ethernet cable. (See Note.)
5. After the LED (D4) on the DC2476A is lit, check the output voltage using a voltmeter. Output voltage should be within  $24.0V \pm 0.3V$ .
6. Turn on the electronic load and increase its load current up to 2.7A. Observe the output voltage regulation, efficiency, and other parameters.
7. Verify  $\overline{T2P}$  response with an oscilloscope as shown in Figure 8. The  $\overline{T2P}$  response to the type of PSE connected to the DC2476A-A is provided in Table 1.

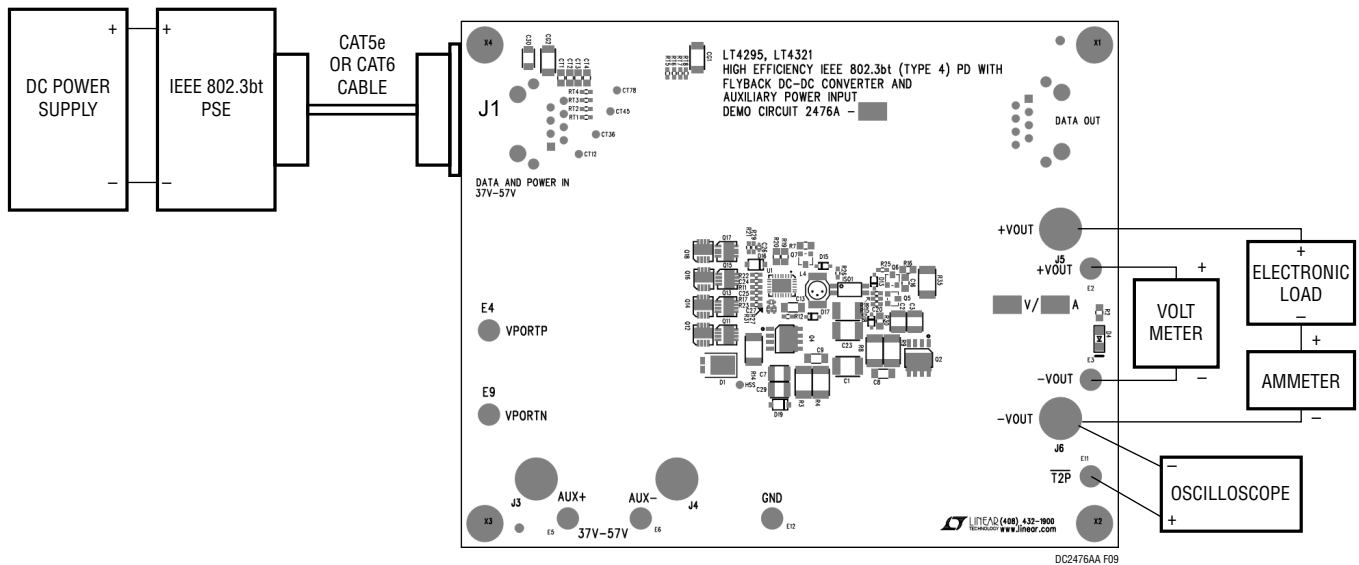


Figure 8. Setup Diagram for PoE Input

Table 1.  $\overline{T2P}$  Response

PSE	$\overline{T2P}$ Response	Negotiated PD Input Power
IEEE	Logic High	13W
	Logic Low	25.5W
	50% Logic High/50% Logic Low, Toggle at 976Hz $\pm 7\%$	51W
	75% Logic High/25% Logic Low, Toggle at 976Hz $\pm 7\%$	71.3W
LTPoE++, 90W	Logic Low	71.3W

## QUICK START PROCEDURE

### Auxiliary Supply Input

1. Place and connect test equipment (voltmeter, ammeter, oscilloscope and electronic load) as shown in Figure 9.
2. Turn down the electronic load to a minimum value and turn off the electronic load.
3. Connect the output of the auxiliary supply to the DC2476A as shown in Figure 9. Turn on the auxiliary supply and set its current limit to 2A. Then increase its output voltage to 48V.
4. Once the LED (D4) on the DC2476A is lit, check the output voltage using a voltmeter. Output voltage should be within  $24.0V \pm 0.3V$ .
5. Turn on the electronic load and increase its load current up to 2.7A. Observe the output voltage regulation, efficiency, and other parameters.
6. Verify  $\overline{T2P}$  response with an oscilloscope as shown in Figure 9. The  $\overline{T2P}$  response during auxiliary power operation is: 75% Logic High/25% Logic Low, Toggle at  $976Hz \pm 7\%$ .

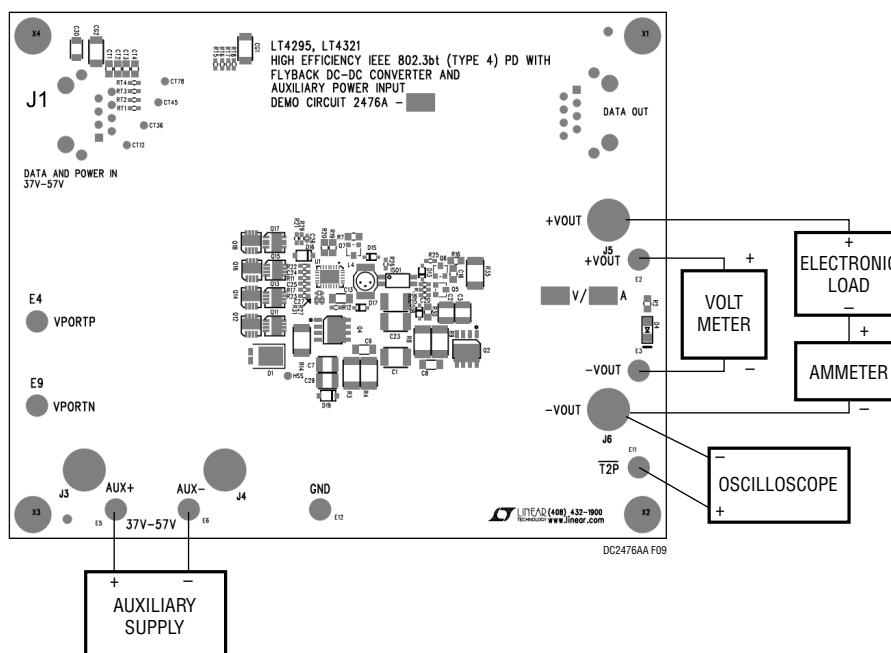
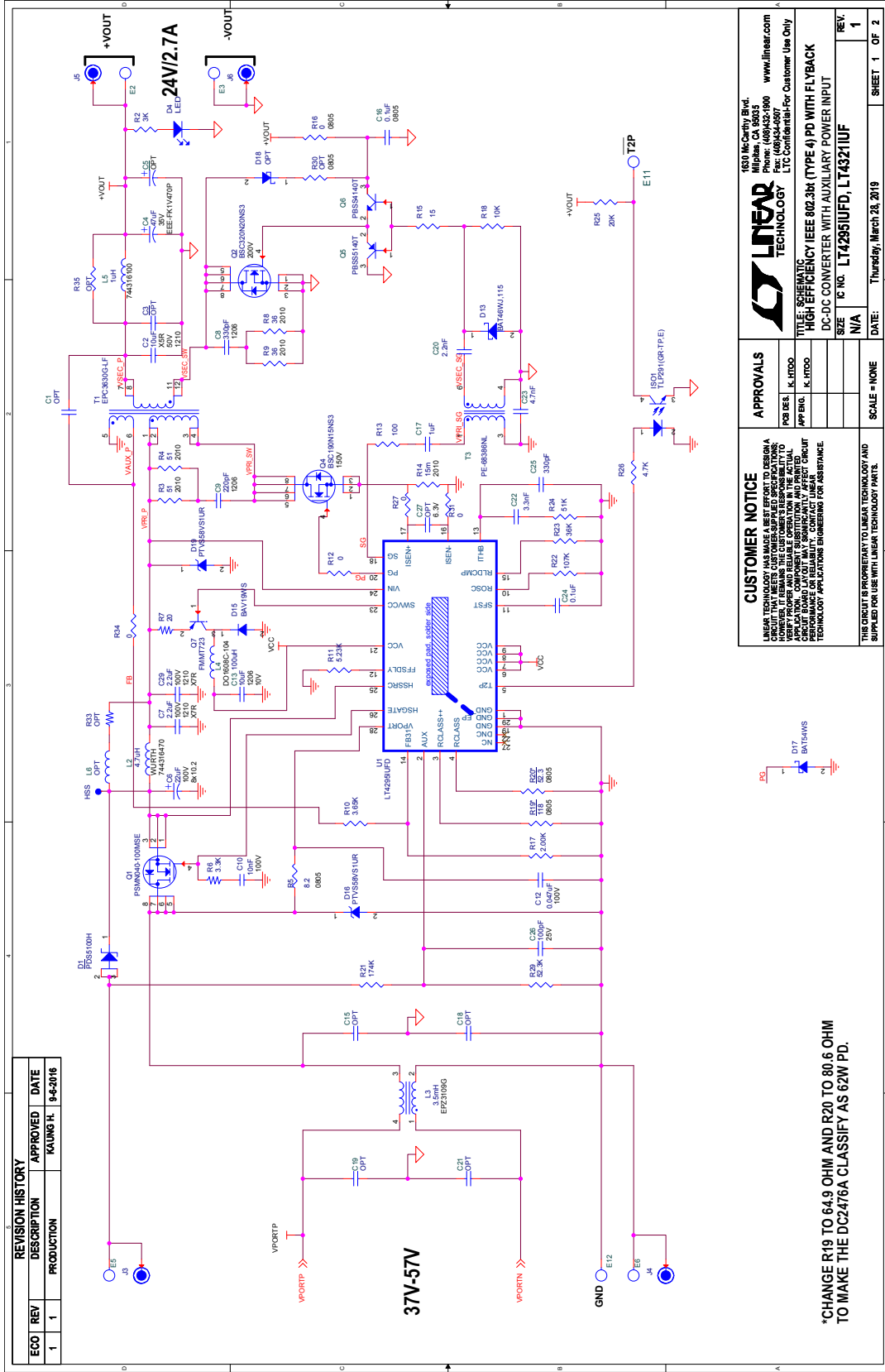


Figure 9. Setup Diagram for Auxiliary Supply Input

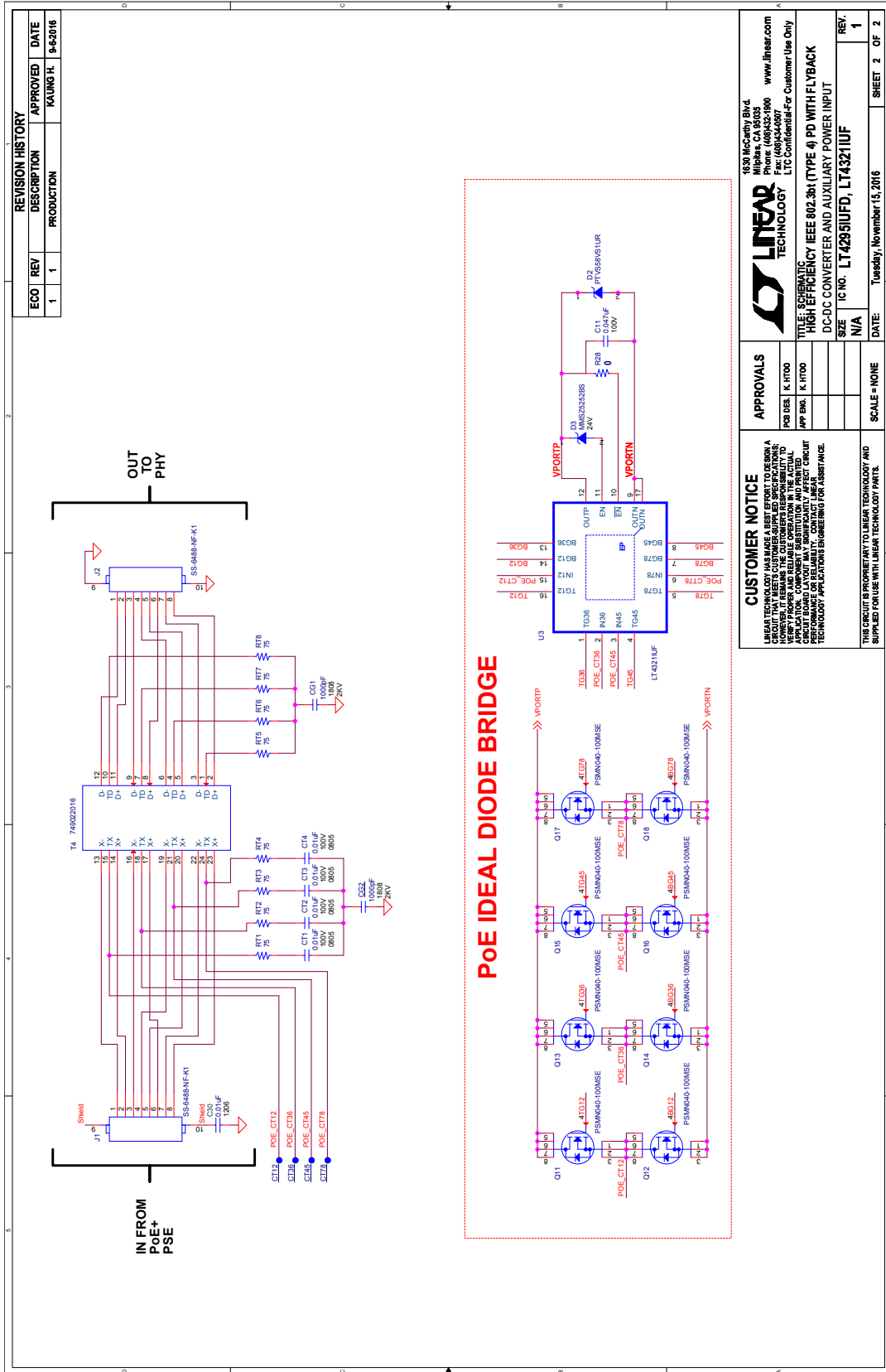
# DEMO MANUAL DC2476A-A

## SCHEMATIC DIAGRAM





SCHEMATIC DIAGRAM



REVISION HISTORY		
ECO	REV	DESCRIPTION
1	1	PRODUCTION

APPROVED		
DATE	APPROVED	DESCRIPTION
9-8-2016	KALING H.	PRODUCTION

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**APPROVALS**  
 PCB DES. | K. HITOO  
 APP ENG. | K. HITOO

**SCALE** = NONE

**DATE**: Tuesday, November 15, 2016

**SHEET** 2 OF 2

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**TITLE: SCHEMATIC**  
**DC-DC CONVERTER AND AUXILIARY POWER INPUT**  
 HIGH EFFICIENCY IEEE 802.3bt (TYPE 4) PD WITH FLYBACK  
 IC NO. LT4295IUFD, LT4321UF

**REV.**  
 SIZE: N/A  
 DATE: Tuesday, November 15, 2016



## ESD Caution

**ESD (electrostatic discharge) sensitive device.** Charged devices and circuit boards can discharge without detection. Although this product features patented or proprietary protection circuitry, damage may occur on devices subjected to high energy ESD. Therefore, proper ESD precautions should be taken to avoid performance degradation or loss of functionality.

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