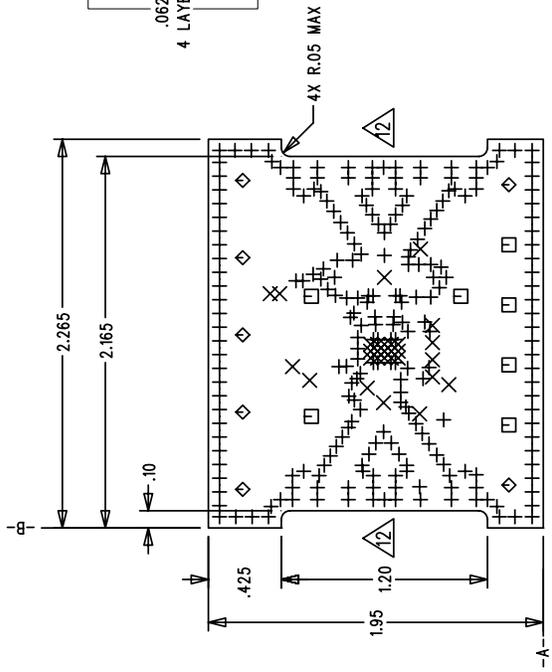


LTC CONFIDENTIAL - For Customer Use Only

DWG NO DC807A		SH 1	REV 1
REVISION HISTORY			
ECO	REV	DATE	APPROVED
	1	06/08/04	
PROTO			



COMPONENT SIDE

HOLE LEGEND

SIZE	QTY	SYM	PLTD	TOL
10	235	+	YES	+/-0.003
15	29	X	YES	+/-0.003
64	7	□	YES	+/-0.003
93	7	◇	YES	+/-0.005

- NOTES: UNLESS OTHERWISE SPECIFIED
- ARTWORK P/N DC807A REV 1.
  - FAB PER IPC-A-600
  - MATERIAL: EPOXY FIBERGLASS, NEMA GRADE FR-4 FINISHED THICKNESS TO BE .062 +/- .003 INCH WITH 2 OZ. COPPER ON TWO OUTER LAYERS AND 1 OZ. COPPER ON TWO INTERNAL LAYERS. FLAMABILITY RATING: 94 V-0 MINIMUM.
  - OUTER DIELECTRIC THICKNESS: TARGET 50-OHM USING 30 MIL TRACE. SEE STACKUP DIAGRAM.
  - SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN. -A- AND -B- ARE PRIMARY DATUMS.
  - BOARD: SELECTIVE PLATED BOARD. SOLDER MASK OVER BARE COPPER. WHITE TIN BOTH SIDES.
  - SOLDER MASK TYPE: WET PHOTO-IMAGEABLE THE PCB COLOR: GREEN
  - SILKSCREEN COMPONENT SIDE WITH WHITE NON-CONDUCTIVE INK.
  - PLATE THRU ALL HOLES WITH COPPER
  - MIN. PLATING THICKNESS: 1 OZ. EXCEPT WHERE PLATING NOT REQUIRED
  7. DRILL: ALL HOLES SHALL BE DRILLED +/- .003 INCH WITH RESPECT TO CTR. OF DRILLED PAD. ALL HOLES FINISHED SIZE AFTER PLATING.
  8. DROP ALL UNUSED PADS ON INNER LAYERS.
  9. DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
  10. CONTROLLED 50 OHM IMPEDANCE(AT 2.5GHz FREQ.) FOR LAYER 1-2.
  11. SUBJECT TO CHANGE BY MANUFACTURER, DEPENDING ON DIELECTRIC CONSTANT DEVIATIONS, USED BY LTC FOR CALCULATIONS.
  12. INNER AND OUTER LAYER COPPER SHALL BE EXPOSED IN TWO INSET AREAS ALONG BOARD EDGES. DO NOT MODIFY INNER LAYER COPPER BACKOFF OUTLINE.

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES ON ANGLE ± P 2 PLACE ± .01 3 PLACE ± .005 INTERPRET DIM AND TOL PER ASME Y14.5M - 1994		CONTRACT NO	
THIRD ANGLE PROJECTION		APPROVALS	DATE
		DRAWN mei	10/07/03
		CHECKED	
		APPROVED	
DO NOT SCALE DRAWING		ENGINEER	
TITLE		FAB, LT5524EFE, IF AMP/ADC DRIVER	
SIZE A	CAGE CODE	DWG NO DC807A	REV 1
SCALE 1/1	FILENAME: 807A-1.PCB	SHEET 1 OF 1	



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