	5	4	3	2	1
D		SOURCE @ 0V - 44V E3	VCC 2.85V - 36V © E1	C1 0.1uF 50V	D
c		RS 0.020 RS 0.020 R1 100	2 8 IN+ VCC VOUT VCC VOUT VEE U1 4 LT6105CMS8 =	5 E2 VOUT 1V/A A.99K	c
в	_	TO LOAD [®] E4		E5 GNE) В
A		CUSTOMER NOTICE LINEAR TECHNOLOGY HAS MADE A BEST EFFORT CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPEC HOWEVER, IT REMAINS THE CUSTOMER'S RESPON VERIFY PROPER AND RELIABLE OPERATION IN TH APPLICATION. COMPONENT SUBSTITUTION AND F CIRUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFE PERFORMANCE OR RELIABILITY. CONTACT LINEA TECHNOLOGY APPLICATIONS ENGINEERING FOR THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHN	E CONTRACT NO. I TO DESIGN A CIFICATIONS; NSIBILITY TO HE ACTUAL PRINTED ECT CIRCUIT AR ASSISTANCE. NOLOGY AND	LINEAR LINEAR TECHNOLOGY	0 McCarthy Blvd. pitas, CA 95035 pne: (408)432-1900 (: (408)434-0507 C Confidential-For Customer Use Only ent Sense AMP REV A
	5	SUPPLIED FOR USE WITH LINEAR TECHNOLOGY	PARTS. DA	TE: Friday, November 30, 2007	SHEET 1 OF 1
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