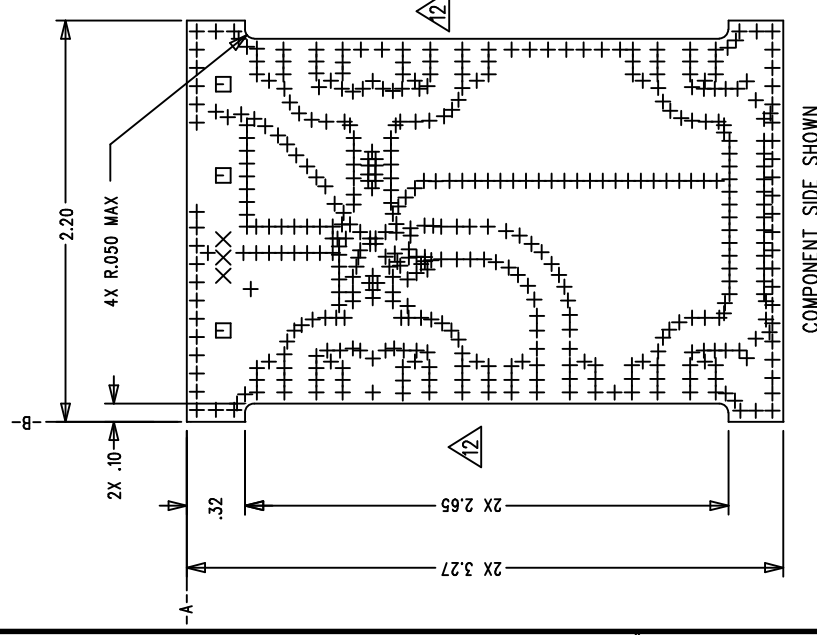


REVISION HISTORY

ECO	REV	DESCRIPTION	DATE	APPROVED
	1	PROTO	08/26/05	

NOTES: UNLESS OTHERWISE SPECIFIED

- ARTWORK P/N DC954A REV 1.
- FAB PER IPC-A-600
- MATERIAL: EPOXY FIBERGLASS, NEMA GRADE FR-4  
FINISHED THICKNESS TO BE .062 +/- .005 INCH  
WITH 2 OZ. COPPER ON TWO OUTER LAYERS AND  
1 OZ. COPPER ON TWO INTERNAL LAYERS.  
FLAMABILITY RATING: 94 V-0 MINIMUM .  
SEE STACKUP DIAGRAM.
- OUTER DIELECTRIC THICKNESS: TARGET 50-OHM USING 30 MIL TRACE.
- SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.  
-A- AND -B- ARE PRIMARY DATUMS.
- BOARD: SELECTIVE PLATED BOARD. SOLDER MASK OVER BARE COPPER.  
WHITE TIN IMMERSION (OMIKRON) BOTH SIDES.  
SOLDER MASK TYPE: WET PHOTO-IMAGEABLE  
THE PCB COLOR: GREEN  
SILKSCREEN COMPONENT SIDE WITH WHITE  
NON-CONDUCTIVE INK.  
PLATE THRU ALL HOLES WITH COPPER  
MIN. PLATING THICKNESS: 1 OZ. EXCEPT  
WHERE PLATING NOT REQUIRED
- DRILL: ALL HOLES SHALL BE DRILLED +/- .003 INCH  
WITH RESPECT TO CTR. OF DRILLED PAD.  
ALL HOLES FINISHED SIZE AFTER PLATING.
- DROP ALL UNUSED PADS ON INNER LAYERS.
- DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
- CONTROLLED 50 OHM IMPEDANCE(AT 2.5GHz FREQ.) FOR LAYER 1-2.
- 17 MILS THICKNESS REFLECTS DIELECTRIC CONSTANT OF 4.1 @ 2GHz.  
SUBJECT TO CHANGE BY MANUFACTURER, DEPENDING ON DIELECTRIC  
CONSTANT DEVIATIONS.
- INNER AND OUTER LAYER COPPER SHALL BE EXPOSED IN  
TWO INSET AREAS ALONG BOARD EDGES.  
DO NOT MODIFY INNER LAYER COPPER BACKOFF OUTLINE.



SIZE	QTY	SYM	PLTD	TOL
10	449	+	YES	+/-0.003
43	3	X	YES	+/-0.003
93	3	□	YES	+/-0.005

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES ON ANGLE ± 1° 2 PLACE ± .01 3 PLACE ± .005 INTERPRET DIM AND TOL PER ASME Y14.5M - 1994	CONTRACT NO.		1630 McCarthy Blvd. Milpitas, CA 95035 Phone: (408)432-1900 Fax: (408)434-0507	
	APPROVALS	DATE	TITLE	
	DRAWN MEI	08/26/05	FAB, LT6402CUD-X, ADC DRIVER	
	CHECKED		SIZE A	DWG NO. DC954A
APPROVED		SCALE 1/1	FILENAME: 954A-1.PCB	SHEET 1 OF 1
DESIGNED BY MEI	08/26/05	REV 1		