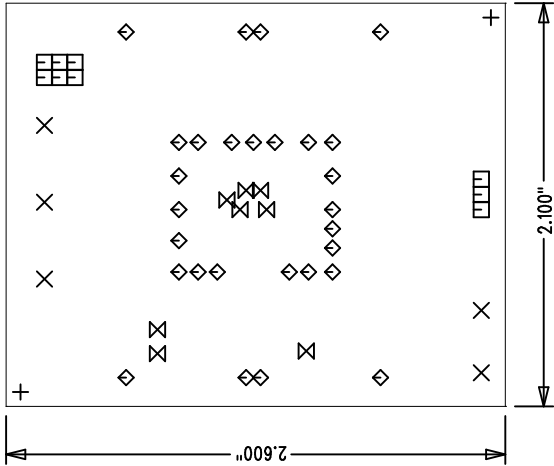


REVISIONS			
REV		APPR	DATE



- ART01 = LAYER 1 ( COMPONENT SIDE )
- ART02 = LAYER 2 (GND)
- ART03 = LAYER 3 (GND)
- ART04 = LAYER 4 (SOLDER SIDE)

**NOTES : Unless Otherwise Specified**

- MATERIAL : FR4 OR EQUIVALENT EPOXY, 2 OZ. COPPER CLAD  
THICKNESS .062 +/--.006 TOTAL OF 4 LAYERS
- FINISH : ALL PLATED HOLES .001 MIN. / .0015 MAX. COPPER PLATE  
WHITE TIN IMMERSION (OMIKRON) BOTH SIDES.  
BEFORE REFLOW , SOLDER MASK OVER BARE COPPER (SMOBC)
- SOLDER MASK : BOTH SIDES USING GREEN LPI
- SILKSCREEN : USING WHITE NON-CONDUCTIVE EPOXY INK
- ALL DIMENSIONS ARE IN INCHES

**Fabrication Drawing**

<b>APPROVALS</b>		<b>LINEAR TECHNOLOGY</b>		1630 McCarthy Blvd. Milpitas, CA 95035 PH: (408)432-1900																
<table border="1"> <tr><td>DRAWN</td><td>INIT</td><td>DATE</td></tr> <tr><td> </td><td> </td><td> </td></tr> <tr><td>CHECK</td><td> </td><td> </td></tr> <tr><td>DESIGN</td><td> </td><td> </td></tr> <tr><td>ENGR</td><td> </td><td> </td></tr> </table>	DRAWN	INIT	DATE				CHECK			DESIGN			ENGR			Title: <b>LT6600CS8</b> <b>Fully Differential LowPass Filter</b>		Size: <b>None</b> Demo Board: <b>DC962A</b>		Rev.
DRAWN	INIT	DATE																		
CHECK																				
DESIGN																				
ENGR																				
Scale = None			Design #		Sht: 1 of 1															

SIZE	QTY	SYM	PLTD	TOL
70	2	+	N0	+/-0.0
65	5	×	YES	+/-0.0
40	9	□	YES	+/-0.0
20	28	◇	YES	+/-0.0
10	8	⊗	YES	+/-0.0