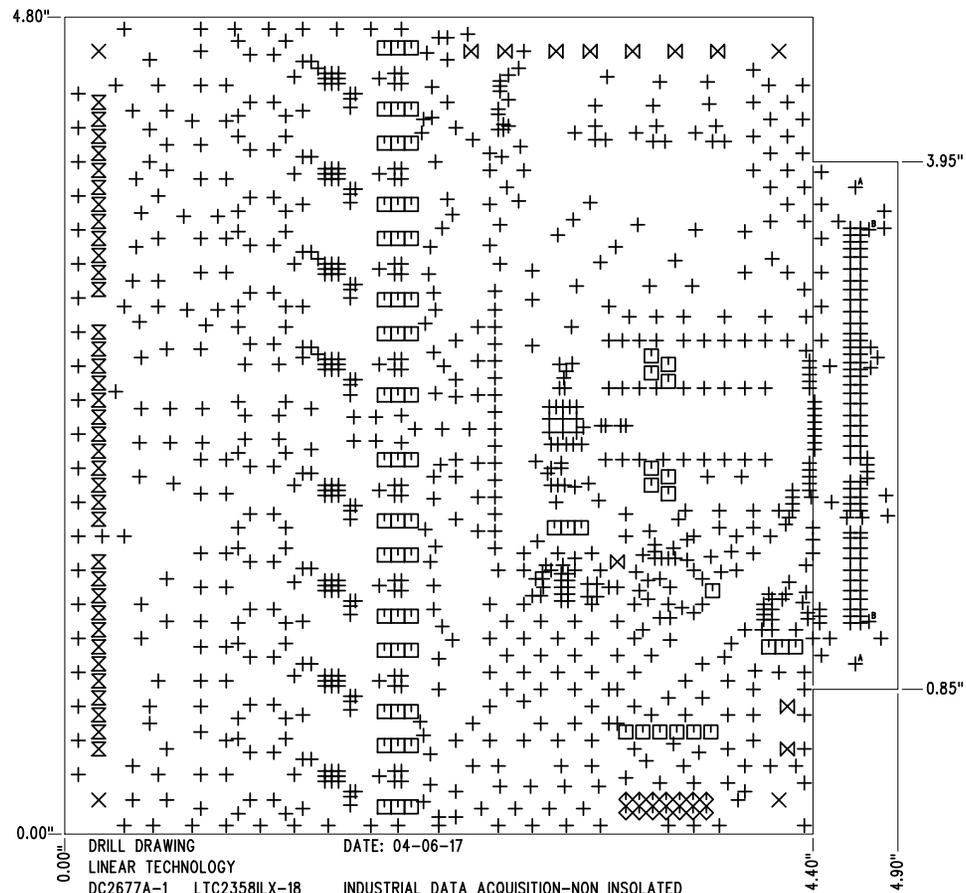
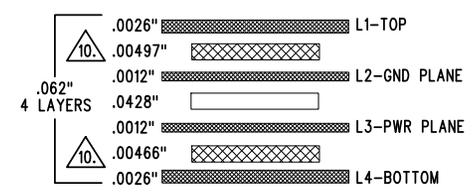


SHOWN FROM TOP SIDE

REVISION HISTORY				
ECO	REV	DESCRIPTION	APP. ENG.	DATE
-	1	1ST PROTOTYPE	NOE Q.	04-06-17

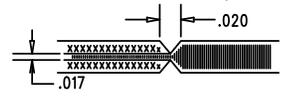


LAYER STRUCTURE



NOTES: UNLESS OTHERWISE SPECIFIED

- FAB PER IPC-A-600.
- MATERIAL: -LEAD FREE ASSEMBLY COMPLIANT, ISOLA FR-370HR OR EQUIVALENT.
-FINISHED THICKNESS TO BE 0.062" +/- .005"
-TOTAL OF 4 LAYERS WITH 2 OZ. CU ON THE OUTER LAYERS AND 1 OZ. CU ON THE INNER LAYERS.
-FLAMMABILITY RATING: 94 V-0 MINIMUM.
- SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.
0.00" ARE PRIMARY DATUMS.
- DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, 0.001" THICK MIN.
-ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
-HOLE LOCATION TOLERANCES ARE +/-0.003" IN RELATION TO CENTER
- FINISH: -SMOBC USING LPI BOTH SIDES, COLOR GREEN.
-GOLD IMMERSION BOTH SIDES.
-FOR SILKSCREENS: USE WHITE NON-CONDUCTIVE INK.
- DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE. PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
- PCBS ARE TO BE RoHS COMPLIANT.
- SCORING FOR PANELIZED PCB (PRODUCTION FAB ONLY):



- CONTROLLED 50 OHM +/-10% IMPEDANCE FOR LAYERS 1-2 AND 6-5 USING 0.009" TRACE AT 2GHz FREQ.

\triangle SUBJECT TO CHANGE BY MANUFACTURER, DEPENDING ON DIELECTRIC CONSTANT DEVIATIONS. PLEASE CONSULT LTC.

DRILL DRAWING DATE: 04-06-17
 LINEAR TECHNOLOGY
 DC2677A-1 LTC2358ILX-18 INDUSTRIAL DATA ACQUISITION-NON INSOLATED

SIZE	QTY	SYM	PLATED	TOL
0.012	941	+	YES	+/-0.003"
0.125	4	X	NO	+/-0.003"
0.035	69	□	YES	+/-0.003"
0.04	14	◇	YES	+/-0.003"
0.045	36	⊗	YES	+/-0.003"
0.063	10	⊗	YES	+/-0.003"
0.125	2	+ ^A	YES	+/-0.003"
0.04	2	+ ^B	NO	+/-0.003"

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES: 0.XX" = ±0.01" 0.XXX" = ±0.005" INTERPRET DIM AND TOL PER ASME Y14.5M-1994 THIRD ANGLE PROJECTION	APPROVALS		1630 MCCARTHY BLVD MILPITAS, CA 95035 PH: (408)432-1900 www.linear.com LTC CONFIDENTIAL- FOR CUSTOMER USE ONLY	
	PCB DES.	KIM T.		TITLE: FABRICATION DRAWING INDUSTRIAL DATA ACQUISITION-NON ISOLATED
	APP ENG.	NOE Q.		
	SCALE = NONE	SIZE		IC NO.
		FILENAME:	DC2677A-1.PCB	SHT 1 OF 1