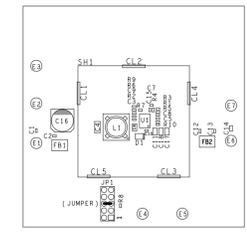


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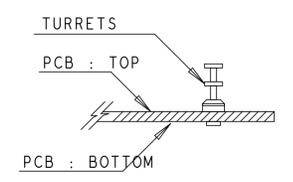
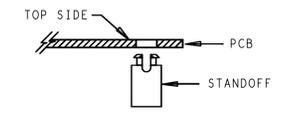
REVISIONS			
REV	DESCRIPTION	DATE	APPROVED
A	INITIAL RELEASE	06May21	X
B	DESIGN CHANGE	16Aug21	X

ASSEMBLY NOTES:

1. BOARD ACCEPTABILITY PER ANALOG DEVICES, INC. SPECIFICATION TST00119 (LATEST REVISION).
2. REPAIRS PER IPC-7711/21(LATEST REVISION) ARE ALLOWED.
3. REPAIRS ARE NOT ALLOWED IN SOLDERMASK FREE AREAS ON EITHER SIDE OF THE BOARD.



INSTALL STANDOFFS AS SHOWN BELOW:



PRIMARY SIDE

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES		APPROVAL		DATE		TITLE	
TOLERANCES		TOLERANCE ENGINEER		DATE		ASSEMBLY	
DECIMALS FRACTIONS ANGLES		BILLY PHILLIPS		01SEP20		EVAL-LT8334-AZ	
.XX ±.000		BOB MACDONALD		01SEP20			
.XXX ±.005		DAVE WILLIAMS		01SEP20			
.XXXX ±.0050		COMPONENT ENGINEER		ADCT LIBRARY		01SEP20	
MATERIAL		HARDWARE RELEASE		X		ddMMyy	
FINISH		PCB DESIGNER		X		ddMMyy	
		PCB ENGINEER		X		ddMMyy	
		CHECKER		X		ddMMyy	
DO NOT SCALE DWG		SIZE		FSCM NO		DRAWING NUMBER	
		D		24355		01-067670	
		SCALE		1/1		SHEET 1 OF 1	



8 7 6 5 4 3 2 1

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED
A	INITIAL RELEASE	06May21	X
B	DESIGN CHANGE	16Aug21	X

4 LAYER STACKUP

_____	PRIMARY SILKSCREEN
_____	PRIMARY SOLDER MASK
_____	PRIMARY SIDE (LAYER 1)
_____	~3.5 MIL LAYER 2
_____	LAYER 3
_____	~3.5 MIL SECONDARY SIDE (LAYER 4)
_____	SECONDARY SOLDER MASK
_____	SECONDARY SILKSCREEN

NOMINAL FINISHED THICKNESS
0.062" +/- 10%

SPECIFICATIONS:

ROHS COMPLIANCE NOTE: HOMOGENOUS MATERIALS IN THIS BOARD SHALL BE COMPLIANT THE EU RoHS DIRECTIVE 2002/95/EC

MATERIALS; ALL LAMINATES AND BONDING MATERIALS SHOULD BE SELECTED FROM IPC-4101 OR IPC-4103, MINIMUM Tg>170degC, Td>300degC, U.L. RATING OF 94 V-0

MATERIAL FAMILY; FR406

CLADDING; EXTERNAL LAYERS 2 OZ. COPPER
INTERNAL LAYERS 1 OZ. COPPER.

NOTE: IF THE LAYER STACKUP CONFLICTS WITH THE ABOVE CLADDING SPECIFICATIONS THEN THE LAYER STACKUP SHALL TAKE PRECEDENCE.

SOLDER MASK; SHALL BE LIQUID PHOTOIMAGEABLE (LPI) APPLIED ON BOTH SIDES OVER BARE COPPER OR GOLD AND SHALL MEET IPC-SM-840 (LATEST REV.) CLASS 3, COLOR GLOSS BLUE

SILK SCREEN; SHALL BE PERMANENT NON-CONDUCTIVE EPOXY INK, COLOR: WHITE
SYNTHETIC INKJET PRINTING ALLOWED FOR DENSE BOARDS, COLOR: WHITE

SURFACE FINISH; ENIG (Electroless Nickel/Immersion Gold)
PER IPC-4552 LATEST REVISION

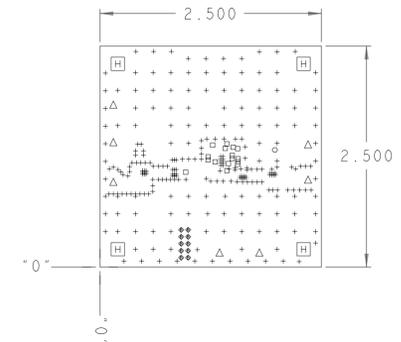
TEST REQUIREMENTS; 100% NETLIST ELECTRICAL VERIFICATION USING CUSTOMER SUPPLIED IPC-D-356 NETLIST FOR OPENS AND SHORTS WHEN "GERBER DATA" IS PROVIDED. THIS VERIFICATION ALSO REQUIRED FOR "ODB++" DATA PER EMBEDDED NETLIST.

REQUIREMENTS:

- REFER TO IPC-6010 SERIES (LATEST REV.), CLASS 2 FOR FABRICATION UNLESS OTHERWISE SPECIFIED.
- ACCEPTABILITY PER ANALOG DEVICES, INC. SPECIFICATION TST00115, (LATEST REVISION.)
- MODIFICATIONS TO THE ARTWORK ARE NOT ALLOWED WITHOUT WRITTEN AUTHORIZATION.
- HOLE PATTERN TOLERANCES FOR UNDIMENSIONED HOLES SHALL BE A DIAMETER OF 0.005 INCHES FROM THEIR TRUE POSITION.
- PLATED HOLE WALL THICKNESS SHALL NOT BE LESS THAN 0.001 INCH MINIMUM AVERAGE, WITH NO READING LESS THAN .0008 BY CROSS SECTION.
- HOLE DIAMETERS APPLY AFTER PLATING.
- FINISHED CONDUCTOR WIDTHS SHALL NOT BE REDUCED FROM THE NOMINAL INDICATED ON THE MASTER PATTERN, BY MORE THAN THE CONDUCTOR THICKNESS.
- MINIMUM DESIGN LINE WIDTH IS .XXX INCH.
- MINIMUM DESIGN SPACING IS .XXX INCH.
- NON-FUNCTIONAL PAD REMOVAL FROM INNER SIGNAL LAYERS MAY BE PERFORMED AFTER CUSTOMER APPROVAL.
- IF PAD SIZES PROVIDED ARE NOT LARGE ENOUGH TO MAINTAIN ANNULAR RING REQUIREMENT, MFR. MAY REQUEST APPROVAL TO TEAR DROP PADS TO MAINTAIN ANNULAR RING. (AT PAD TO TRACE INTERSECTION ONLY AND ELECTRICAL INTEGRITY MUST BE MAINTAINED.)

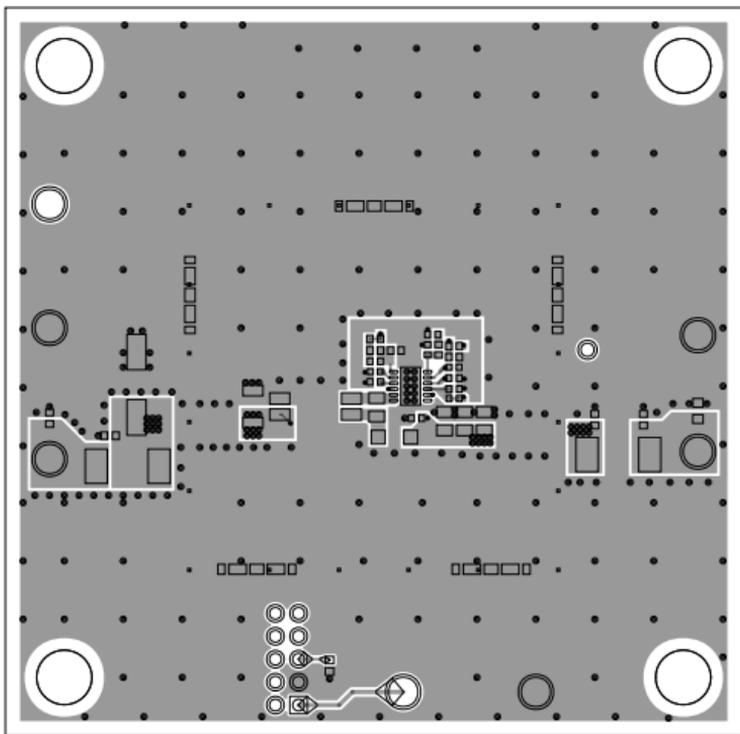
HOLE TOLERANCE
UNLESS SPECIFIED
PLATED: +/- .003
NON PLATED: +/- .001

FINISHED HOLES IN MILS				
ALL UNITS ARE IN MILS				
FIGURE	SIZE	PLATED	QTY	TOLERANCE/NOTES
o	6.0	PLATED	16	
*	10.0	PLATED	264	
*	40.0	PLATED	10	
o	45.0	PLATED	1	
Δ	100.0	PLATED	7	
□	187.0	NON-PLATED	4	
TOTAL HOLES:			302	



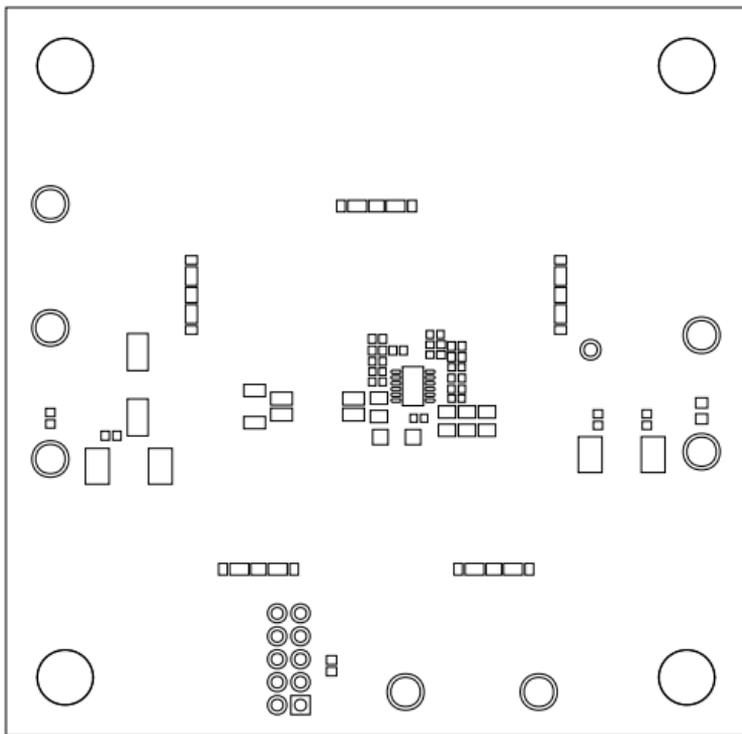
PRIMARY SIDE

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES	APPROVAL	DATE					
TOLERANCES	BILLY PHILLIPS	01SEP20					
DECIMALS FRACTIONS ANGLES	ADCT ENGINEER	01SEP20					
.XX +/- .010	ADCT LIBRARY	01SEP20					
.XXX +/- .005	ADCT LIBRARY	01SEP20	TITLE				
.XXX +/- .005	ADCT LIBRARY	01SEP20	FABRICATION EVAL-LT8334-AZ				
.XXX +/- .005	ADCT LIBRARY	01SEP20	HARDWARE RELEASE				
	ADCT LIBRARY	01SEP20	FINISH	SIZE	FSCM NO	DRAWING NUMBER	REV
	ADCT LIBRARY	01SEP20	X	D	24355	09-067670	B
	ADCT LIBRARY	01SEP20	DO NOT SCALE DWG	SCALE	1/1	SHEET 1 OF 1	



L1 PRIMARY
08-067670-01
EVAL-LT8334-AZ REV B

PRIMARY SIDE
08-067670 REV B
DATE:8-17-21

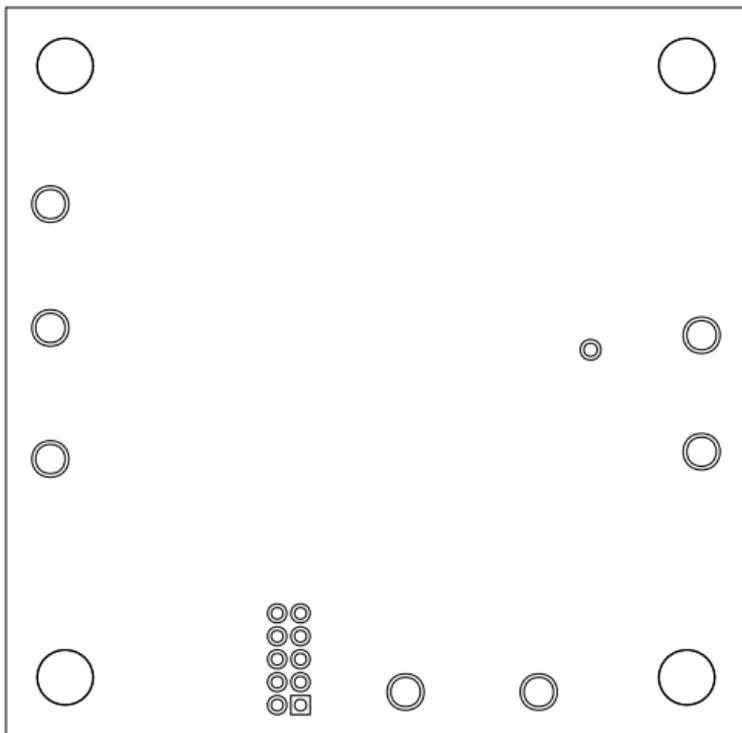


SOLDERMASK PRIMARY

08-067670-04

EVAL-LT8334-AZ REV B

DATE:8-17-21

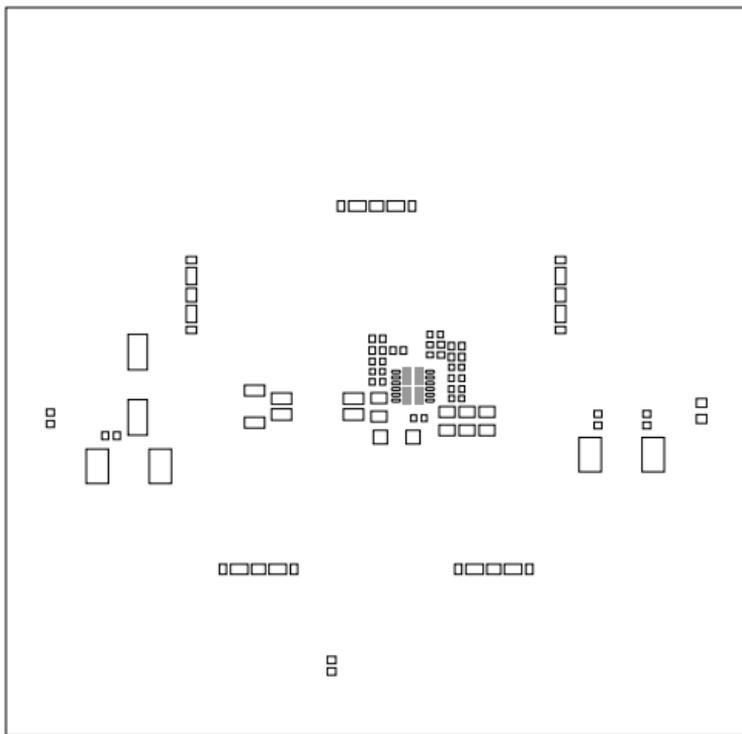


SOLDERMASK SECONDARY

08-067670-06

EVAL-LT8334-AZ REV B

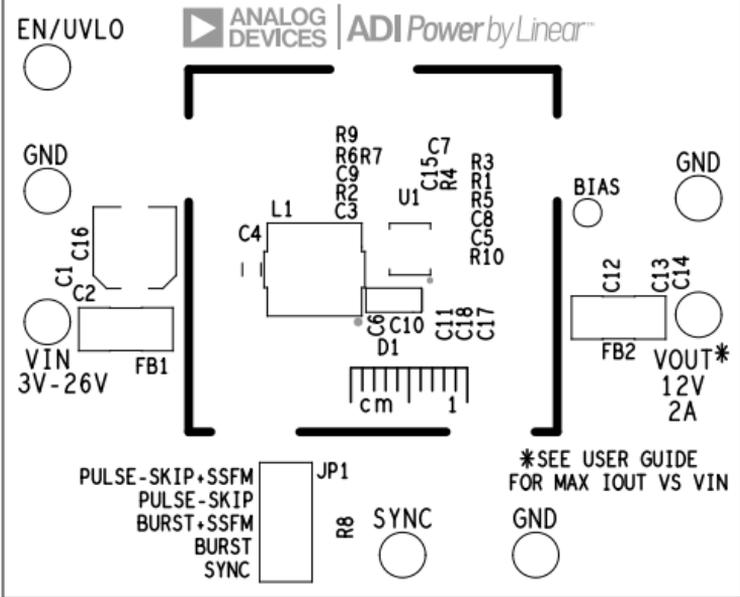
DATE:8-17-21



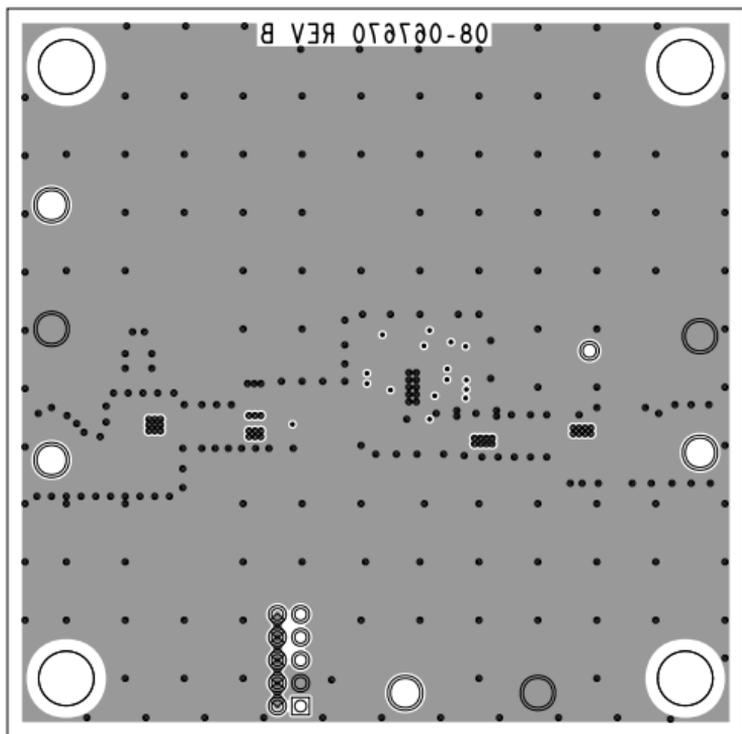
PASTEMASK PRIMARY
08-067670-13
EVAL-LT8334-AZ REV B DATE:8-17-21

EVAL-LT8334-AZ

LOW I_q BOOST/SEPIC/INVERTING CONVERTER WITH 5A, 40V SWITCH

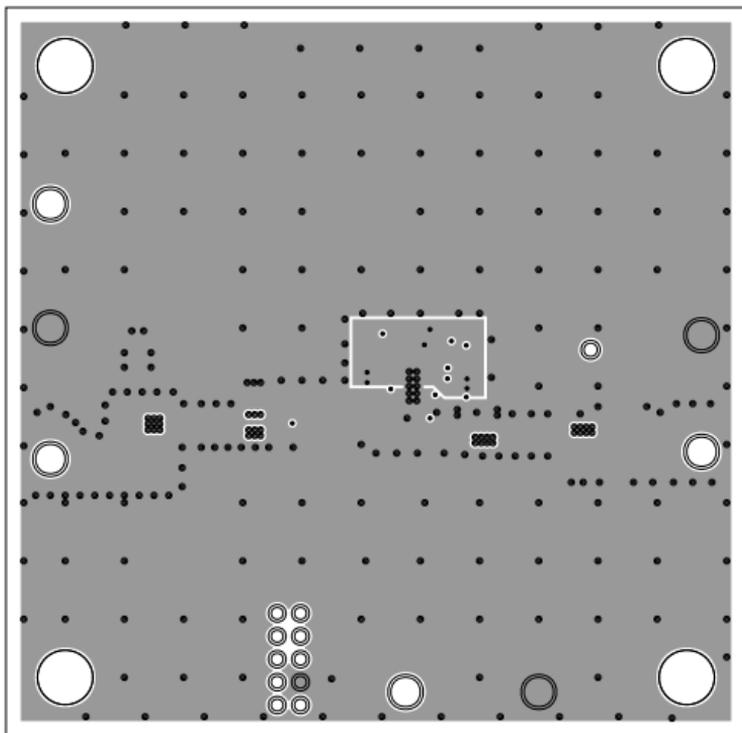


SILKSCREEN PRIMARY
 08-067670-03
 EVAL-LT8334-AZ REV B DATE: 8-17-21



L4 SECONDARY
08-067670-02
EVAL-LT8334-AZ REV B

SECONDARY SIDE
08-067670 REV B
DATE:8-17-21

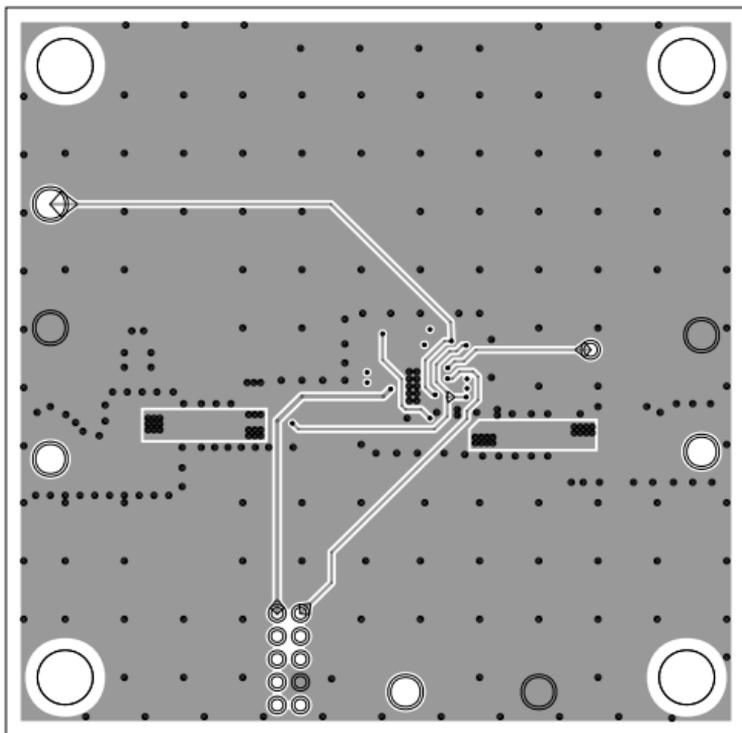


L2 GND/SIGNAL

08-067670-07

EVAL-LT8334-AZ REV B

DATE:8-17-21



L3 GND/SIGNAL

08-067670-08

EVAL-LT8334-AZ REV B

DATE:8-17-21