

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED
A	INITIAL RELEASE	06May21	X
B	DESIGN CHANGE	16Aug21	X

### 4 LAYER STACKUP

_____	PRIMARY SILKSCREEN
_____	PRIMARY SOLDER MASK
_____	PRIMARY SIDE (LAYER 1)
_____	~3.5 MIL LAYER 2
_____	LAYER 3
_____	~3.5 MIL SECONDARY SIDE (LAYER 4)
_____	SECONDARY SOLDER MASK
_____	SECONDARY SILKSCREEN

NOMINAL FINISHED THICKNESS  
0.062" +/- 10%

### SPECIFICATIONS:

ROHS COMPLIANCE NOTE: HOMOGENOUS MATERIALS IN THIS BOARD SHALL BE COMPLIANT THE EU RoHS DIRECTIVE 2002/95/EC

MATERIALS; ALL LAMINATES AND BONDING MATERIALS SHOULD BE SELECTED FROM IPC-4101 OR IPC-4103, MINIMUM Tg>170degC, Td>300degC, U.L. RATING OF 94 V-0

MATERIAL FAMILY; FR406

CLADDING; EXTERNAL LAYERS 2 OZ. COPPER  
INTERNAL LAYERS 1 OZ. COPPER.

NOTE: IF THE LAYER STACKUP CONFLICTS WITH THE ABOVE CLADDING SPECIFICATIONS THEN THE LAYER STACKUP SHALL TAKE PRECEDENCE.

SOLDER MASK; SHALL BE LIQUID PHOTOIMAGEABLE (LPI) APPLIED ON BOTH SIDES OVER BARE COPPER OR GOLD AND SHALL MEET IPC-SM-840 (LATEST REV.) CLASS 3, COLOR GLOSS BLUE

SILK SCREEN; SHALL BE PERMANENT NON-CONDUCTIVE EPOXY INK, COLOR: WHITE  
SYNTHETIC INKJET PRINTING ALLOWED FOR DENSE BOARDS, COLOR: WHITE

SURFACE FINISH; ENIG (Electroless Nickel/Immersion Gold) PER IPC-4552 LATEST REVISION

TEST REQUIREMENTS; 100% NETLIST ELECTRICAL VERIFICATION USING CUSTOMER SUPPLIED IPC-D-356 NETLIST FOR OPENS AND SHORTS WHEN "GERBER DATA" IS PROVIDED. THIS VERIFICATION ALSO REQUIRED FOR "ODB++" DATA PER EMBEDDED NETLIST.

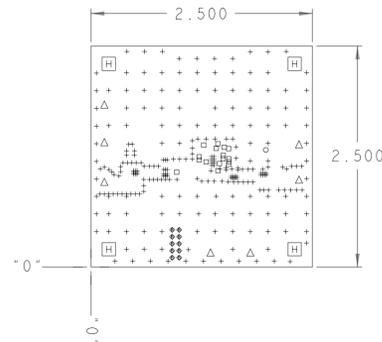
### REQUIREMENTS:

- REFER TO IPC-6010 SERIES (LATEST REV.), CLASS 2 FOR FABRICATION UNLESS OTHERWISE SPECIFIED.
- ACCEPTABILITY PER ANALOG DEVICES, INC. SPECIFICATION TST00115, (LATEST REVISION.)
- MODIFICATIONS TO THE ARTWORK ARE NOT ALLOWED WITHOUT WRITTEN AUTHORIZATION.
- HOLE PATTERN TOLERANCES FOR UNDIMENSIONED HOLES SHALL BE A DIAMETER OF 0.005 INCHES FROM THEIR TRUE POSITION.
- PLATED HOLE WALL THICKNESS SHALL NOT BE LESS THAN 0.001 INCH MINIMUM AVERAGE, WITH NO READING LESS THAN .0008 BY CROSS SECTION.
- HOLE DIAMETERS APPLY AFTER PLATING.
- FINISHED CONDUCTOR WIDTHS SHALL NOT BE REDUCED FROM THE NOMINAL INDICATED ON THE MASTER PATTERN, BY MORE THAN THE CONDUCTOR THICKNESS.
- MINIMUM DESIGN LINE WIDTH IS .XXX INCH.
- MINIMUM DESIGN SPACING IS .XXX INCH.
- NON-FUNCTIONAL PAD REMOVAL FROM INNER SIGNAL LAYERS MAY BE PERFORMED AFTER CUSTOMER APPROVAL.
- IF PAD SIZES PROVIDED ARE NOT LARGE ENOUGH TO MAINTAIN ANNULAR RING REQUIREMENT, MFR. MAY REQUEST APPROVAL TO TEAR DROP PADS TO MAINTAIN ANNULAR RING. (AT PAD TO TRACE INTERSECTION ONLY AND ELECTRICAL INTEGRITY MUST BE MAINTAINED.)

HOLE TOLERANCE  
UNLESS SPECIFIED  
PLATED: +/- .003  
NON PLATED: +/- .001

FINISHED HOLES IN MILS				
ALL UNITS ARE IN MILS				
FIGURE	SIZE	PLATED	QTY	TOLERANCE/NOTES
o	6.0	PLATED	16	
*	10.0	PLATED	264	
*	40.0	PLATED	10	
o	45.0	PLATED	1	
Δ	100.0	PLATED	7	
[H]	187.0	NON-PLATED	4	

TOTAL HOLES: 302



### PRIMARY SIDE

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES	APPROVAL	DATE				
TOLERANCES	BILLY PHILLIPS	01SEP20				
DECIMALS FRACTIONS ANGLES	FORMER ENGINEER BOB MACDONALD	01SEP20	TITLE FABRICATION EVAL-LT8334-AZ			
.XX +/- .010 .XXX +/- .005 .XXXX +/- .0050	FORMER SYSTEMS DAVE WILLIAMS	01SEP20				
MATERIAL	COMPONENT ENGINEER ADGT LIBRARY	01SEP20	SIZE	FSCM NO	DRAWING NUMBER	REV
FINISH	HWWARE RELEASE X	ddMMyy	D	24355	09-067670	B
DO NOT SCALE DWG	PBR DESIGNER X PDR ENGINEER X CHECKER X	ddMMyy ddMMyy ddMMyy	SCALE	1/1	SHEET	1 OF 1