

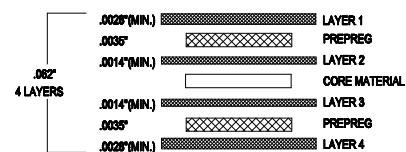
SIZE	QTY	SYM	PLATED	TOL
0.01	212	+	YES	+/-0.003
0.095	7	□	YES	+/-0.003
0.035	10	◇	YES	+/-0.003
0.07	2	⊗	NO	+/-0.003
0.006	3	⊗	YES	+/-0.003

ADDITIONAL REQUIREMENT FOR PROTOTYPE FAB ONLY:

1. OUTGOING INSPECTION REPORT (BASED ON ACTUAL MEASUREMENTS AND CROSS SECTION).

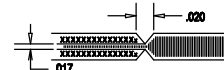
1. PROVIDE COMPLIANCE CERTIFICATES FOR RoHS, REACH AND CONFLICT-FREE MINERALS.
2. SOLDERABILITY BOARD WITH TEST RESULTS.
3. OUTGOING INSPECTION REPORT (BASED ON ACTUAL MEASUREMENTS AND CROSS SECTION).
4. VACUUM PACKED WITH DESICCANT.
5. FULL PANEL WITH NO REJECT.

LAYER STRUCTURE



NOTES: UNLESS OTHERWISE SPECIFIED

1. FAB PER IPC-A-800.
2. MATERIAL: -LEAD FREE ASSEMBLY COMPLIANT, ISOLA FR-370HR OR EQUIVALENT.
- FINISHED THICKNESS TO BE 0.082 +/- .005
 - TOTAL OF 1 LAYERS WITH 2 OZ. CU ON THE OUTER LAYERS AND 1 OZ. CU ON THE INNER LAYERS.
 - FLAMMABILITY RATING: 94 V-0 MINIMUM.
3. SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.
- 0.007" ARE PRIMARY DATUMS.
4. DRILLING: -DRILL HOLES PER SCHEDULE PLATE THROUGH HOLES WITH MAXIMUM 0.001 THICKNESS.
- ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
 - HOLE LOCATION TOLERANCES ARE +/-0.003 IN RELATION TO CENTER
5. FINISH: -SMOBC USING LP1 BOTH SIDES, COLOR BLUE.
- GOLD IMMERSION BOTH SIDES.
 - FOR SILKSCREENS: USE WHITE NON-CONDUCTIVE INK.
6. DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
- PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
7. PCBs ARE TO BE ROHS COMPLIANT.
8. SCORING FOR PANELIZED PCB (PRODUCTION FAB ONLY):



UNLESS OTHERWISE SPECIFIED

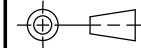
DIMENSIONS ARE IN INCHES
TOLERANCES:

0.XX" = ± 0.01 "
0.XXX" = ± 0.005 "

0.XXX" = ± 0.005 "
INTERPRET DIM AND TOL

INTERPRET DIM AND TOL
PER ASME Y14.5M-1994

THIRD ANGLE PROJECTION



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POWER BY
LINEAR

TITLE: FABRICATION DRAWING

28V, 5A LOW IQ SYNCHRONOUS
STEP-UP SILENT SWITCHER WITH PAS

SIZE	IC NO.	IT8337 IV	R
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SIZE	IC NO.	LT8337JV	REV.
N/A		EVAL-LT8337-A7	

N/A	EVAL ET0597 AZ	
FILENAME:		SHT 1 OF