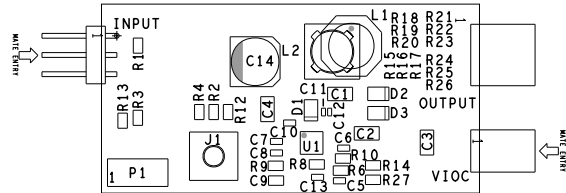


REVISIONS			
REV	DESCRIPTION	DATE	APPROVED
A	INITIAL RELEASE	24NOV18	J VIERNES
B	AS PER ECR-086393	06MAY19	J VIERNES

ASSEMBLY NOTES:


1.
- BOARD ACCEPTABILITY PER ANALOG DEVICES, INC. SPECIFICATION TST00119 (LATEST REVISION).
2.
- REPAIRS PER IPC-7711/21(LATEST REVISION) ARE ALLOWED.
3.
- REPAIRS ARE NOT ALLOWED IN SOLDERMASK FREE AREAS ON EITHER SIDE OF THE BOARD.
4.
- RoHS COMPLIANCE: ASSEMBLY VENDOR SHOULD ASSURE COMPLIANCE WITH LEAD-FREE AND RoHS PCB ASSEMBLY STANDARDS (EU RoHS DIRECTIVE 2002/95/EC).
5.
- INSTALL SOLDER BLOB USING THE TABLE BELOW (SEE DETAIL A) AND PLACE MARK ON TICK BOX IN THE PRIMARY SIDE



BOM	PARTS	TICK BOX
05-051143-01-B	R15, R16, R17, R24, R25, R26	CUK
05-051143-02-B	R18, R19, R20, R21, R22, R23	SEPIC
05-051143-03-B	R21, R22, R23	BOOST

DETAIL A  
SCALE FACTOR: 1:4

PRIMARY SIDE

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES  TOLERANCES  DECIMALS   FRACTIONS   ANGLES .XX   -.010   --1/32   -- 2 .XXX   -.005 .XXXX   -.0050			APPROVAL		DATE		<div><div></div><div>ANALOG DEVICES</div></div> <div>WWM DIVISION 804 WOBURN STREET WILMINGTON, MA 01887</div>							
			TEMPLATE ENGINEER N/A		N/A									
			HARDWARE SERVICES N/A		N/A									
			HARDWARE SYSTEMS N/A		N/A									
MATERIAL			TEST ENGINEER J VIERNES		24NOV18		TITLE  ASSEMBLY  SCP-LT8362 EVAL-Z							
			COMPONENT ENGINEER N/A		N/A									
			TEST PROCESS N/A		N/A									
			HARDWARE RELEASE N/A		N/A									
FINISH			DESIGNER L EVANGELIO		22OCT18		SIZE	FSCM NO	DRAWING NUMBER	REV				
			PTD ENGINEER J VIERNES		24NOV18									
			CHECKER N/A		N/A									
DO NOT SCALE DWG							SCALE		1 / 1		SHEET		1 OF 1	

EVALUATION BOARD