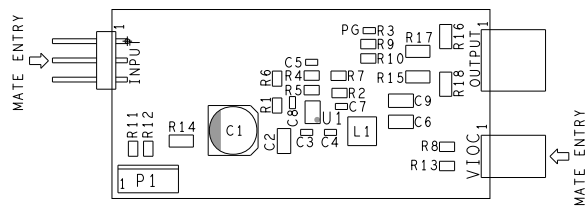



REVISIONS			
REV	DESCRIPTION	DATE	APPROVED
A	INITIAL RELEASE	13DEC19	A.SERQUINA

ASSEMBLY NOTES:

1.
- BOARD ACCEPTABILITY PER ANALOG DEVICES, INC. SPECIFICATION TST00119 (LATEST REVISION).
2.
- REPAIRS PER IPC-7711/21(LATEST REVISION) ARE ALLOWED.
3.
- REPAIRS ARE NOT ALLOWED IN SOLDERMASK FREE AREAS ON EITHER SIDE OF THE BOARD.
4.
- RoHS COMPLIANCE: ASSEMBLY VENDOR SHOULD ASSURE COMPLIANCE WITH LEAD-FREE AND RoHS PCB ASSEMBLY STANDARDS (EU RoHS DIRECTIVE 2002/95/EC).



PRIMARY SIDE

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES			APPROVAL		DATE		<div><div></div><div>ANALOG DEVICES</div></div> <div>WWM DIVISION 804 WOBURN STREET WILMINGTON, MA 01887</div>			
TOLERANCES			TEMPLATE ENGINEER		N/A					
DECIMALS FRACTIONS ANGLES .XX -.010 --1/32 -- 2 .XXX -.005 .XXXX -.0050			HARDWARE SERVICES		N/A					
			HARDWARE SYSTEMS		N/A					
MATERIAL			TEST ENGINEER		N/A		TITLE ASSEMBLY SCP-LT8618 EVALZ			
			COMPONENT ENGINEER A.CAUILAN		05DEC18					
			TEST PROCESS		N/A					
			HARDWARE RELEASE K.JABATAN		05DEC18					
FINISH			DESIGNER L.EVANGELIO		23OCT19		SIZE	FSCM NO	DRAWING NUMBER	REV
			PTD ENGINEER A.SERQUINA		13DEC19					
			CHECKER N/A		N/A					
DO NOT SCALE DWG							SCALE	1 / 1		SHEET 1 OF 1

EVALUATION BOARD